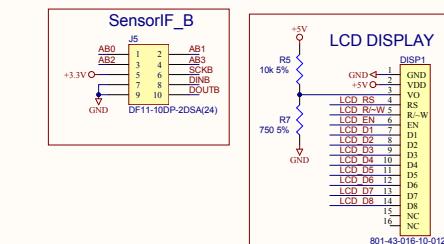
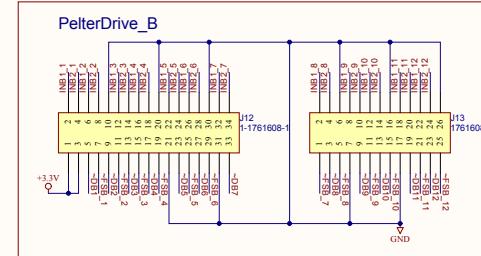
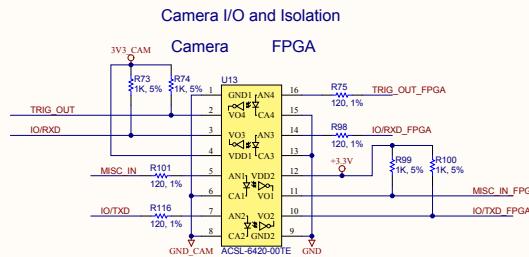
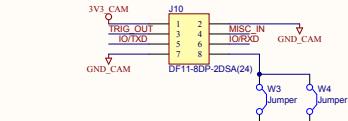
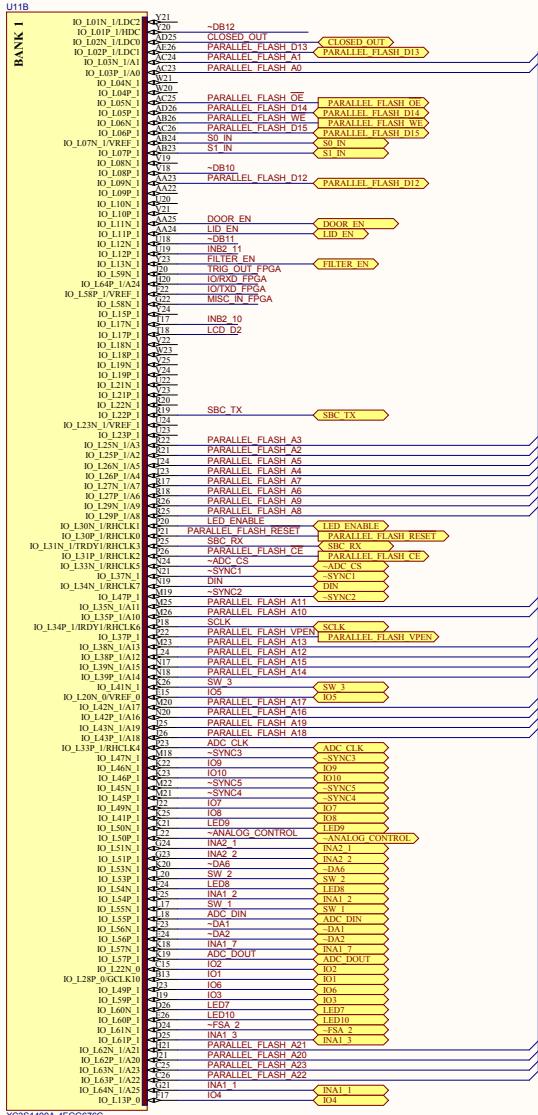
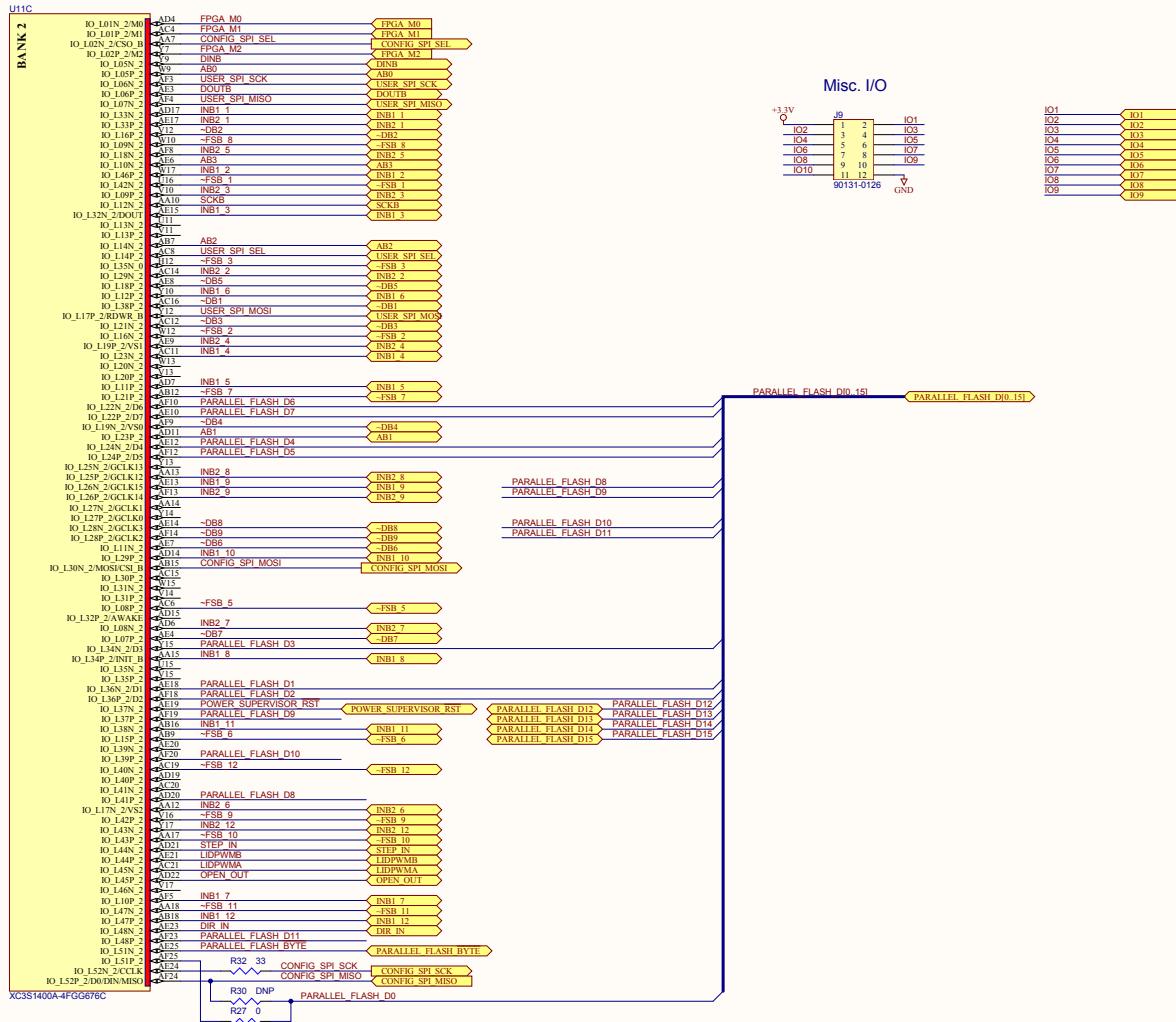


PUDC_B = Pull Up During Configuration

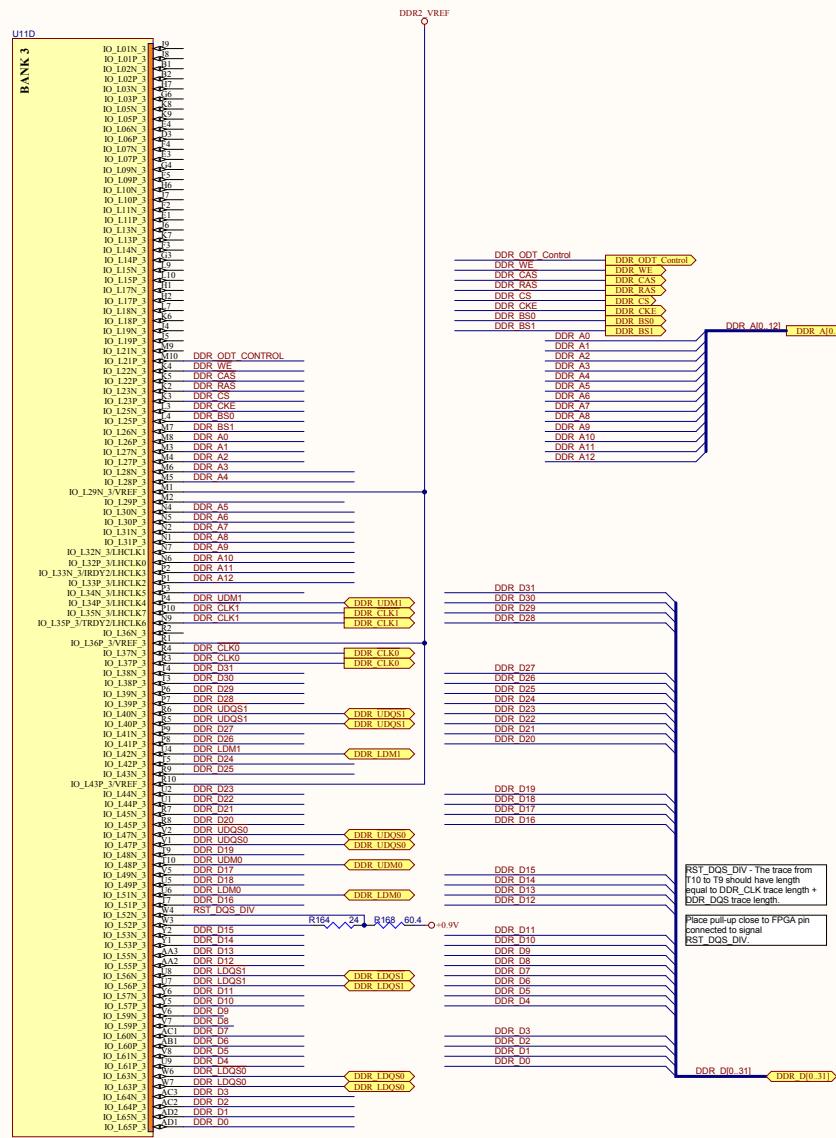
REVISION HISTORY	REV	DESCRIPTION	DATE	PART NUMBER: PCBS-SCM-0914	PART NAME: System Control	
	A	Initial Release	31 Mar 2010	ELECTRONIC ASSEMBLY #: PCBA-SUB-0914		
				PROJECT: PlateCycler		
					DATE	SIGNATURE
				DESIGNER		
				CHECKED BY		
				MANAGER		
				FILENAME: System Control - Bank 0.SchDoc		
				 390 Wakara Way, Salt Lake City, UT 84108 - (801) 736-6354 - www.idahotech.com		PROPERTY AND CONFIDENTIAL:
					SIZE: C	
					REV: 01	
					SHEET 1 OF 12	

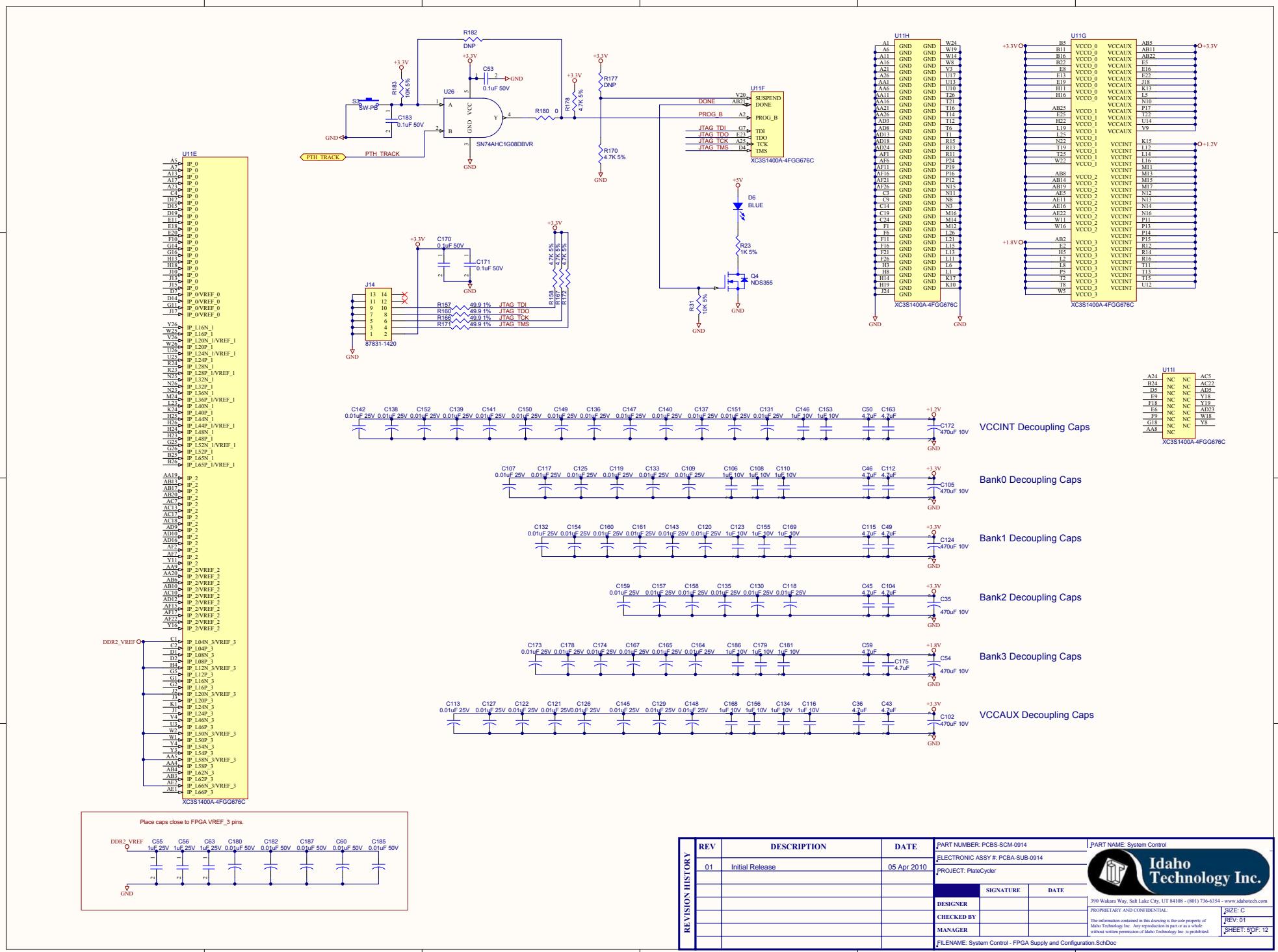


REVISION HISTORY	REV	DESCRIPTION	DATE	PART NUMBER: PCB-S-SCM-0914	PART NAME: System Control
	01	Initial Release	05 Apr 2010	ELECTRONIC ASSY #: PCBA-SUB-0914	PROJECT: PlateCycler
			SIGNATURE	DATE	390 Wakara Way, Salt Lake City, UT 84108 - (801) 736-6354 - www.idahotech.com
		DESIGNER			PROPRIETARY AND CONFIDENTIAL: The information contained in this drawing is the sole property of Idaho Technology Inc. Any reproduction in part or as a whole without written permission of Idaho Technology Inc. is prohibited.
		CHECKED BY			SIZE: C
		MANAGER			REV: 01
				FILENAME: System Control - Bank1.SchDoc	SHEET: 2 OF: 12



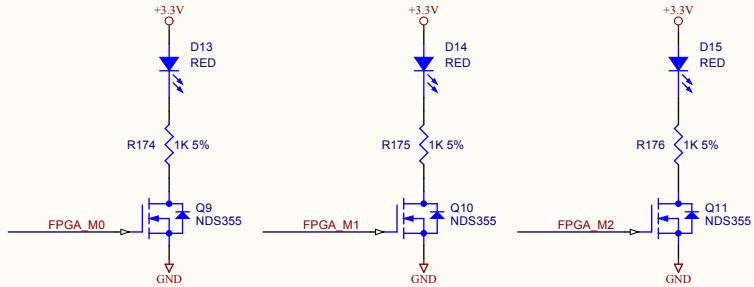
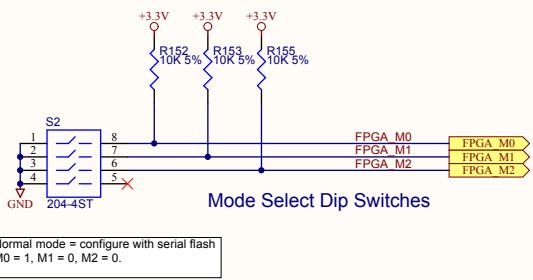
REVISION HISTORY	REV	DESCRIPTION	DATE	PART NUMBER: PCB-S-SCM-0914 ELECTRONIC ASSY #: PCBA-SUB-0914	PART NAME: System Control
				PROJECT: PlateCycler	
01	Initial Release	05 Apr 2010			
				SIGNATURE	DATE
				DESIGNER	390 Wakara Way, Salt Lake City, UT 84108 - (801) 736-6534 - www.idahotech.com
				CHECKED BY	PROPRIETARY AND CONFIDENTIAL: The information contained in this drawing is the sole property of Idaho Technology Inc. Any reproduction in part or as a whole without written permission of Idaho Technology Inc. is prohibited
				MANAGER	SIZE: C REV: 01 SHEET: 3 OF: 12
				FILENAME: System Control - Bank2.SchDoc	





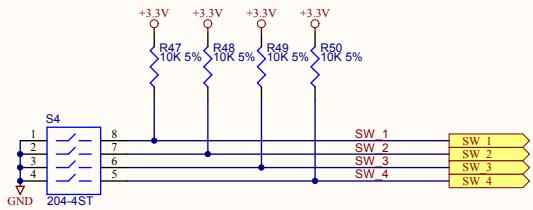
A

A



B

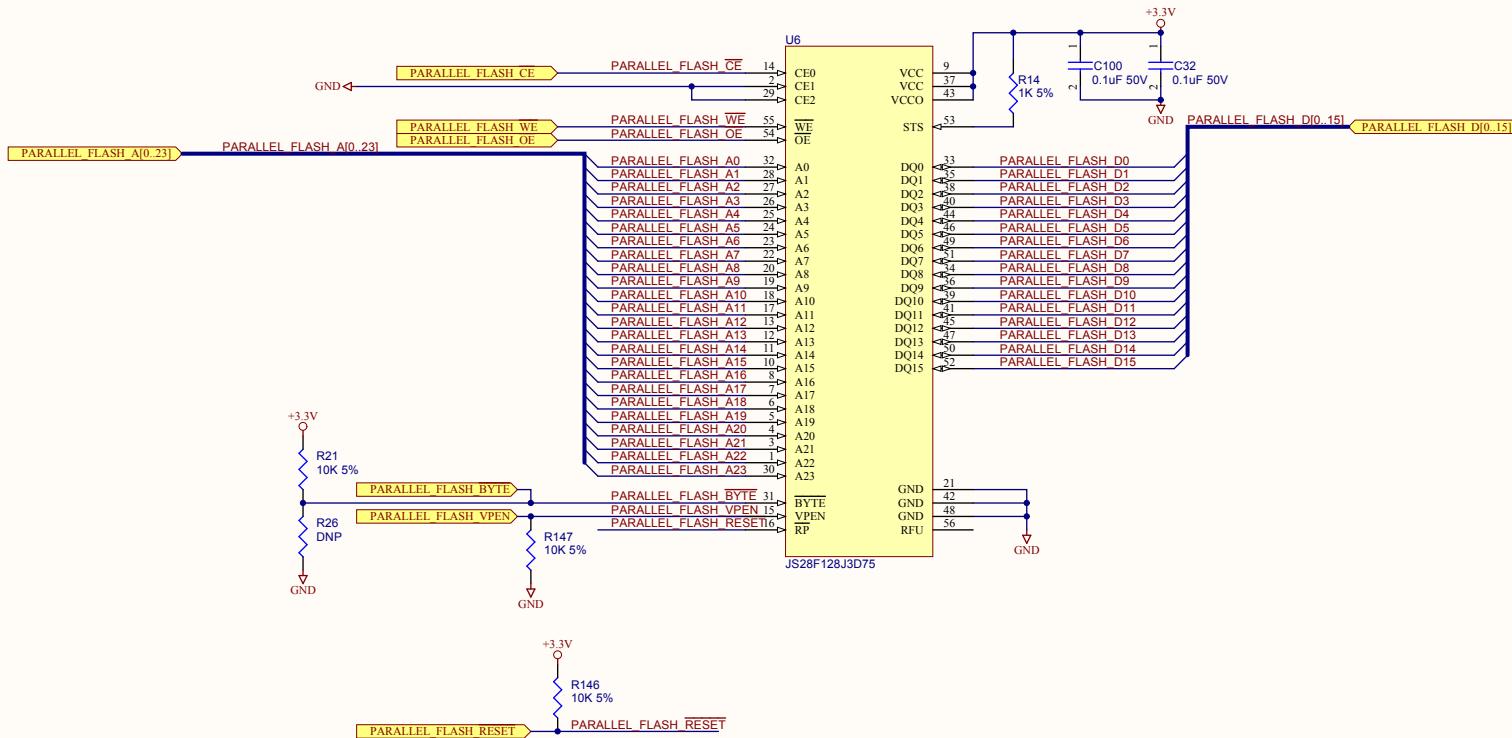
B



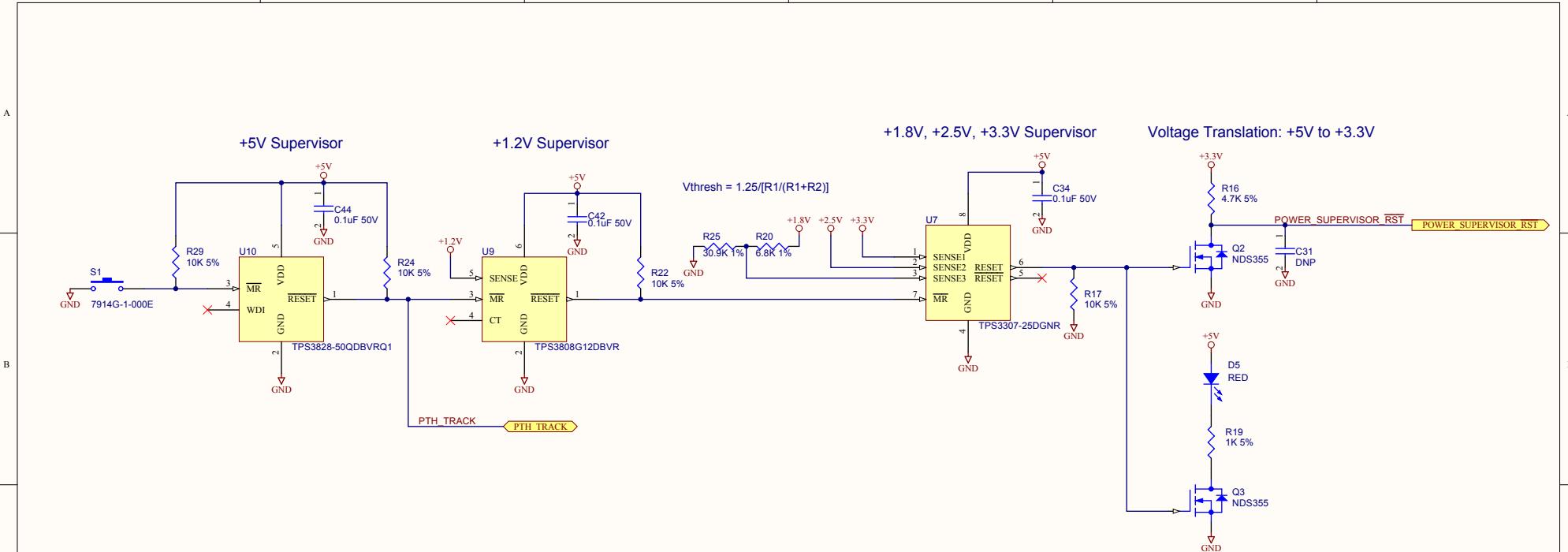
C

C

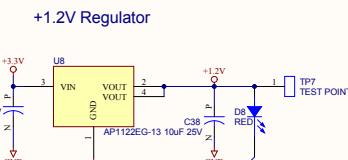
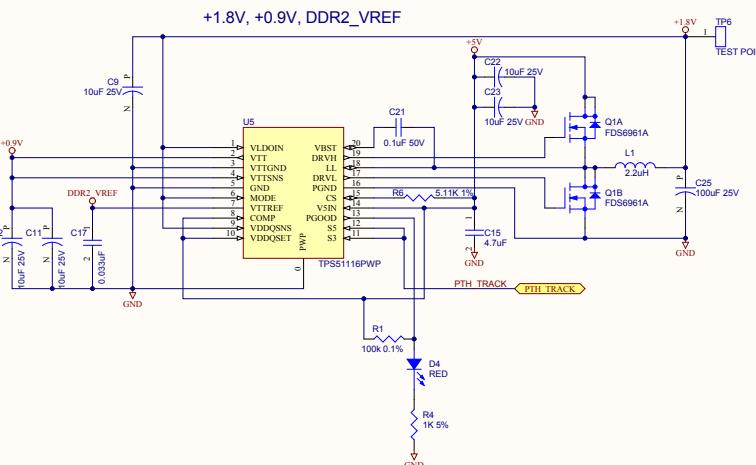
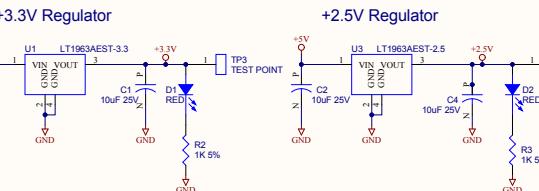
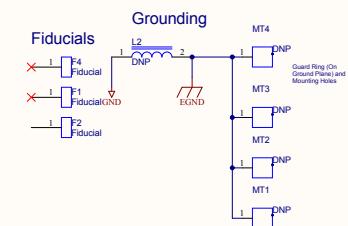
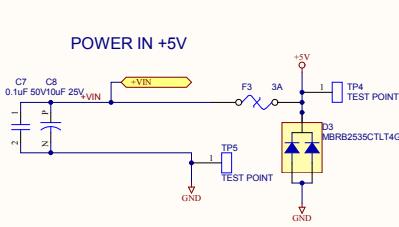
REVISION HISTORY	REV	DESCRIPTION	DATE	PART NUMBER: PCBS-SCM-0914	PART NAME: System Control
	01	Initial Release	05 Apr 2010	ELECTRONIC ASSY #: PCBA-SUB-0914	PROJECT: PlateCycler
	DESIGNER				
	CHECKED BY				
	MANAGER				
				390 Wakara Way, Salt Lake City, UT 84108 - (801) 736-6354 - www.idahotech.com	
				PROPRIETARY AND CONFIDENTIAL:	SIZE: B
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					SHEET: 6 OF: 12
				FILENAME: System Control - Mode DIP Switch.SchDoc	



REVISION HISTORY	REV	DESCRIPTION	DATE	PART NUMBER: PCBS-SCM-0914	PART NAME: System Control
	01	Initial Release	05 Apr 2010	ELECTRONIC ASSY #: PCBA-SUB-0914	PROJECT: PlateCycler
	DESIGNER				
	CHECKED BY				
	MANAGER				
				390 Wakara Way, Salt Lake City, UT 84108 - (801) 736-6354 - www.idahotech.com	
				PROPRIETARY AND CONFIDENTIAL:	SHEET: B
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					SHEET: 7 OF: 12
				FILENAME: System Control - Parallel Flash.SchDoc	



REVISION HISTORY	REV	DESCRIPTION	DATE	PART NUMBER: PCBS-SCM-0914	PART NAME: System Control
	01	Initial Release	05 Apr 2010	ELECTRONIC ASSY #: PCBA-SUB-0914	PROJECT: PlateCycler
	DESIGNER				
	CHECKED BY				
	MANAGER				
				390 Wakara Way, Salt Lake City, UT 84108 - (801) 736-6354 - www.idahotech.com	
				PROPRIETARY AND CONFIDENTIAL:	SIZE: B
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					SHEET: 8 OF: 12
				FILENAME: System Control - Power Supervisors.SchDoc	



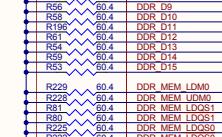
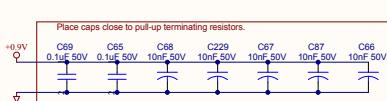
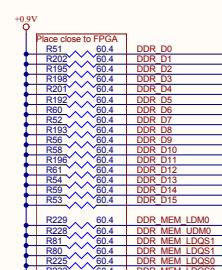
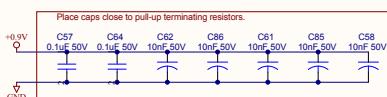
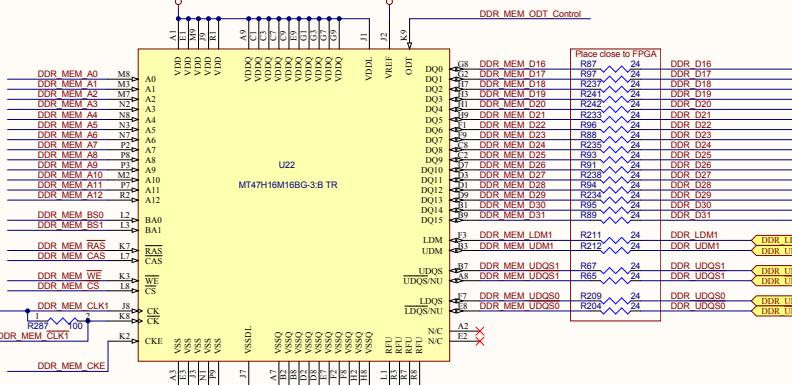
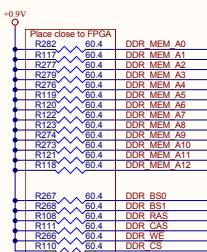
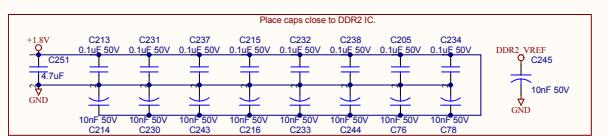
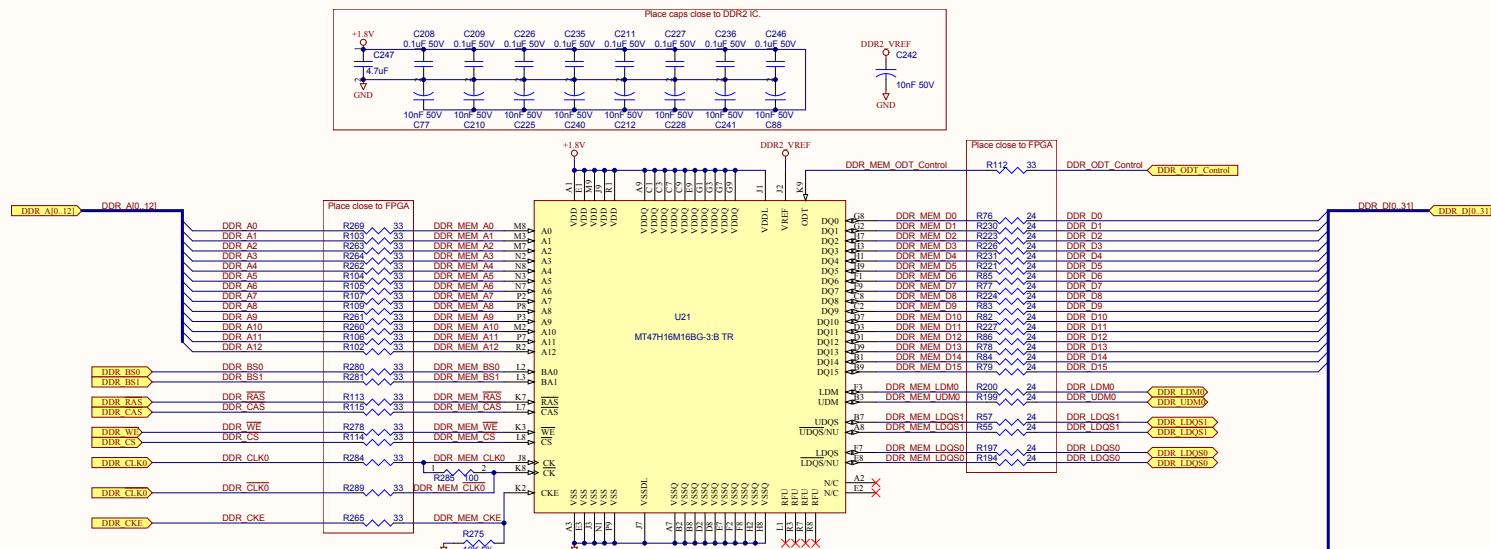
Power Considerations:

1. The PHY datasheet suggests the following:

It is strongly recommended that the PCB board have one solid ground plane and at least one split power plane with 1VZ and 1VU copper areas. Ideally the PCB should have solid planes for each of the supply voltages. The interplane capacitance between the supply and ground planes should be minimized by using vias for decoupling in addition, filling unused board areas on signal planes with copper and connecting them to the proper power plane to also increase the interplane capacitance."
2. The PHY datasheet suggests the following:

"1 oz. copper is recommended for the power and ground planes."

REVISION HISTORY	REV	DESCRIPTION	DATE	PART NUMBER: PCB5-SCM-0914	PART NAME: System Control
	01	Initial Release	05 Apr 2010	ELECTRONIC ASSY #: PCBA-SUB-0914	
			PROJECT: PlateCyder		
			SIGNATURE	DATE	
	DESIGNER				390 Wakara Way, Salt Lake City, UT 84108 - (801) 736-6354 - www.idahotech.com
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	MANAGER				SIZE: C REV: 01 The information contained in this drawing is the sole property of Idaho Technology Inc. All rights are reserved. Duplication in part or as a whole without written permission of Idaho Technology Inc. is prohibited.
					FILENAME: System Control - Power.SchDoc SHEET: 9 OF: 12



REVISION HISTORY	REV	DESCRIPTION	DATE	PART NUMBER: PCB5-SCM-0914		PART NAME: System Control
				ELECTRONIC ASSY #: PCBA-SUB-0914		
	01	Initial Release	05 Apr 2010	PROJECT: PlateCycler		 Idaho Technology Inc.
				SIGNATURE	DATE	
						<small>390 Wakara Way, Salt Lake City, UT 84108 - (801) 736-6354 - www.idiotech.com</small> <small>PROPRIETARY AND CONFIDENTIAL</small> <small>The information contained in this drawing is the sole property of Idaho Technology Inc. Any unauthorized part in whole or in part, without written permission of Idaho Technology Inc., is prohibited</small>
						<small>SIZE: C</small> <small>REV: 01</small> <small>SHEET: 10 / DIF: 12</small>
				FILENAME: System Control - SDRAM.SchDoc		

A

A

B

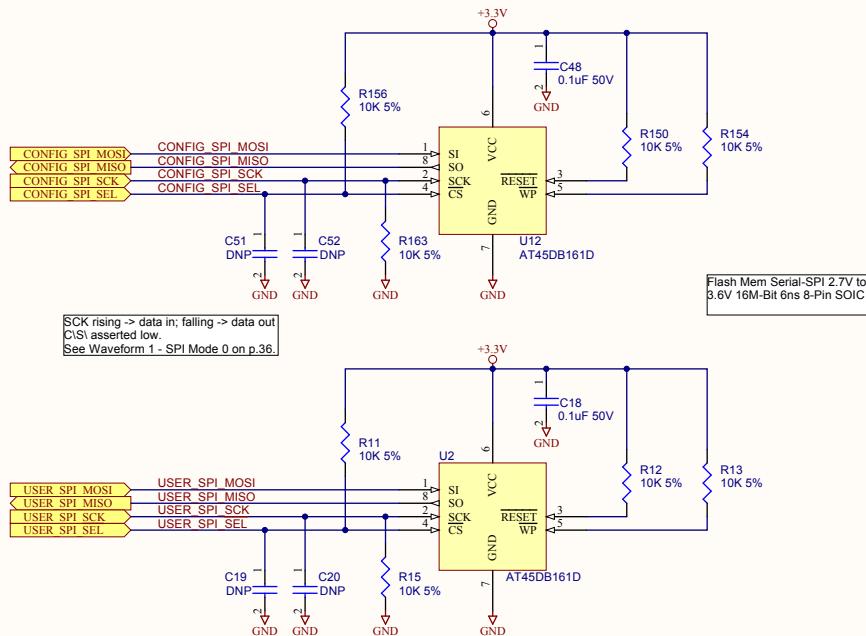
B

C

C

D

D



REVISION HISTORY	REV	DESCRIPTION	DATE	PART NUMBER: PCBS-SCM-0914	PART NAME: System Control
	01	Initial Release	05 Apr 2010	ELECTRONIC ASSY #: PCBA-SUB-0914	PROJECT: PlateCycler
	DESIGNER				
	CHECKED BY				
	MANAGER				
	FILENAME: System Control - Serial Flash.SchDoc				



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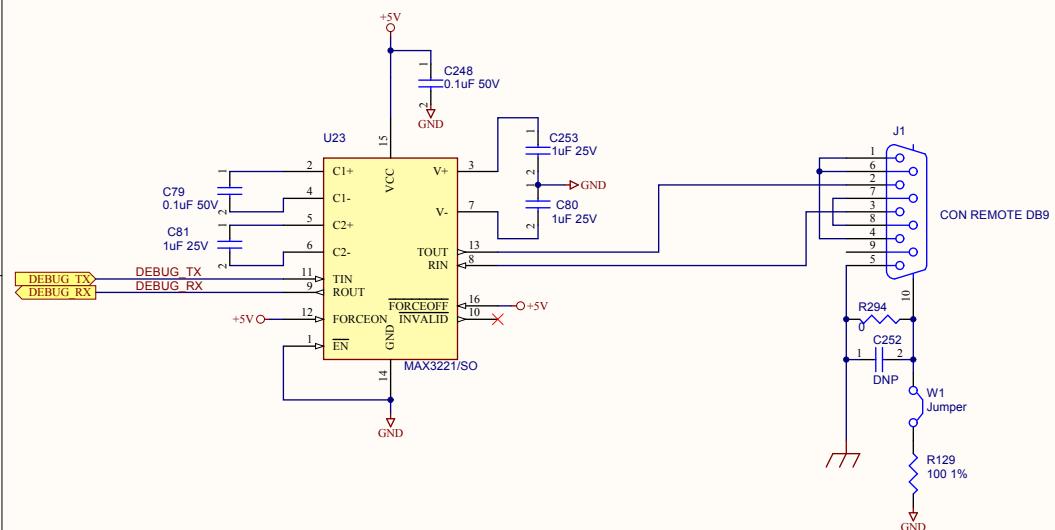
REV: 01

SHEET: 11 OF: 12

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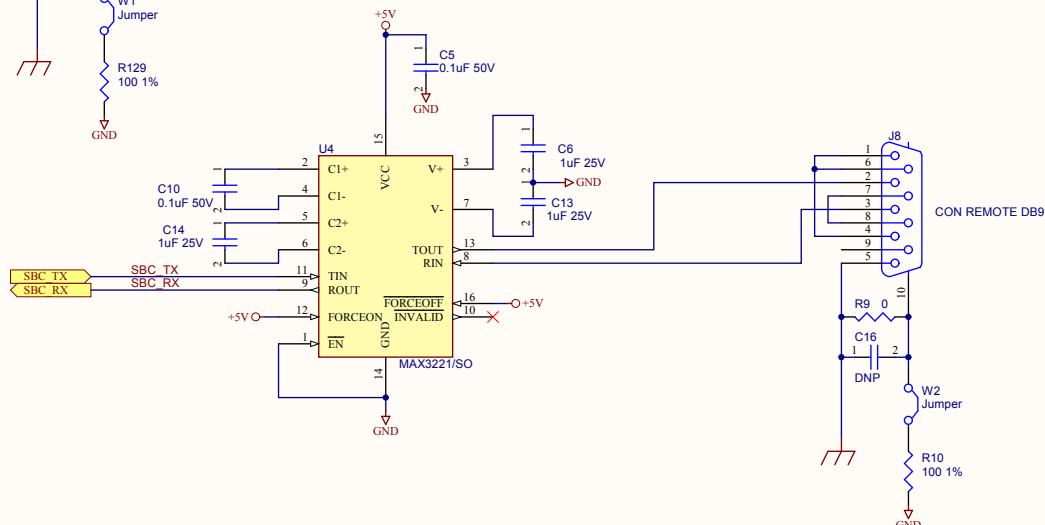
A

A



B

B



C

C

REVISION HISTORY	REV	DESCRIPTION	DATE	PART NUMBER: PCBS-SCM-0914	PART NAME: System Control
	01	Initial Release	05 Apr 2010	ELECTRONIC ASSY #: PCBA-SUB-0914	PROJECT: PlateCycler
	DESIGNER				SIGNATURE
	CHECKED BY				DATE
	MANAGER				
					FILENAME: System Control - Serial Port.SchDoc

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SIZE: B
 REV: 01
 SHEET: 12 OF: 12

1 2 3 4 5 6

