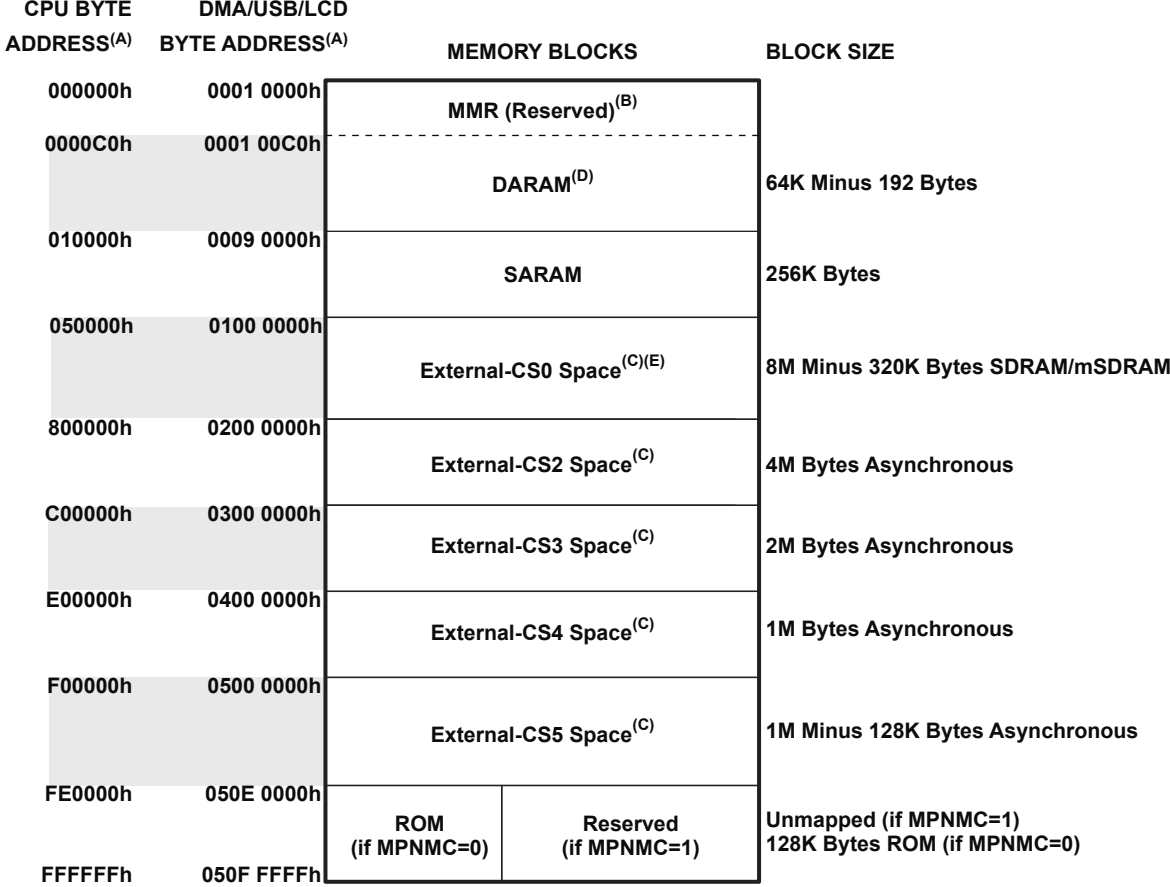

A. Address shown represents the first byte address in each block.
B. The first 192 bytes are reserved for memory-mapped registers (MMRs).
C. Reading/Writing to/from unmapped returns zeros.
D. The USB and LCD controllers do not have access to DARAM.
E. The CS0 space can be accessed by CS0 *only* or by CS0 *and* CS1.