```
stm::initTM< BitEager >
                                     stm::initTM< BitEagerRedo >
                                        stm::initTM< BitLazy >
                                        stm::initTM< ByEAR >
                                      stm::initTM< ByteEager >
                                    stm::initTM< ByteEagerRedo >
                                       stm::initTM< ByteLazy >
                                         stm::initTM< CGL >
                                        stm::initTM< CToken >
                                     stm::initTM< CTokenTurbo >
                                          stm::initTM< LLT >
                                         stm::initTM< MCS >
                                         stm::initTM< Nano >
                                       anonymous_namespace
                                       {norec::cpp}::NOrec
                                          _Generic::initialize
anonymous_namespace {norec::cpp}::onSwitchTo
                                      stm::initTM< NOrecPrio >
                                       stm::initTM< OrecALA >
                                    stm::initTM< OrecEagerRedo >
                                       stm::initTM< OrecELA >
                                       stm::initTM< OrecFair >
                                        stm::initTM< Pipeline >
                                       stm::initTM< ProfileTM >
                                       stm::initTM< RingALA >
                                       stm::initTM< RingSW >
                                         stm::initTM< Serial >
                                         stm::initTM< Swiss >
                                         stm::initTM< Ticket >
                                          stm::initTM< TLI >
                                         stm::initTM< TML >
                                       stm::initTM< TMLLazy >
```