

Table 21. USARTs

USART pinout	Configuration	GPIO configuration
USARTx_TX	Full duplex	Alternate function push-pull
	Half duplex synchronous mode	Alternate function push-pull
USARTx_RX	Full duplex	Input floating / Input pull-up
	Half duplex synchronous mode	Not used. Can be used as a general IO
USARTx_CK	Synchronous mode	Alternate function push-pull
USARTx_RTS	Hardware flow control	Alternate function push-pull
USARTx_CTS	Hardware flow control	Input floating/ Input pull-up

Table 17. Port bit configuration table

Configuration mode		CNF1	CNF0	MODE1	MODE0	PxODR register		
General purpose output	Push-pull	0	0	01 10 11 see <i>Table 18</i>		0 or 1		
	Open-drain		1			0 or 1		
Alternate Function output	Push-pull	1	0					don't care
	Open-drain		1					don't care
Input	Analog input	0	0	00				don't care
	Input floating		1					don't care
	Input pull-down	1	0			0		
	Input pull-up					1		

Table 18. Output MODE bits

MODE[1:0]	Meaning
00	Reserved
01	Max. output speed 10 MHz
10	Max. output speed 2 MHz
11	Max. output speed 50 MHz