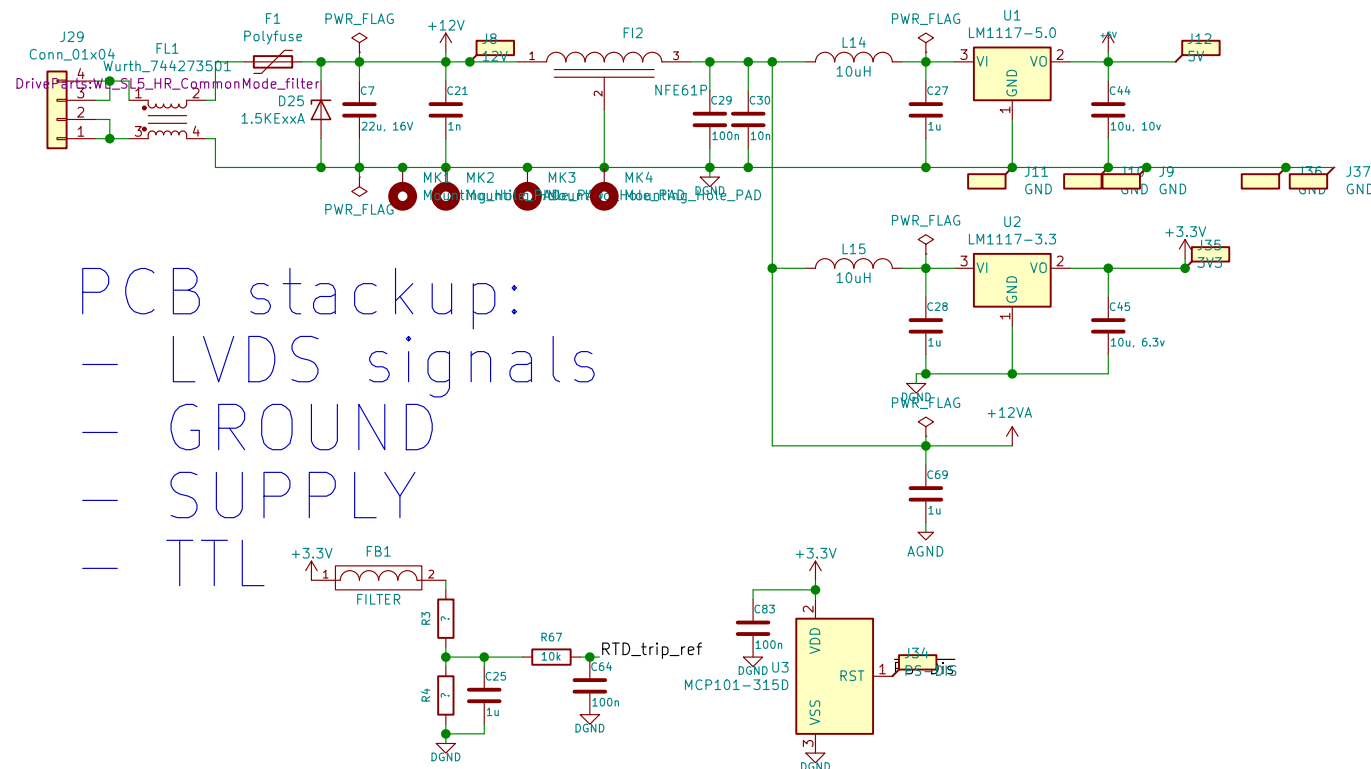



Supply

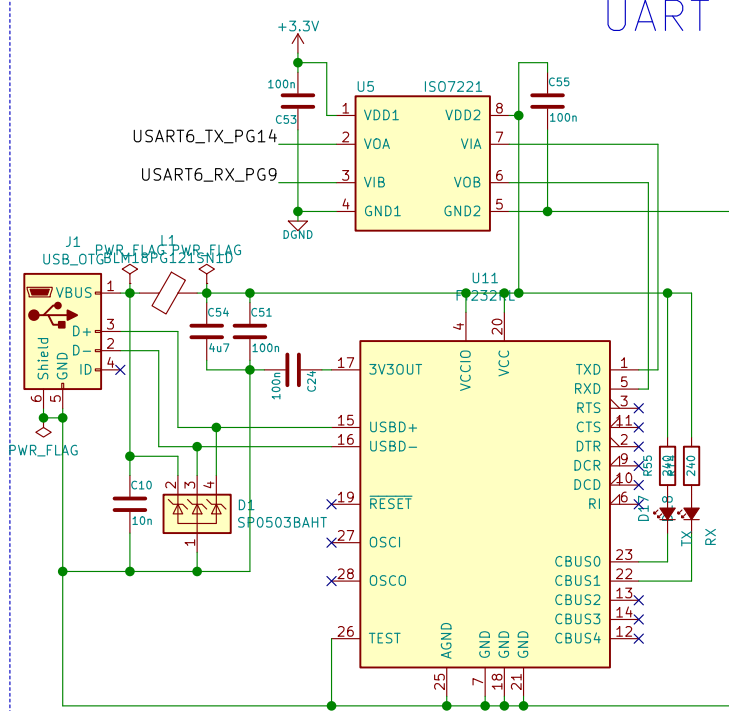


PCB stackup:

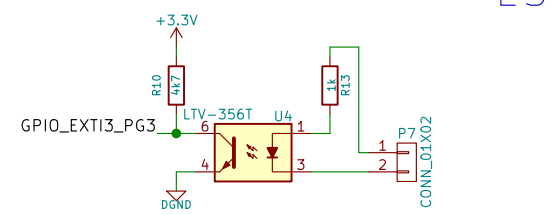
- LVDS signals
- GROUND
- SUPPLY
- TTL



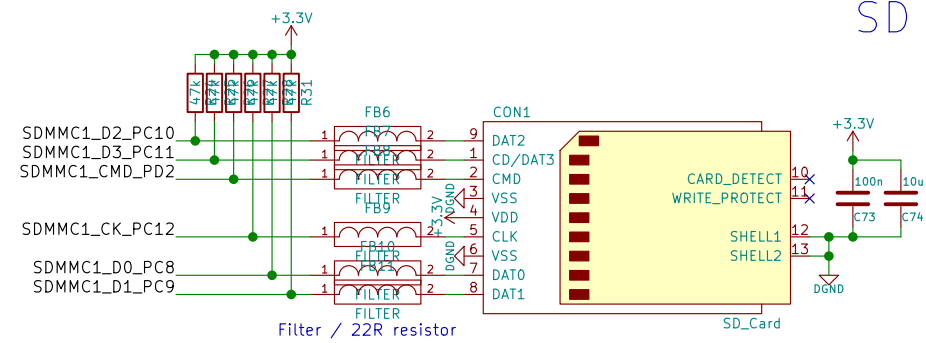
UART



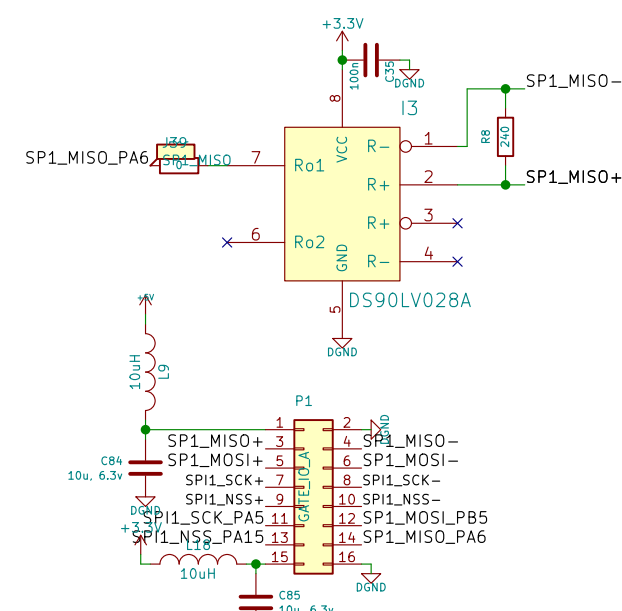
ESTOP



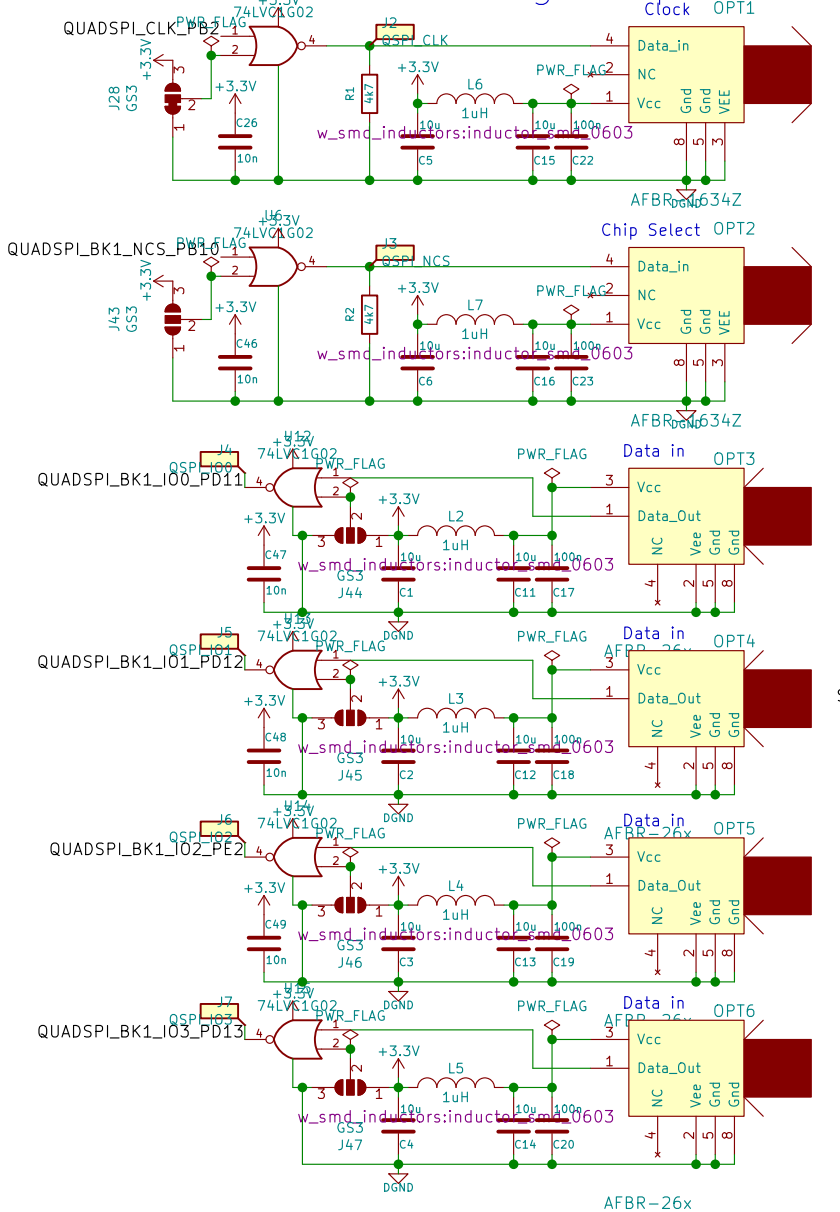
SD



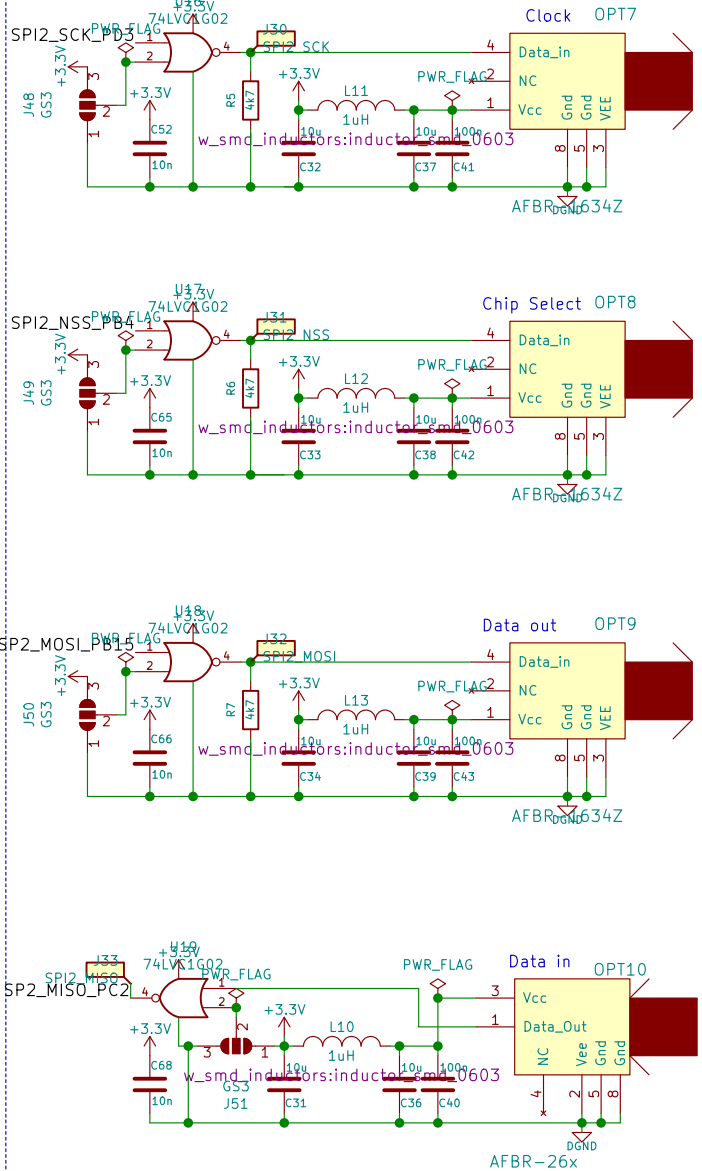
Encoder, BiSS (SPI1)



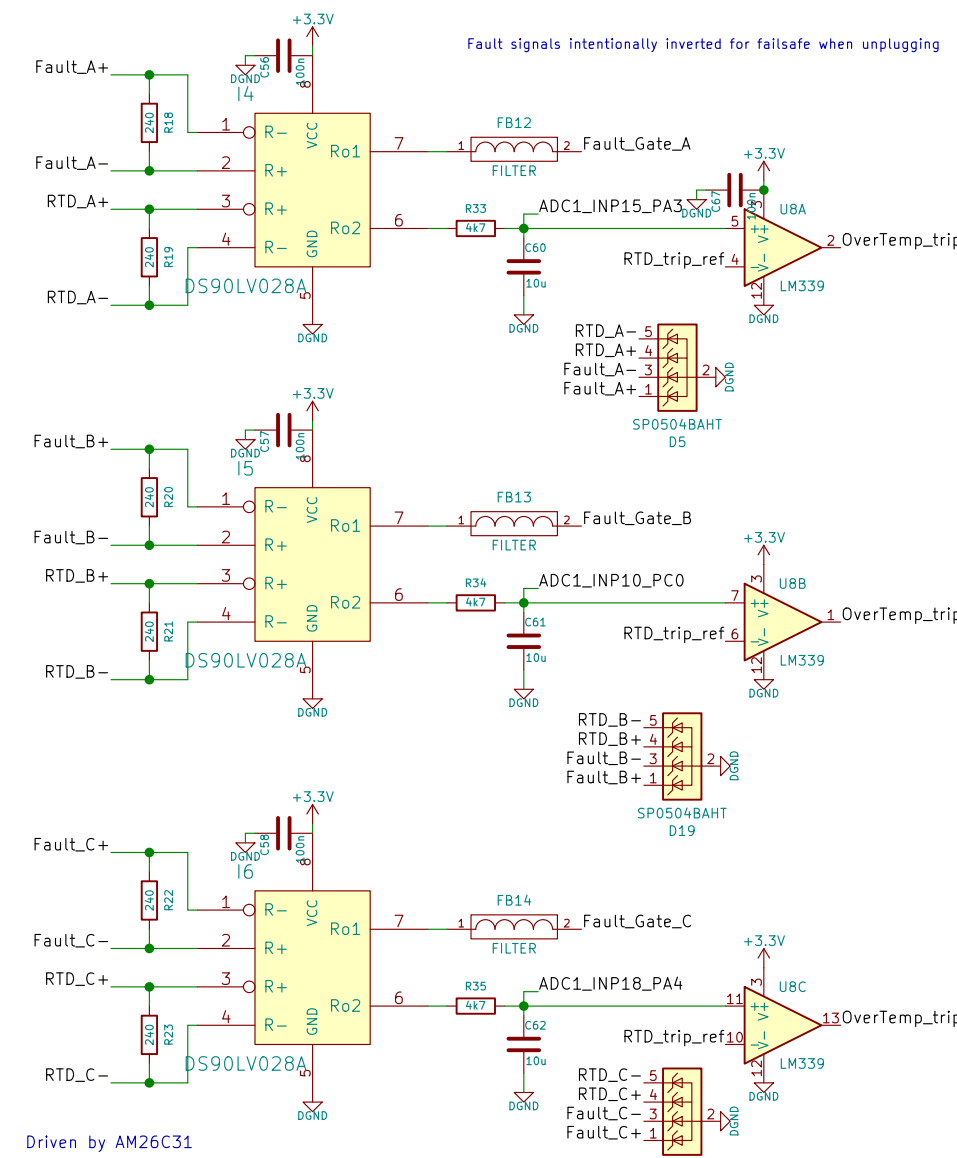
Current sensing – quad SPI



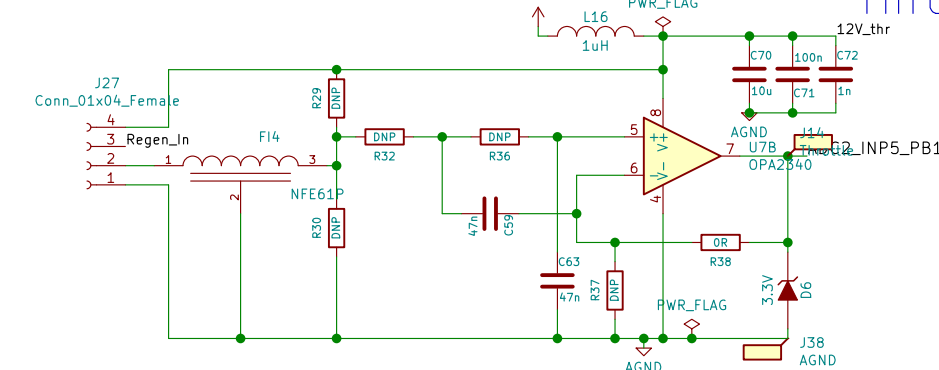
SPI2 (Spare)



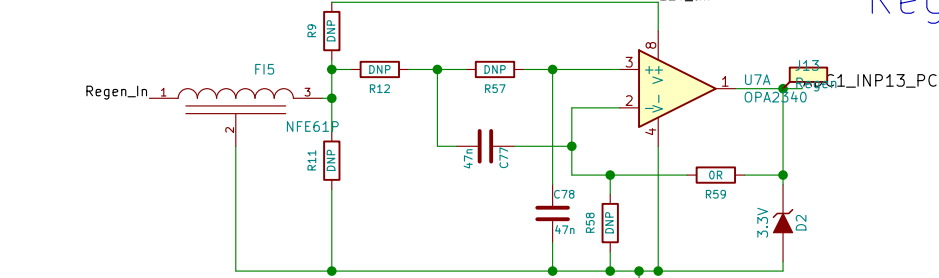
Gate driver faults



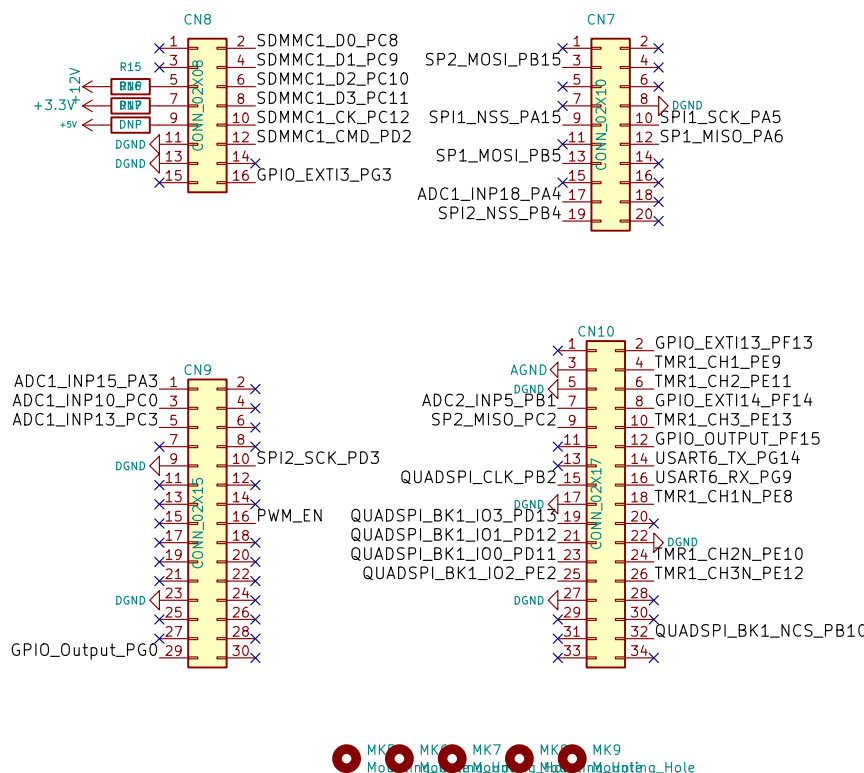
Throttle



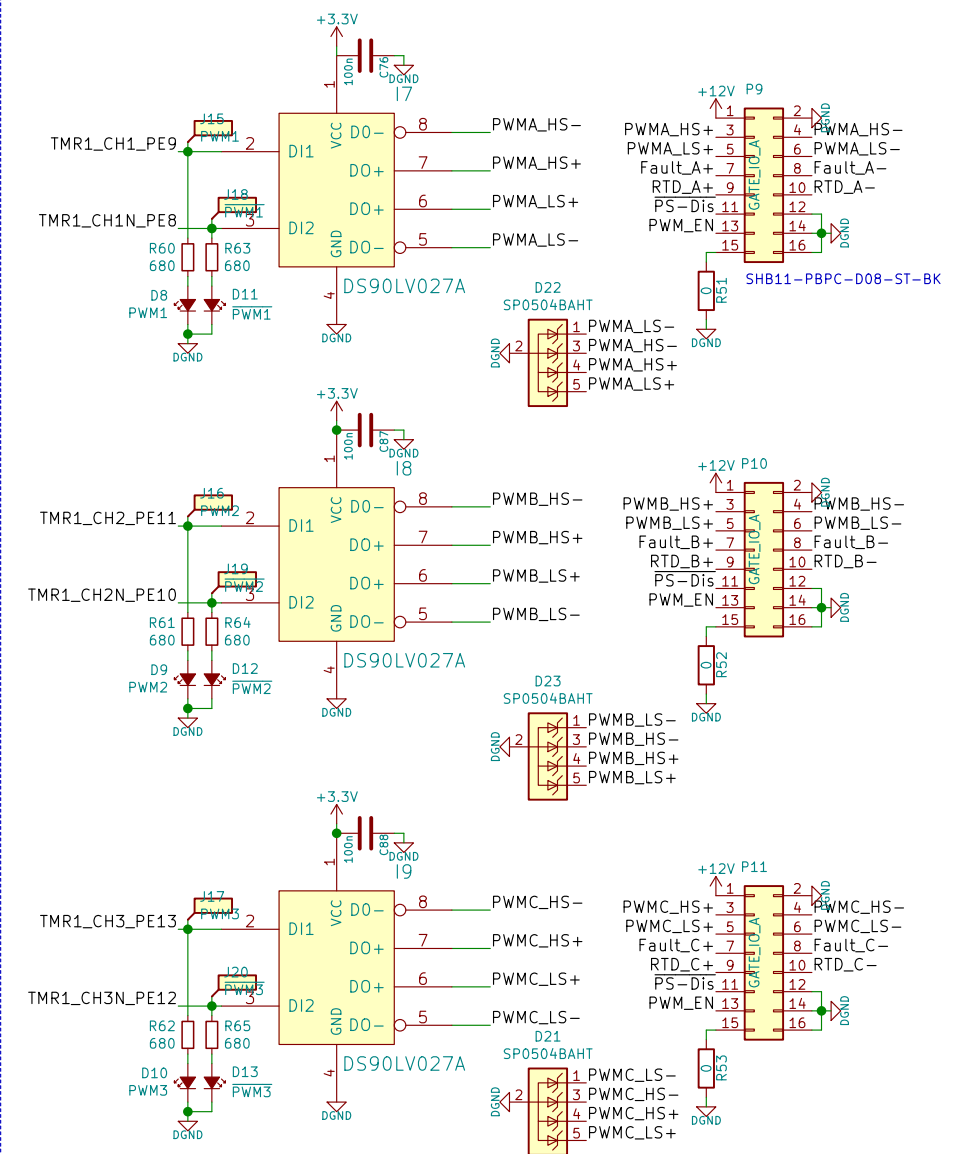
Regen



Nucleo-H743ZI connections



PWM



Fault logic

