

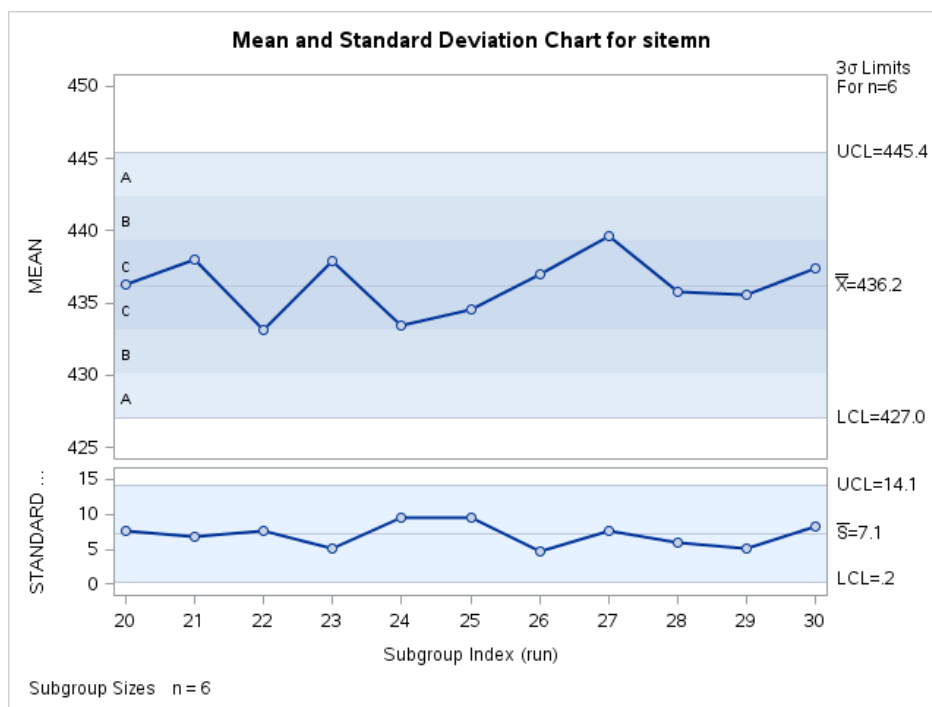
# STATS 528: Final Project

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## 1 Trial Control Limits

Limiting inspection to runs 20 through 30, we find that the average between wafer variability is  $\bar{s} = 7.1$  and that the average etch rate is  $\bar{\bar{x}} = 436.2$ .



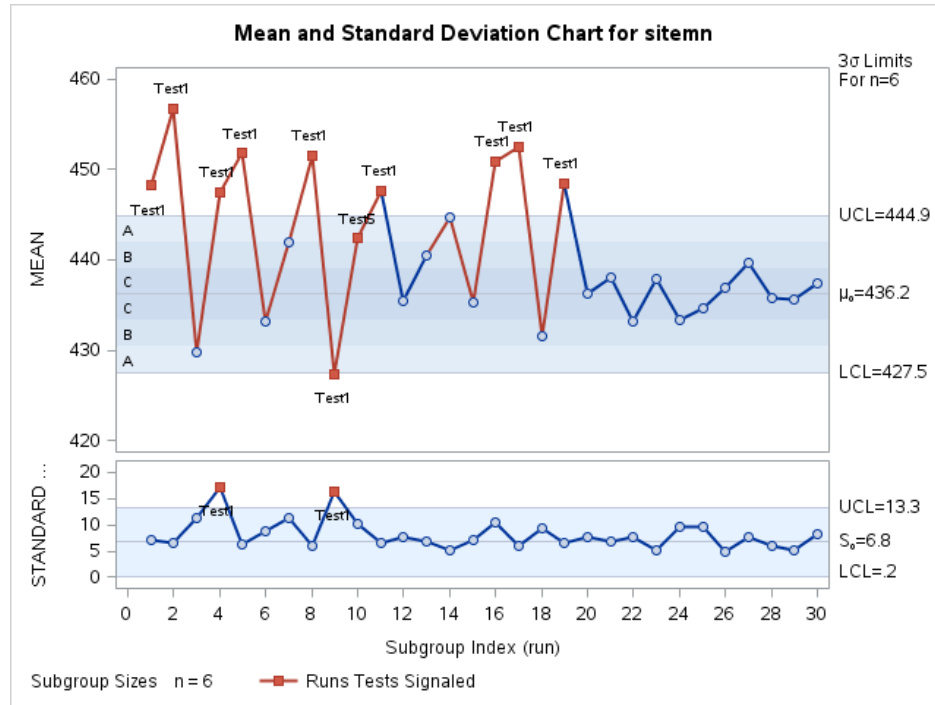
Because there were 6 observations (i.e. wafers) in each sample, we have  $A_3 = 1.287$  and upper and lower control limits may be calculated:

$$UCL = \bar{\bar{x}} + A_3 \bar{s} = 436.2 + 1.287 \cdot 7.1 = 445.338$$

$$LCL = \bar{\bar{x}} - A_3 \bar{s} = 436.2 - 1.287 \cdot 7.1 = 427.062$$

## 2 Analysis of Process Stability

The generated  $\bar{x}$  and  $s$  charts using the above control limits present several points of interest:



It seems that the runs can be separated into three phases: runs 1 through 10, 11 through 19, and 20 through 30.

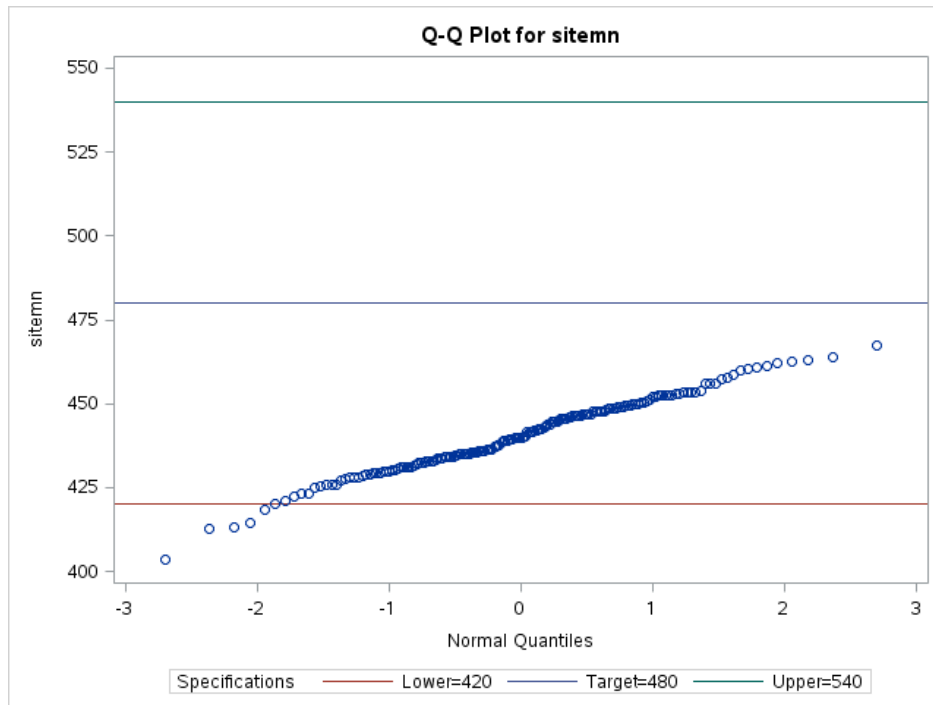
1. First, in runs 1 through 10, there is a great amount of variability in sample averages and the sample averages appear to have a higher mean than that from the trial ( $\mu = 436.2$ ). An out-of-control signal is thrown on 7 of these 10 runs. Clearly the process is not in control across these runs. Upon questioning Mr. Ewma, it was mentioned that in between each of these runs, the flow controller device was shutdown and restarted as was the typical operating procedure. After run 10, the device was left running in the interest of providing better measurements. This shutdown-restart process may have added to the increased variability in etch rate measurements in runs 1 through 10.
2. Runs 11 through 19 have less variability in mean etch rate and sample standard deviations also appear to be less variable. But 4 out-of-control signals are still thrown. The mean etch rate also still appears to be above the aforementioned trial value. It is fair to say that the process is still out of control in runs 11 through 19.
3. After run 19, the process seems to have come under control. No out-of-control signals were thrown after run 19 as variability in average etch rate measurements appears to have dropped. Mr. Ewma also mentioned that after run 19, the flow controller underwent a recalibration maintenance procedure. This is the likely cause of the drastic drop in average etch rate variability.

### 3 Process Capability

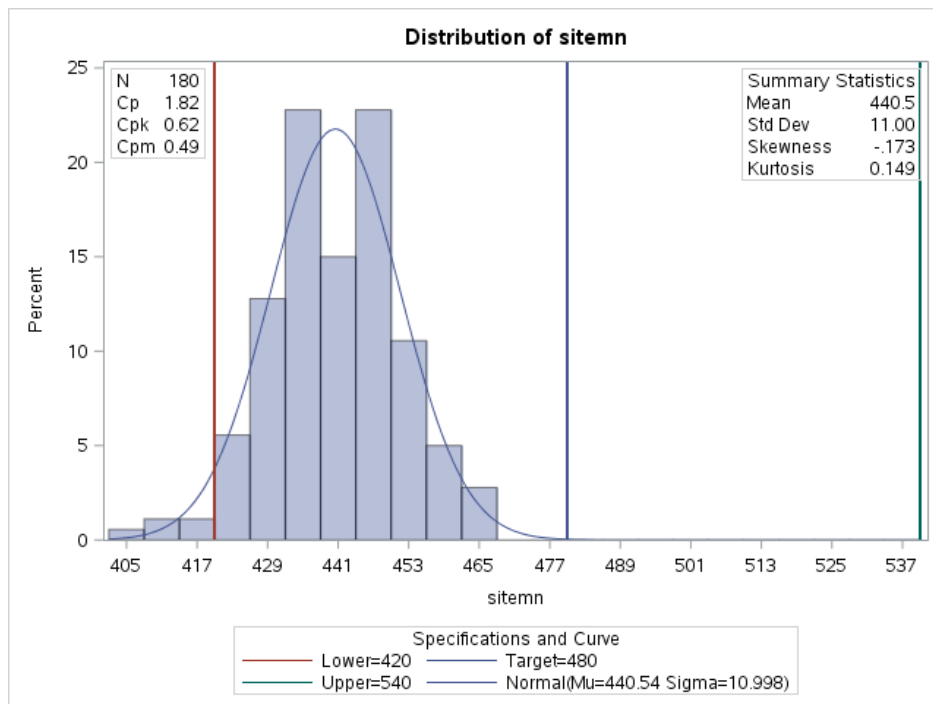
#### 3.1 Normality Assumption

We can see from the SAS output that tests for normality all have large p-values so no evidence in this respect is given that the data isn't normally distributed (see attached SAS code, page 5)

Points on the Q-Q plot lie closely to a straight line giving some evidence that the data is distributed normally.

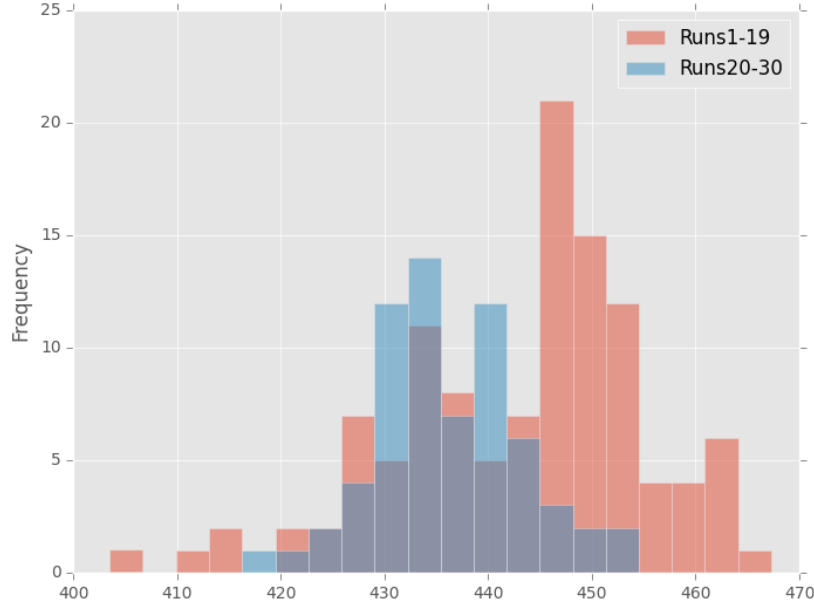


We can see from a histogram of the sample averages that the mean rates are a little left skewed and also that the process is not centered within the specification limits (420 to 540). There also appears to be some bi-modal aspect to the data.



From a histogram with overlapping categories below, we can see that the two distributions are coming from those before run 20 and those after. Also, the left skewness seen above is a byproduct of the the runs

before run 20. In spite of all this, I believe it is fine to assume that the sample average etch rates are normally distributed.



Since the engineers seem to understand that the process came under control after run 19, perhaps process capability could be assessed using that subset. It doesn't seem wise to assess process capability over data obtained when the process is known to be operating in sub-optimal conditions.

### 3.2 Capability Assessment

It can be seen in the attached SAS output on page 8,  $C_p = 1.82$  but it is easy to see in the SAS generated histogram above that the process is not even close to being centered within the specification limits. Thus, it's no surprise the  $C_{pk}$  is 0.62 which shows that the process may have capability issues. Such a low  $C_{pk}$  means that a large portion of mean etch rates lie out of the specification limits. Assuming that means are normally distributed we have:

$$z = \frac{LSL - \bar{x}}{\hat{\sigma}} = \frac{420 - 440.5}{11} = -1.864$$

so 3.119% will fall below specification. If only the last 11 runs were used to calculate process capability, the conclusions would not be greatly different:

$$z = \frac{LSL - \bar{x}}{\hat{\sigma}} = \frac{420 - 436.2}{7.005} = -2.313$$

and 1.037% will fall below specification.

## 4 Variance components

Let estimated between run, between wafer, and between site variability be denoted by  $\hat{\sigma}_{run}^2, \hat{\sigma}_{wafer}^2, \hat{\sigma}_{site}^2$  respectively. We can manually calculate these values and obtain

$$\begin{aligned}\hat{\sigma}_{run}^2 &= 59.573 \\ \hat{\sigma}_{wafer}^2 &= 75.227 \\ \hat{\sigma}_{site}^2 &= 1464.615.\end{aligned}$$

Using maximum-likelihood estimation we obtain

$$\begin{aligned}\hat{\sigma}_{run}^2 &= 33.83 \\ \hat{\sigma}_{wafer}^2 &= 0 \\ \hat{\sigma}_{site}^2 &= 1390.32.\end{aligned}$$

Regardless of the method of variance estimation, it's clear that the lion's share of variation occurs between sites. This appears to be almost completely due to site 9, which has a mean etch rate almost 25% higher than the other sites. If site 9 is removed from the data then manual estimation yields

$$\begin{aligned}\hat{\sigma}_{run}^2 &= 61.857 \\ \hat{\sigma}_{wafer}^2 &= 82.852 \\ \hat{\sigma}_{site}^2 &= 143.329.\end{aligned}$$

and maximum-likelihood estimation provides

$$\begin{aligned}\hat{\sigma}_{run}^2 &= 48.05 \\ \hat{\sigma}_{wafer}^2 &= 64.94 \\ \hat{\sigma}_{site}^2 &= 143.33.\end{aligned}$$

In conclusion, if the etch rate for site 9 could be put in line with the etch rates for the other sites, the largest source of etch rate variability can be done away with. Also, if the flow controller is not restarted at each run and is properly calibrated on a continual basis, then the process would more consistently be brought under control.