Jonathan G. Tao

jon.g.tao@gmail.com http://jgtao.me (415) 812-0180

Education

Bachelor of Science in Electrical Engineering - Expected March 2016
Bachelor of Science in Computer Engineering - Expected March 2016
University of California, Davis with GPA: 3.84/4.00 (Engineering Dean's Honor List, 8 quarters)

Relevant Course Work

- Digital System Design and Verilog HDL, Analog Circuit Analysis and Design, Device Physics, Electricity and Magnetism, Probabilistic Analysis of Electrical and Computer Systems, Signals and Systems, Embedded Systems.
- Software Development and C/C++ Programming, Discrete Mathematics, Data Structures, Algorithm Design and Analysis, Computer Architecture.
- Technical Writing in Engineering.

Skills

- Use of oscilloscopes, function generators, multimeters, and logic analyzers to characterize and verify circuits.
- EDA (e.g. CadSoft Eagle, Kicad, Quartus) with final assembly and testing of circuit boards.
- Hardware description and testing with SPICE (e.g. HSPICE, LTSPICE) and Verilog HDL.
- Breadboarding, soldering with both through-hole and surface mount components, and wire-wrapping.
- Programming in C/C++ and MIPS/ARM assembly. Scientific programming with MATLAB/Octave. Automation, testing, and verification with UNIX shell.
- Operating systems software development for Linux/Unix and Windows.
- Embedded systems software development for Cypress PSoC, Texas Instruments MSP, Atmel AVR, and ARM Cortex based platforms.
- Embedded programming with both device registers and high level API. Familiarity with use of embedded communication protocols and peripheral devices.
- Revision control with Git, compilation automation with Make, and development with platform specific IDEs.
- Typesetting in LaTeX and HTML.
- Professional working proficiency in Cantonese Chinese.

Experience and Projects

New Product Introduction Electrical Engineer - Internship at Keysight Technologies 2015

- Verified power sequencing and reliability in a modular 20GHz PXI Vector Signal Generator with SPICE simulations.
- Wrote test automation scripts to significantly shorten the time needed to generate netlists from schematics, simulate the circuit, and perform evaulation of circuit reliability.

Electric Vehicle Management Electronics and Telemetry - Senior Design Project 2014 - 2015

- High voltage electric vehicle battery management system.
- CAN bus sensor network logging and wireless telemetry to a custom desktop application.

Formula SAE Student Electric - Race Car Design Competition 2013 - Present

- The team took 3rd place at the SAE Electric International competition of 2014 in Lincoln, Nebraska.
- Programmed a supervisory control unit for catching and resolving faults to prevent harm to hardware and people.
- Designed and built both the hardware and software of a driver dashboard user interface for monitoring the vehicle's drivetrain and battery in real time.
- Developed a KS-108 LCD driver with basic font and geometry rendering routines. Created a complementary program to convert PNG images into text hexadecimal constants to be easily inserted into C program source code of the dashboard user interface.

Ludum Dare 28 - 48 Hour Game Development Competition 2013

- Wrote a 2D survival platformer game from scratch about killer bunnies and holy hand grenades with C++ and the Allegro library including art and music within 48 hours.
- Ported the game from Linux to Windows within the competition for cross-platform distribution.