

Design of Digital Systems Laboratory

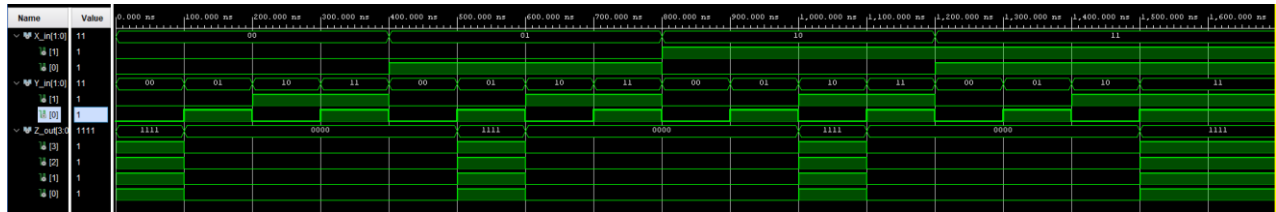
Assignment 1

Jonathan Mazurkiewicz

By submitting this report, you attest that you neither have given nor have received any assistance (including writing, collecting data, plotting figures, tables, or graphs, or using previous student assignments as a reference), and you further acknowledge that giving or receiving such assistance will result in a failing grade for this course.

Your Signature: Jonathan Mazurkiewicz

Simulation Results



This lab is a comparator. There are 4 instances of this comparator, coded in different ways. This waveform shows that when x and y are equal, z should be high. In all 16 cases, as expected, the output is correct.

Area Implementation

Design Runs														
DRC		Power		Timing										
%														
	WNS	TNS	WHS	THS	WBSS	TPWS	Total Power	Failed Routes	Methodology	RQA Score	QoR Suggestions	LUT	FF	BRAM
Complete!												3	0	0
1 Complete!	NA	NA	NA	NA		NA	4.738	0				3	0	0

Timing Implementation

n/a

Video Explanation

<https://youtu.be/jK45mGFkGhE>