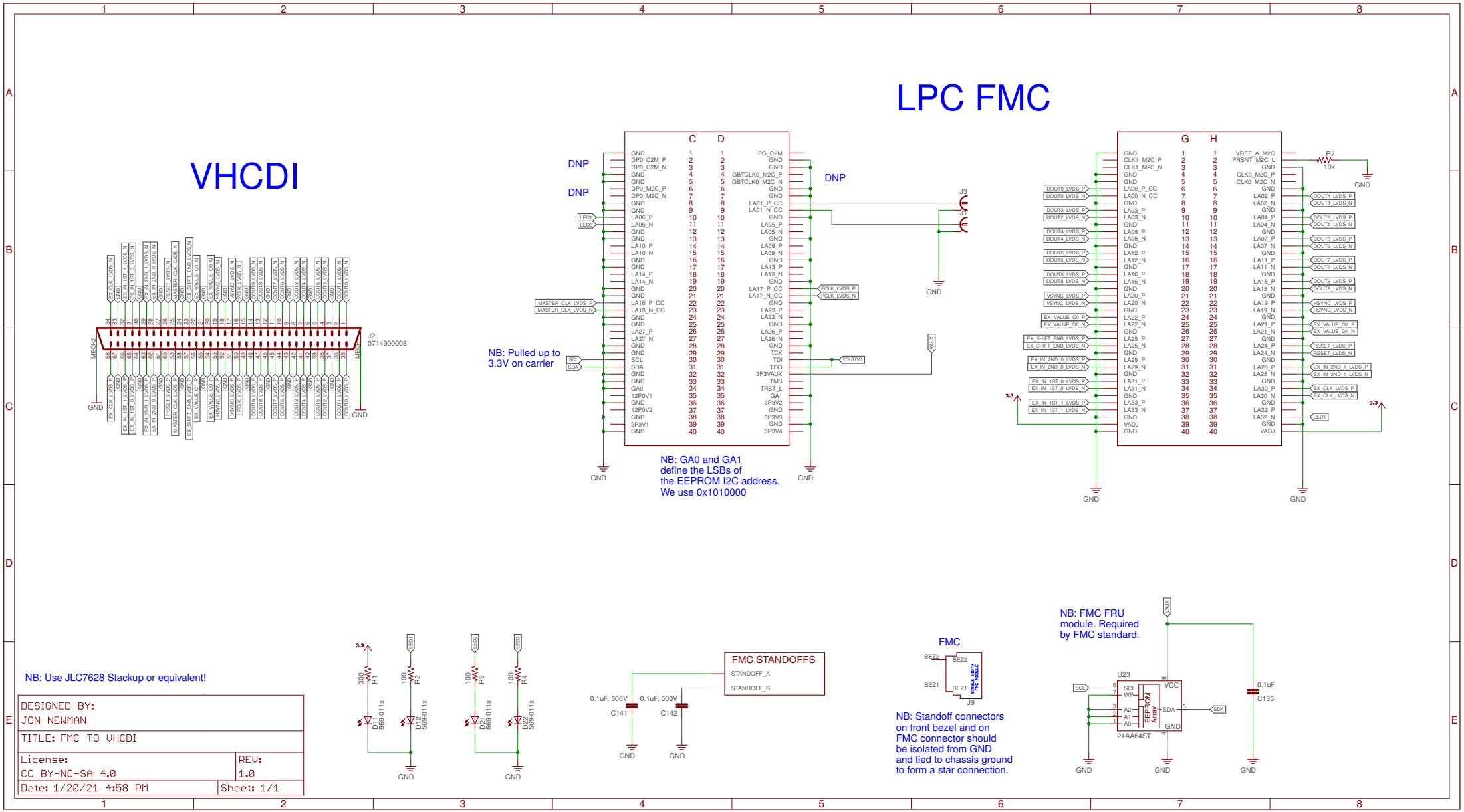
VHCDI
LPC FMC
J2
0714300008
MECH2
MECH1
GND
DNP
DNP
DNP
J3
NB: Pulled up to 3.3V on carrier
NB: GA0 and GA1 define the LSBs of the EEPROM I2C address. We use 0x1010000
R7
10k
3.3
VAUX
NB: Use JLC7628 Stackup or equivalent!
R1 300
R2 100
R3 100
R4 100
D11 569-011x
D12 569-011x
D21 569-011x
D22 569-011x
LED1
LED2
LED3
FMC STANDOFFS
STANDOFF_A
STANDOFF_B
0.1uF, 500V
C141
C142
FMC
J9
NB: Standoff connectors on front bezel and on FMC connector should be isolated from GND and tied to chassis ground to form a star connection.
NB: FMC FRU module. Required by FMC standard.
U23
24AA64ST
EEPROM Array
VCC
SDA
SCL
WP
A2
A1
A0
0.1uF
C135
DESIGNED BY:
JON NEWMAN
TITLE: FMC TO VHCDI
License:
CC BY-NC-SA 4.0
REV:
1.0
Date: 1/20/21 4:58 PM
Sheet: 1/1