

Jonathan Huang

Work Experience

SOFTWARE FREELANCER – Full Stack Web Developer
Artesia, CA

07/2019 - Present

- Developed 10+ full-stack web applications with React, JavaScript, CSS, Bootstrap, HTML, Node.js, Express.js, AWS, MySQL and MongoDB.
- Developed machine learning, deep learning, NLP, CV model in Python with Tensorflow2.0/Keras, OpenCV, Sci-kit Learn, Pandas, Seaborn, etc.
- Planned, wrote, and debugged web applications and software with complete accuracy.

I-MAXX EXCHANGE INC - Co-founder

Montebello, CA

05/2016 - 06/2019

- Provided executive leadership generating \$3 million gross revenue in 2017 and \$5 million gross revenue in 2018 with 10% net profit
- Established operational SOP including startup proactive culture, organization structure, staff training, accounting practice, inventory control, marketing and advertisement, SEO/SMO/SMM, e-commerce website, sourcing & procurement, sales and finance all from ground up.
- Provided cross-functional and remote team management with active communication and problem solving on the spot.

BD BIOSCIENCES, FW Quality Group – Sr. Embedded Software Quality Engineer

San Jose, CA

02/2009 - 10/2014

- Developed test requirements, designed test cases, and performed testing to assess the flow cytometry FPGA data acquisition correctness.
- Participated in Agile Scrum process to collaborate with cross-functional stakeholders to ensure on-time and on-target delivery.
- Developed C# test cases and scripts in WPF GUI for unit, integration, performance, acceptance, stress, and regression testing.

ATMEL CORPORATION, Advanced Product Group - Software Engineer

San Jose, CA

11/2006 - 08/2008

- Designed CryptoMemory/RF with SHA1 cryptography library/driver API and encryption/authentication applications in C & C++.
- Developed test cases, test scripts and demonstration GUI in VB .NET.

Contact Information

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LinkedIn:

<https://www.linkedin.com/in/jonathan-huang-5594a95/>

Portfolio / GitHub:

<https://jonathanhuang.netlify.app/>

<https://jonnohuang.github.io/>

<https://github.com/jonnohuang/>

Technical Skills

(* Proven skills by LinkedIn Skill Assessment)

- *JavaScript, *Node.js, Express, React
- *Python 3 Programming
- C, *C#, *C++ 11/14/17 Programming
- *REST API, MVC, OOP, GraphQL
- Functional Programming, Microservice
- Responsive Web Design, Scala, Spark
- MongoDB, Mongoose, Atlas, *MySQL
- Jest, Mocha, Chai, Sinon, Docker
- AWS (EC2, S3, Route53, CloudFront)
- Lambda, API Gateway, CI/CD tools
- Git/GitHub, Firebase, Netlify, Heroku
- ES6/7/8/9, HTML5, CSS3, Bootstrap4/5
- React (Hooks, React Router, Redux)
- Go (Golang), TypeScript, Kotlin
- TensorFlow 2.0/Keras/Scikit-Learn
- Machine Learning, Linear/Regression
- Deep Learning, CNN, RNN, GAN
- Computer Vision, OpenCV
- Natural Language Processing
- Linux, Unix, VS .NET, Android Studio
- Kernel/RTOS design and debug

ATMEL CORPORATION, ASIC Group - Software Engineer

San Jose, CA

03/2006 - 11/2006

- ZigBee network software full stack development.
- Collaborated with system architect to design a proprietary kernel with AVR MCU and ZigBee stack from concept to development.
- Coding the ZigBee Network and Application stack in C & C++.

ATMEL CORPORATION, USB Group - Application Engineer

San Jose, CA

09/2002 - 03/2006

- Provided hardware, firmware, software and application support for the embedded dual ARM based USB Host product for over 60 customers.
- Led the cross-functional collaboration with hardware, firmware, and marketing teams located on-site and offshore.
- Developed, debug, and tested embedded ARM based USB2.0 MSD Driver, File System, and Application in C programming language.
- Collaborated with HW designers to design, debug, and bring up SDK board for ARM system processor with USB, LCD, UART peripherals.
- Developed Test and Demonstration GUI tool in Visual Basic .NET.
- Prepared technical documents, including datasheet, software API usage guides, application notes, and SDK .NET package deployment.

ATMEL CORPORATION, CPLD Group – Design / Product Engineer

San Jose, CA

01/2001 - 09/2002

- CPLD and EEPROM IC design, simulation, layout, debug, and application support using Cadence, Synopsys, and Altera CAD tools.
- Verilog and VHDL coding for design, simulation, and test bench.
- Developed test programs in C/C++ and wafer probing.

Education

SANTA CLARA UNIVERSITY - Santa Clara, CA

09/2011 – 12/2013

Master of Science: Engineering Management and Leadership

Specialize: CS Web & Mobile Development, Distributed Systems

UNIVERSITY OF BRITISH COLUMBIA - Vancouver, BC

09/1996 – 12/2000

Bachelor of Applied Science: Electrical Engineering

Technical Skills (continued)

- ARM/AVR/MCU/PLD/Flash/EEPROM
- ASIC & Embedded System
- USB, UART, USART, I2C, SPI, TTL
- Oscilloscope and IDE/JTAG Test tools
- Wafer probing and board level debug
- Blockchain/Ethereum/Solidity

Management Skills

- Agile Scrum Processing
- Cross-functional Team Collaboration
- Full R&D Cycle Experience
- Multi-functional Technical Experience
- Project planning, scheduling & control
- Strong Interpersonal Skills
- Team-oriented
- Problem-solving
- Multi-tasking
- Customer Support

Certifications

[Go Bootcamp: Learn to Code with Golang \(+1000 exercises!\)](#) - Udemy 10/2020

[Python for Computer Vision with OpenCV&Deep Learning](#) - Udemy 09/2020

[React NodeJS Express & MongoDB – The MERN Fullstack Guide](#) - Udemy 09/2020

[Python for Data Science and Machine Learning Bootcamp](#) - Udemy 03/2020

[Machine Learning with TensorFlow on Google Cloud Platform](#) - Coursera 01/2020

Languages

Proficient in both English and Mandarin