

November 17th, 2021

Prof. Ayman Elnaggar

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We, the members of this group, hereby admit that all members had distributed the assigned workload and all members fulfilled their part of the project (Milestone 1). With Keatin contributing to the ALU, Jonny contributing to RAM, Memory Control, MUX from memory control to RAM, MUX from memory control to Register Bank, and Noah Contributing to Register Bank Design and Memory Control.

Sincerely,

Keatin Colegrave ID- #41615592



Jonny Lake ID - #43265818



Noah Hoover ID - #43893346

