V8: Hooking up the Ignition to the Turbofan

Leszek Swirski & Ross McIlroy

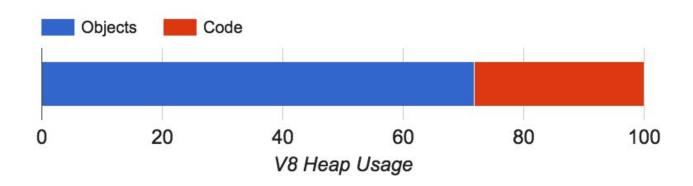
Google London



Ignition + Turbofan pipeline

Why a new pipeline?

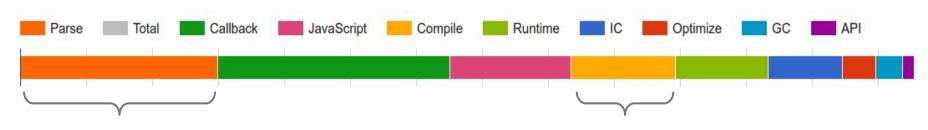
Reduce memory usage



Why a new pipeline?

Reduce memory usage

Reduce startup time



33% of time spent parsing + compiling

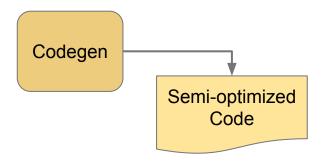
Why a new pipeline?

Reduce memory usage

Reduce startup time

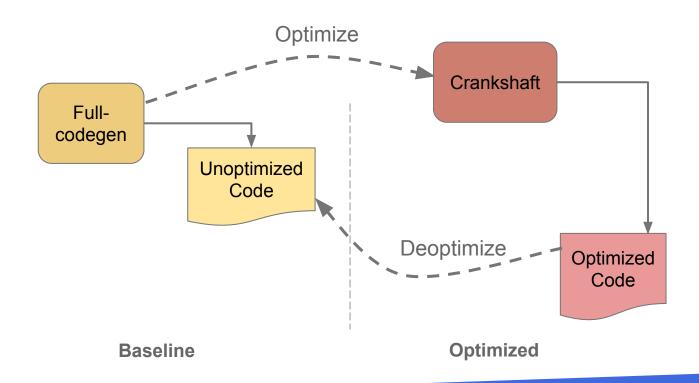
Reduce complexity

Compiler Pipeline (2008)

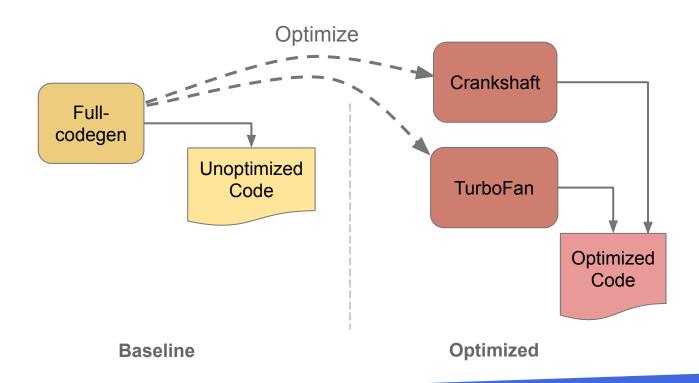


Baseline

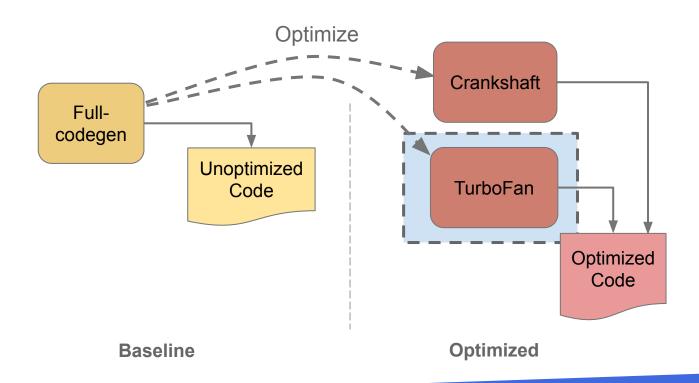
Compiler Pipeline (2010)



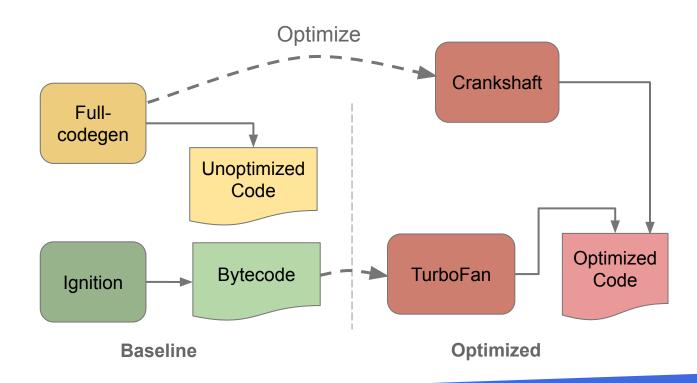
Compiler Pipeline (2015)



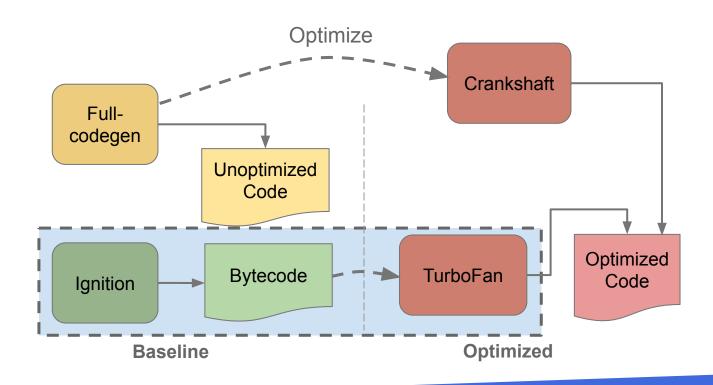
Compiler Pipeline (2015)



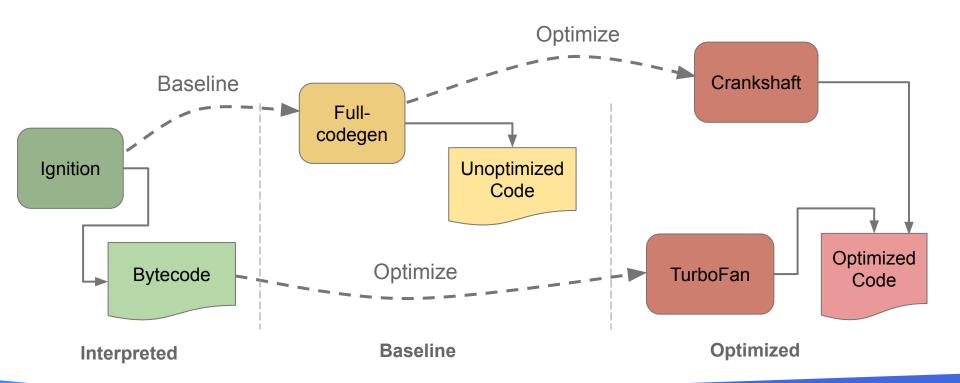
Compiler Pipeline (2016)



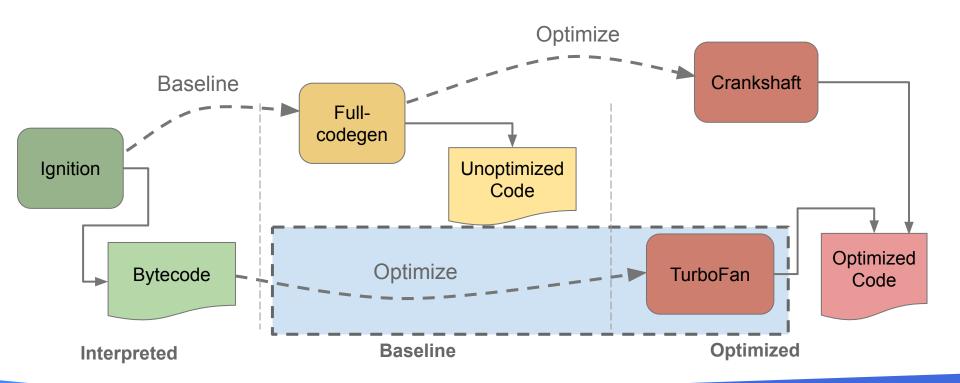
Compiler Pipeline (2016)



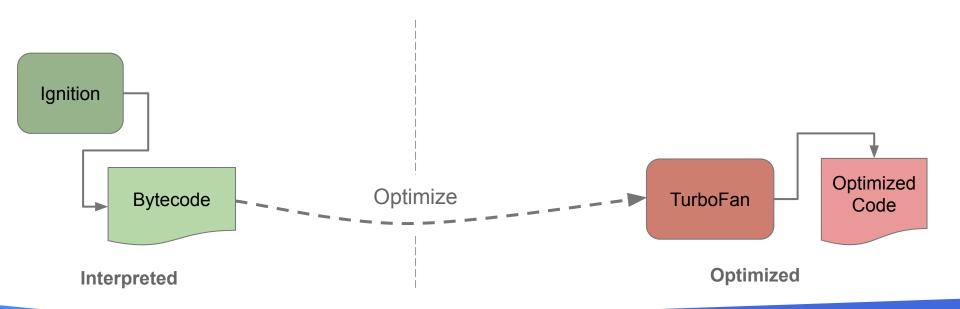
Compiler Pipeline (Svelte Devices)



Compiler Pipeline (Svelte Devices)



Compiler Pipeline (2017)



Bytecode to graph

Ignition to Turbofan

```
StackCheck
                           0 : 7d
                           1 : 1c 02
                                     Ldar a1
function f(a, b) {
                           3: 28 03 02 Add a0, [2]
                          6 : 1d fa
                                        Star r0
  var result = a + b:
                          8 : 1c 02 Ldar a1
  if (a > b) {
                         10 : 4f 03 03 TestGreaterThan a0, [3]
    result = a - b;
                         13 : 74 09
                                        JumpIfFalse [9] (@22)
                                     Ldar a1
                         15 : 1c 02
  return result;
                         17: 29 03 04 Sub a0, [4]
                         20 : 1d fa
                                        Star r0
                          22 : 1c fa
                                    Ldar r0
                          24 : 81
                                        Return
```

Sea-of-nodes graph

(Turbofan)

Bytecode

(Ignition)

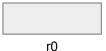
Text

```
StackCheck
0 : 7d
1 : 1c 02
             Ldar a1
3 : 28 03 02 Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
          Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
             Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
             Ldar r0
24 : 81
              Return
```

Parameters



Registers



Accumulator

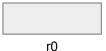


0	:	7d			StackCheck
1	:	1 c	02		Ldar a1
3	:	28	03	02	Add a0, [2]
6	:	1d	fa		Star r0
8	:	1 c	02		Ldar a1
10	:	4f	03	03	TestGreaterThan a0, [3]
13	:	74	09		JumpIfFalse [9] (@22)
15	:	1 c	02		Ldar a1
17	:	29	03	04	Sub a0, [4]
20	:	1d	fa		Star r0
22	:	1 c	fa		Ldar r0
24	:	81			Return

Parameters



Registers



Accumulator



0 : 7d	StackCheck
1 : 1c 02	Ldar a1
3 : 28 03 02	Add a0, [2]
6 : 1d fa	Star r0
8 : 1c 02	Ldar a1
10 : 4f 03 03	TestGreaterThan a0, [3]
13 : 74 09	JumpIfFalse [9] (@22)
15 : 1c 02	Ldar a1
17 : 29 03 04	Sub a0, [4]
20 : 1d fa	Star r0
22 : 1c fa	Ldar r0
24 : 81	Return

Parameters



Registers



Accumulator

```
StackCheck
0 : 7d
1 : 1c 02
              Ldar a1
3 : 28 03 02
             Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
          Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
          Ldar r0
24 : 81
              Return
```

Parameters



Registers



IU

Accumulator

```
StackCheck
0 : 7d
1 : 1c 02
              Ldar a1
 3: 28 03 02 Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
          Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
          Ldar r0
24 : 81
              Return
```

Parameters

0x00000004	0x00000010
a0	a1

Registers



. •

Accumulator

```
StackCheck
0 : 7d
1 : 1c 02
          Ldar a1
3 : 28 03 02 Add a0, [2]
6 : 1d fa
             Star r0
8 : 1c 02
          Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
             Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
             Star r0
22 : 1c fa
          Ldar r0
24 : 81
              Return
```

Parameters



Registers



Accumulator

```
StackCheck
0 : 7d
1 : 1c 02
             Ldar a1
 3 : 28 03 02 Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
          Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
          Ldar r0
24 : 81
              Return
```

Parameters

0x00000004	0x00000010
a0	a1

Registers



Accumulator

```
StackCheck
0 : 7d
1 : 1c 02
          Ldar a1
3 : 28 03 02 Add a0, [2]
6 : 1d fa
             Star r0
8 : 1c 02
          Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
             Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
             Star r0
22 : 1c fa
          Ldar r0
24 : 81
              Return
```

Parameters



Registers



Accumulator

```
StackCheck
0 : 7d
1 : 1c 02
              Ldar a1
 3 : 28 03 02 Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
          Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
             Ldar r0
24:81
              Return
```

Parameters

0x00000004	0x00000010
a0	a1

Registers



Accumulator

```
StackCheck
0 : 7d
1 : 1c 02
          Ldar a1
3 : 28 03 02 Add a0, [2]
6 : 1d fa
             Star r0
8 : 1c 02 Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
          Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
             Star r0
22 : 1c fa
             Ldar r0
24:81
              Return
```

Parameters

a0	a1
0x00000004	0x00000010

Registers



r0

Accumulator

```
StackCheck
0 : 7d
1 : 1c 02
             Ldar a1
3 : 28 03 02 Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
          Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
              Ldar r0
24:81
              Return
```

Parameters

0x00000004	0x00000010
a0	a1

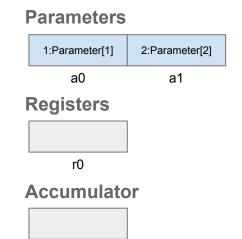
Registers



Accumulator

1:Parameter[1] 2:Parameter[2] 0:Start

```
StackCheck
0 : 7d
1 : 1c 02
              Ldar a1
 3 : 28 03 02 Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
          Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
             Ldar r0
24:81
              Return
```



0 : 7d	StackCheck	Parameters	
1 : 1c 02 3 : 28 03 02	Ldar a1 Add a0, [2]	1:Parameter[1]	2:Parameter[2]
6 : 1d fa	Star r0	a0	a1
8 : 1c 02	Ldar a1	Registers	
10 : 4f 03 03	TestGreaterThan a0, [3]	rtogiotoro	
13 : 74 09	JumpIfFalse [9] (@22)		
15 : 1c 02	Ldar a1	0	
17 : 29 03 04	Sub a0, [4]	r0	
20 : 1d fa	Star r0	Accumulato	r
22 : 1c fa	Ldar r0		
24 : 81	Return		

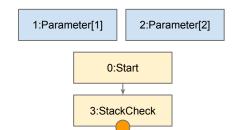
1:Parameter[1] 2:Parameter[2]

0:Start

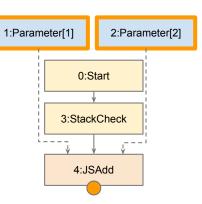
3:StackCheck

0	:	7d		StackCheck	Parameters	
1	:	1c 0	2	Ldar a1	4.00	OrDonomonton[O]
3	:	28 0	3 02	Add a0, [2]	1:Parameter[1]	2:Parameter[2]
6	:	1d f	a	Star r0	a0	a1
8	:	1c 0	2	Ldar a1	Registers	
10	:	4f 0	3 03	TestGreaterThan a0, [3]	Registers	
13	:	74 0	9	JumpIfFalse [9] (@22)		
15	:	1c 0	2	Ldar a1		
17	:	29 0	3 04	Sub a0, [4]	r0	
20	:	1d f	a	Star r0	Accumulato	r
22	:	1c f	a	Ldar r0		
24	:	81		Return	2:Parameter[2]	

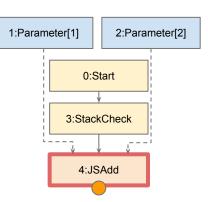
Paramotors



0 :	: 7	d		StackCheck	Parameters	
1 :	: 1	c 02		Ldar a1	1:Daramatar[1]	2:Parameter[2]
3 :	: 2	8 03	02	Add a0, [2]	1:Parameter[1]	Z.Farameter[Z]
6 :	: 1	d fa		Star r0	a0	a1
8 :	: 1	c 02		Ldar a1	Registers	
10 :	: 4	f 03	03	TestGreaterThan a0, [3]	registers	
13 :	: 7	4 09		JumpIfFalse [9] (@22)		
15 :	: 1	c 02		Ldar a1	rO	
17 :	: 2	9 03	04	Sub a0, [4]	r0	
20 :	: 1	d fa		Star r0	Accumulato	r
22	: 1	c fa		Ldar r0	0.0	
24 :	: 8	1		Return	2:Parameter[2]	



		7d			StackCheck	Parameters	
1	:	1c	02		Ldar a1	1:Darameter[1]	2:Parameter[2]
3	:	28	03	02	Add a0, [2]	1:Parameter[1]	Z.Farameter[2]
6	:	1d	fa		Star r0	a0	a1
8	:	1 c	02		Ldar a1	Registers	
10	:	4f	03	03	TestGreaterThan a0, [3]	rtogistors	
13	:	74	09		JumpIfFalse [9] (@22)		
15	:	1c	02		Ldar a1		
17	:	29	03	04	Sub a0, [4]	r0	
20	:	1d	fa		Star r0	Accumulato	or
22	:	1c	fa		Ldar r0		
24	:	81			Return	4:JSAdd	



0	:	7d			StackCheck
1	:	1 c	02		Ldar a1
3	:	28	03	02	Add a0, [2]
6	:	1d	fa		Star r0
8	:	1 c	02		Ldar a1
10	:	4f	03	03	TestGreaterThan a0, [3]
13	:	74	09		JumpIfFalse [9] (@22)
15	:	1 c	02		Ldar a1
17	:	29	03	04	Sub a0, [4]
20	:	1d	fa		Star r0
22	:	1 c	fa		Ldar r0
24	:	81			Return

Parameters

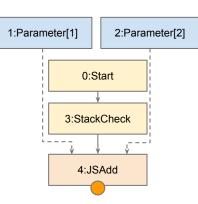
1:Parameter[1] 2:Parameter[2] a0 a1

Registers

4:JSAdd r0

Accumulator

4:JSAdd



1	:		03		StackCheck Ldar a1 Add a0, [2] Star r0
8	·	1c	02		Ldar a1
10	:	4f	03	03	TestGreaterThan a0, [3]
13	:	74	09		JumpIfFalse [9] (@22)
15	:	1c	02		Ldar a1
17	:	29	03	04	Sub a0, [4]
20	:	1d	fa		Star r0
22	:	1c	fa		Ldar r0
24	:	81			Return

Parameters

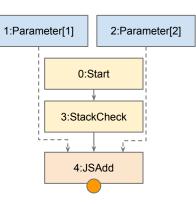
1:Parameter[1] 2:Parameter[2] a0 a1

Registers

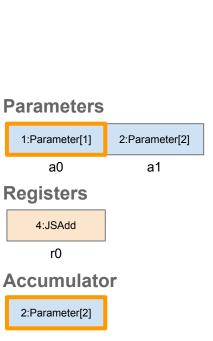
4:JSAdd r0

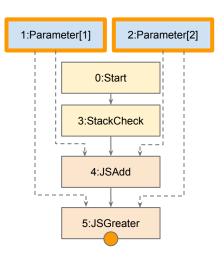
Accumulator

2:Parameter[2]



1 3 6	:		02 03 fa	02	StackCheck Ldar a1 Add a0, [2] Star r0 Ldar a1
10	:	4f	03	03	TestGreaterThan a0, [3]
13	:	74	09		JumpIfFalse [9] (@22)
15	:	1 c	02		Ldar a1
17	:	29	03	04	Sub a0, [4]
20	:	1d	fa		Star r0
22	:	1c	fa		Ldar r0
24	:	81			Return





```
StackCheck
0 : 7d
1 : 1c 02
              Ldar a1
 3: 28 03 02 Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
           Ldar a1
10 : 4f 03 03
             TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
17 : 29 03 04
              Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
              Ldar r0
24:81
              Return
```

Parameters

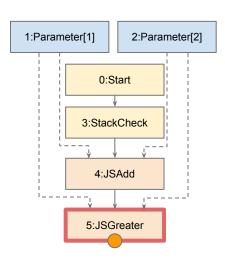
1:Parameter[1] 2:Parameter[2] a0 a1

Registers

4:JSAdd r0

Accumulator

5:JSGreater



```
StackCheck
 0 : 7d
 1 : 1c 02
              Ldar a1
 3 : 28 03 02 Add a0, [2]
 6 : 1d fa
              Star r0
 8 : 1c 02
           Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
              JumpIfFalse [9] (@22)
13 : 74 09
15 : 1c 02
              Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
             Ldar r0
24:81
              Return
```

Parameters

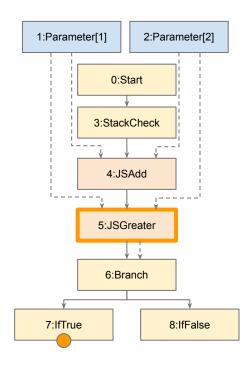
1:Parameter[1] 2:Parameter[2] a0 a1

Registers

4:JSAdd r0

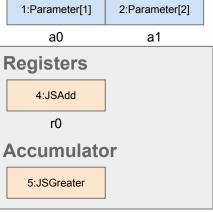
Accumulator

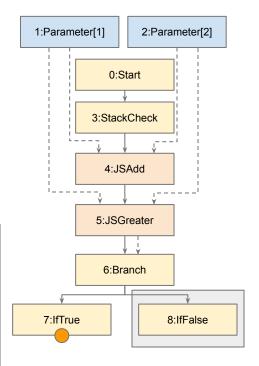
5:JSGreater



```
StackCheck
0 : 7d
1 : 1c 02
             Ldar a1
3: 28 03 02 Add a0, [2]
6 : 1d fa
             Star r0
8 : 1c 02
          Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
              JumpIfFalse [9] (@22)
13 : 74 09
15 : 1c 02
             Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
             Star r0
22 : 1c fa
          Ldar r0
24:81
              Return
```

Parameters |





```
StackCheck
 0 : 7d
 1 : 1c 02
              Ldar a1
 3 : 28 03 02 Add a0, [2]
 6 : 1d fa
              Star r0
 8 : 1c 02
           Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
              Ldar r0
24:81
              Return
```

Registers Accumulator Parent

4:JSAdd 5:JSGreater 8:IfFalse

Parameters

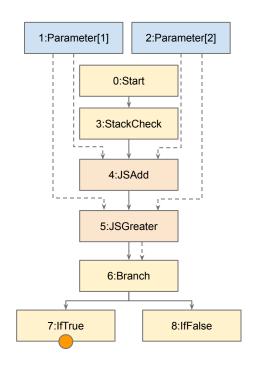
1:Parameter[1] 2:Parameter[2] a0 a1

Registers

4:JSAdd r0

Accumulator

5:JSGreater



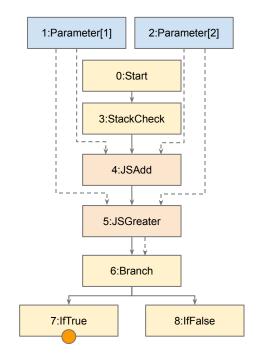
22:

```
StackCheck
0 : 7d
1 : 1c 02
              Ldar a1
 3 : 28 03 02 Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
           Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13: 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
17 : 29 03 04
              Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
              Ldar r0
24:81
              Return
```

Registers Accumulator Parent

4:JSAdd 5:JSGreater 8:IfFalse

Parameters 2:Parameter[2] 1:Parameter[1] a1 a0 Registers 4:JSAdd r0 Accumulator 2:Parameter[2]



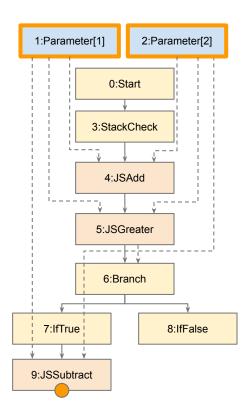
22:

```
StackCheck
0 : 7d
1 : 1c 02
              Ldar a1
 3: 28 03 02 Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
           Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13: 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
              Sub a0, [4]
17 : 29 03 04
20 : 1d fa
              Star r0
22: 1c fa
              Ldar r0
24:81
              Return
```

Registers Accumulator Parent

4:JSAdd 5:JSGreater 8:IfFalse

Parameters 1:Parameter[1] 2:Parameter[2] a0 a1 Registers 4:JSAdd r0 **Accumulator** 2:Parameter[2]



```
StackCheck
 0 : 7d
 1 : 1c 02
              Ldar a1
 3: 28 03 02 Add a0, [2]
 6 : 1d fa
              Star r0
 8 : 1c 02
           Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13: 74 09
               JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
              Sub a0, [4]
17 : 29 03 04
20 : 1d fa
              Star r0
22: 1c fa
              Ldar r0
24:81
               Return
```

Registers Accumulator Parent

4:JSAdd 5:JSGreater 8:IfFalse

Parameters

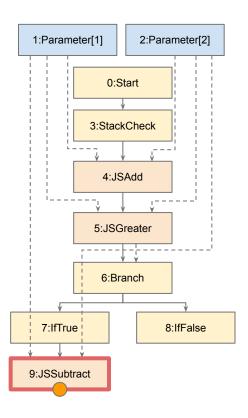
1:Parameter[1] 2:Parameter[2] a0 a1

Registers

4:JSAdd r0

Accumulator

9:JSSubtract



0	:	7d		StackCheck
1	:	1 c	02	Ldar a1
3	:	28	03 02	Add a0, [2]
6	:	1d	fa	Star r0
8	:	1c	02	Ldar a1
10	:	4f	03 03	TestGreaterThan a0, [3]
13	:	74	09	JumpIfFalse [9] (@22)
15	:	1 c	02	Ldar a1
17	:	29	03 04	Sub a0, [4]
20	:	1d	fa	Star r0
22	:	1 c	fa	Ldar r0
24	:	81		Return

Registers Accumulator Parent

4:JSAdd 5:JSGreater 8:IfFalse

Parameters

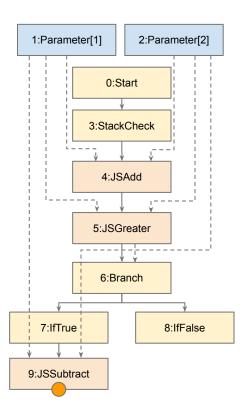
1:Parameter[1] 2:Parameter[2] a0 a1

Registers

9:JSSubtract r0

Accumulator

9:JSSubtract



22:

```
StackCheck
0 : 7d
1 : 1c 02
              Ldar a1
 3 : 28 03 02 Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
           Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
17: 29 03 04 Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
              Ldar r0
24:81
              Return
```

Parameters

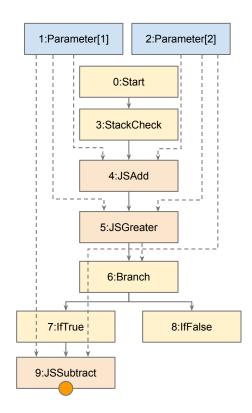
1:Parameter[1] 2:Parameter[2] a0 a1

Registers

9:JSSubtract r0

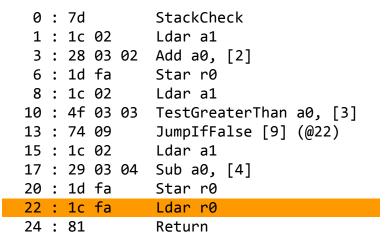
Accumulator

9:JSSubtract



22:





Registers Accumulator Parent

4:JSAdd 5:JSGreater 8:IfFalse

Parameters

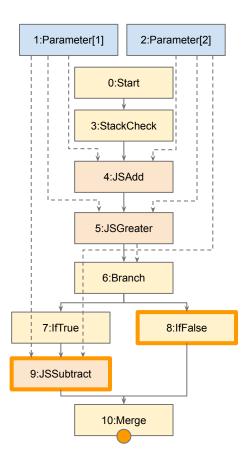
1:Parameter[1] 2:Parameter[2] a0 a1

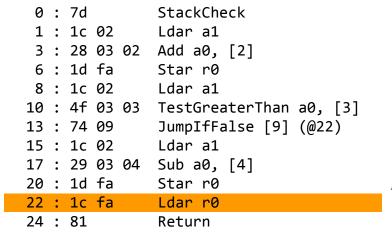
Registers

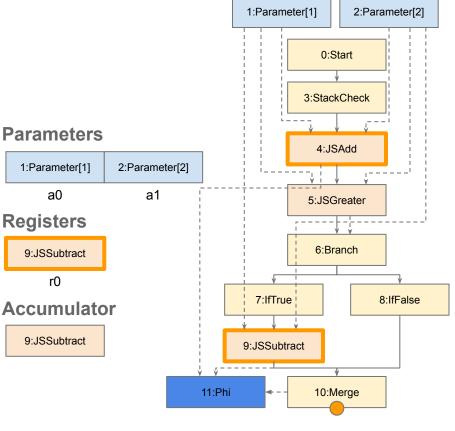
9:JSSubtract r0

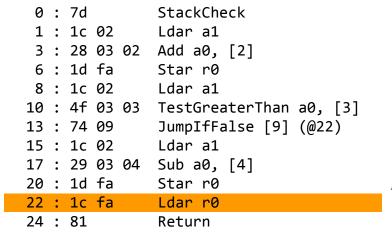
Accumulator

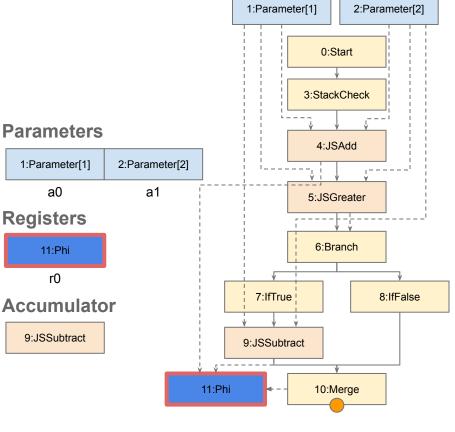
9:JSSubtract

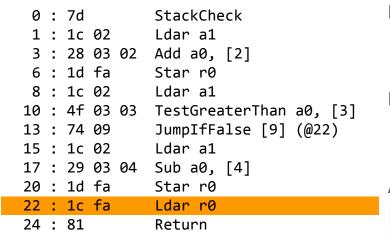


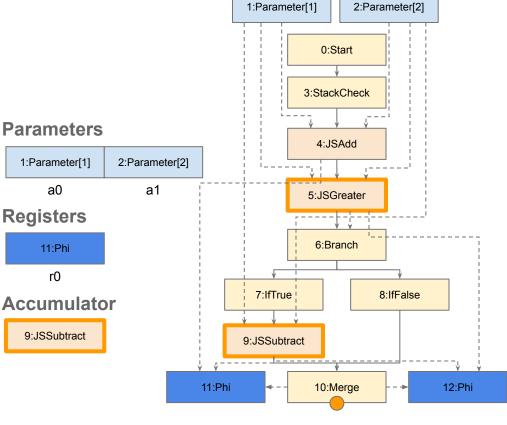


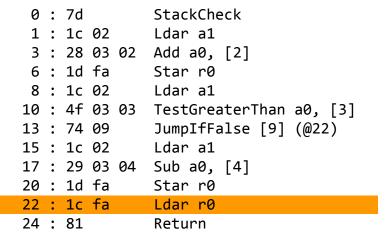










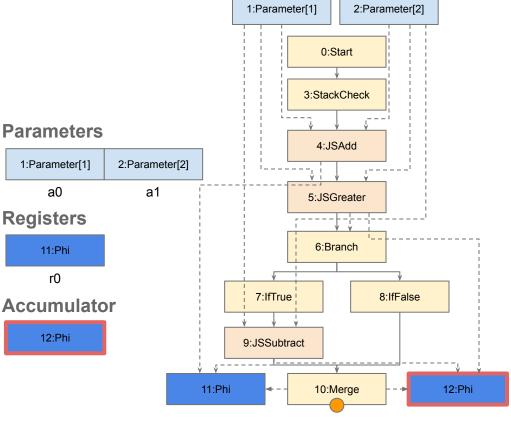


Parent

8:IfFalse

Registers Accumulator

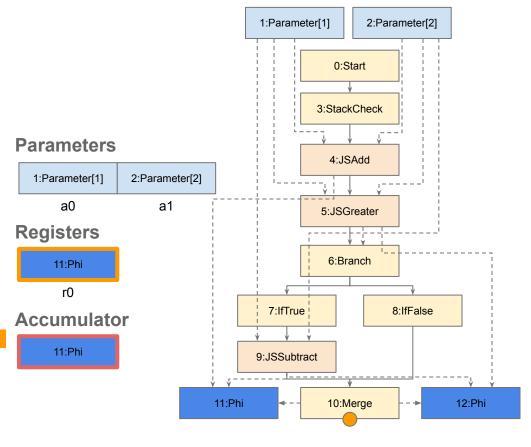
4:JSAdd 5:JSGreater



0 . 74

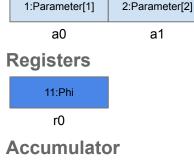
0	:	7a			StackCheck
1	:	1 c	02		Ldar a1
3	:	28	03	02	Add a0, [2]
6	:	1d	fa		Star r0
8	:	1c	02		Ldar a1
10	:	4f	03	03	TestGreaterThan a0, [3]
13	:	74	09		JumpIfFalse [9] (@22)
15	:	1 c	02		Ldar a1
17	:	29	03	04	Sub a0, [4]
20	:	1d	fa		Star r0
22	:	1c	fa		Ldar r0
24	:	81			Return

C+ackChack

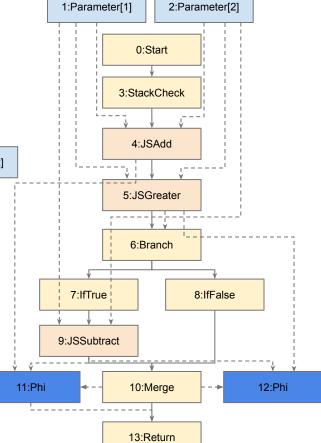


```
StackCheck
0 : 7d
1 : 1c 02
              Ldar a1
 3 : 28 03 02 Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
           Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13: 74 09
               JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
              Sub a0, [4]
17 : 29 03 04
20 : 1d fa
              Star r0
22 : 1c fa
              Ldar r0
24:81
               Return
```

Parameters



11:Phi



```
StackCheck
0 : 7d
1 : 1c 02
              Ldar a1
 3: 28 03 02 Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
           Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13: 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
              Sub a0, [4]
17 : 29 03 04
20 : 1d fa
              Star r0
22: 1c fa
              Ldar r0
24:81
              Return
```

Registers Accumulator Parent

4:JSAdd 5:JSGreater 8:IfFalse

Parameters

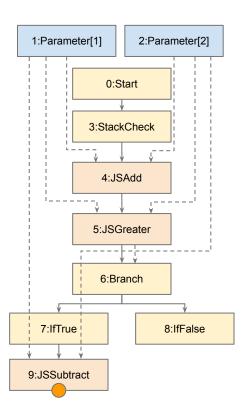
1:Parameter[1] 2:Parameter[2] a0 a1

Registers

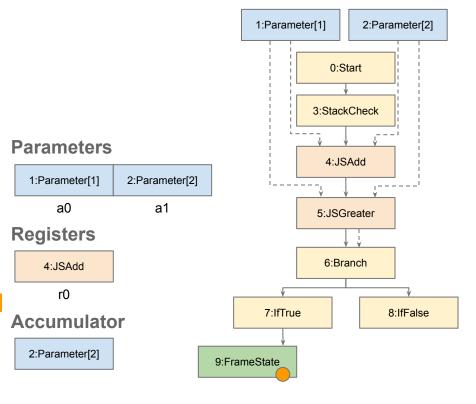
4:JSAdd r0

Accumulator

2:Parameter[2]



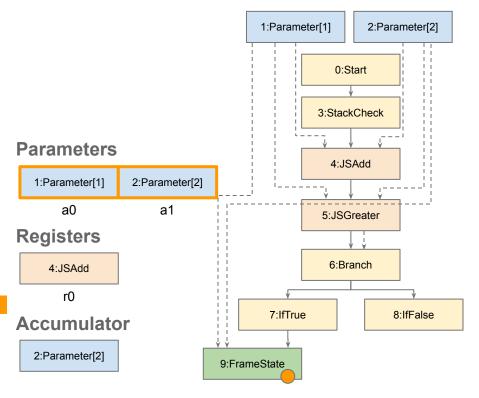
```
StackCheck
0 : 7d
1 : 1c 02
              Ldar a1
 3 : 28 03 02 Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
           Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
               JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
              Sub a0, [4]
17 : 29 03 04
20 : 1d fa
              Star r0
22 : 1c fa
              Ldar r0
24:81
               Return
```



```
StackCheck
0 : 7d
 1 : 1c 02
               Ldar a1
 3 : 28 03 02
               Add a0, [2]
6 : 1d fa
               Star r0
8 : 1c 02
               Ldar a1
10 : 4f 03 03
               TestGreaterThan a0, [3]
13: 74 09
               JumpIfFalse [9] (@22)
15 : 1c 02
               Ldar a1
               Sub a0, [4]
    29 03 04
20 : 1d fa
               Star r0
22: 1c fa
               Ldar r0
24:81
               Return
```

Registers Accumulator Parent

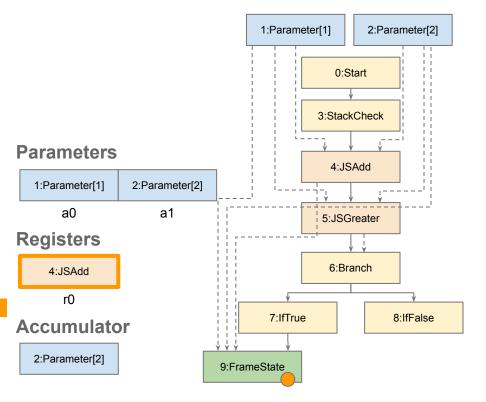
4:JSAdd 5:JSGreater 8:IfFalse



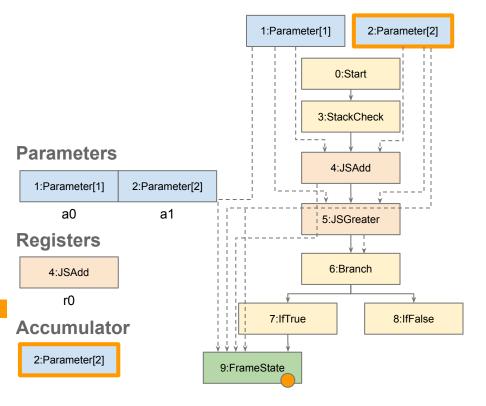
22:

```
StackCheck
0 : 7d
 1 : 1c 02
               Ldar a1
 3 : 28 03 02
               Add a0, [2]
6 : 1d fa
               Star r0
8 : 1c 02
               Ldar a1
10 : 4f 03 03
               TestGreaterThan a0, [3]
13: 74 09
               JumpIfFalse [9] (@22)
15 : 1c 02
               Ldar a1
               Sub a0, [4]
    29 03 04
20 : 1d fa
               Star r0
22: 1c fa
               Ldar r0
24:81
               Return
```

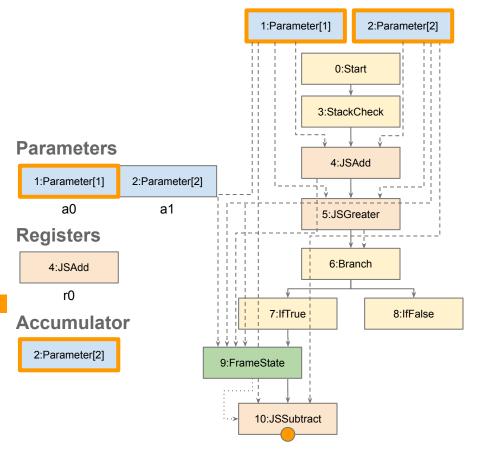




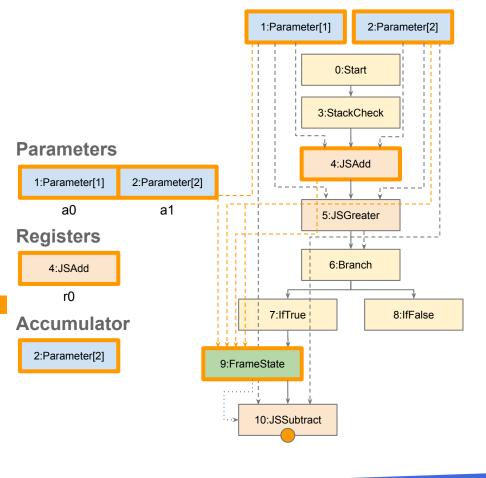
```
StackCheck
0 : 7d
 1 : 1c 02
               Ldar a1
 3 : 28 03 02
               Add a0, [2]
6 : 1d fa
               Star r0
8 : 1c 02
               Ldar a1
10 : 4f 03 03
               TestGreaterThan a0, [3]
13 : 74 09
               JumpIfFalse [9] (@22)
15 : 1c 02
               Ldar a1
               Sub a0, [4]
    29 03 04
20 : 1d fa
               Star r0
22: 1c fa
               Ldar r0
24:81
               Return
```



StackCheck 0 : 7d 1 : 1c 02 Ldar a1 Add a0, [2] 3 : 28 03 02 6 : 1d fa Star r0 8 : 1c 02 Ldar a1 10 : 4f 03 03 TestGreaterThan a0, [3] 13 : 74 09 JumpIfFalse [9] (@22) Ldar a1 15 : 1c 02 Sub a0, [4] 03 04 20: 1d fa Star r0 22: 1c fa Ldar r0 24:81 Return



StackCheck 0 : 7d 1 : 1c 02 Ldar a1 3 : 28 03 02 Add a0, [2] 6 : 1d fa Star r0 1c 02 Ldar a1 10 : 4f 03 03 TestGreaterThan a0, [3] 13 : 74 09 JumpIfFalse [9] (@22) 15 : 1c 02 Ldar a1 Sub a0, [4] 03 04 20 : 1d fa Star r0 22: 1c fa Ldar r0 24:81 Return



```
StackCheck
 0 : 7d
 1 : 1c 02
              Ldar a1
 3 : 28 03 02
              Add a0, [2]
 6 : 1d fa
              Star r0
 8 : 1c 02
           Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
17 : 29 03 04
              Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
              Ldar r0
24:81
              Return
```

Parameters

0xfe902ab5	0x1237ab57	
a0	a1	

Registers

0xba8320fd

r0

Accumulator

0x1237ab57

```
StackCheck
0 : 7d
1 : 1c 02
              Ldar a1
 3 : 28 03 02
              Add a0, [2]
6 : 1d fa
              Star r0
8 : 1c 02
           Ldar a1
10 : 4f 03 03 TestGreaterThan a0, [3]
13 : 74 09
              JumpIfFalse [9] (@22)
15 : 1c 02
              Ldar a1
17 : 29 03 04
              Sub a0, [4]
20 : 1d fa
              Star r0
22 : 1c fa
              Ldar r0
24:81
              Return
```

Parameters

 0xfe902ab5
 0x1237ab57

 a0
 a1

Registers

0xba8320fd r0

Accumulator

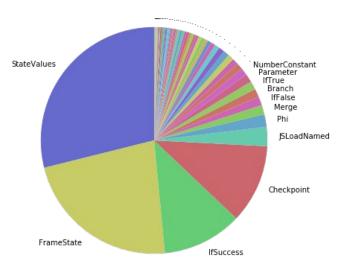
0xad80b1cf

Bytecode Restrictions to Simplifying Graph Creation

- Always deopt to a bytecode
- Well-scoped basic blocks
 - Exception handlers cover a single linear range of bytecode
- No irreducible control flow
- Single backwards-branch to loop header
- Registers in loop-closed form

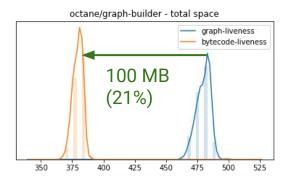
Static analysis of bytecode

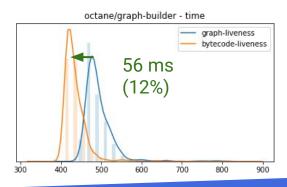
- Pre-analyze bytecode before building graph
 - Liveness analysis (for deoptimisation frame states)
 - Loop assignment analysis (for loop phis)
- Don't generate unnecessary nodes
 - Avoid memory overhead (40+ bytes per node)
 - Avoid graph traversals



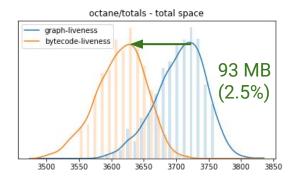
- Previously iterated traversal over basic blocks
 - Create liveness maps and state value nodes during graph building
 - Re-create state value nodes based on liveness afterward
- Now iterated passes over bytecode array
 - State value nodes created only once

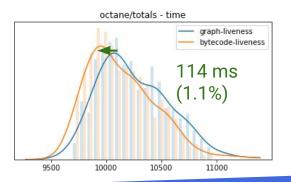
- Previously iterated traversal over basic blocks
 - Create liveness maps and state value nodes during graph building
 - Re-create state value nodes based on liveness afterward
- Now iterated passes over bytecode array
 - State value nodes created only once



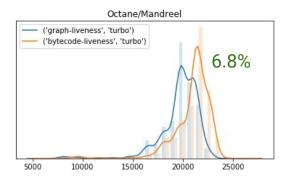


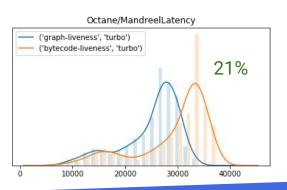
- Previously iterated traversal over basic blocks
 - Create liveness maps and state value nodes during graph building
 - Re-create state value nodes based on liveness afterward
- Now iterated passes over bytecode array
 - State value nodes created only once





- Previously iterated traversal over basic blocks
 - Create liveness maps and state value nodes during graph building
 - Re-create state value nodes based on liveness afterward
- Now iterated passes over bytecode array
 - State value nodes created only once





Burning away complex control (Generators)

- Javascript Generators can yield expressions at arbitrary points
 - Can introduce irreducible control flow
 - Solution: transform into switch statements on hidden token in prologue and loop headers
 - Result: Turbofan doesn't need to know anything about generator control flow

Burning away complex control (Generators)

```
function f(hidden token) {
                                              switch(hidden token)
                                                  case %normal%: break;
                                                  case %resume%: goto loop;
                                              var i = 0;
function* f() {
                                              loop:
    var i = 0;
                                                   switch(hidden token)
    while (true) {
                                                       case %normal%: break;
        yield i++;
                                                       case %resume%: goto resume;
                                                  hidden token = %resume%;
                                                  return i++;
                                              resume:
                                                  hidden token = %normal%;
                                                  goto loop;
```

Burning away complex control (try-finally)

- try-finally exception handlers
 - Exit differently depending on what triggered the finally block (fall-through, return or throw)
 - Solution: switch statement at end of finally block
 - Result: Turbofan doesn't need to know anything about try-finally control flow

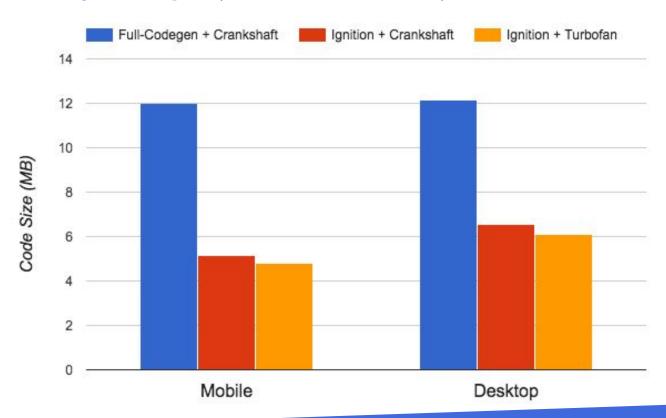
Burning away complex control (try-finally)

```
try {
    if a() return;
} finally {
    b();
}
```

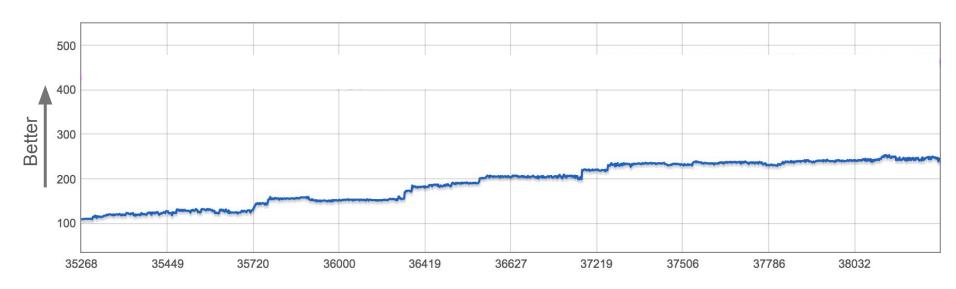
```
try:
    if a() {
        finally token = %return%;
        goto finally;
    finally_token = %fallthrough%;
    goto finally;
throw handler:
    finally token = %throw%;
finally:
    b();
    switch(finally token)
        case %return%: return;
        case %throw%: rethrow();
        case %fallthrough%: break;
```

Performance Results

Code Memory Usage (Real Websites)

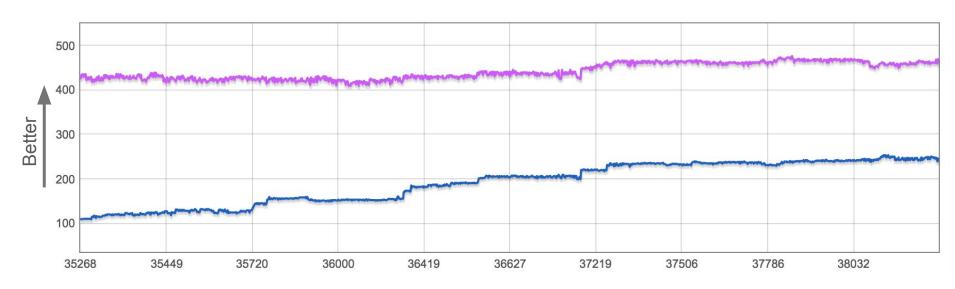


Ignition vs Full-Codegen



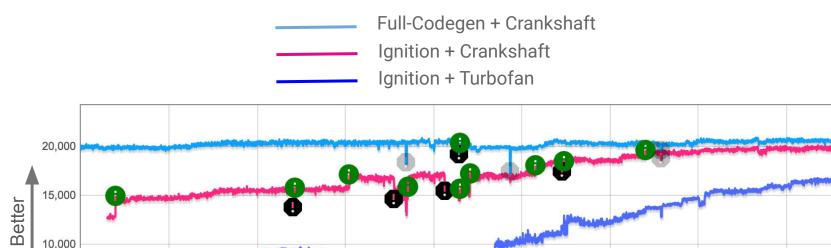
Octane (Nexus 5)
Crankshaft and TurboFan disabled

Ignition vs Full-Codegen



Octane (Nexus 5)
Crankshaft and TurboFan disabled

Octane Performance

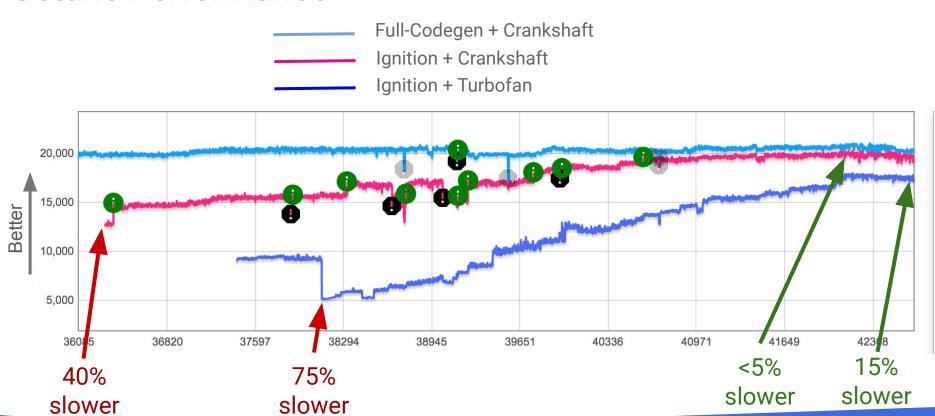




10,000

5,000

Octane Performance

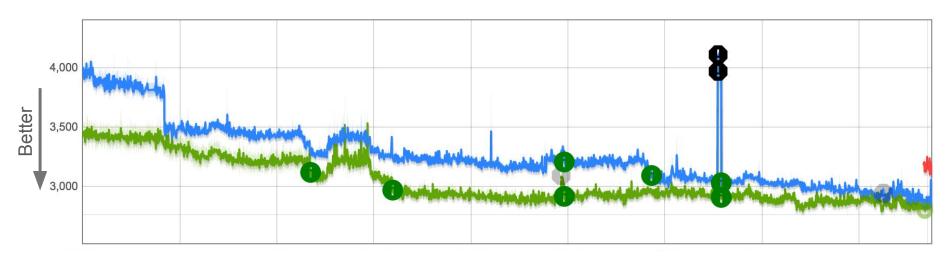


Speedometer Performance

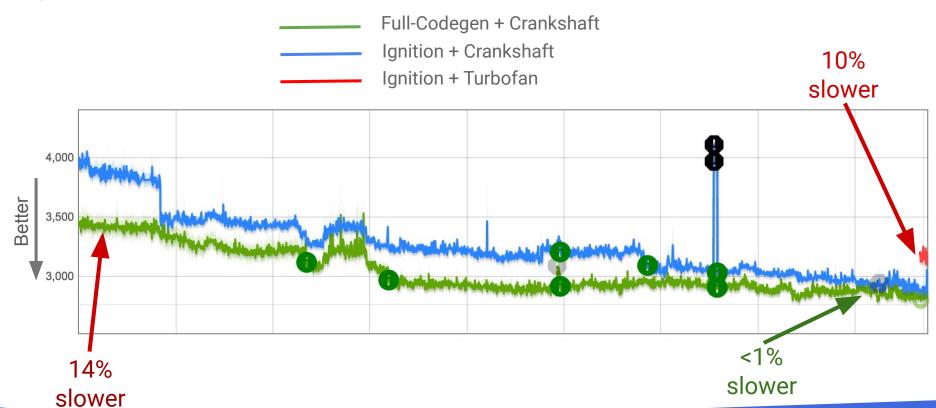
Full-Codegen + Crankshaft

_____ Ignition + Crankshaft

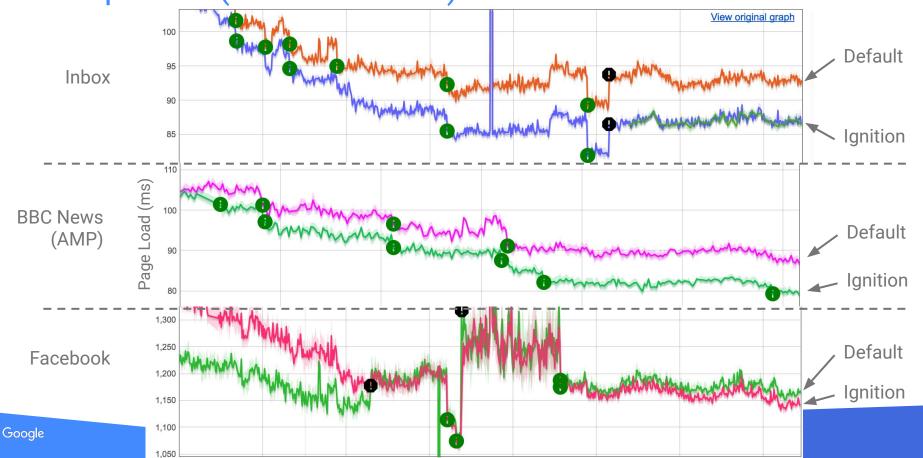
_____ Ignition + Turbofan



Speedometer Performance



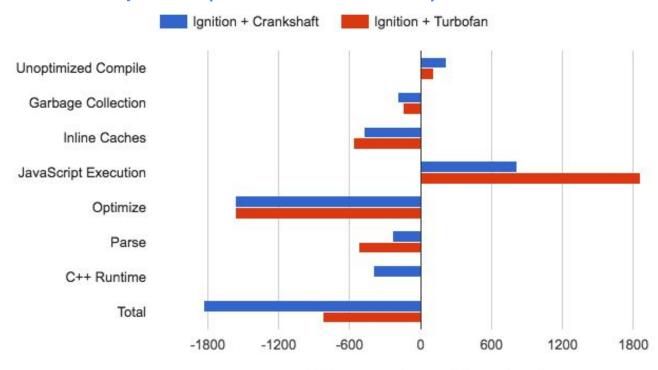
Startup Time (Real Websites)



Startup Time (Real Websites)



Where time is spent (Real Websites)



Difference in runtime (ms)

Summary

- Ignition + Turbofan is the future of V8
- Restrictions on bytecode can simplify optimized graph creation
- Optimizing for real-world exposes different trade-offs