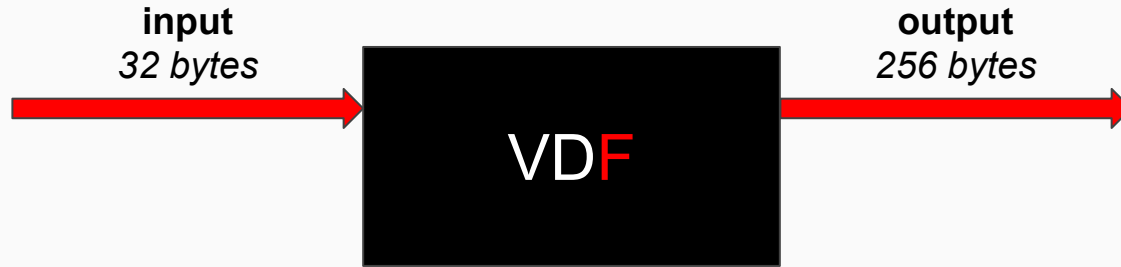


RSA VDFs

crash course



Verifiable Delay Function (VDF)



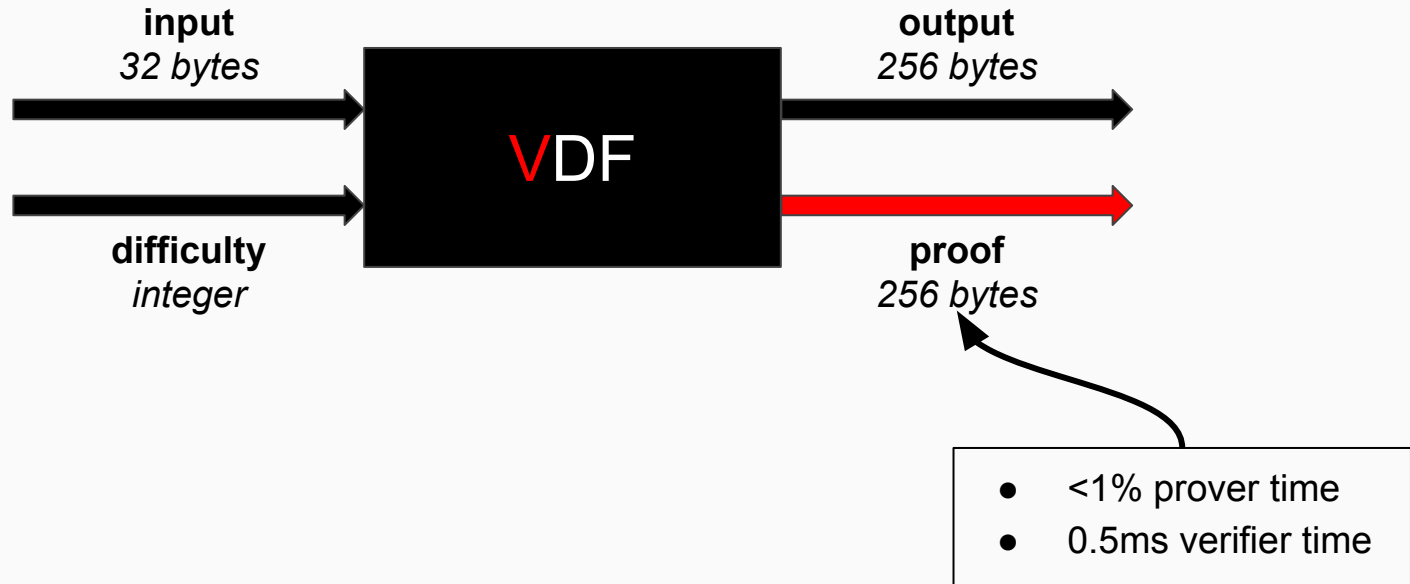
Verifiable Delay Function (VDF)



Verifiable Delay Function (VDF)



Verifiable Delay Function (VDF)



Modular exponentiation

SQUARING

$$x \rightarrow x^2 \% N$$

N # unknown factorisation 2048-bit RSA modulus

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T SQUARINGS

$$x \rightarrow x^2 \rightarrow x^4 \rightarrow \dots \rightarrow x^{2^{**}T}$$

x # VDF input

T # time parameter

Modular exponentiation

SQUARING

$$x \rightarrow x^2 \% N$$

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$$x \rightarrow x^2 \rightarrow x^4 \rightarrow \dots \rightarrow x^{2^{**}T}$$

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OUTPUT

$$y = x^{2^{**}T} \% N$$

y # VDF output

Time-lock puzzles and timed-release Crypto

Ronald L. Rivest*, Adi Shamir**, and David A. Wagner***

Revised March 10, 1996



OUTPUT

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Timelock puzzle

- set by Ron Rivest in 1999
- designed to take 35 years

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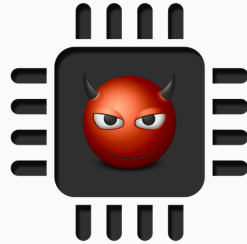
**SUPRA
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Universitesi



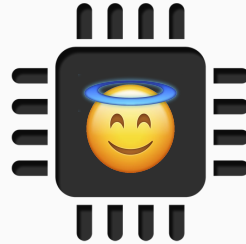
Safety assumption

attacker hardware



vs

commodity hardware



speed advantage $\leq A_{\max}$

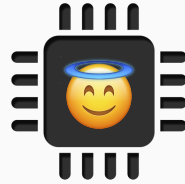
Safety assumption

CPU

1 us

FPGA

30 ns



1 ns



COSMOS



ethereum



Filecoin

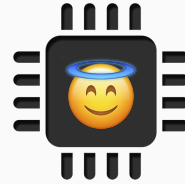
Safety assumption

CPU

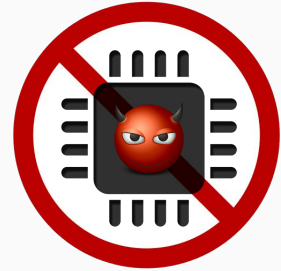
1 us

FPGA

30 ns



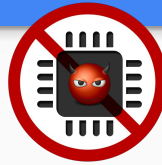
1 ns



16 ps



Lower bounds



16 ps

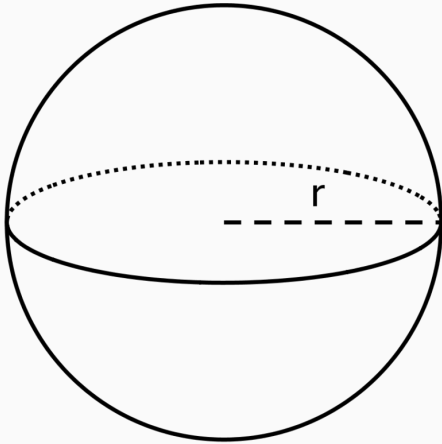
physics

complexity theory

⋮

Lower bounds

physics



< 4.8mm radius
per squaring

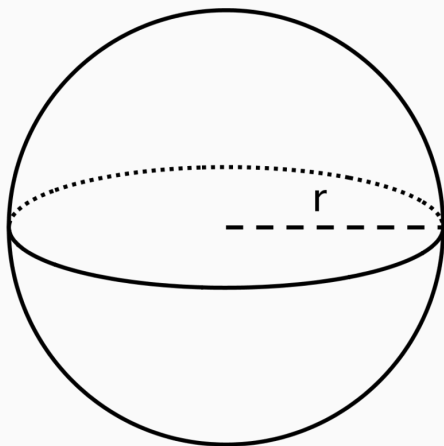


16 ps

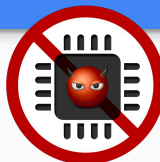


complexity theory

physics



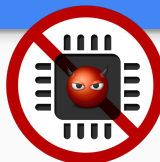
< 4.8mm radius
per squaring



16 ps

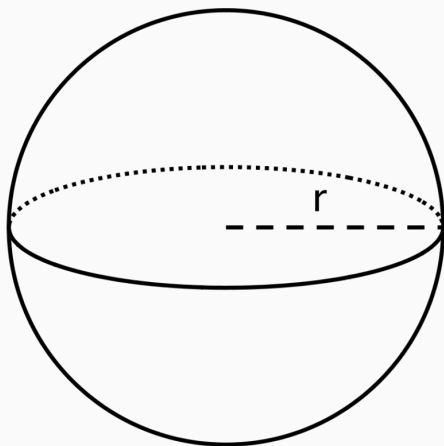
complexity theory

Result: n -bit modular squaring
requires $\log(n)$ depth in the
average case.



16 ps

physics



< 4.8mm radius
per squaring

complexity theory

Result: n -bit modular squaring requires $\log(n)$ depth in the average case.

Caveats:

- single squaring
- binary representation
- fan-in 2 gates

Applications

Use cases

randomness



randomness



- 100 people, one by one, enter a dark room to reroll a set of dice.



randomness



- 100 people, one by one, enter a dark room to reroll a set of dice.
- Lights turn on after the last person, revealing a fair random number.



randomness



- 100 people, one by one, enter a dark room to reroll a set of dice.
- Lights turn on after the last person, revealing a fair random number.
- The VDF ensures lights are not turned on early.



Use cases

randomness



proof of space



proof of replication



Use cases

randomness



proof of space



proof of replication



proof of history



anti-frontrunning



randomness



proof of space



proof of replication



proof of history



anti-frontrunning



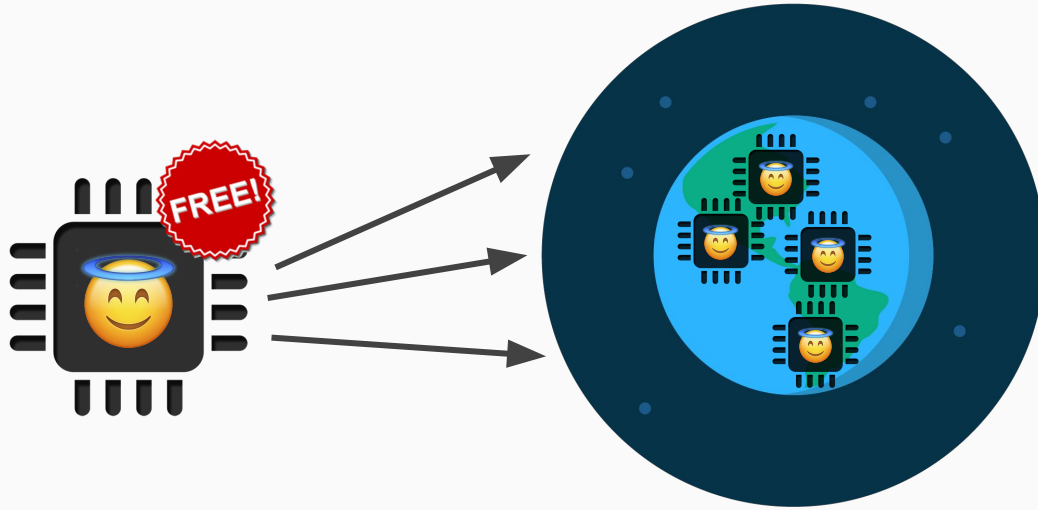
long tail

- objective fork choice
- expiring zk-proofs
- guaranteed output delivery
- timelocks

Liveness assumption

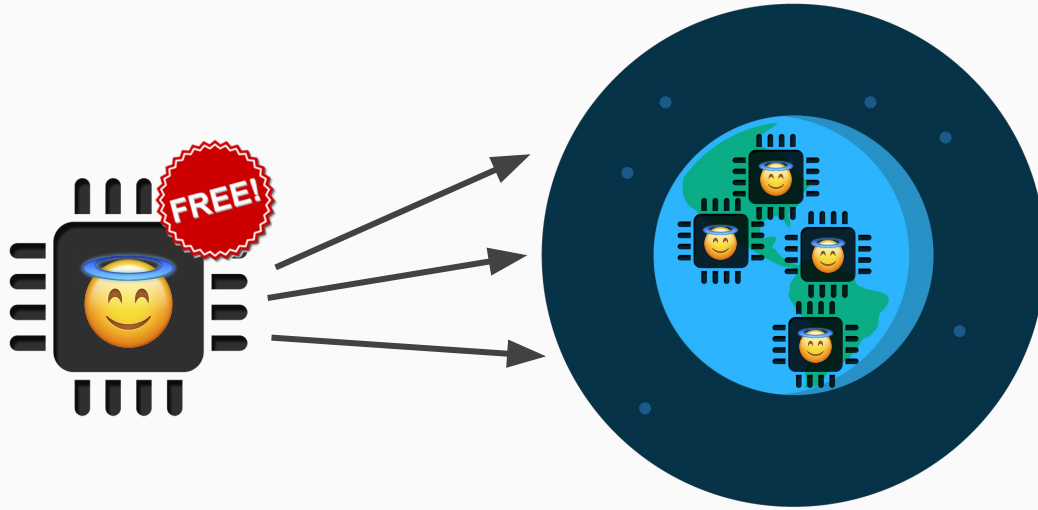
≥ 1 online VDF rig

Liveness assumption



≥ 1 online VDF rig

Liveness assumption



VDF Alliance

≥ 1 online VDF rig

Provers

- “Verifiable Delay Functions”—**Boneh, Bonneau, Bünz, Fisch**
- “Efficient Verifiable Delay Functions”—**Wesolowski**
- “Simple Verifiable Delay Functions”—**Pietrzak**

- “Verifiable Delay Functions”—**Boneh, Bonneau, Bünz, Fisch**
- “Efficient Verifiable Delay Functions”—**Wesolowski**
- “Simple Verifiable Delay Functions”—**Pietrzak**

} published
June 2018

- “A Survey of Two Verifiable Delay Functions”—**Boneh, Bünz, Fisch**

OUTPUT

$$y = x^{2**T} \% N$$

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PROOF

$$p = x^{2^{**}T // r} \% N$$

r # random 128-bit prime (Fiat-Shamir)

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PROOF

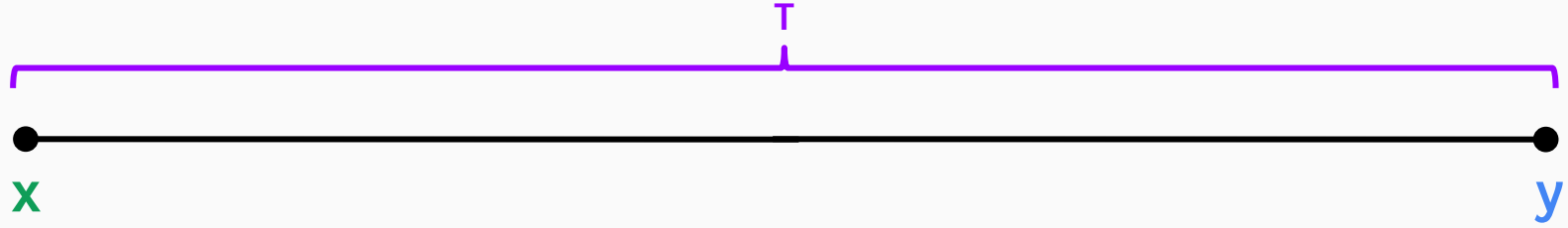
$$p = x^{2^{**}T//r} \% N$$

r # random 128-bit prime (Fiat-Shamir)

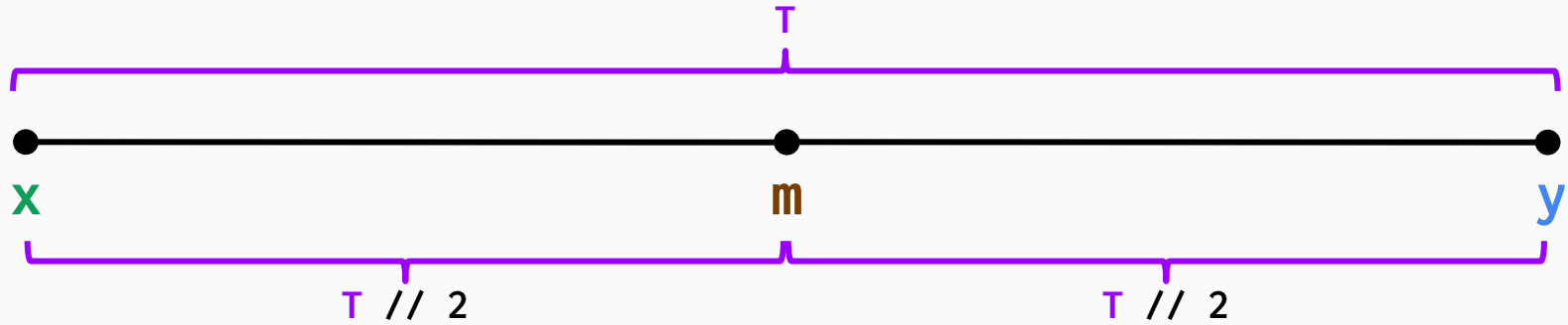
VERIFICATION

$$y == p^r * x^{2^{**}T \% r} \% N$$

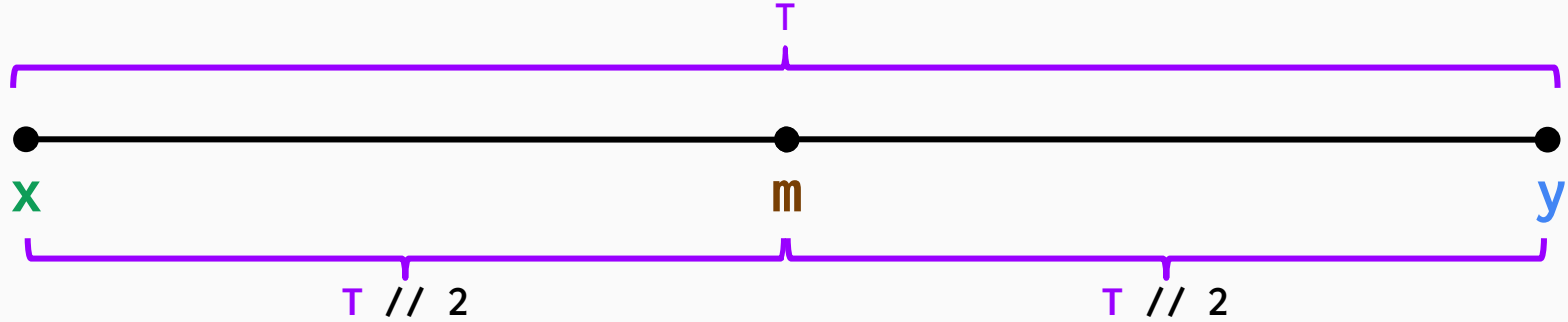
Pietrzak prover



Pietrzak prover



Pietrzak prover

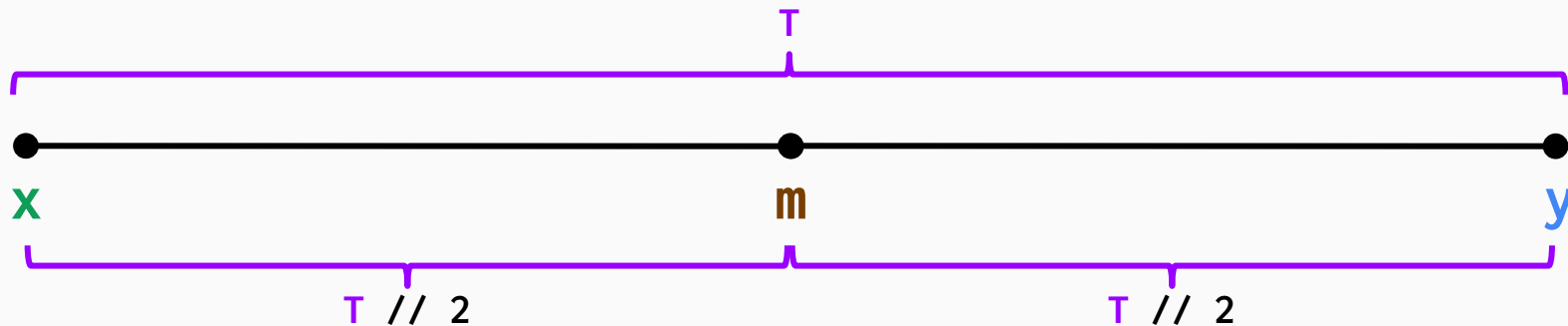


$$y == x^{2^{**}T}$$

\Leftrightarrow

$$y == m^{2^{**}(T // 2)} \text{ and } m == x^{2^{**}(T // 2)}$$

Pietrzak prover



$$y == x^{2^{**}T}$$

\Leftrightarrow

$$y == m^{2^{**}(T // 2)} \text{ and } m == x^{2^{**}(T // 2)}$$

\Leftrightarrow

$$r \text{ random and } ym^r == (mx^r)^{2^{**}(T // 2)}$$

RSA modulus

Unsatisfactory approaches

RSA challenge

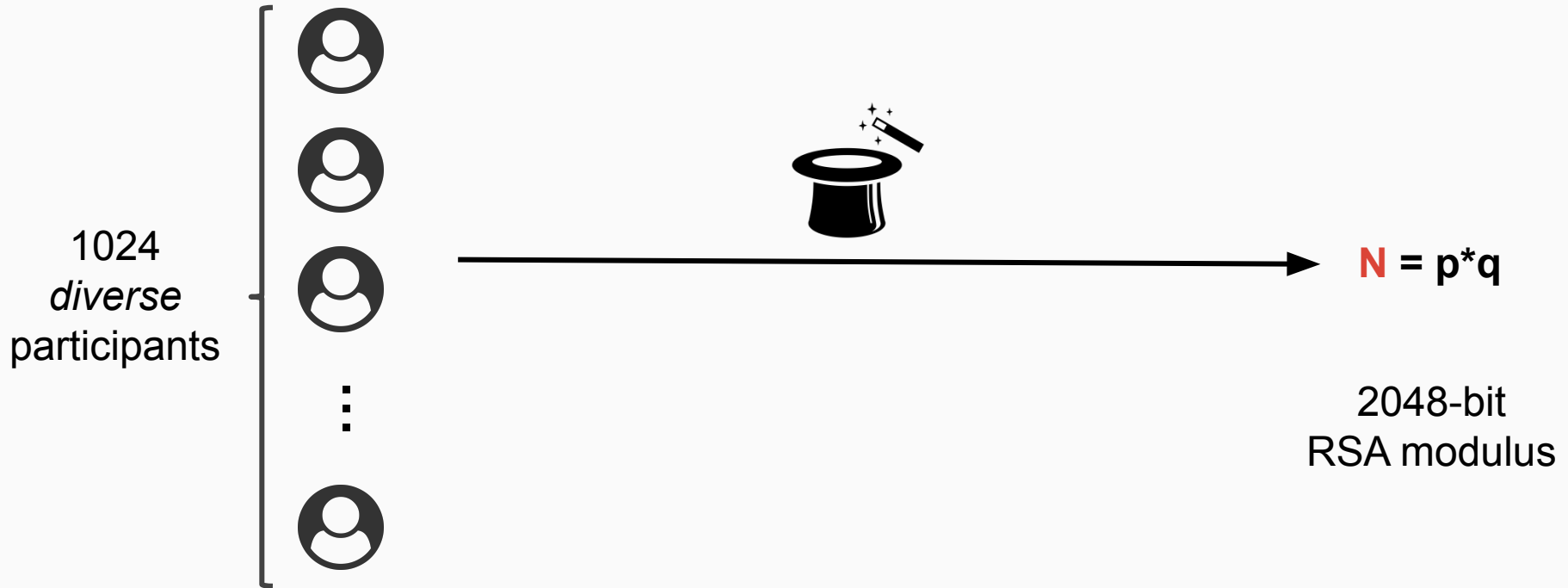
1991

Unsatisfactory approaches

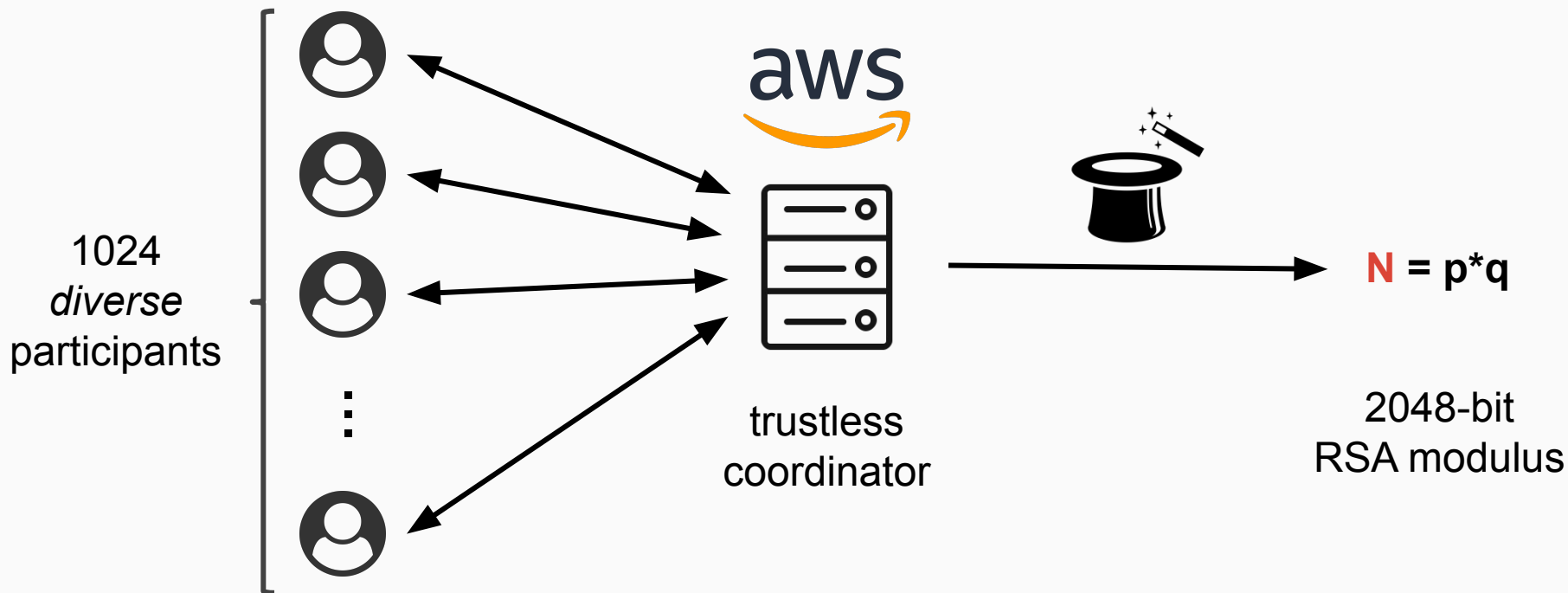
RSA challenge

RSA UFOs

RSA ceremony



RSA ceremony



RSA MPC

modulus size	2048 bits (two 1024-bit factors)
security	$(n - 1)$ -maliciously secure
participants	1024

modulus size	2048 bits (two 1024-bit factors)
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LIGERO



modulus size	2048 bits (two 1024-bit factors)
security	$(n - 1)$ -maliciously secure
participants	1024

synchronicity	synchronous
communication	<100 MB
duration	<10 minutes
rounds	<10 rounds

LIGERO



Passive adversary

- constructive sieving
- compute products (threshold AHE)
- Boneh-Franklin bi-primality test

Passive adversary

- constructive sieving
- compute products (threshold AHE)
- Boneh-Franklin bi-primality test

Active adversary

- reveal failures
- zk-prove success

Generate candidates (additively homomorphic encryption)

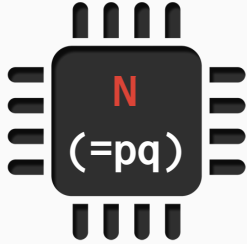
- **secret keys** sk_i
- **shared key** PK
- **encryption** $Enc_{PK}(m)$
- **decryption** $\Sigma Dec_{sk_i}(c)$
- **shares** p_i, q_i

Generate candidates (additively homomorphic encryption)

- **secret keys** sk_i
- **shared key** PK
- **encryption** $Enc_{PK}(m)$
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- **shares** p_i, q_i

encrypt	$Enc_{PK}(p_i)$
sum	$Enc_{PK}(p)$
encrypt	$Enc_{PK}(p * q_i)$
sum	$Enc_{PK}(p * q)$
decrypt	$Dec_{sk_i}(Enc_{PK}(p * q))$
sum	$p * q$

Rebirth of RSA cryptography



1ns per operation

RSA VDFs

2018

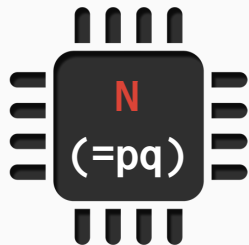
RSA accumulators

2002
2007
2018

RSA SNARKs

2019

Rebirth of RSA cryptography



1ns per operation

RSA VDFs

2018

RSA accumulators

2002
2007
2018

RSA SNARKs

2019

class groups (CPU)
10us per operation

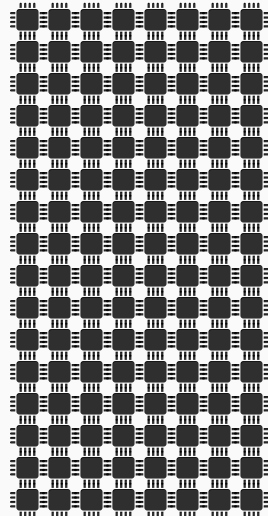


RSA SNARK "Supersonic" prover time

$d \cdot \log(d)$ exponentiations

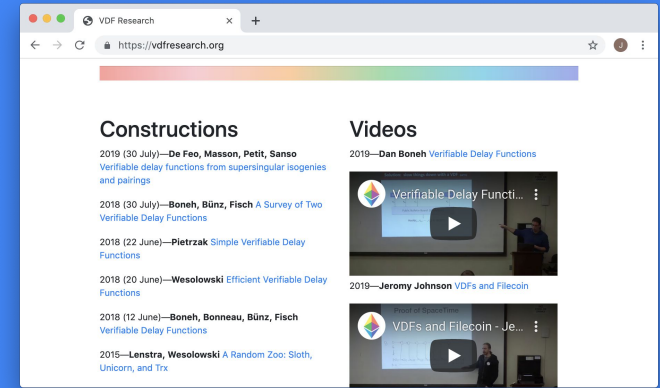
\sim

$128 \cdot d \cdot \log(d)$ multiplications

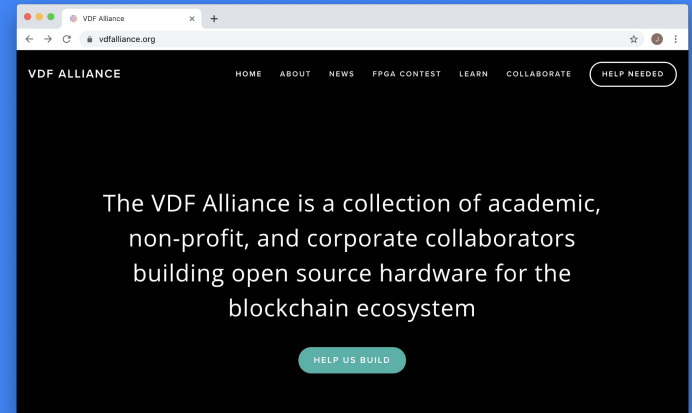


gates	prover time (128 cores)	proof size
2^{10}	10 us	10kB
2^{20}	20 ms	20kB
2^{30}	30 s	30kB

thank you :)



vdfresearch.org



vdfalliance.org