JONYA CHEN

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CURRENT ADDRESS	PERMANENT ADDRESS
1797 Shattuck Ave Apt 413	20 Reach Run
Berkeley, CA 94709	Ithaca, NY 14850

EDUCATION

UC Berkeley, College of Engineering, Berkeley, CA   Expected May 2017
Master of Engineering in Electrical Engineering and Computer Sciences

Cornell University, College of Engineering, Ithaca, NY   August 2012 - May 2016
Bachelor of Science in Electrical and Computer Engineering, minor in Computer Science
Cumulative GPA: 3.4
Relevant Coursework: Object-Oriented Programming and Data Structures • Digital Logic and Computer Organization • Circuits • Embedded Systems • Discrete Structures • Information Retrieval • Artificial Intelligence • Digital Signal Processing • Computer Architecture • Microcontrollers • Computer Networks

RELEVANT EXPERIENCE

Cisco, San Jose, CA   May 2016 – August 2016
Software Development Intern
Developed an application that would allow configuration of Cisco wireless access points through BLE (Bluetooth Low Energy) technology protocol

Intel Corporation, Hudson, MA   May 2015 – August 2015
Pre-Silicon Validation Intern
Performed coding tasks and implemented simulation tools to improve team's validation process for next generation server processor • Created test benches using SystemVerilog, Open Verification Methodology, and Perl scripting • Spearheaded the setup of a volume validation system utilized by entire team

Microsoft, Seattle, WA   May 2014 – August 2014
Software Development Intern
Created BingSky, a Windows 8.1 application that enhances a user's experience when exploring the sky in the August 2014 shipped release of Bing Maps Preview • Designed, planned, and implemented 3D real-time visual features within the map environment using DirectX graphics and C++ • Constructed an algorithm for determining celestial body positions based on user system time

Computer Systems Laboratory – Batten Research Group, Ithaca, NY   May 2013 – August 2013
Undergraduate Hardware Researcher
Quantified how much specialization can improve the performance and energy efficiency of microprocessors • Designed, tested, and evaluated my own hardware coprocessor • Utilized an FPGA board in order to implement a sorting algorithm and analyze factors such as area, cycle time, or energy

ADDITIONAL EXPERIENCES

CS 4700: Artificial Intelligence, Ithaca, NY   August 2015 – December 2015
Teaching Assistant
Held weekly office hours on artificial intelligence concepts such as heuristic search, natural-language processing, and machine learning • Evaluated students work to determine command of course material

edX MOOC: The Computing Technology Inside Your Smartphone, Ithaca, NY   February 2015 – May 2016
Teaching Assistant
Taught digital logic fundamentals for a Cornell online course with over 18,000 students from 180+ countries

Theta Tau Professional Engineering Fraternity, Ithaca, NY   September 2013 – May 2016
President
Presided over a student-run organization of 80 members to develop and execute professional development, philanthropic service, and social events for the engineering community

Cornell Engineering Ambassadors, Ithaca, NY   March 2013 – May 2016
Co-President
Conducted campus tours and introduced prospective students to life in Cornell's College of Engineering

OTHER ACTIVITIES

Cornell Society of Women Engineers [Publicity Director, Corporate Relations Liaison], Cornell IEEE (Institute of Electrical and Electronics Engineers) [Publicity Chair] • Alpha Epsilon Phi Sorority [Class Vice-President, Social Chair] • Cornell Engineering Leadership Student Advisory Board

HONORS AND AWARDS

Cornell Engineering Dean's List (2014-2016) • Microsoft Diversity Award and Scholarship (2014) • Cornell ECE Early Career Research Scholars Grant recipient (2013) • Cornell Society of Women Engineers Initiative Award (2013) • Cornell Engineering John McMullen Dean's Scholar (2012)

SKILLS

Software: Autodesk Inventor, Altera Quartus, Microsoft Visual Studio, Cadence, Vim, Git, UNIX/Linux, LaTeX
Programming: Java, Matlab, Verilog, SystemVerilog, Assembly, C/embedded, C++, Perl, HTML, CSS
Electronics: Analog & digital circuit design, breadboarding, soldering, FPGA design, signals analysis