

# JONYA CHEN

(724) 799-5969    jc957@cornell.edu

## CURRENT ADDRESS

301 Eddy Street Apt #1  
Ithaca, NY 14850

## PERMANENT ADDRESS

20 Reach Run  
Ithaca, NY 14850

## EDUCATION

**Cornell University**, College of Engineering, Ithaca, NY  
*Bachelor of Science in Electrical and Computer Engineering, minor in Computer Science*

Expected May 2016

Cumulative GPA: 3.4

Relevant Coursework: Object-Oriented Programming and Data Structures • Digital Logic and Computer Organization • Circuits • Embedded Systems • Microelectronics • Discrete Structures • Information Retrieval • Artificial Intelligence • Digital VLSI Design • Digital Signal Processing • Computer Architecture • Designing with Microcontrollers • Management in Technology • Computer Networks and Telecommunications

## RELEVANT EXPERIENCE

**Intel Corporation**, Hudson, MA

May 2015 – August 2015

*Pre-Silicon Validation Intern*

Performed coding tasks and implemented simulation tools to improve team's validation process for next generation server processor • Created test benches using SystemVerilog, Open Verification Methodology, and Perl scripting • Spearheaded the setup of a volume validation system utilized by entire team

**Microsoft**, Seattle, WA

May 2014 – August 2014

*Software Development Intern*

Created BingSky, a Windows 8.1 application that enhances a user's experience when exploring the sky in the August 2014 shipped release of Bing Maps Preview • Designed, planned, and implemented 3D real-time visual features within the map environment using DirectX graphics and C++ • Constructed an algorithm for determining celestial body positions based on user system time • Collaborated in a team of three interns and other full-time employees on the Geospatial team throughout the 12-week internship

**Computer Systems Laboratory – Batten Research Group**, Ithaca, NY

May 2013 – August 2013

*Undergraduate Hardware Researcher*

Quantified how much specialization can improve the performance and energy efficiency of microprocessors • Designed, tested, and evaluated my own hardware coprocessor • Utilized an FPGA board in order to implement a sorting algorithm and analyze factors such as area, cycle time, or energy

## ADDITIONAL EXPERIENCES

**CS 4700: Artificial Intelligence**, Ithaca, NY

August 2015 – Present

*Teaching Assistant*

Held weekly office hours on artificial intelligence concepts such as heuristic search, natural-language processing, and machine learning • Evaluated students work to determine command of course material

**edX MOOC: The Computing Technology Inside Your Smartphone**, Ithaca, NY

February 2015 – Present

*Teaching Assistant*

Served as a TA for a Cornell online course with over 18,000 students from 180+ countries • Taught fundamentals of computing technology, including digital logic, computer organization, application software, and advanced performance techniques

**Cornell Engineering Ambassadors**, Ithaca, NY

March 2013 – Present

*Co-President*

Conducted campus tours and introduced prospective students to life in Cornell's College of Engineering

**Theta Tau Professional Engineering Fraternity**, Ithaca, NY

September 2013 – Present

*President*

Presided over a student-run organization of 80 members to develop and execute professional development, philanthropic service, and social events for the engineering community

## OTHER ACTIVITIES

Cornell Society of Women Engineers [Publicity Director, Corporate Relations Liaison], Cornell IEEE (Institute of Electrical and Electronics Engineers) [Publicity Chair] • Alpha Epsilon Phi Sorority [Class Vice-President, Social Chair] • Cornell Engineering Leadership Student Advisory Board

## HONORS AND AWARDS

Cornell Engineering Dean's List (2014-2015) • Microsoft Diversity Award and Scholarship (2014) • Cornell ECE Early Career Research Scholars Grant recipient (2013) • Cornell Society of Women Engineers Initiative Award (2013) • Cornell Engineering John McMullen Dean's Scholar (2012)

## SKILLS

**Software:** Autodesk Inventor [3D modeling], Altera Quartus [FPGA designing], Eclipse [Java IDE], Microsoft Visual Studio, Cadence [circuit design], Vim, Git, UNIX/Linux, LaTeX

**Programming Languages:** Java, Matlab, Verilog, SystemVerilog, Assembly, C/embedded, C++, Perl

**Electronics:** Analog & digital circuit design, breadboarding, soldering, FPGA design, signals analysis