**Utilization and Timing**

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  | SLices | Flip-Flops | LUTs | IOs | Max freq | %logic | %routing |
| Vendor | 374/49152 | 400/98304 | 634/98304 | 101/768 | 183.513 | 50.6 | 49.4 |
| ADDCORE | 550/49152 | 665/98304 | 694/98304 | 101/768 | 183.513MHz | 50.6% | 49.4% |

Both designs share the same critical path (and thus the same max freq, %logic and %critical) of the output of component NORM1 to the output of component NORM2