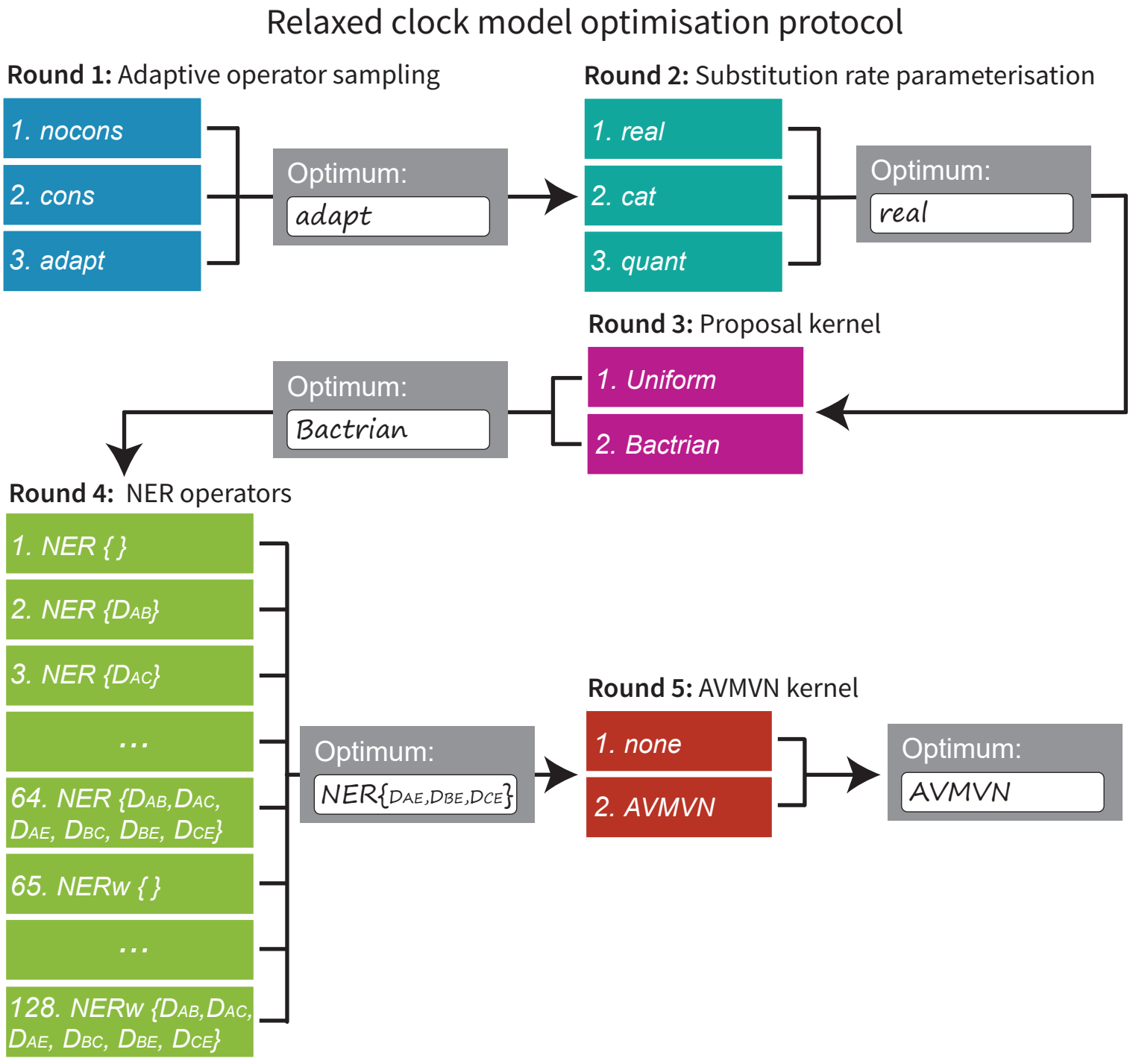
Relaxed clock model optimisation protocol
Round 1: Adaptive operator sampling
1. nocons
2. cons
3. adapt
Optimum:
adapt
Round 2: Substitution rate parameterisation
1. real
2. cat
3. quant
Optimum:
real
Round 3: Proposal kernel
1. Uniform
2. Bactrian
Optimum:
Bactrian
Round 4: NER operators
1. NER { }
2. NER {DAB}
3. NER {DAC}
...
64. NER {DAB,DAC, DAE, DBC, DBE, DCE}
65. NERw { }
...
128. NERw {DAB,DAC, DAE, DBC, DBE, DCE}
Optimum:
NER{DAE,DBE,DCE}
Round 5: AVMVN kernel
1. none
2. AVMVN
Optimum:
AVMVN