

Execution Ordered Instructions														
Line #	Instuction	Destination	Source 1	Source 2/ Immediate Value	Hex Code	Psudocode	MIF#	Hex MIF	Label	Goto	Dr. Fourney's Comments	My Comments	PC	PC HE X
1	LDU	R1	R0	#14	004003A3	B LDU R1, R0, #14	0	0			Effects the address of sub1		1	1
2	LDU	R19	R0	#31	04C007E3	B LDU R19, R0, #31	1	1			Rom is 0-63		2	2
3	ROL	R26	R19	R0	98340680	A ROL R26, R19, R0	2	2			C<=0 ; R26<=62 //DECIMAL	WE ROTATE THE CARRY IN MSB of ABCD is 1, Sign extend to get FFFF	3	3
4	LD	R20	R0	#43981	052AF362	B LD R20, R0, #43981	3	3			ImmediateBlock_Out = FFFFABCD		4	4
5	ROL	R21	R20	R0	A02A0680	A ROL R21, R20, R0	4	4			Sets Carry Flag		5	5
6	ROR	R22	R26	R0	D02C0640	A ROR R22, R26, R0	5	5					6	6
7	JSR	R0	R1	R0	08001040	A JSR R0, R1, R0	6	6		sub1			7	7
15	ADD	R25	R19	#29	9E400741	B ADD R25, R19, #29	14	E	sub1		R[25] is 60		15	F
16	SUB	R0	R25	#60	C8000F04	B SUB R0, R25, #60	15	F			R[0] is 0	60-60=0...	16	10
17	STA	R20	R0	#118	05001D90	B STA R20, R0, #118	16	10			RAM \$64-\$127	You meant 64 to 127 DECIMAL	17	11
18	JMP	R0	R19	R0	98001000	A JMP R0, R19, R0	17	11		always	R[19] is 31		18	12
32	AND	R6	R20	#63	A1800FC8	B AND R6, R20, #63	31	1F	smwh				32	20
33	OR	R7	R6	#28	31C00709	B OR R7, R6, #28	32	20					33	21
34	STIX	R20	R7	#50	3D000C91	B STIX R20, R7, #50	33	21					34	22
35	LDIX	R8	R0	#118	02001DA1	B LDIX R8, R0, #118	34	22					35	23
36	LDA	R14	R0	#79	038013E0	B LDA R14, R0, #79	35	23					36	24
37	RTS	R0	R30	R0	F00010C0	A RTS R0, R30, R0	36	24	end				37	25
8	NOP	R0	R0	R0	0000003F	A NOP R0, R0, R0	7	7					8	8
9	BSR			#9	000002B1	C BSR #10	8	8					9	9
19	LDIX	R28	R25	#3	CF0000E1	B LDIX R28, R25, #3	18	12	sub2		From 63 (M63 contains 0)		19	13
20	COMP	R29	R28	R0	E03A0300	A COMP R29, R28, R0	19	13			1's comp		20	14
21	ADD	R27	R29	R28	EF360040	A ADD R27, R29, R28	20	14			-1		21	15
22	BNE	R0	R28	#8	E000020A	B BNE R0, R28, #8	21	15		always		We may have miss interpreted the intent of this part of the Psuedocode	22	16
30	MOVE	R28	R0	R0	00380800	A MOVE R28, R0, R0	29	1D	always				30	1E
31	BRA			-8	FFFFFE30	C BRA #-8	30	1E		again			31	1F
23	BEQ	R27	R28	#3	E6C000CC	B BEQ R27, R28, #3	22	16	again	same			23	17
24	BLT	R28	R27	#5	DF00014F	B BLT R28, R27, #5	23	17		smlr	!(R[27]<R[28])	R27=-1 : R28=0 NOT EQUAL DON'T BRANCH R27=-1 < R28=0 (0>-1) NOT less than DON'T BRANCH	24	18
25	BNE	R27	R28	#2	E6C0008A	B BNE R27, R28, #2	24	18		diff			25	19
27	RTS	R0	R30	R0	F00010C0	A RTS R0, R30, R0	26	1A	same				27	1B
10	LDIX	R11	R0	#61	02C00F61	B LDIX R11, R0, #61	9	9				MEM[61] HAS NOT BEEN LOADED... So R[11] gets Zero...	10	A
11	OR	R12	R11	#42400	5B296809	B OR R12, R11, #42400	10	A					11	B
12	AND	R13	R11	#42400	5B696808	B AND R13, R11, #42400	11	B					12	C
13	XOR	R9	R20	#4660	A2448D0B	B XOR R9, R20, #4660	12	C					13	D
14	BRA			# -1	FFFFFFF0	C BRA #-1	13	D	ELOOP	ELOOP		Infinite Loop	14	E
13	XOR	R9	R20	#4660	A2448D0B	B XOR R9, R20, #4660	12	C					13	D