



# JUAN

## ORDÓÑEZ CEREZO



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España



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## ABOUT ME

Interested in new technologies, TICs and IoT. Lover for electronic and embedded systems. Curious, creative, resilient and timely but most especially fun and friendly.

## PERSONAL SKILLS

Microsoft Power Point

Microsoft Project

Microsoft Excell

Microsoft Word

## EDUCATION AND CERTIFICATES

2013-2018 Graduated in Telecommunications Technologies(Electronic Speciality)  
University of Granada, Granada, Spain

## WORK EXPERIENCE

2017 INDRA S.A. Hardware and Software Engineer 1 Málaga  
Project for O2 (London) and Telefónica.

2018 Infineon Technologies (Working Student) Munich, Germany  
PCB Designer, Hardware engineer

## TRAINING

2016 University Superior Course in UAVs programming Madrid  
Rey Juan Carlos University

2016 University Superior Course in Python programming. Granada  
University of Granada

2017 SolidWorks Modelation and 3D Simulation. Specially in electronics components. Granada  
University of Granada, 300 hours

2016 Business Plan Talent UGR emprendedora  
120 hours

2018 Advanced PCB Designer  
Fedével Academy

2018 Learn the Essentials of VHDL and FPGA Development  
Fedével Academy 180 hours

## TECHNICAL SKILLS

- Experience with embedded system and hardware design work.
- Considerable experience in construction of quadcopters FPV and autonomous UAVs with GPS as well as images recognition.
- Considerable experience in Matlab and Simulink.
- PSpice like tool to simulate electronics circuits.
- Outstanding facility with VHDL, Verilog, C, Python.
- Abilities in analog and digital circuit design.
- Knowledge in the use of tools like KNX, PLC, SCADA
- Profound leadership and communication skills.

## LANGUAGES

Spanish Mother tongue

English Acquired tongue  
Understanding B2, Speaking B2, Writing B2

## THESIS AND PUBLICATIONS

Control System in open FPGAs for autonomous robots  
University of Granada, Rey Juan Carlos University