JUAN
ORDÓÑEZ CEREZO

23-June-1995

Spain

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ABOUT ME

Interested in new technologies, TICs and IoT. Lover for electronic and embedded systems. Curious, creative, resilient and timely but most especially fun and friendly.

PERSONAL SKILLS

- Timeliness
- Sports
- Problems Resolution
- Team Work

Link to my GitHub page with the current CV such as some projects (PCB, Verilog Projects).

EDUCATION AND CERTIFICATES

2013-2018 Graduated in Telecommunications Technologies (ELECTRONIC SPECIALITY)
University of Granada, Granada, Spain

Master's in ELECTRONIC SYSTEMS FOR INTELLIGENT ENVIRONMENTS
University of Málaga, Málaga, Spain

Current Master in Business Administration
EAE Business School, Madrid, Spain

WORK EXPERIENCE

2017 INDRA S.A. Hardware ans Software Engineer 1 — Málaga
Project for O2 (London) and Telefónica.

2018 CITIC (Atarfil S.A) — CITIC Granada
Electronic adaptation UAVs for ground exploration.

2018 Infineon Technologies (Working Student) — Munich, Germany
PCB Designer, Hardware engineer.

2019 Indra Sistemas (Spanish Army) — Madrid, Spain
RF & Hardware designer, Hardware engineer.

Current Sainsel Sistemas Navales (Aerospace Program) — Madrid, Spain
Hardware Design engineer.

TRAINING

2016 University Superior Course in UAVs programming. — Madrid
Rey Juan Carlos University

2016 University Superior Course in Python programming. — Granada
University of Granada

2017 SolidWorks Modelation and 3D Simulation. Speciality in electronics components. — Granada
University of Granada, 300 hours

2016 Business Plan Talent. — UGR emprendedora.
120 hours

2018 Advanced PCB Designer with Altium.
Fedevel Academy

2018 Learn the Essentials of VHDL and FPGA Development.
Fedevel Academy 180 hours

TECHNICAL SKILLS

- Experience with embedded system and hardware design work, Altium.
- Considerable experience in construction of quadcopters FPV and autonomous UAVs with GPS as well as images recognition.
- Considerable experience in Mattlab and Simulink.
- Outstanding facility with VHDL, Verilog, C, Python and PSpice.

LANGUAGES

Spanish Mother tongue

English Acquired tongue
Understanding B2, Speaking B2, Writing B2.

THESIS AND PUBLICATIONS

Control System in open FPGAs for autonomous robots (MDPI Electronics) Distinction

University of Granada, Rey Juan Carlos University