JUAN
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ABOUT ME

Interested in new technologies, TICs and IoT. Lover for electronic and embedded systems. Curious, creative, resilient and timely but most especially fun and friendly.

PERSONAL SKILLS

Timeliness

Sports

Problems Resolution

Team Work

Link to my GitHub page with the current CV such as some projects (PCB, Verilog Projects).

EDUCATION AND CERTIFICATES

2013-2018	Graduated in Telecommunications Technologies (ELECTRONIC SPECIALITY) University of Granada, Granada, Spain

WORK EXPERIENCE

2017	INDRA S.A. Hardware ans Software Engineer 1 Project for O2 (London) and Telefónica.	Málaga
2018	CITIC (Atarfil S.A) Electronic adaptation UAVs for ground exploration.	CITIC Granada
2018	Infineon Technologies (Working Student) PCB Designer, Hardware engineer.	Munich, Germany

TRAINING

2016	University Superior Course in UAVs programming Rey Juan Carlos University	Madrid
2016	University Superior Course in Python programming. University of Granada	Granada
2017	SolidWorks Modelation and 3D Simulation. Speciality in electronics components. University of Granada, 300 hours	Granada
2016	Business Plan Talent 120 hours	UGR emprendedora
2018	Advanced PCB Designer Fedevel Academy	
2018	Learn the Essentials of VHDL and FPGA Development Fedevel Academy 180 hours	

TECHNICAL SKILLS

- Experience with embedded system and hardware design work.
- Considerable experience in construction of quadcopters FPV and autonomous UAVs with GPS as well as images recognition.
- Considerable experience in Mattlab and Simulink.
- PSpice like tool to simulate electronics circuits.
- Outstanding facility with VHDL, Verilog, C, Python.
- Abilities in analog and digital circuit design.
- Knowledge in the use of tools like KNX, PLC, SCADA.

LANGUAJES

Spanish	Mother tongue
English	Acquired tongue Understanding B2, Speaking B2, Writing B2.

THESIS AND PUBLICATIONS

	Control System in open FPGAs for autonomous robots University of Granada, Rey Juan Carlos University	Distinction