Program: **>>>+>[-]>.**

RAM:

0	4	125	42	3	18	0	77

D: 0

DP: 0

LS: 0

V	A	S	Z
0	0	0	1

Output:

Program: **>>>+>[-]>.**

RAM:

0	4	125	42	3	18	0	77

D: 0

DP: 1

LS: 0

V	A	S	Z
0	1	0	1

Output:

Program: >>>+>[-]>.

RAM:

0	4	125	42	3	18	0	77

D: 0

DP: 2

LS: 0

V	A	S	Z
0	1	0	1

Output:

Program: >>>+>[-]>.

RAM:

| 0 | 4 | 125 | 42 | 3 | 18 | 0 | 77 |

D: 0

DP: 3

LS: 0

V	A	S	Z
0	1	0	1

Output:

Program: **>>>+>[-]>.**

RAM:

0	4	125	42	3	18	0	77

D: 42

DP: 3

LS: 0

V	A	S	Z
0	0	0	0

Output:

Program: >>>+>[-]>.

RAM:

0	4	125	42	3	18	0	77

+1

D 43

DP 3

V	A	S	Z
1	0	0	0

LS 0
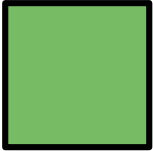
Output:

Program: **>>>+>[-]>.**

RAM: 0 4 125 43 3 18 0 77

D: 43

DP: 3

LS: 0

V	A	S	Z
0	0	0	0

Output:

Program: **>>>+>[-]>.**

RAM:

0	4	125	43	3	18	0	77

D: 43

DP: 4

LS: 0

V	A	S	Z
0	1	0	0

Output:

Program: **>>>+>[-]>.**

RAM:

0	4	125	43	3	18	0	77

D: 3

DP: 4

LS: 0

V	A	S	Z
0	0	0	0

Not 0!

Output:

Program: **>>>+>[-]>.**

RAM:

0	4	125	43	3	18	0	77

D: 2

-1

DP: 4

LS: 0

V	A	S	Z
1	0	0	0

Not 0!

Output:

Program: **>>>+>[-]>.**

RAM:

0	4	125	43	3	18	0	77

D: 1 *(-1)*

DP: 4

LS: 0

V	A	S	Z
1	0	0	0

Not 0!

Output:

Program: **>>>+>[-]>.**

RAM:

0	4	125	43	3	18	0	77

D: 0 (-1)

DP: 4

LS: 0

V	A	S	Z
1	0	0	1

Z: 0!

Output:

Program: **>>>+>[-]>.**

RAM:
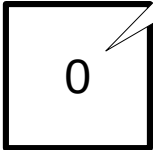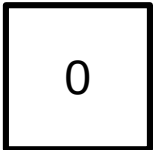
0	4	125	43	0	18	0	77

D: 0

DP: 4

LS: 0

V	A	S	Z
0	0	0	1

Output:

Program: **>>>+>[-]>.**

RAM:

0	4	125	43	0	18	0	77

D: 0

DP: 5

LS: 0

V	A	S	Z
0	1	0	1

Output:

Program: **>>>+>[-]>.**

RAM:

0	4	125	43	0	18	0	77

D: 0

DP: 5

LS: 0

V	A	S	Z
0	1	0	1

Output: 18