

Power Electronics Project - Measurements

Input Regulation ($Z_{out} = 200\Omega$)

Minimum input voltage is actually 7.8V, circuit was not able to make it to 5V even.

figures @ min and max voltage: CH1 - SW1 CH2 - SW2

$V_{in}(V)$	$V_{out}(V)$
7.8	12.076
10.0	12.077
12.0	12.069
14.0	12.064
16.0	12.063
18.0	12.062
20.0	12.064
22.0	12.857
24.0	12.789

Load regulation & Efficiency

$Z_{load}(\Omega)$	$V_{out}(V)$	$I_{in}(A)$
200	12.062	0.13
100	12.077	0.17
50	12.078	0.25
24	12.063	0.41
12	12.064	0.75