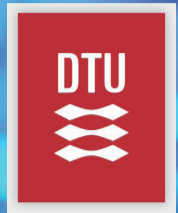


DTU Chip Day



16 April 2024

12:30 – 17:00 DTU – ROOM 101-M1

Are you curious about cutting edge technology, analog and digital chip design and verification?

Join this day and hear directly from the Danish companies on their stories, their challenges, their job offers and their future!

12:30: Sandwiches & drinks

13:00 Introduction

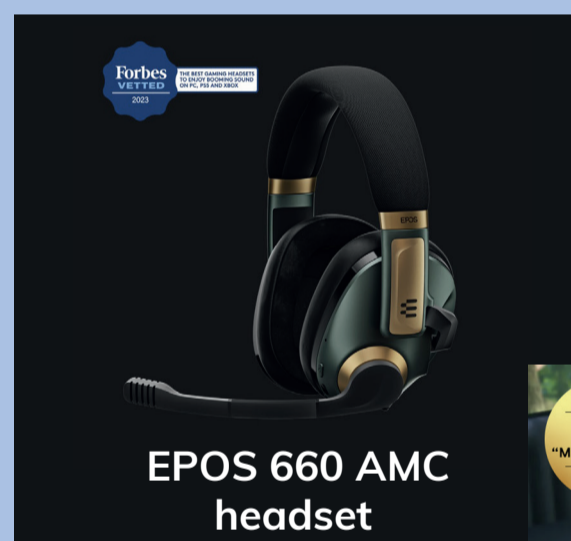
- Industry keynote
- Chip design disciplines
- Chip design education at DTU
- A low power continuous time linear equalizer for 224 Gb/s PAM4 signals

14:00 Technical presentations

- Open FPGA development flow with CI/CD
- Digital signal processing (DSP) on custom IC
- Magnetic / inductance (MI) link between hearing aids
- Power integrated circuit design
- Open-source based verification of digital IC

16:00 Wrap up

- Quiz & prizes
- Drinks & networking



Win a headset!



Register here



Date of removal: 17 April 2024

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