

VIA C7 thermal sensor...
VIA Nano thermal sensor...

No
No

Some Super I/O chips contain embedded sensors. We have to write to standard I/O ports to probe them. This is usually safe.

Do you want to scan for Super I/O sensors? (YES/no): y

Probing for Super-I/O at 0x2e/0x2f

Trying family `National Semiconductor/ITE'...

No

Trying family `SMSC'...

No

Trying family `VIA/Winbond/Nuvoton/Fintek'...

Yes

Found `Nuvoton NCT6779D Super IO Sensors'

Success!

(address 0x290, driver `to-be-written')

Probing for Super-I/O at 0x4e/0x4f

Trying family `National Semiconductor/ITE'...

No

Trying family `SMSC'...

No

Trying family `VIA/Winbond/Nuvoton/Fintek'...

No

Trying family `ITE'...

No

Some systems (mainly servers) implement IPMI, a set of common interfaces through which system health data may be retrieved, amongst other things.

We first try to get the information from SMBIOS. If we don't find it there, we have to read from arbitrary I/O ports to probe for such

interfaces. This is normally safe. Do you want to scan for IPMI

interfaces? (YES/no): ☐