FPGA configuration

1 DE2-115 Altera FPGA

2 Connecting FPGA

3 System device monitor

The FPGA board is found and listed as a new device named USB-Blaster

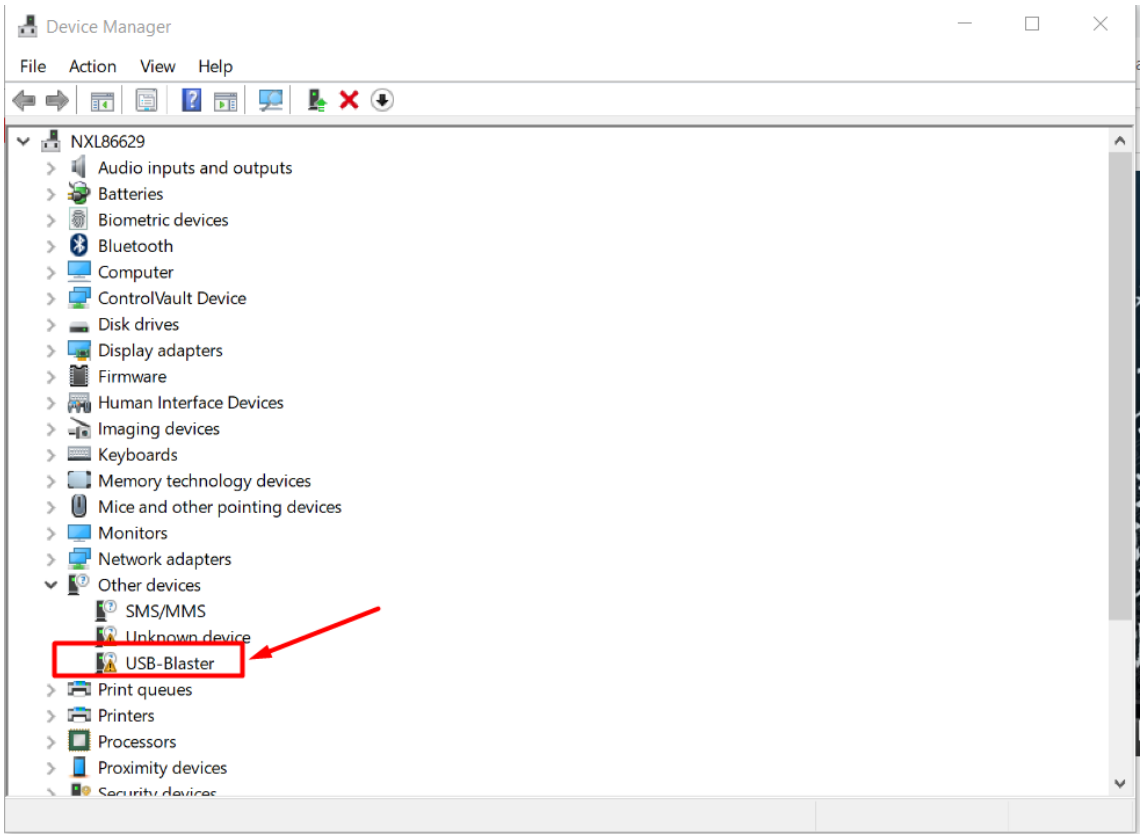


Figure 1 USB-Blaster device

4 Installing drivers for FPGA

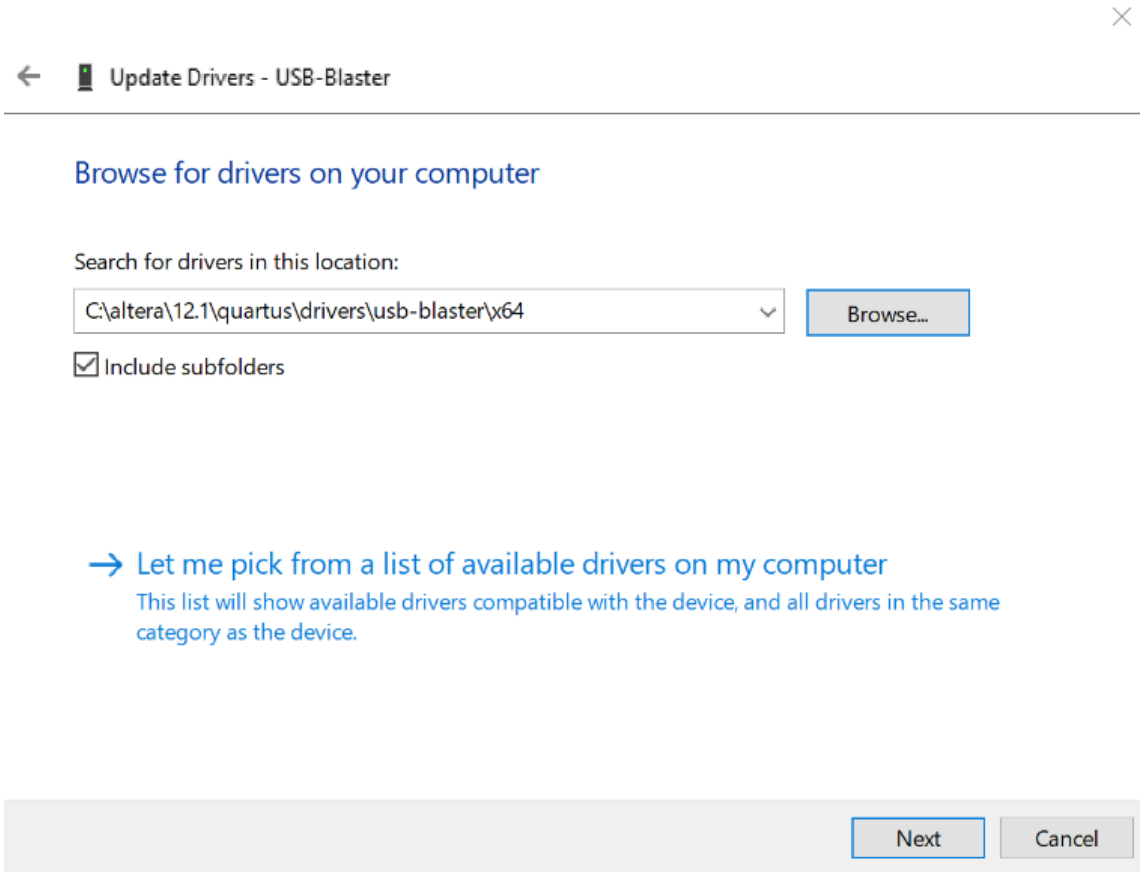


Figure 2 Locating drivers (select usb-blaster folder)

5 Already installed driver

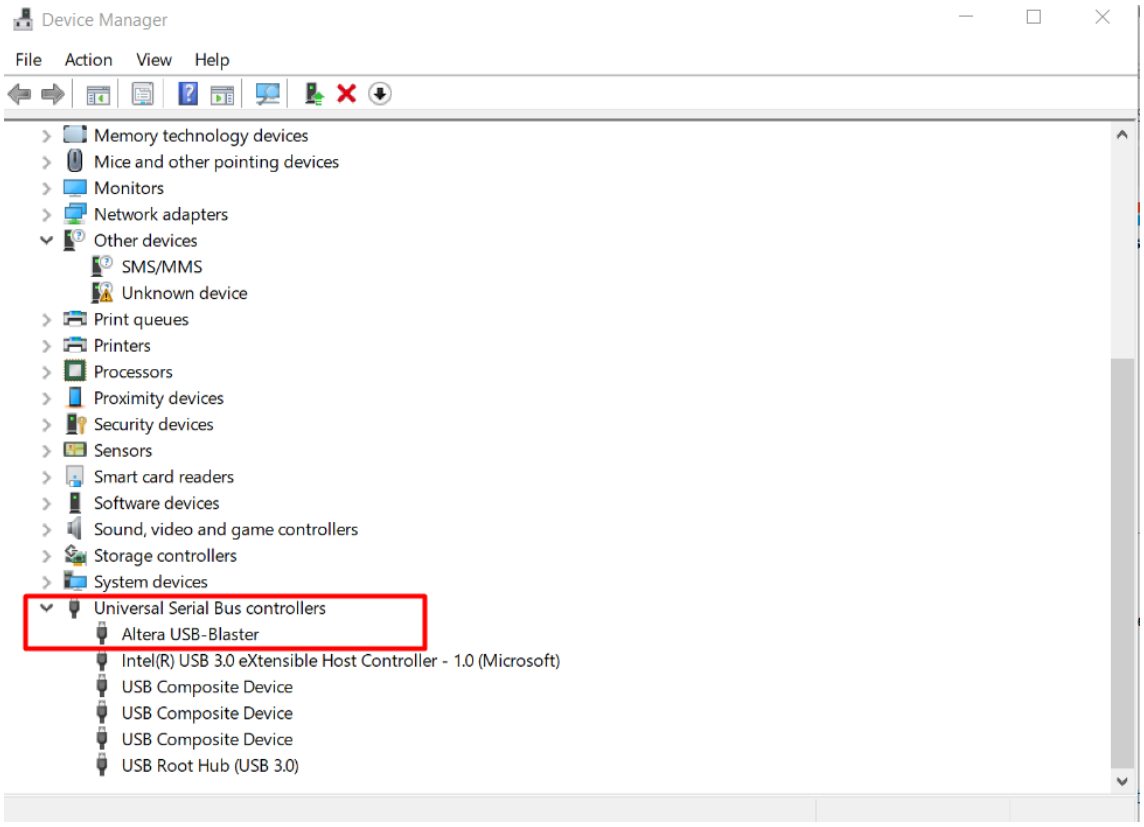
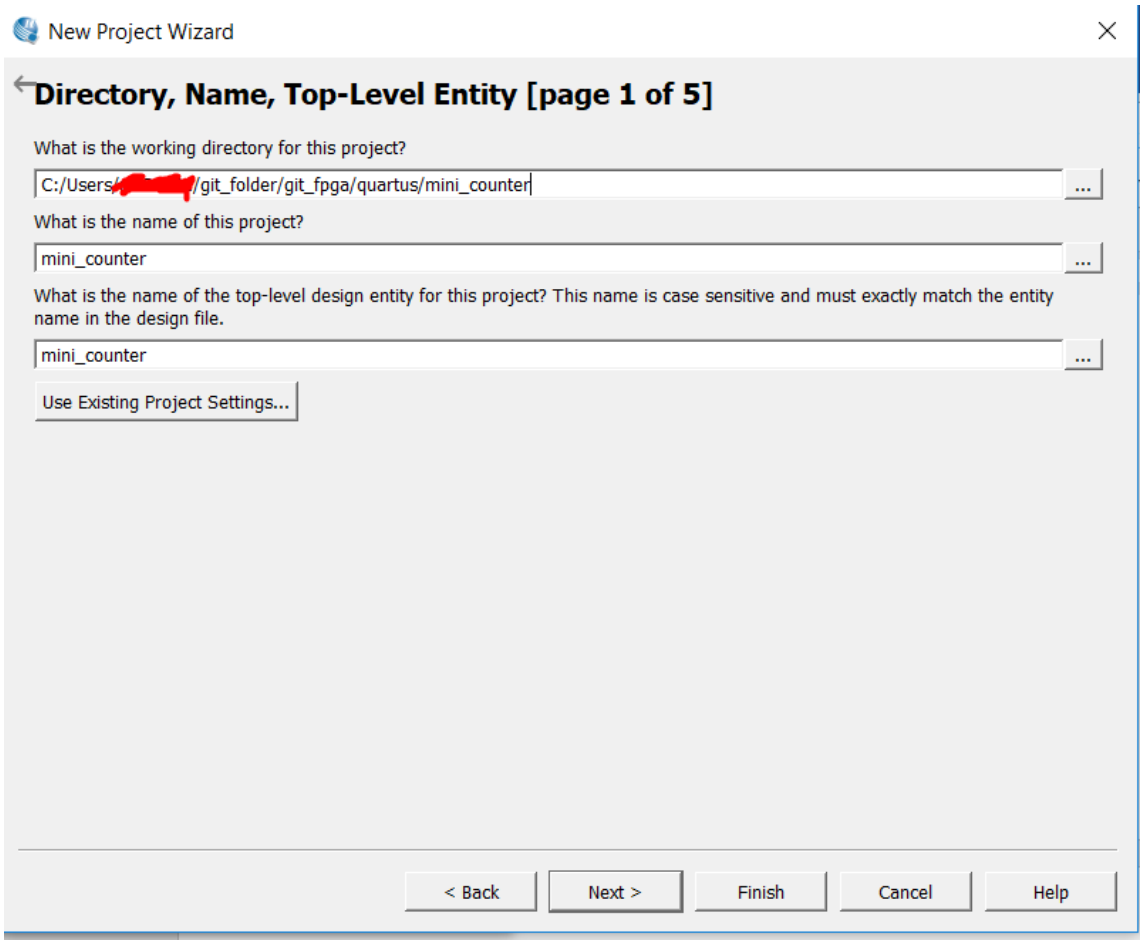


Figure 3 Correct detection

6 Opening Quartus

6.1 Define project and set location



6.2 New project device settings

Skip project wizard step 2 and select device in step 3

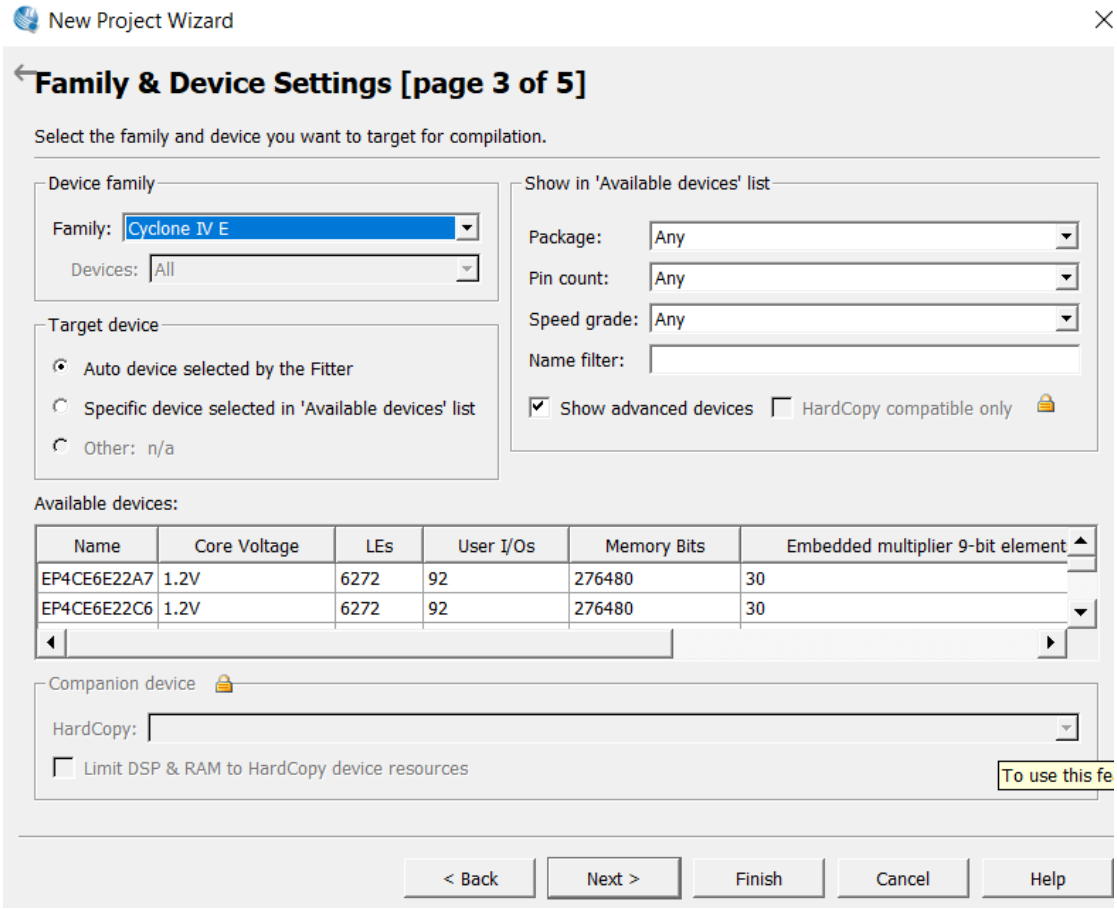


Figure 4 Select Cyclone IV

FPGA board cyclone IV chip can be looked to find correct device name:

EP4CE115F29C7N

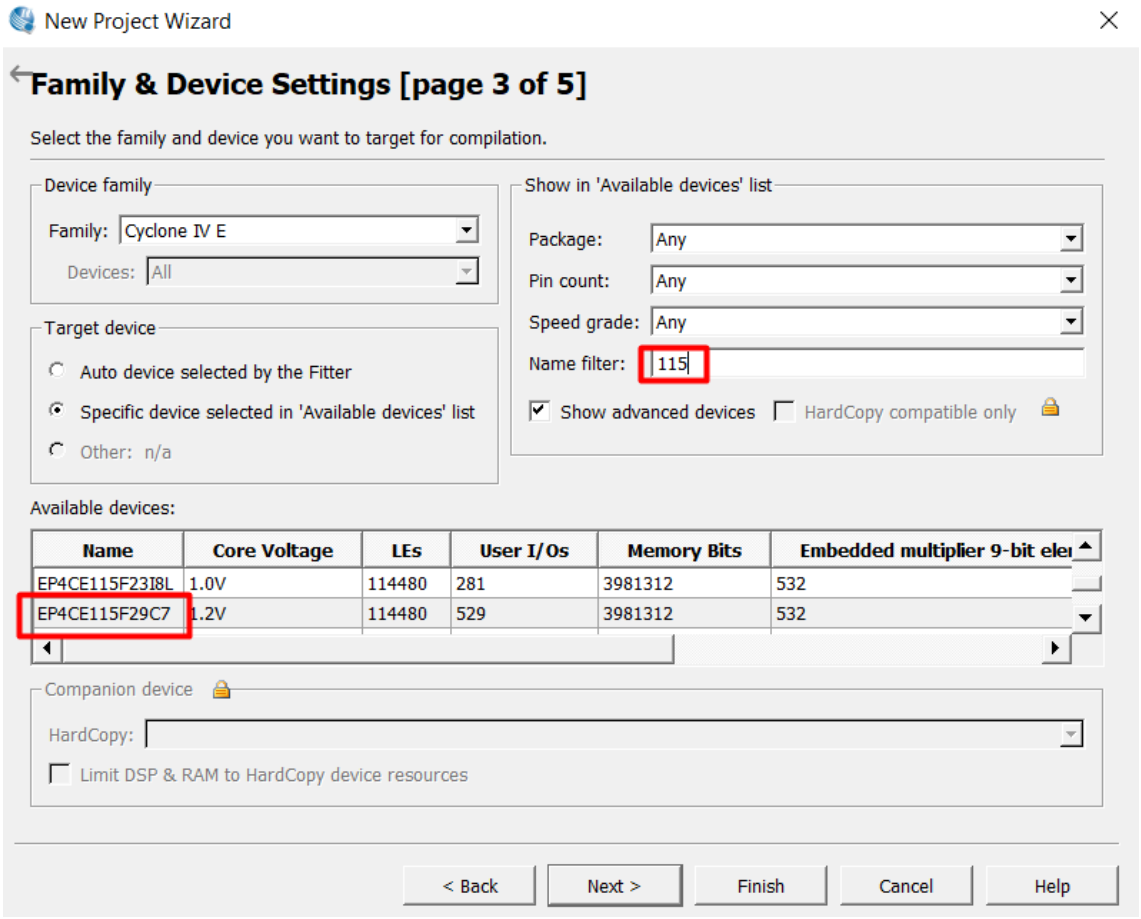
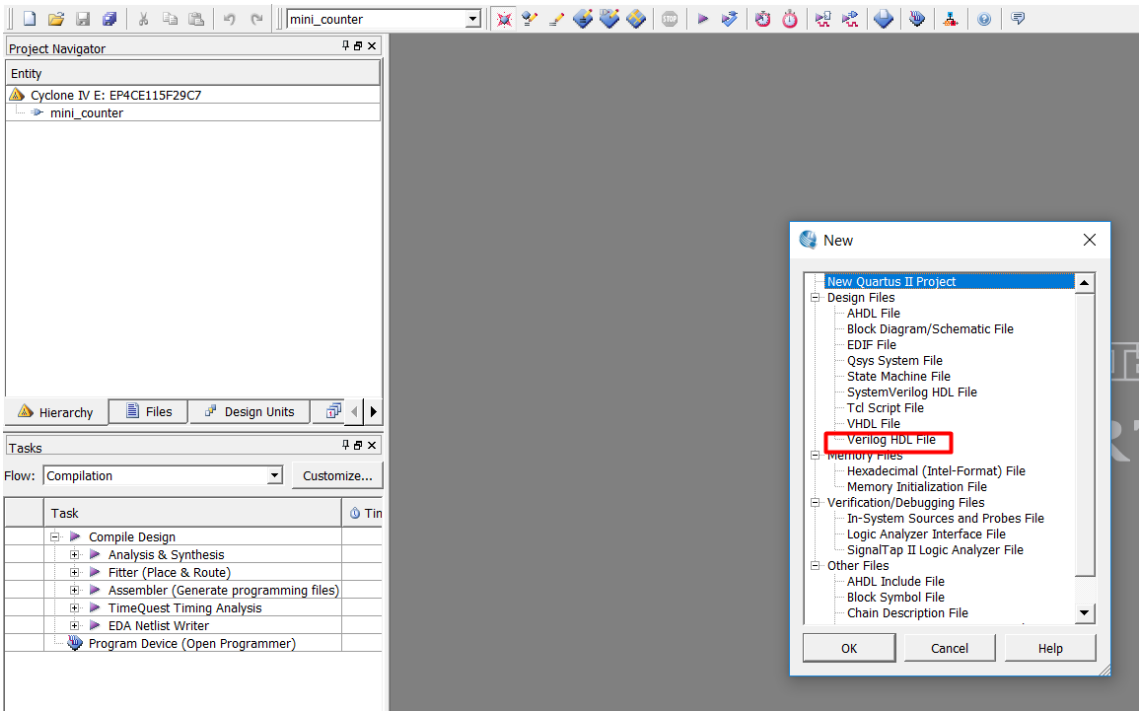
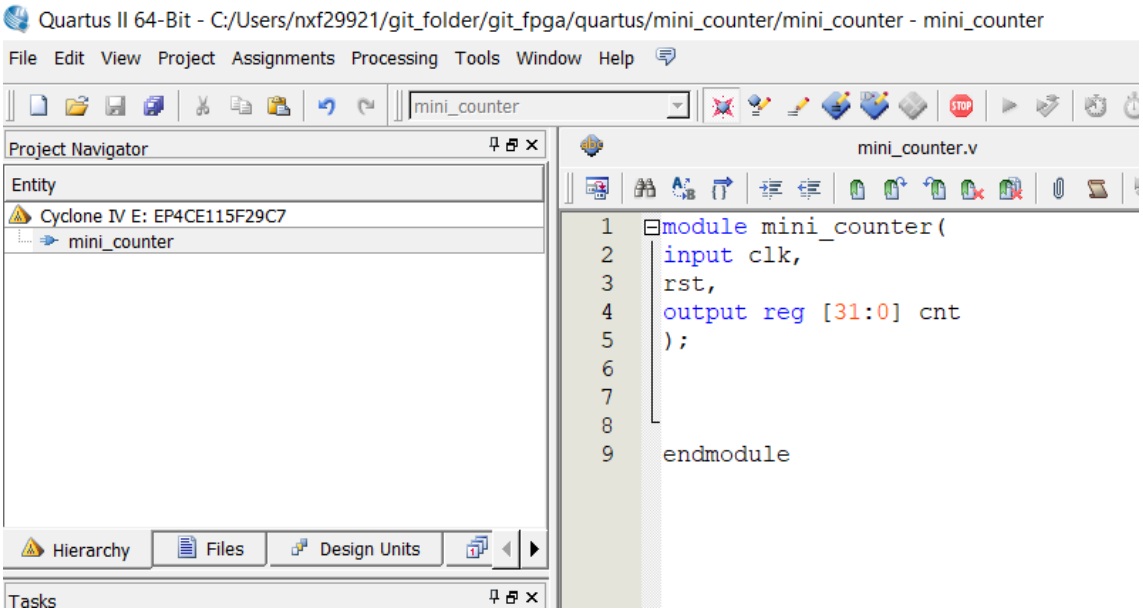


Figure 5 Filter 115 to add EP4CE115F29C7N

6.3 Add a new Verilog file



6.4 Adding Verilog module



7 Reference

7.1 Web documents

7.1.1 Terasic setup https://www.youtube.com/watch?v=5R5Tw_zSKZM

7.1.2 Terasic driver guide http://www.terasic.com.tw/wiki/Altera_USB_Blaster_Driver_Installation_Instructions

7.1.3 Git https://git-scm.com/download/win

7.1.4 Notepad++ https://notepad-plus-plus.org/download/v7.6.html

7.1.5 Git setup https://git-scm.com/book/en/v2/Getting-Started-First-Time-Git-Setup