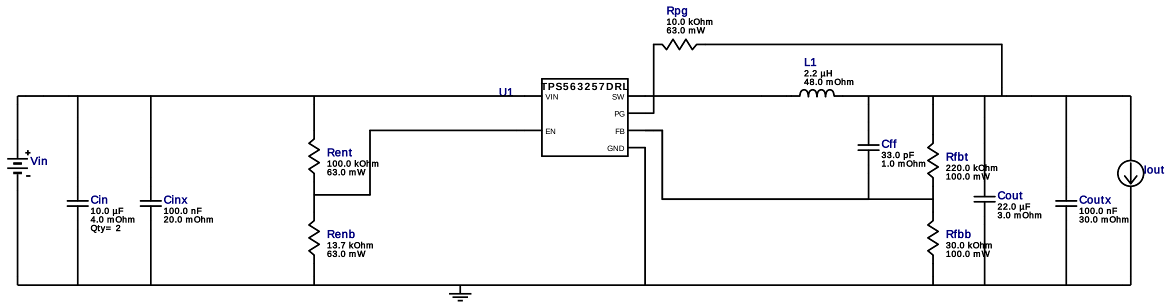
VinMin = 10.0V
VinMax = 16.0V
Vout = 5.0V
Iout = 2.0A

Device = TPS563257DRLR
Topology = Buck
Created = 2023-12-06 00:40:19.583
BOM Cost = $0.62
BOM Count = 13
Total Pd = 1.67W

WEBENCH ® Design Report

Design : 15 TPS563257DRLR
TPS563257DRLR 10V-16V to 5.00V @ 2A

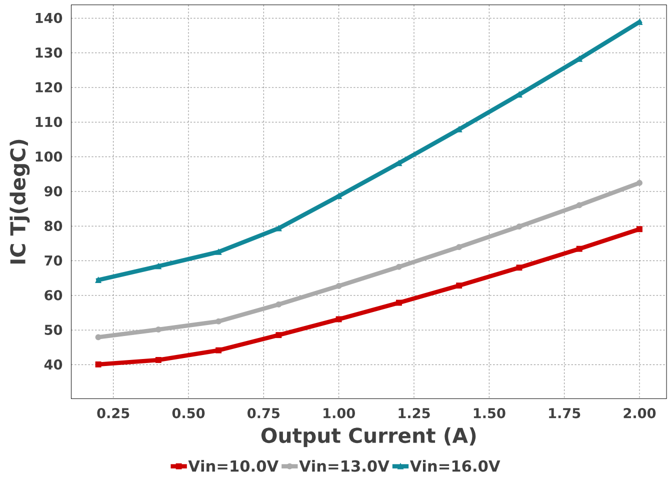


Electrical BOM

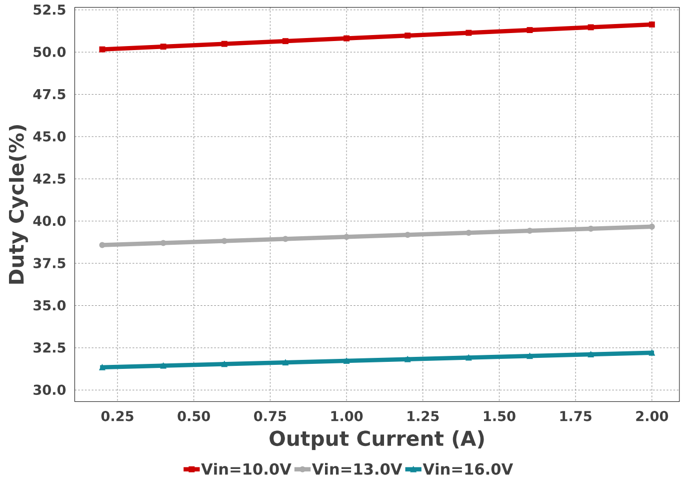
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cff	MuRata	GRM1555C1E330JA01D Series= C0G/NP0	Cap= 33.0 pF ESR= 1.0 mOhm VDC= 25.0 V IRMS= 0.0 A	1	$0.01	0402 3 mm^2
Cin	MuRata	GRM21BR61E106MA73L Series= X5R	Cap= 10.0 uF ESR= 4.0 mOhm VDC= 25.0 V IRMS= 2.8 A	2	$0.04	0805 7 mm^2
Cinx	MuRata	GRM188R71H104KA93D Series= X7R	Cap= 100.0 nF ESR= 20.0 mOhm VDC= 50.0 V IRMS= 3.8 A	1	$0.02	0603 5 mm^2
Cout	MuRata	GRM21BR61A226ME44L Series= X5R	Cap= 22.0 uF ESR= 3.0 mOhm VDC= 10.0 V IRMS= 3.84 A	1	$0.09	0805 7 mm^2
Coutx	MuRata	GRM188R71E104KA01D Series= X7R	Cap= 100.0 nF ESR= 30.0 mOhm VDC= 25.0 V IRMS= 1.51 A	1	$0.01	0603 5 mm^2
L1	Pulse Engineering	PA4332.222NLT	L= 2.2 µH 48.0 mOhm	1	$0.26	PA4332 27 mm^2
Renb	Vishay-Dale	CRCW040213K7FKED Series= CRCW..e3	Res= 13.7 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2
Rent	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2
Rfbb	Yageo	RC0603FR-0730KL Series= ?	Res= 30.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	$0.01	0603 5 mm^2
Rfbt	Yageo	RC0603FR-07220KL Series= ?	Res= 220.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	$0.01	0603 5 mm^2
Rpg	Vishay-Dale	CRCW040210K0FKED Series= CRCW..e3	Res= 10.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2

Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
U1	Texas Instruments	TPS563257DRLR	Switcher	1	$0.10	DRL0006A 7 mm²

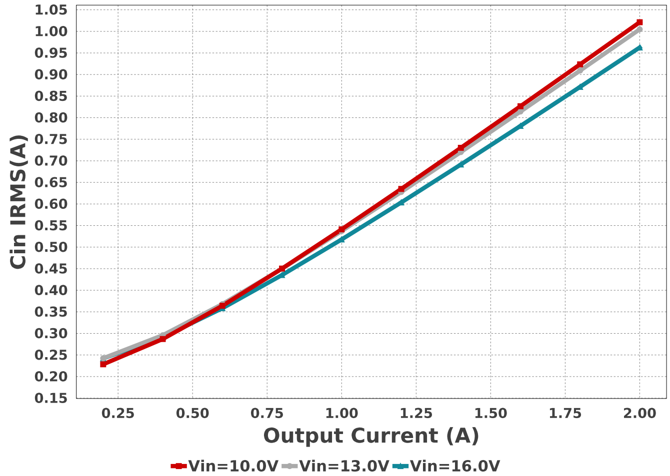
IC Tj



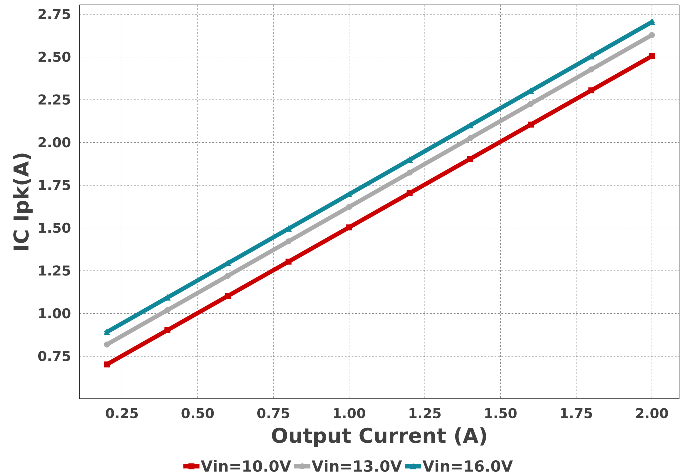
Duty Cycle



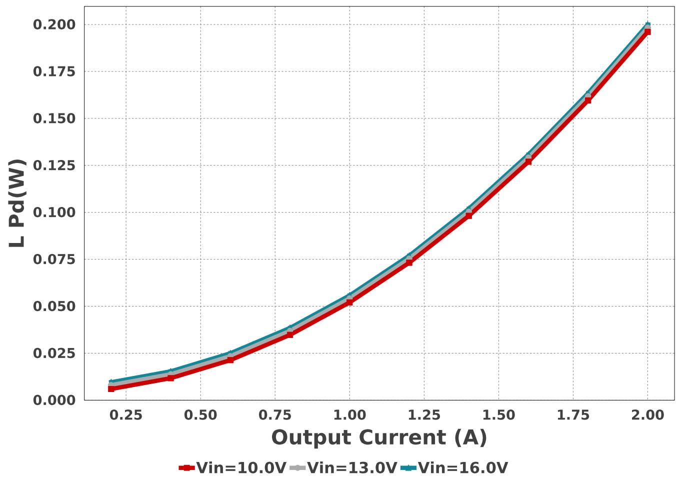
Cin IRMS



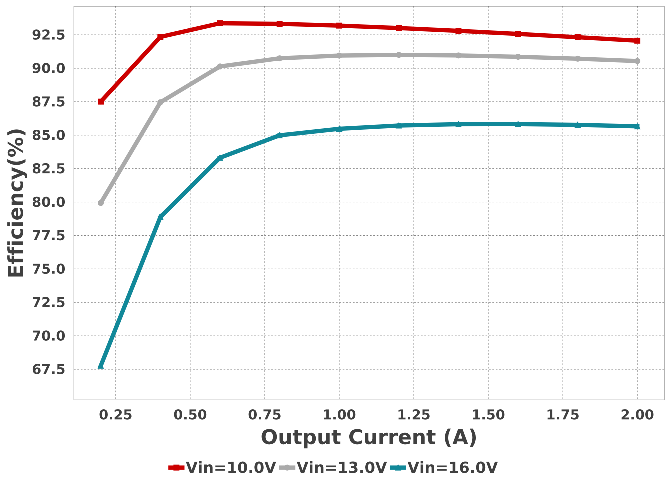
IC Ipk



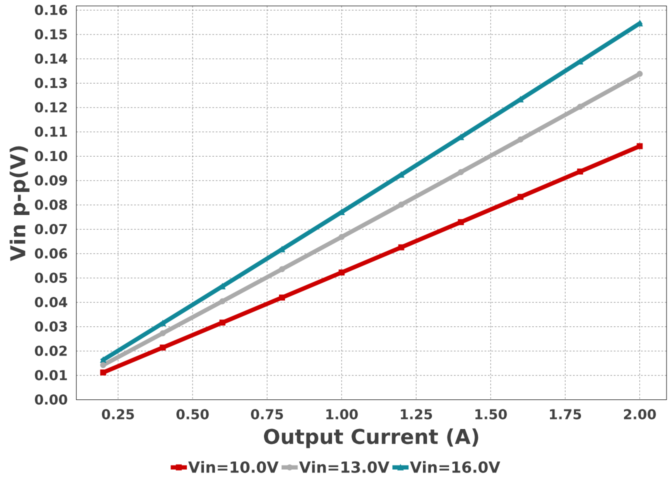
L Pd



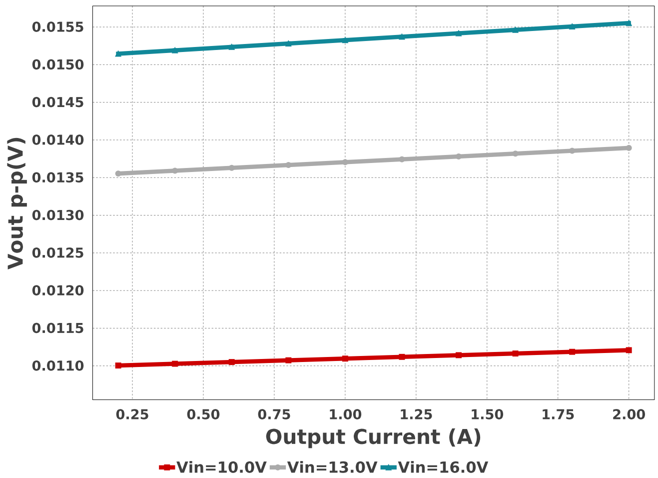
Efficiency



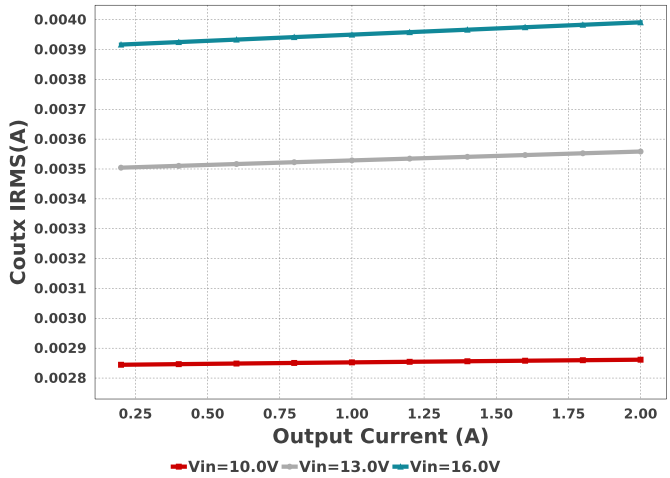
Vin p-p



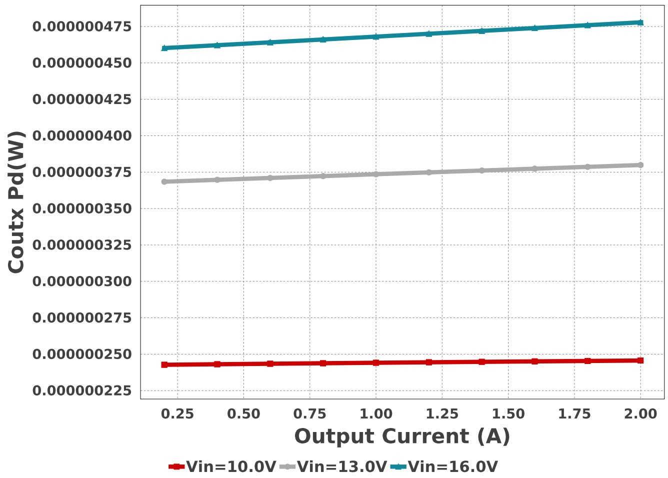
Vout p-p



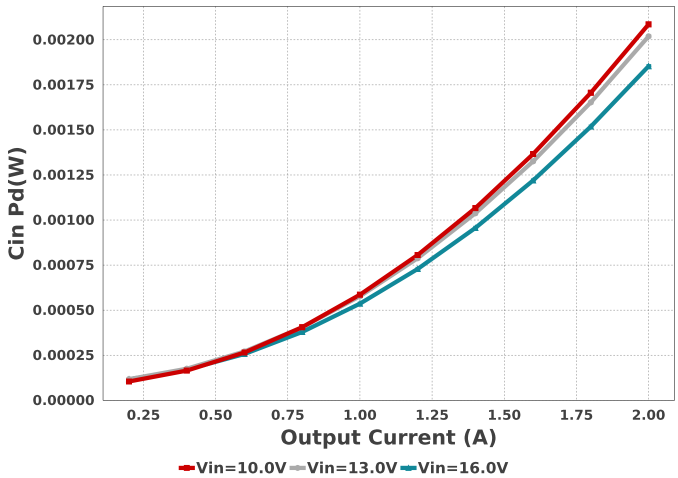
Coutx IRMS



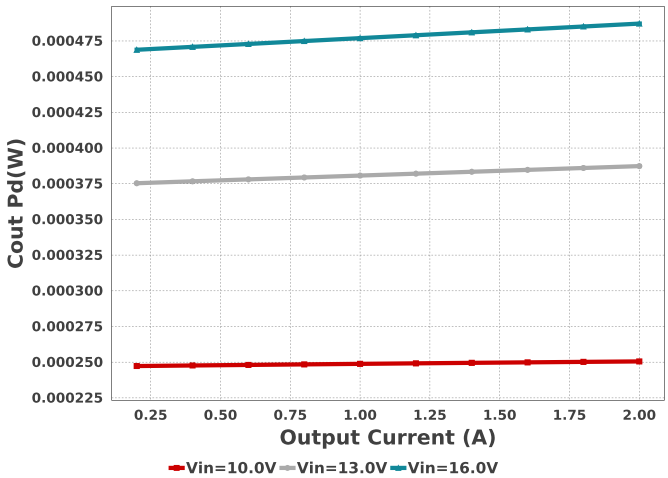
Coutx Pd

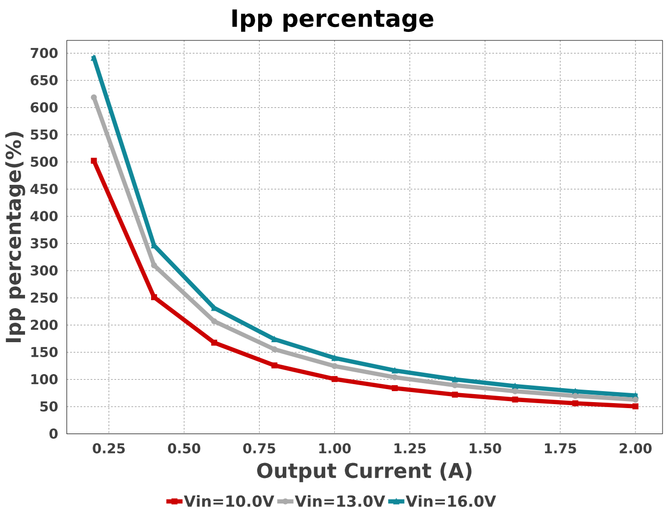


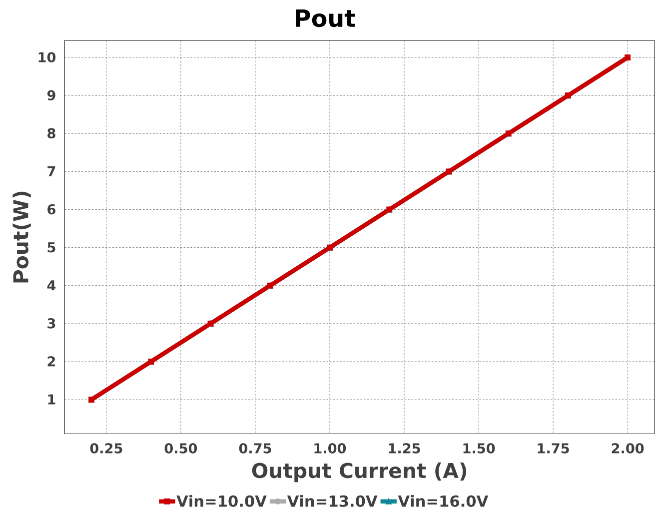
Cin Pd

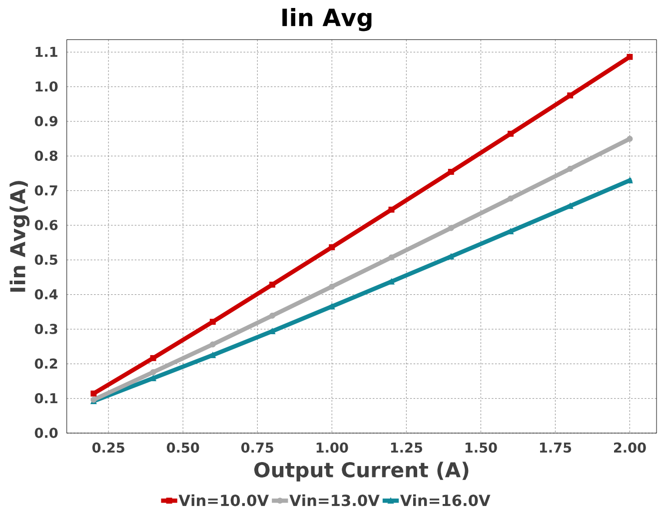


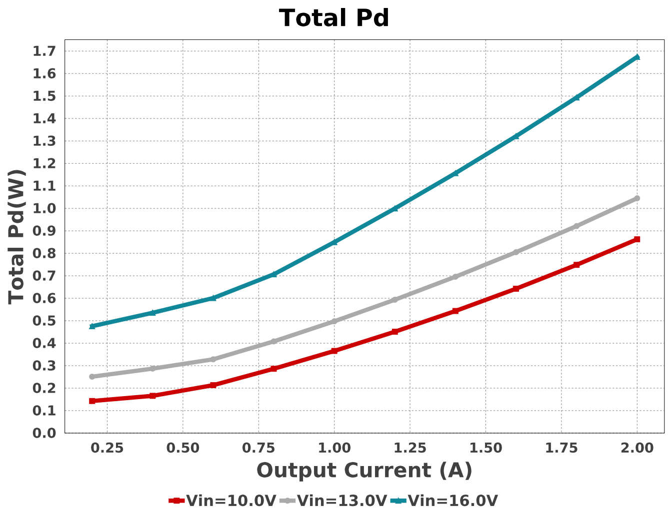
Cout Pd

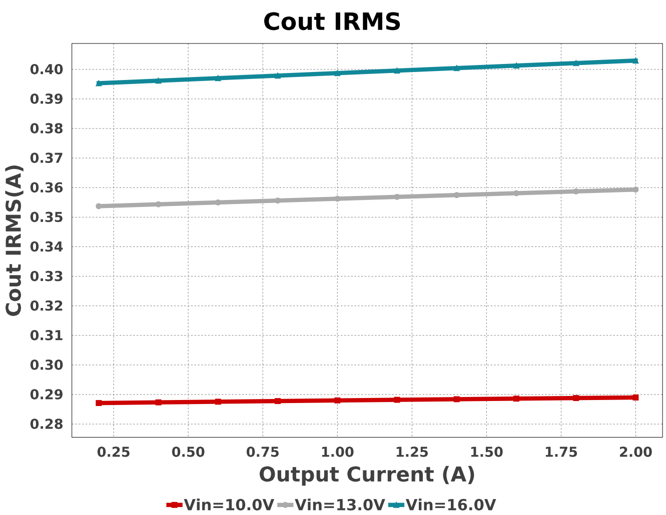


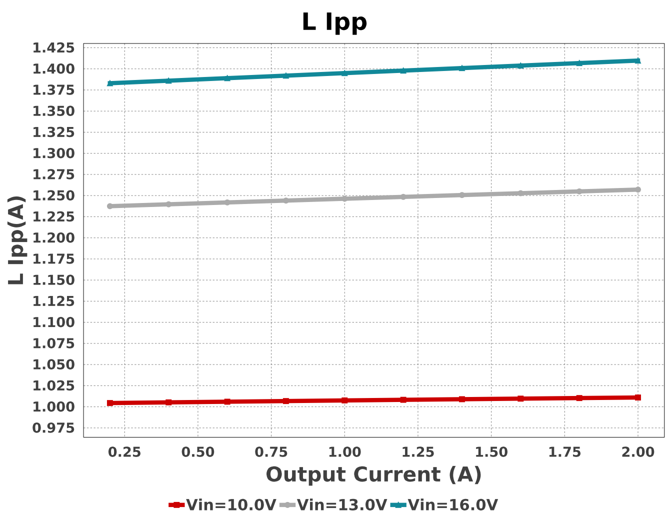

Ipp percentage
Ipp percentage(%)
700
650
600
550
500
450
400
350
300
250
200
150
100
50
0
0.25 0.50 0.75 1.00 1.25 1.50 1.75 2.00
Output Current (A)
Vin=10.0V Vin=13.0V Vin=16.0V

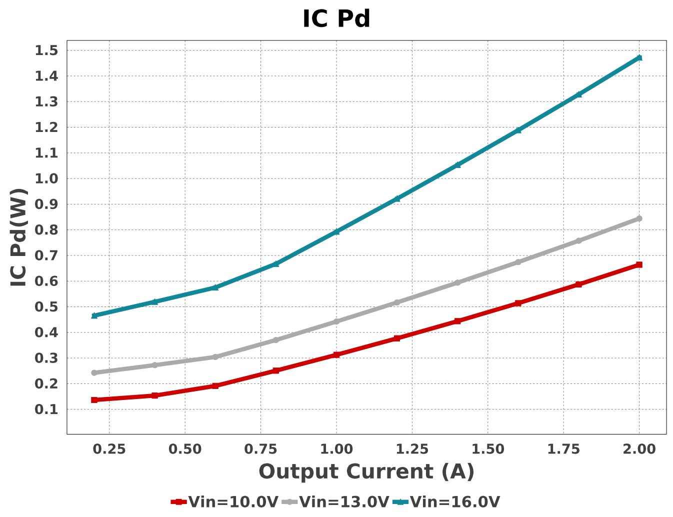

Pout
Pout(W)
10
9
8
7
6
5
4
3
2
1
0.25 0.50 0.75 1.00 1.25 1.50 1.75 2.00
Output Current (A)
Vin=10.0V Vin=13.0V Vin=16.0V


Iin Avg
Iin Avg(A)
1.1
1.0
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0.0
0.25 0.50 0.75 1.00 1.25 1.50 1.75 2.00
Output Current (A)
Vin=10.0V Vin=13.0V Vin=16.0V


Total Pd
Total Pd(W)
1.7
1.6
1.5
1.4
1.3
1.2
1.1
1.0
0.9
0.8
0.7
0.6
0.5
0.4
0.3
0.2
0.1
0.0
0.25 0.50 0.75 1.00 1.25 1.50 1.75 2.00
Output Current (A)
Vin=10.0V Vin=13.0V Vin=16.0V


Cout IRMS
Cout IRMS(A)
0.40
0.39
0.38
0.37
0.36
0.35
0.34
0.33
0.32
0.31
0.30
0.29
0.28
0.25 0.50 0.75 1.00 1.25 1.50 1.75 2.00
Output Current (A)
Vin=10.0V Vin=13.0V Vin=16.0V


L Ipp
L Ipp(A)
1.425
1.400
1.375
1.350
1.325
1.300
1.275
1.250
1.225
1.200
1.175
1.150
1.125
1.100
1.075
1.050
1.025
1.000
0.975
0.25 0.50 0.75 1.00 1.25 1.50 1.75 2.00
Output Current (A)
Vin=10.0V Vin=13.0V Vin=16.0V



Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	962.658 mA	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	1.853 mW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	402.975 mA	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	487.17 µW	Capacitor	Output capacitor power dissipation
5.	Coutx IRMS	3.991 mA	Capacitor	Output capacitor_x RMS ripple current
6.	Coutx Pd	477.86 nW	Capacitor	Output capacitor_x power loss
7.	IC Ipk	2.705 A	IC	Peak switch current in IC
8.	IC Pd	1.472 W	IC	IC power dissipation
9.	IC Tj	138.911 degC	IC	IC junction temperature
10.	IC Tolerance	9.0 mV	IC	IC Feedback Tolerance
11.	ICThetaJA Effective	74.0 degC/W	IC	IC junction-to-ambient thermal resistance with TI EVM
12.	Iin Avg	729.63 mA	IC	Average input current
13.	Ipp percentage	70.489 %	Inductor	Inductor ripple current percentage (with respect to average inductor current)
14.	L Ipp	1.41 A	Inductor	Peak-to-peak inductor ripple current
15.	L Pd	199.95 mW	Inductor	Inductor power dissipation
16.	Cin Pd	1.853 mW	Power	Input capacitor power dissipation
17.	Cout Pd	487.17 µW	Power	Output capacitor power dissipation
18.	Coutx Pd	477.86 nW	Power	Output capacitor_x power loss
19.	IC Pd	1.472 W	Power	IC power dissipation
20.	L Pd	199.95 mW	Power	Inductor power dissipation
21.	Total Pd	1.674 W	Power	Total Power Dissipation
22.	BOM Count	13	System Information	Total Design BOM count
23.	Duty Cycle	32.207 %	System Information	Duty cycle
24.	Efficiency	85.659 %	System Information	Steady state efficiency
25.	FootPrint	85.0 mm^2	System Information	Total Foot Print Area of BOM components
26.	Frequency	1.123 MHz	System Information	Switching frequency
27.	Iout	2.0 A	System Information	Iout operating point
28.	Mode	CCM	System Information	Conduction Mode
29.	Pout	10.0 W	System Information	Total output power
30.	Total BOM	$0.62	System Information	Total BOM Cost
31.	Vin	16.0 V	System Information	Vin operating point
32.	Vin p-p	154.565 mV	System Information	Peak-to-peak input voltage
33.	Vout	5.0 V	System Information	Operational Output Voltage
34.	Vout Actual	5.0 V	System Information	Vout Actual calculated based on selected voltage divider resistors
35.	Vout Tolerance	3.304 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
36.	Vout p-p	15.552 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	2.0	Maximum Output Current
VinMax	16.0	Maximum input voltage
VinMin	10.0	Minimum input voltage
Vout	5.0	Output Voltage
base_pn	TPS563257	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

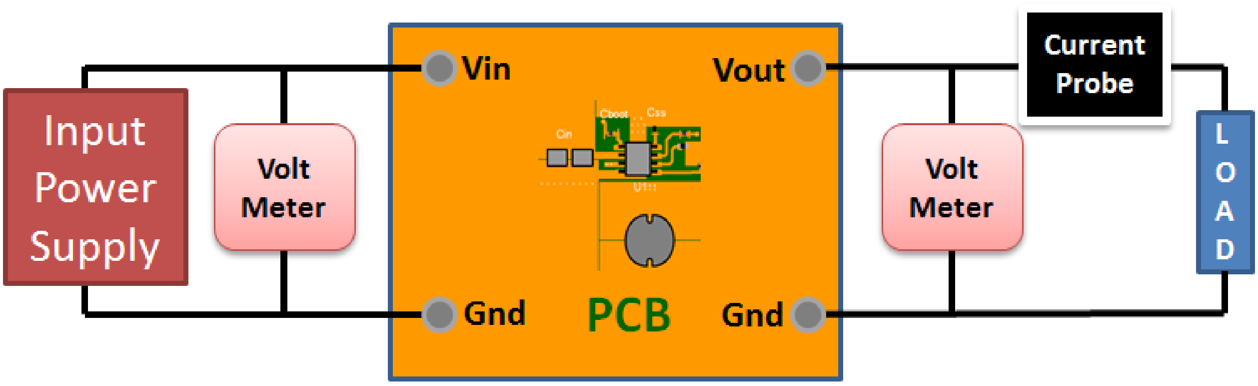
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 10.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum Iout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

1. Master key : DDBD7590F6C2AE06[v1]

2. **TPS563257** Product Folder : https://www.ti.com/product/TPS563252 : contains the data sheet and other resources.

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