(P1)

Ox abcdef 12

Memory

Data	Address
12	0
ef	1
cd	2
ab	3

Little Endian

→ Least significant data
to most significant data bit

Binary

0001 0010

1110 1111

1100 1101

1010 1011

Ox abcdef 12

Binary

1010	1011
1100	1101
1110	1111
0001	0010

Data	address
ab	0
cd	1
ef	2
12	3

Big Endian

→ Most significant data
to Least significant data bit