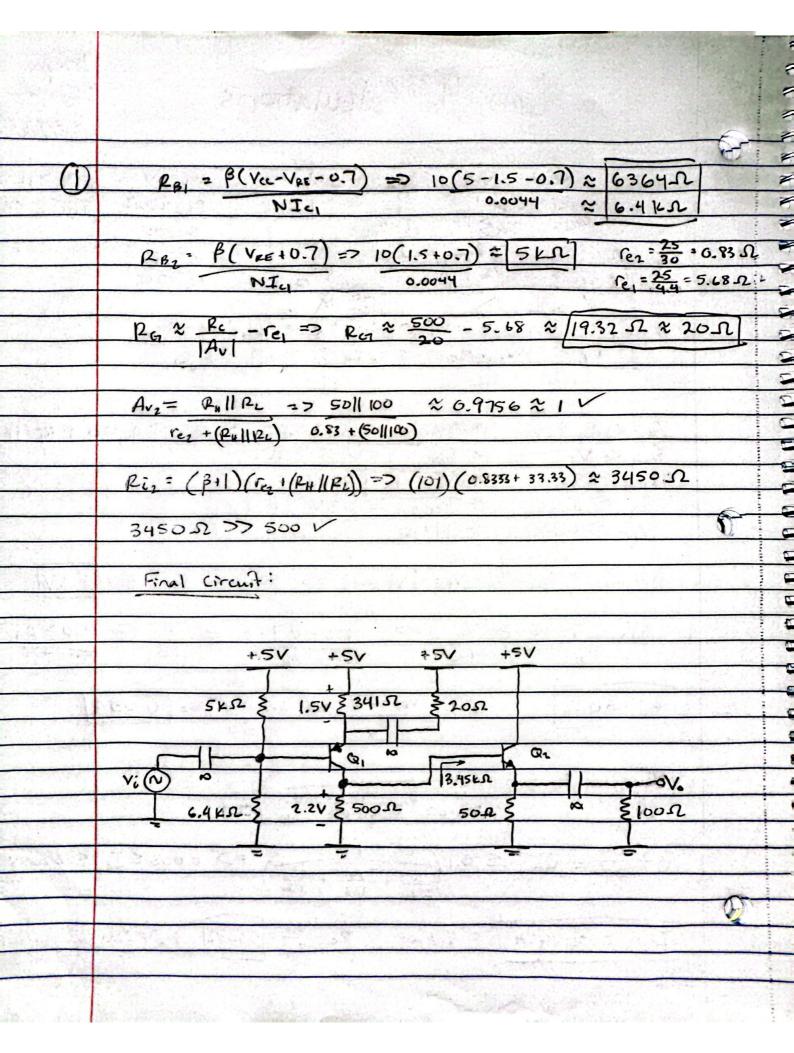
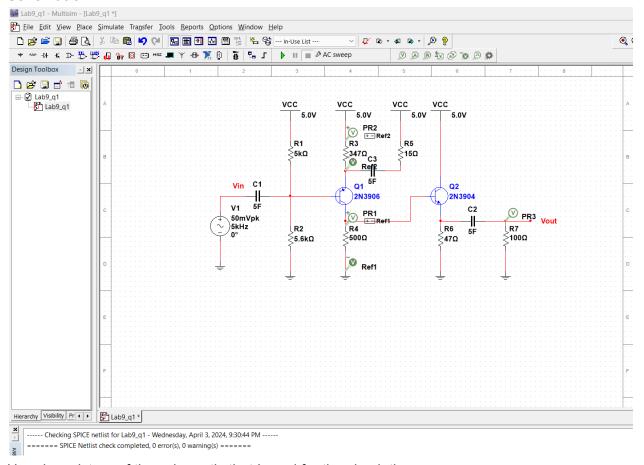


Scanned with CamScanner



Scanned with CamScanner

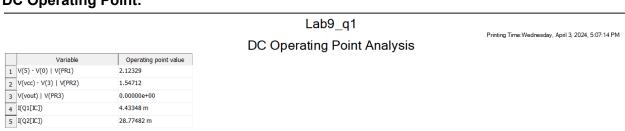
Schematic:



Here is a picture of the schematic that I used for the simulations.

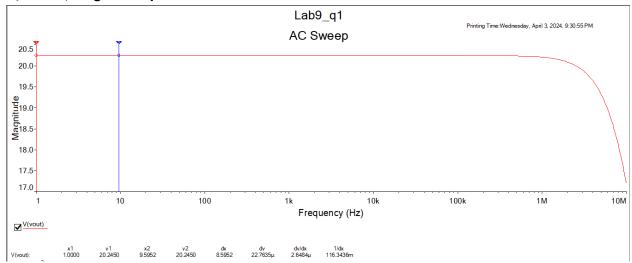
Some changes I made were for R_{B1} and R_{G} , I adjusted the values to 5.6k and 15 respectively since the DC operating point and A_V was slightly off. I also used 347 for R_E and 47 for R_H , in order to make it more convenient to use for the lab.

DC Operating Point:

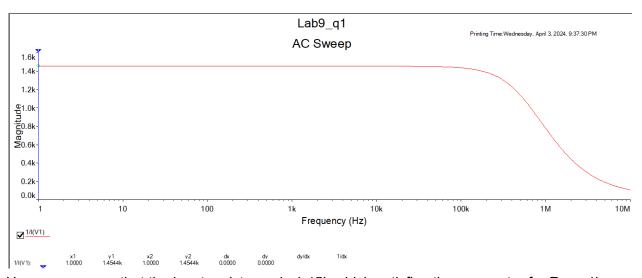


For the DC Operating point, we can see that the values for V_{RC} (2.12V), V_{RE} (1.54V), I_{C1} (4.43mA), and I_{C2} (28.7mA) are consistent with the calculated values.

A_V and R_i magnitude plots:

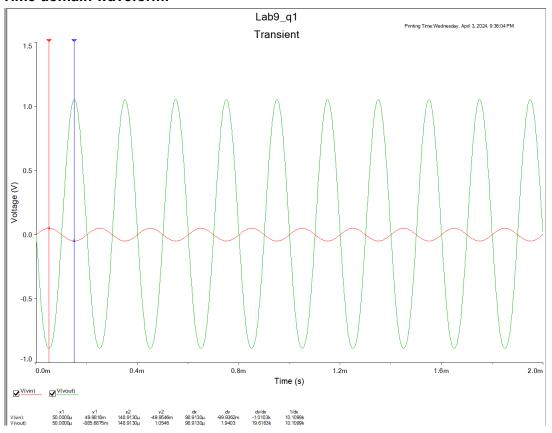


Here we can see that the magnitude of $A_{\scriptscriptstyle V}$ for this circuit is 20.25 which has little to no difference to the parameter.



Here we can see that the input resistance is 1.45k which satisfies the parameter for $R_i \ge 1$ k.

Time domain waveform:



Here is a photo of the transient plot which shows the output voltage amplitude at 1.05V.

THD:

						Printing Time:Wednesday, April 3, 2024, 9:35:08 PN
	alysis for V(vou					
2 DC compor	nent: 0.0388689					
3 No. Harmo	nics: 10					
4 THD:	4.4047 %					
5 Grid size:	256					
6 Interpolation	on Degree: 1					
7						
8 Harmonic	Frequency	Magnitude	Phase	Norm. Mag	Norm. Phase	
9 0	0	0.0388689	0	0.0394844	0	
10 1	5000	0.984412	179.98	1	0	
11 2	10000	0.0416922	-90.117	0.0423524	-270.1	
12 3	15000	0.0112555	-179.91	0.0114337	-359.89	
13 4	20000	0.00340069	89.7275	0.00345454	-90.253	
14 5	25000	0.00167415	0.112637	0.00170066	-179.87	
15 6	30000	0.000727165	-90.335	0.000738679	-270.32	
16 7	35000	0.000496544	-179.99	0.000504406	-359.97	
17 8	40000	0.000164716	89.6648	0.000167324	-90.315	
18 9	45000	0.000166168	0.0371392	0.000168799	-179.94	
19 10	50000	2.12207e-05	-91.555	2.15568e-05	-271.53	
20						

Here, the THD is 4.4% which satisfies the parameter for THD <= 5%.