

AN13861

PN7160 card emulation

Rev. 1.0 — 26 April 2023

Application note

Document Information

Information	Content
Keywords	NFCC, NFC, CE, PN7160
Abstract	This document provides information about the PN7160 card emulation feature.



Revision history

Rev	Date	Description
1.0	20230426	Initial version

1 Introduction

The goal of this document is to give examples on how to properly set card emulation (CE) for specific CE scenario. For detailed explanation of CE architecture check user manual [\[5\]](#).

For hardware settings of card emulation, refer to [\[13\]](#) and [\[14\]](#).

Requirements:

- Knowledge of MCUXpresso and/or Android and/or Linux
- PN7160 Knowledge, for example, how to push configuration files on Linux/Android. [PN7160 Android porting guide [\[10\]](#), PN7160 Linux porting guide [\[11\]](#), NXP-NCI2.0 MCUXpresso examples guide [\[12\]](#)].

2 Card emulation by the DH-NFCEE

Scenario 1 (Card emulation by the DH-NFCEE) is a scenario, where the device host is responsible for emulating a card. An external Reader/Writer accesses the DH-NFCEE emulating the contactless card, through the PN7160.

[Figure 1](#) shows the flow of communication. We can see that in this scenario PN7160 is just a device in the middle, responsible for forwarding the communication from the external Reader/Writer to DH-NFCEE. For more information, check UM [\[5\]](#).

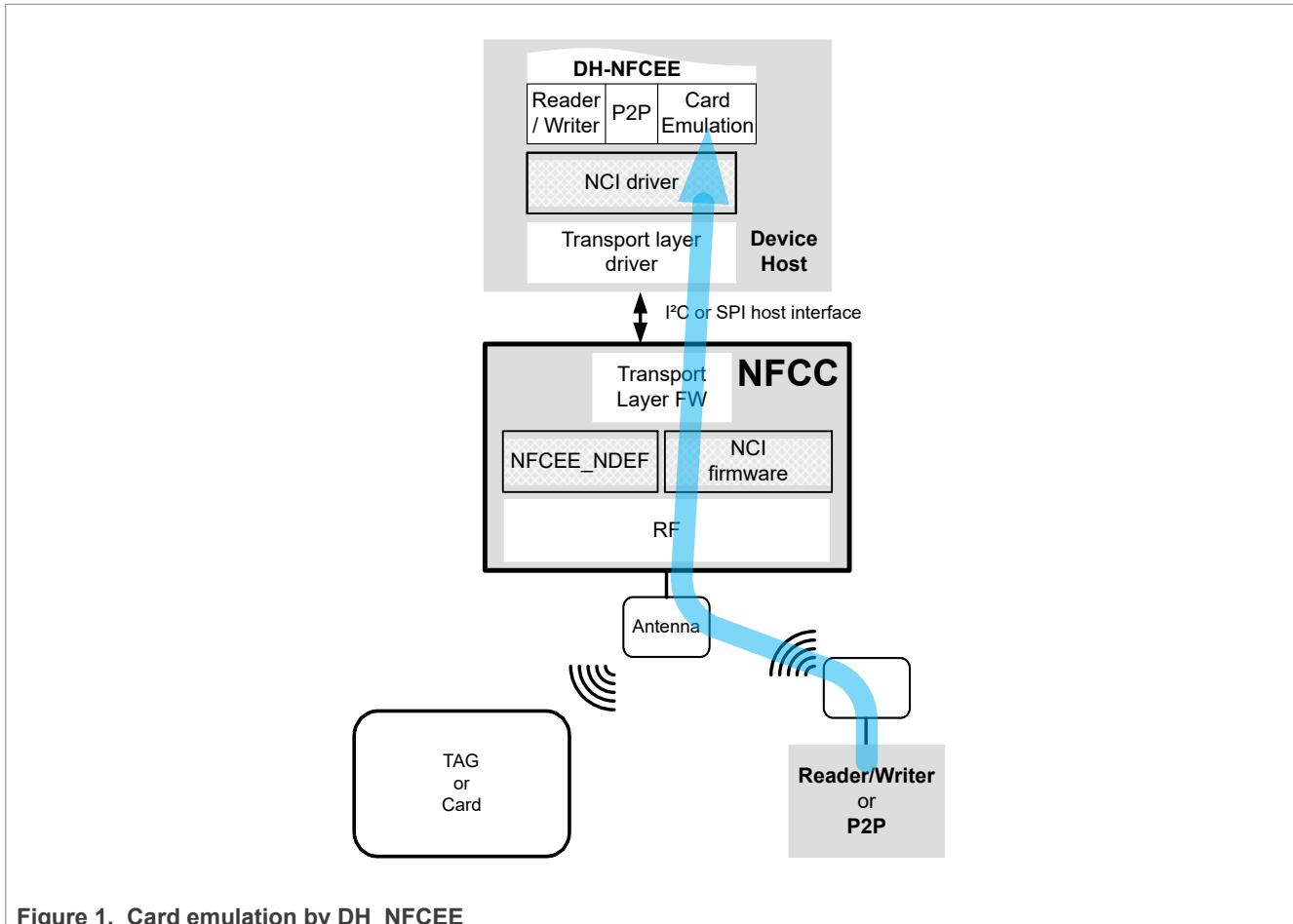


Figure 1. Card emulation by DH_NFCEE

3 Card emulation over NFCC

[Figure 2](#) shows scenario 2 (card emulation over NFCC). In this scenario, the card is emulated on the NFCC. We can access to NFCEE_NDEF either via DH-NFCEE or from RF field. For more information, check UM [5].

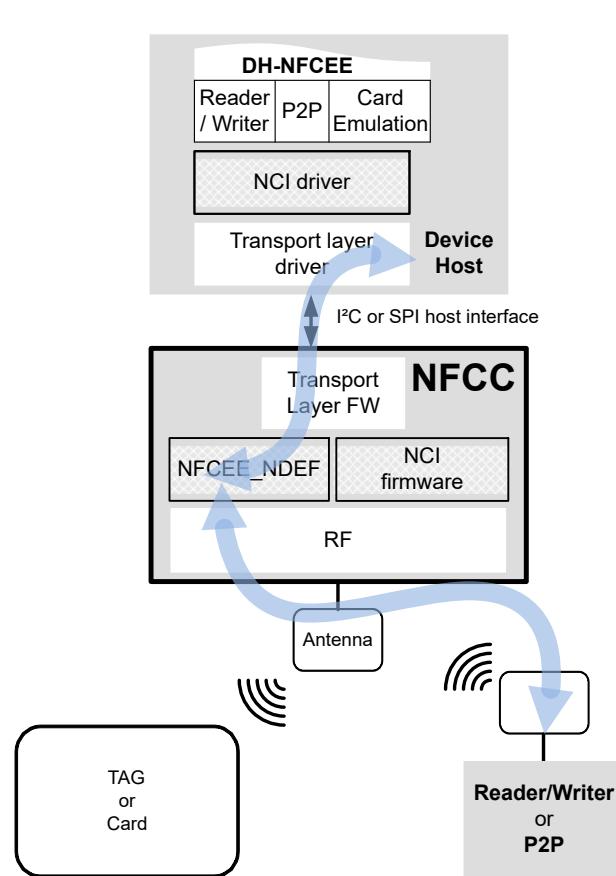


Figure 2. Card emulation over NFCC

4 Switching between scenarios

To switch between scenario 1 and scenario 2 on Android or Linux, we can use configuration file (libnfc-nxp.conf). There we must set the NXP_T4T_NFCEE_ENABLE flag.

If we want to use scenario 1 (card emulation by the DH-NFCEE): **NXP_T4T_NFCEE_ENABLE = 0x00**.

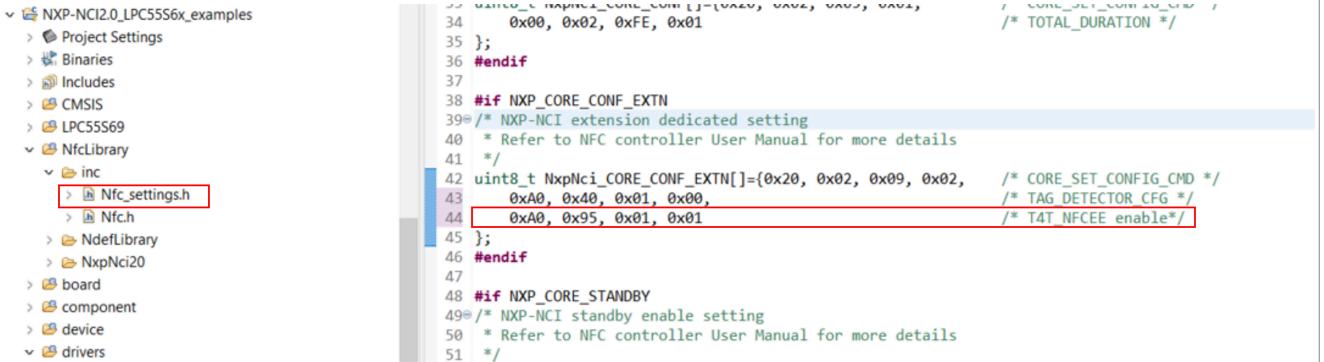
If we want to use scenario 2 (card emulation over NFCC): **NXP_T4T_NFCEE_ENABLE = 0x01**.

In the background, this flag is converted to NCI command. DH sends a CORE_SET_CONFIG with parameter 0xA095 in RF_IDLE_STATE. [Figure 3](#) shows NXP_T4T_NFCEE_ENABLE flag.

```
#####
#T4T NFCEE ENABLE
#bit pos 0 = Enable T4T NFCEE from NFCC (Only wired R/W activated, RF read only activated)
NXP_T4T_NFCEE_ENABLE=0x00
```

Figure 3. T4T NFCEE enable in Linux or Android

In the MCUXpresso example, the process is a bit different. For enabling T4T_NFCEE, we must send an NCI command. Refer to [Figure 4](#) for location. Example is available [8].



The screenshot shows the MCUXpresso IDE interface. On the left, the project structure is displayed under 'NXP-NCI2.0_LPC55S6x_examples'. It includes 'Project Settings', 'Binaries', 'Includes', 'CMSIS', 'LPC55S69', 'NfcLibrary' (which is expanded to show 'inc' and 'Nfc_settings.h'), 'NdefLibrary', 'NxpNci20', 'board', 'component', 'device', and 'drivers'. On the right, the code editor displays the 'Nfc_settings.h' file. The code is as follows:

```
34     0x00, 0x02, 0xFE, 0x01           /* TOTAL_DURATION */
35 };
36 #endif
37
38 #if NXP_CORE_CONF_EXTN
39 /* NXP-NCI extension dedicated setting
40 * Refer to NFC controller User Manual for more details
41 */
42 uint8_t NxpNci_CORE_CONF_EXTN[]={0x20, 0x02, 0x09, 0x02,           /* CORE_SET_CONFIG_CMD */
43     0xA0, 0x40, 0x01, 0x00,           /* TAG DETECTOR CFG */
44     0xA0, 0x95, 0x01, 0x01};          /* T4T_NFCEE enable*/
45 };
46 #endif
47
48 #if NXP_CORE_STANDBY
49 /* NXP-NCI standby enable setting
50 * Refer to NFC controller User Manual for more details
51 */
```

Figure 4. T4T NFCEE enable in MCUXpresso

5 Card emulation configuration

The goal of this chapter is to explain, how to configure CE. Since many settings are used between both versions of CE, every explained setting is valid for both scenario if not stated otherwise.

CE on either technology NFC-A or technology NFC-B, the PN7160 only supports the ISO-DEP protocol. ISO-DEP protocol always mapped to ISO-DEP interface. For more information, check UM [5].

To understand everything explained in following chapters, the knowledge of following standards is mandatory [NCI 2.0 [3], ISOIEC - 14443/3 [1], ISOIEC - 14443/4 [2], NFC Digital protocol [4]].

Together with settings described in following sections, NCI [3] provides other configurations, which can be used together with CE, for example, RF_FIELD_INFO and RF_NFCEE_ACTION. Explanation can be found in [3].

5.1 Type A configuration

Table is showing parameters and values for specific value for Type A configuration.

Table 1. Type A settings values

Name	Explanation	Value	Scenario 1	Scenario 2
LA_BIT_FRAME_SDD	Byte 1 of SENS_RES. SENS_RES explanation here: [4]	0x30	Supported	Supported
LA_PLATFORM_CFG	Byte 2 of SENS_RES. SENS_RES explanation here: [4]	0x31	Supported	Supported
LA_SEL_INFO	Value used to generate SEL_RES. Check [4]	0x32	Supported	Supported
LA_NFCID1	This sets ID. Check digital. [3] and [4]. Max supported size of UID is 7 bytes.	0x33	Supported	Supported
LI_A_RATS_TB1	For setting Frame Waiting Time (FWI) and startup Frame Guard Time (SFGU). For more details, check [1]	0x58	Supported	Supported
LI_A_HIST_BY	Setting Historical Bytes. For more details, check [1] and [4]	0x59	Supported	Supported
LI_A_BIT_RATE	Setting up maximum supported bit rates. For more details, check [3]	0x5B	Supported	Supported
LI_A_RATC_TC1	Support of CID. For more details, check [2] and [3]	0x5C	Supported	Supported

LA_BIT_FRAME_SDD and LA_PLATFORM_CFG are used to build SENS_RES. SENS_RES which is hardcoded to 0x0400 on Linux and Android.

LA_SEL_INFO is used to build SEL_RES. SEL_RES is hardcoded to 0x20 on Linux and Android.

In MCUXpresso project configurations are set in Nfc_settings.h. Codeblock below, shows exact variable.

```
uint8_t NxpNci_CORE_CONF[]={ 0x20, 0x02, 0x31, 0x0F,
    0x85, 0x01, 0x01,
    0x28, 0x01, 0x00,
    0x21, 0x01, 0x00,
    0x30, 0x01, 0x08,
    0x31, 0x01, 0x03,
    0x32, 0x01, 0x60,
    0x38, 0x01, 0x01,
    0x33, 0x04, 0x01, 0x02, 0x03, 0x04,
    0x54, 0x01, 0x06,
    0x50, 0x01, 0x02,
    0x5B, 0x01, 0x00,
    0x80, 0x01, 0x01,
    0x81, 0x01, 0x01,
    0x82, 0x01, 0x0E,
    0x18, 0x01, 0x01
};
```

Configurations are configured in libnfc-nxp.conf for Android and Linux. Codeblock below shows exact location.

```
#####
# Core configuration settings
NXP_CORE_CONF={ 20, 02, 31, 0F,
 85, 01, 01,
 28, 01, 00,
 21, 01, 00,
 30, 01, 08,
 31, 01, 03,
 32, 01, 60,
 38, 01, 01,
 33, 04, 01, 02, 03, 04,
 54, 01, 06,
 50, 01, 02,
 5B, 01, 00,
 80, 01, 01,
 81, 01, 01,
 82, 01, 0E,
 18, 01, 01
}
```

We can see most of settings when we read our emulated card with TagInfo application. [Figure 5](#) shows TagInfo read for scenario 1 and [Figure 6](#) shows TagInfo read for scenario 2.



Figure 5. TagInfo read - Scenario 1

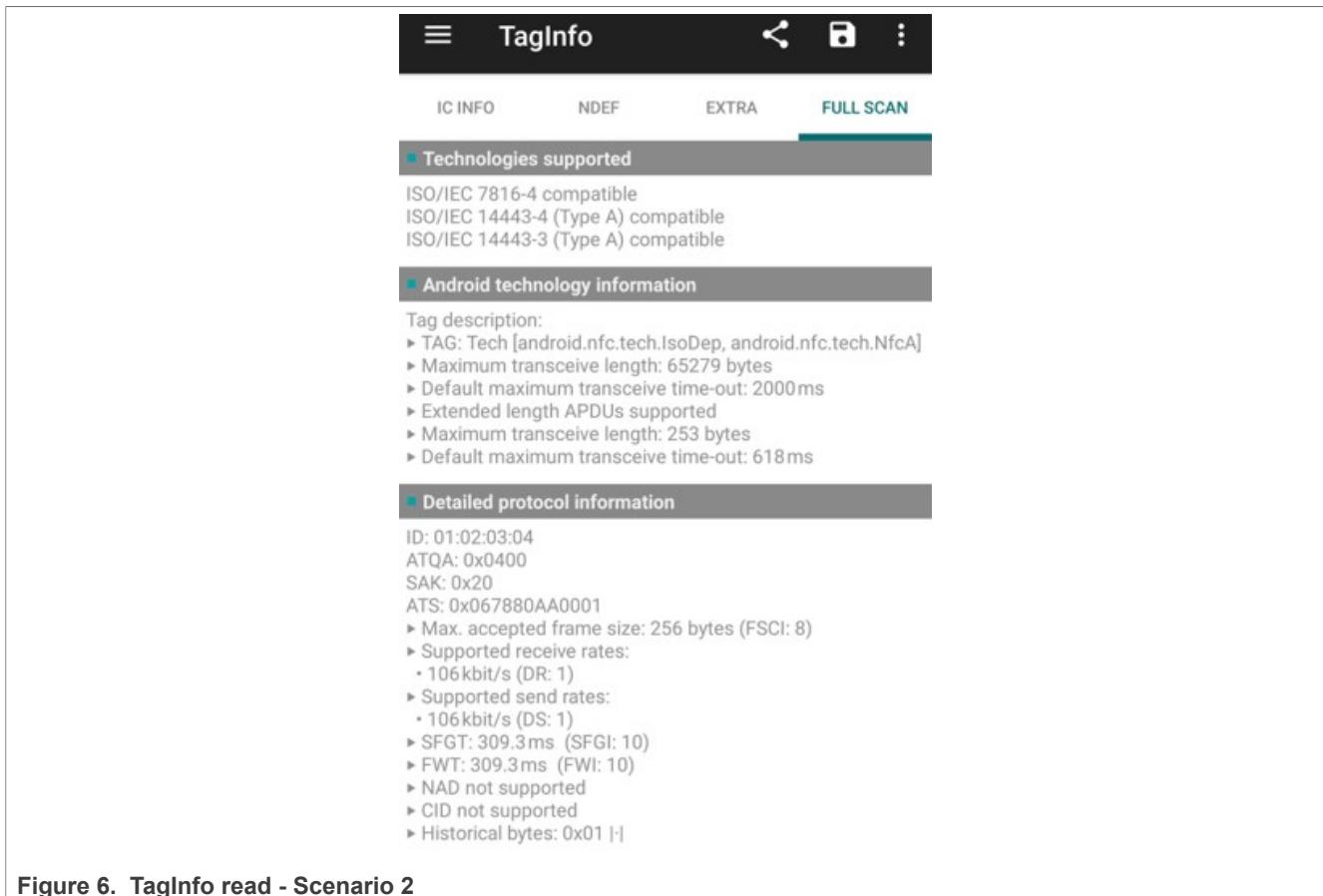


Figure 6. TagInfo read - Scenario 2

5.2 Type B configuration

Table is showing parameters and values for Type B configuration.

Table 2. Type A settings values

Name	Explanation	Value	Scenario 1	Scenario 2
LB_SENSB_INFO	Used to generate Byte 2 of Protocol info within SENSB_RES. Check [4] and [3]	0x38	Supported	Supported
LB_NFCID0	NFC-B identifier of the NFC Forum Device. NFCID0 is always 4 bytes long. Check [4]	0x39	Supported	Supported
LB_APPLICATION_DATA	Application data (Bytes 6-9) of SENSB_RES. Check [4]	0x3A	Supported	Supported
LB_SFGI	Startup Frame Guard Time. Check [4]	0x3B	Supported	Supported
LB_FWI_ADC_FO	Byte 3 of Protocol info within SENSB_RES. Check [4]	0x3C	Supported	Supported
LB_BIT_RATE	Setting up maximum supported bit rates. For more details, check [3]	0x3E	Supported	Supported
LI_B_H_INFO_RESP	Higher Layer - Response field of the ATTRIB. Check [4]	0x5A	Supported	Supported

LB_SENSB_INFO is on Linux and Android hardcoded to 0x01.

[Section 5.1](#) shows location, where we set those parameters.

6 Configuration sequence

After configuration parameters are set, couple of commands must be sent that the parameters are taken into consideration. This document is explaining how commands look like, to understand them into details check UM [5] and NCI specification [3].

6.1 RF_DISCOVER_MAP_CMD

Since PN7160 CE supports only ISO-DEP protocol and RF interface, this command has only one option for set CE mode. [Figure 7](#) shows example of RF_DISCOVER_MAP_CMD. RF protocol and RF interface for listen mode (CE) is always set to ISO-DEP. For detailed explanation of this command, check [3].

```
[NCI][COMMAND][21 00 0A 03 04 03 02 05 03 03 80 01 08]
  RF_DISCOVER_MAP_CMD
    * Number of Mapping Configurations = 3 [0x03]
      --> Mapping Configuration N° 1
        - RF Protocol = {Std} PROTOCOL_ISO_DEP [0x04]
        - Mode         = the RF Interface is mapped to the RF Protocol both in Listen Mode and Poll Mode [0x03]
        - RF Interface = {Std} ISO-DEP RF Interface [0x02]

      --> Mapping Configuration N° 2
        - RF Protocol = {Std} PROTOCOL_NFC_DEP [0x05]
        - Mode         = the RF Interface is mapped to the RF Protocol both in Listen Mode and Poll Mode [0x03]
        - RF Interface = {Std} NFC-DEP RF Interface [0x03]

      --> Mapping Configuration N° 3
        - RF Protocol = {NXP PN547-PN548} MIFARE Classic [0x80]
        - Mode         = the RF Interface is mapped to the RF Protocol in Poll Mode [0x01]
        - RF Interface = RF-IF = RFU [0x08]
```

[Figure 7. RF_DISCOVER_MAP_CMD example](#)

This command cannot be changed from configuration files. If someone want to change RF_DISCOVER_MAP_CMD command in Linux or Android, change must be done in HAL layer.

6.2 CORE_SET_CONFIG_CMD

With CORE_SET_CONFIG_CMD command parameters from [Section 5](#) are set. For detailed explanation of this command, check [3].

6.3 RF_SET_LISTEN_MODE_ROUTING_CMD

Description of this command is in UM [5] and in NCI specification [3]. For range of this application note, it is only important, that this command is different between both CE scenarios.

[Figure 8](#) and [Figure 9](#) shows difference in command between both scenarios.

```
[NCI] [COMMAND] [21 01 17 00 04 03 04 00 00 FE FE 01 03 00 11 04 01 03 00 01 05 00 03 00 39 00]
  RF_SET_LISTEN_MODE_ROUTING_CMD
    * More           = Last Message [0x00]
    * Number of Routing Entries = 4 [0x04]
      --> Routing Entry N° 1
        - Type     = RFU [0x03]
        - Length   = 4 [0x04]
        - Value    = [0x00 0x00 0xFE 0xFE]
          + Route    = DH NFCEE ID [0x00]
          + Power State = RFU [0x00]

      --> Routing Entry N° 2
        - Type     = Protocol-based routing entry [0x01]
        - Length   = 3 [0x03]
        - Value    = [0x00 0x11 0x04]
          + Route    = DH NFCEE ID [0x00]
          + Power State = RFU [0x11]
          + Protocol  = {Std} PROTOCOL_ISO_DEP [0x04]

      --> Routing Entry N° 3
        - Type     = Protocol-based routing entry [0x01]
        - Length   = 3 [0x03]
        - Value    = [0x00 0x01 0x05]
          + Route    = DH NFCEE ID [0x00]
          + Power State = Switched on [0x01]
          + Protocol  = {Std} PROTOCOL_NFC_DEP [0x05]

      --> Routing Entry N° 4
        - Type     = Technology-based routing entry [0x00]
        - Length   = 3 [0x03]
        - Value    = [0x00 0x39 0x00]
          + Route    = DH NFCEE ID [0x00]
          + Power State = RFU [0x39]
          + Technology = {Std} NFC_RF_TECHNOLOGY_A [0x00]
```

Figure 8. RF_SET_LISTEN_MODE_ROUTING_CMD for scenario 1

```
[NCI][COMMAND][21 01 22 00 05 02 09 10 3B D2 76 00 00 85 01 01 03 04 00 00 FE FE 01 03 00 11 04 01 03 00 01 05 00 03 00 39 00]
RF_SET_LISTEN_MODE_ROUTING_CMD
  * More          = Last Message [0x00]
  * Number of Routing Entries = 5 [0x05]
    --> Routing Entry N° 1
      - Type      = AID-based routing entry [0x02]
      - Length    = 9 [0x09]
      - Value     = [0x10 0x3B 0xD2 0x76 0x00 0x00 0x85 0x01 0x01]
        + Route    = Dynamically assigned by the NFCC [0x10]
        + Power State = RFU [0x3B]
        + AID      = [0xD2 0x76 0x00 0x00 0x85 0x01 0x01]

    --> Routing Entry N° 2
      - Type      = RFU [0x03]
      - Length    = 4 [0x04]
      - Value     = [0x00 0x00 0xFE 0xFE]
        + Route    = DH NFCEE ID [0x00]
        + Power State = RFU [0x00]

    --> Routing Entry N° 3
      - Type      = Protocol-based routing entry [0x01]
      - Length    = 3 [0x03]
      - Value     = [0x00 0x11 0x04]
        + Route    = DH NFCEE ID [0x00]
        + Power State = RFU [0x11]
        + Protocol = (Std) PROTOCOL_ISO_DEP [0x04]

    --> Routing Entry N° 4
      - Type      = Protocol-based routing entry [0x01]
      - Length    = 3 [0x03]
      - Value     = [0x00 0x01 0x05]
        + Route    = DH NFCEE ID [0x00]
        + Power State = Switched on [0x01]
        + Protocol = (Std) PROTOCOL_NFC_DEP [0x05]

    --> Routing Entry N° 5
      - Type      = Technology-based routing entry [0x00]
      - Length    = 3 [0x03]
      - Value     = [0x00 0x39 0x00]
        + Route    = DH NFCEE ID [0x00]
        + Power State = RFU [0x39]
        + Technology = (Std) NFC_RF_TECHNOLOGY_A [0x00]
```

Figure 9. RF_SET_LISTEN_MODE_ROUTING_CMD for scenario 2

6.4 RF_DISCOVER_CMD

RF_DISCOVER_CMD [3] responsibility is starting discovery loop. To select for which technologies we want to poll and/or listen, we must set parameters. For Linux and Android, parameters are located in libnfc-nci.conf.

For selecting listen mode, HOST_LISTEN_TECH_MASK must be set. Following values are supported:

1. 0x01 for Type A only
2. 0x02 for Type B only
3. 0x03 for Type A and Type B

In MCUXpresso project, selection of technologies is done in source file (for example nfc_example_RWandCE.c), where DiscoveryTechnologies variable need to be set.

Figure 10 shows example of RF_DISCOVER_CMD.

```
[NCI][COMMAND][21 03 03 01 80 01]
RF_DISCOVER_CMD
  * Number of Configurations = 1 [0x01]
    --> Configuration N° 1
      - RF Technology and Mode = {Std} NFC_A_PASSIVE_LISTEN_MODE [0x80]
      - Discovery Frequency   = RF Technology and Mode will be executed in every discovery period [0x01]
```

Figure 10. RF_DISCOVER_CMD example

7 Example description

NXP provides example for both scenarios. Scenario 2 is done in two different examples, since access to CE is possible either from DH or from RF.

Examples can be found here: MCUXpresso [8], Linux (scenario 1) [7], Linux (scenario 2) [9].

7.1 Scenario 1 description

Like mentioned in [Section 7.1](#), DH hosts CE. PN7160 forwards communication to DH, for that we must develop a state machine on DH. The state machine in MCUXpresso example can be found in T4T_NDEF_emu.c, Linux example can be found here: [\[7\]](#).

When external reader is in range, PN7160 reports RF_INTF_ACTIVATED_NTF ([\[3\]](#)) with all information.

[Figure 11](#) shows example of RF_INTF_ACTIVATED_NTF. After this command, external reader will start sending commands for read/write NDEF and so on.

```
[NCI] [NOTIFICATION] [61 05 0c 01 02 04 80 ff 01 00 80 00 00 01 80]
RF_INTF_ACTIVATED_NTF
  * RF Discovery ID          = 1 [0x01]
  * RF Interface              = {Std} ISO-DEP RF Interface [0x02]
  * RF Protocol                = {Std} PROTOCOL_ISO_DEP [0x04]
  * Activation RF Technology and Mode = {Std} NFC_A_PASSIVE_LISTEN_MODE [0x80]
  * Max Data Packet Payload Size = 255 [0xFF]
  * Initial Number of Credits   = 1 [0x01]
  * Length of RF Technology Specific Parameters = 0 [0x00]
  * RF Technology Specific Parameters = []

  * Data Exchange RF Technology and Mode = {Std} NFC_A_PASSIVE_LISTEN_MODE [0x80]
  * Data Exchange Transmit Bit Rate      = {Std} NFC_BIT_RATE_106: 106 Kbit/s [0x00]
  * Data Exchange Receive Bit Rate        = {Std} NFC_BIT_RATE_106: 106 Kbit/s [0x00]
  * Length of Activation Parameters       = 1 [0x01]
  * Activation Parameters                 = [0x80]
    --> RATS Command Param = [0x80]
    - FSD = 256 (Frame Size for proximity coupling Device) [FSDI = 0x8]
    - DID = (Device ID) [0x0]
```

Figure 11. Example of RF_INTF_ACTIVATED_NTF

In Linux and MCUXpresso examples, we must develop a state machine on the DH. To achieve this, some rules must be followed. Check [\[6\]](#) for command set.

To test it, we can use **TagInfo** for read NDEF and **TagWrite** for write NDEF.

Codeblock below shows state machine for MCUXpresso example (similar must be done for Linux).

```
void T4T_NDEF_EMU_Next(unsigned char *pCmd, unsigned short Cmd_size, unsigned char *pRsp,
 unsigned short *pRsp_size)
{
    bool eStatus = false;

    if (!memcmp(pCmd, T4T_NDEF_EMU_APP_Select, sizeof(T4T_NDEF_EMU_APP_Select)))
    {
        *pRsp_size = 0;
        eStatus = true;
        eT4T_NDEF_EMU_State = NDEF_Application_Selected;
    }
}
```

```
else if (!memcmp(pCmd, T4T_NDEF_EMU_CC_Select, sizeof(T4T_NDEF_EMU_CC_Select)))
{
    if(eT4T_NDEF_EMU_State == NDEF_Application_Selected)
    {
        *pRsp_size = 0;
        eStatus = true;
        eT4T_NDEF_EMU_State = CC_Selected;
    }
}
else if (!memcmp(pCmd, T4T_NDEF_EMU_NDEF_Select, sizeof(T4T_NDEF_EMU_NDEF_Select)))
{
    *pRsp_size = 0;
    eStatus = true;
    eT4T_NDEF_EMU_State = NDEF_Selected;
}
else if (!memcmp(pCmd, T4T_NDEF_EMU_Read, sizeof(T4T_NDEF_EMU_Read)))
{
    if(eT4T_NDEF_EMU_State == CC_Selected)
    {
        unsigned short offset = (pCmd[2] << 8) + pCmd[3];
        unsigned char length = pCmd[4];

        if(length <= (sizeof(T4T_NDEF_EMU_CC) + offset + 2))
        {
            memcpy(pRsp, &T4T_NDEF_EMU_CC[offset], length);
            *pRsp_size = length;
            eStatus = true;
        }
    }
    else if (eT4T_NDEF_EMU_State == NDEF_Selected)
    {
        unsigned short offset = (pCmd[2] << 8) + pCmd[3];
        unsigned char length = pCmd[4];

        if(length <= (T4T_NdefMessage_size + offset + 2))
        {
            T4T_NDEF_EMU_FillRsp(pRsp, offset, length);
            *pRsp_size = length;
            eStatus = true;
        }
    }
}
else if (!memcmp(pCmd, T4T_NDEF_EMU_Write, sizeof(T4T_NDEF_EMU_Write)))
{
    if (eT4T_NDEF_EMU_State == NDEF_Selected)
    {
        unsigned short offset = (pCmd[2] << 8) + pCmd[3];
        unsigned char length = pCmd[4];
        if(offset + length <= sizeof(T4T_NdefMessageWritten))
        {
            memcpy(&T4T_NdefMessageWritten[offset-2], &pCmd[5], length);
            pT4T_NdefMessage = T4T_NdefMessageWritten;
            T4T_NdefMessage_size = (pCmd[5] << 8) + pCmd[6];
            *pRsp_size = 0;
            eStatus = true;
        }
    }
}

if (eStatus == true)
{
    memcpy(&pRsp[*pRsp_size], T4T_NDEF_EMU_OK, sizeof(T4T_NDEF_EMU_OK));
    *pRsp_size += sizeof(T4T_NDEF_EMU_OK);
} else
{
```

```
    memcpy(pRsp, T4T_NDEF_EMU_NOK, sizeof(T4T_NDEF_EMU_NOK));
    *pRsp_size = sizeof(T4T_NDEF_EMU_NOK);
    T4T_NDEF_EMU_Reset();
}
```

On Android, everything is done in AOSP MW code and usually no need to change anything.

7.2 Scenario 2 description

Scenario 2 is a bit more complex, since access to NFCEE_NDEF can be made from DH or from RF field. Following chapters describe, how to access from DH and from RF.

7.2.1 DH access

DH access example can be found here (Linux ([\[7\]](#), [\[9\]](#)), MCUXpresso ([\[8\]](#))). On Android, everything is handled by AOSP middleware and T4TDemo app ([\[15\]](#)). In this section, MCUXpresso codeblocks are used. To access NFCEE_NDEF through DH, specific command order must be followed and state need to be "RFST_IDLE" check [\[5\]](#).

1. Selection of Scenario 2 must be done, like described in [Section 4](#)
2. Send NFCEE_DISCOVER_CMD
3. Send NFCEE_MODE_SET_CMD
4. Open communication channel with CORE_CONN_CREATE_CMD

Codeblock below shows MCUXpresso project, with commands mentioned above:

```
void Configure_NFCEE_NDEF_and_Open_Logical_connection() {
    uint8_t NFCEE_DISCOVER_CMD[] = {0x22, 0x00, 0x00};
    uint8_t NFCEE_MODE_SET_CMD[] = {0x22, 0x01, 0x02, 0x10, 0x01};
    uint8_t CORE_CONN_CREATE_CMD[] = {0x20, 0x04, 0x06, 0x03, 0x01, 0x01, 0x02, 0x10, 0x00};

    uint8_t Answer[MAX_NCI_FRAME_SIZE];
    uint16_t AnswerSize;

    NxpNci_HostTransceive(NFCEE_DISCOVER_CMD, sizeof(NFCEE_DISCOVER_CMD), Answer,
    sizeof(Answer), &AnswerSize);
    NxpNci_WaitForReception(Answer, sizeof(Answer), &AnswerSize, TIMEOUT_INFINITE);

    NxpNci_HostTransceive(NFCEE_MODE_SET_CMD, sizeof(NFCEE_MODE_SET_CMD), Answer,
    sizeof(Answer), &AnswerSize);
    NxpNci_WaitForReception(Answer, sizeof(Answer), &AnswerSize, TIMEOUT_INFINITE);
    NxpNci_WaitForReception(Answer, sizeof(Answer), &AnswerSize, TIMEOUT_INFINITE);

    NxpNci_HostTransceive(CORE_CONN_CREATE_CMD, sizeof(CORE_CONN_CREATE_CMD), Answer,
    sizeof(Answer), &AnswerSize);
    NxpNci_WaitForReception(Answer, sizeof(Answer), &AnswerSize, TIMEOUT_INFINITE);
}
```

After the communication channel is open, there is read and write-access. Command set is explained in [\[6\]](#).

IMPORTANT NOTICE:

After all actions are finished, communication channel must be closed with CORE_CONN_CLOSE_CMD (0x20, 0x05, 0x01, 0x05)!

7.2.2 RF access

For RF access on Android and Linux, the only change is to switch between scenarios in configuration file. In MCUXpresso project three modifications are needed:

1. Switch between scenarios
2. RF_SET_LISTEN_MODE_ROUTING_CMD must be modified inside NxpNci20.c in function NxpNci_ConfigureMode. For more details, check example ([\[8\]](#)) and [Section 6.3](#).
3. Send NFCEE_DISCOVER_CMD and NFCEE_MODE_SET_CMD

After all changes, RF_DISCOVER_CMD can be send and RF access to NFCEE_NDEF is established.

If TagInfo is used, the state machine must be reused, but the DH will always reports "6A 82" (NOK). Check the example for more details.

8 Abbreviations

Table 3.

Abbr.	Meaning
AN	application note
CE	card emulation
DH	device host
DH-NFCEE	NFC Execution Environment running on the DH
FW	firmware
ISO/IEC	International Standard Organization / International Electrotechnical Community
MW	middleware
NCI	NFC controller interface
NFC	near-field communication
NFCC	NFC controller (i.e. PN7160)
RF	radio frequency
RFU	reserved for future use
UM	user manual

9 References

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