Lab 1 OpenCPI Application Development



Objectives

- 1. Create FSK loopback (FPGA internal) OpenCPI Application XML (OAS) using the IDE
- 2. Run application on Matchstiq Z1 hardware





Overview

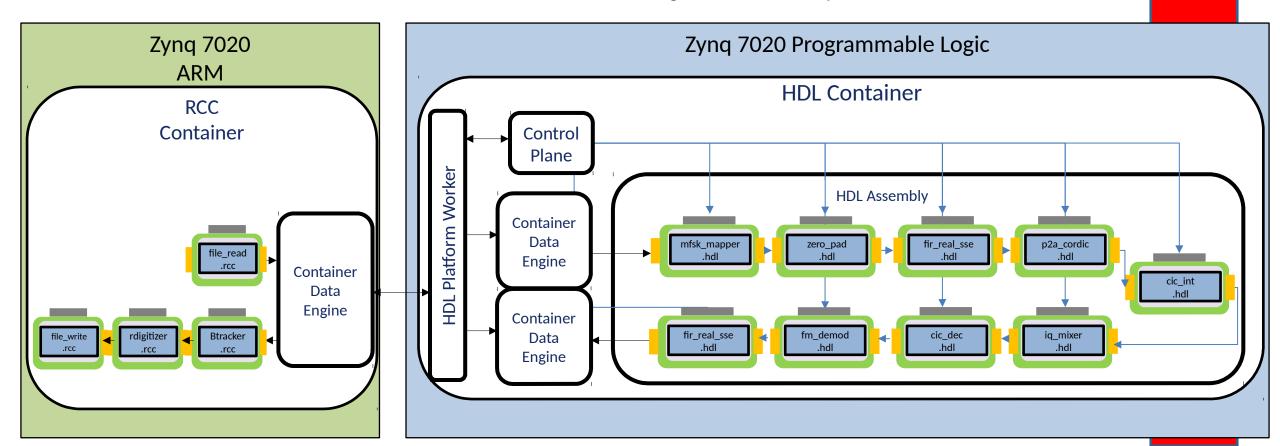
- A common use case for OpenCPI is the reuse of components from multiple libraries to construct applications for heterogeneous systems.
- An OpenCPI Application Specification (OAS) XML describes the connections and initial property settings of the components.
 - The ANGRYVIPER (AV) IDE helps generate this XML file graphically
- The generated XML is used by the ocpirun utility program during the execution of the application onto a platform.





Overview

- The reference application performs FSK modulation/demodulation:
 - Modulation
 - Read Input File -> FSK Symbol Mapper -> Zero-pad -> Pulse Shape -> FM Modulate -> Interpolate
 - Demodulation
 - Decimate -> Demodulate -> Filter -> Baud Track -> Digitize -> Write Output File







Using ocpirun utility

- - Open **;⇔CPI**

- The utility program ocpirun provides a simple way to execute applications
- Usage is: ocpirun app.xml
 - app.xml is a OAS file like the one which will be generated with the IDE in this lab
- The arguments passed to ocpirun can specify how the application is run

Option	Letter	Description	
Dump	d	Dump all readable properties after initialization, and again after execution, to stderr.	
Verbose	V	Be verbose in describing what is happening.	
Log Level	I	For this execution, set the OpenCPI log level to the given level. 8 and 10 are commonly used.	
Time	t	Stop execution after this many seconds. This is useful when there is no definition of "done" for the application.	

More detail on ocpirun can be found in the **OpenCPI Application Development Guide**document

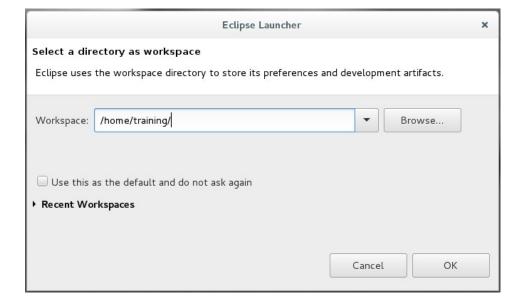
Application Development Flow

- 1. Add components to the OAS
- 2. Specify non-default properties for the components
- 3. Make connections between the components
- 4. Setup deployment platform
- 5. Run and test the application





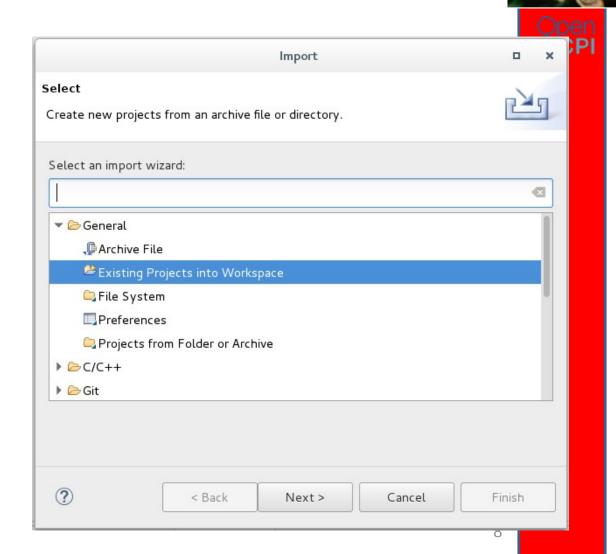
- Start AV IDE and set the workspace to:
 - /home/training/
- Exit the welcome screen



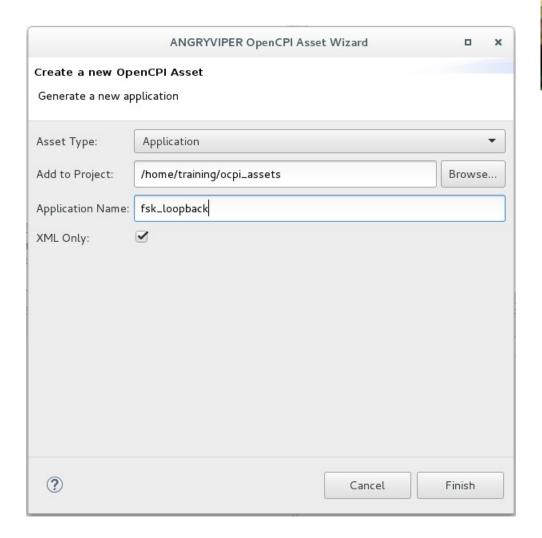




- Import pre-built projects: core and assets
 - The **core** project contains some basic components, including workers to read and write files.
 - Another project called assets is included. It contains a number of components used in this lab.
 - Pre-built projects are located at:
 - ~/ocpi_core
 - ~/ocpi_assets
- To import project into eclipse:
 - File -> Import...
 - "Existing Projects into Workspace"



- Create new application in an existing project
- To create an application
 - In Project Explorer, right click ocpi.assets:
 - New -> OpenCPI Asset Wizard
 - Asset Type: Application
 - Add to Project: ocpi_assets
 - Application Name: fsk_loopback
 - XML only: Yes







- Delete the ocpi.core.nothing component
 - This worker is automatically placed by the framework to ensure the generated OAS can be executed without editing the generated file
- To add a component
 - 1. Within the Project Explorer tab and using the provided table, navigate into the 'specs' directory of the appropriate Project:Library
 - 2. Drag spec file onto Application Editor
 - 3. Recommended: Name component
 - There are 2 instances of fir_real_ssespec.xml. To distinguish the instances, name one 'tx_fir' and the other 'rx_fir'

Component Specs Required				
Name	Project : Library			
file_read_spec.xml	Core Project : components			
mfsk_mapper-spec.xml	Assets: components/comms_comps			
zero_pad-spec.xml	Assets: components/util_comps			
fir_real_sse-spec.xml	Assets: components/dsp_comps			
phase_to_amp_cordic-spec.xml	Assets: components/dsp_comps			
cic_int-spec.xml	Assets: components/dsp_comps			
complex_mixer-spec.xml	Assets: components/dsp_comps			
cic_dec-spec.xml	Assets: components/dsp_comps			
rp_cordic-spec.xml	Assets: components/dsp_comps			
fir_real_sse-spec.xml	Assets: components/dsp_comps			
baudTracking-spec.xml	Assets: components/dsp_comps			
real_digitizer-spec.xml	Assets: components/dsp_comps			
file_write_spec.xml	Core Project : components			

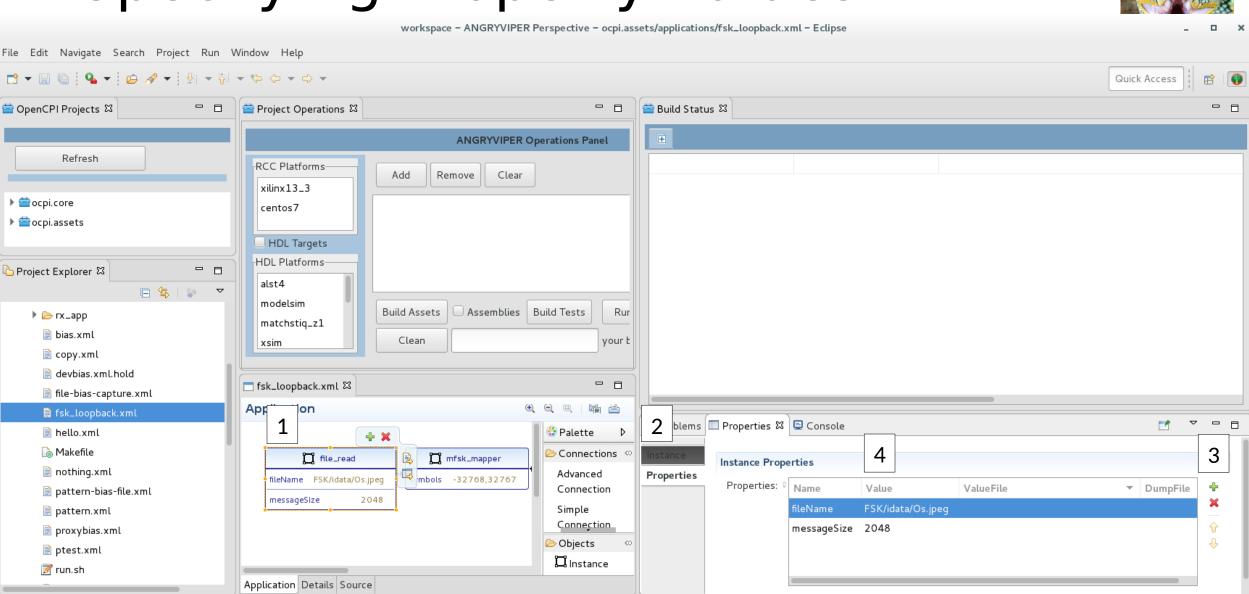


- Set property values
 - To specify a property value (diagram on next slide)
 - 1)Right click on instance -> 'Show in Properties View'
 - 2)Click Properties Tab -> Properties
 - 3)Click green plus sign on right side of tab -> Instance Property
 - 4)Add 'Name' and 'Value'

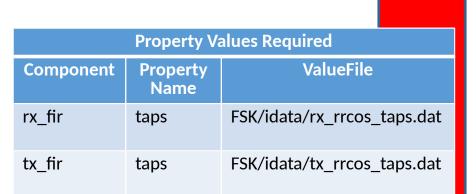


Property Values Required					
Component	Property Name	Value			
file_read	fileName	FSK/idata/Os.jpeg			
file_read	messageSize	2048			
mfsk_mapper	symbols	-32768, 32767			
zero_pad	num_zeros	38			
phase_to_amp_cordic	magnitude	20000			
phase_to_amp_cordic	STAGES	16			
cic_int	R	16			
cic_int	ACC_WIDTH	28			
complex_mixer	enable	False			
cic_dec	R	16			
cic_dec	ACC_WIDTH	28			
baudTracking	SPB	39			
baudTracking	BaudAvrCount	10			
file_write	fileName	out.out			

Specifying Property Values



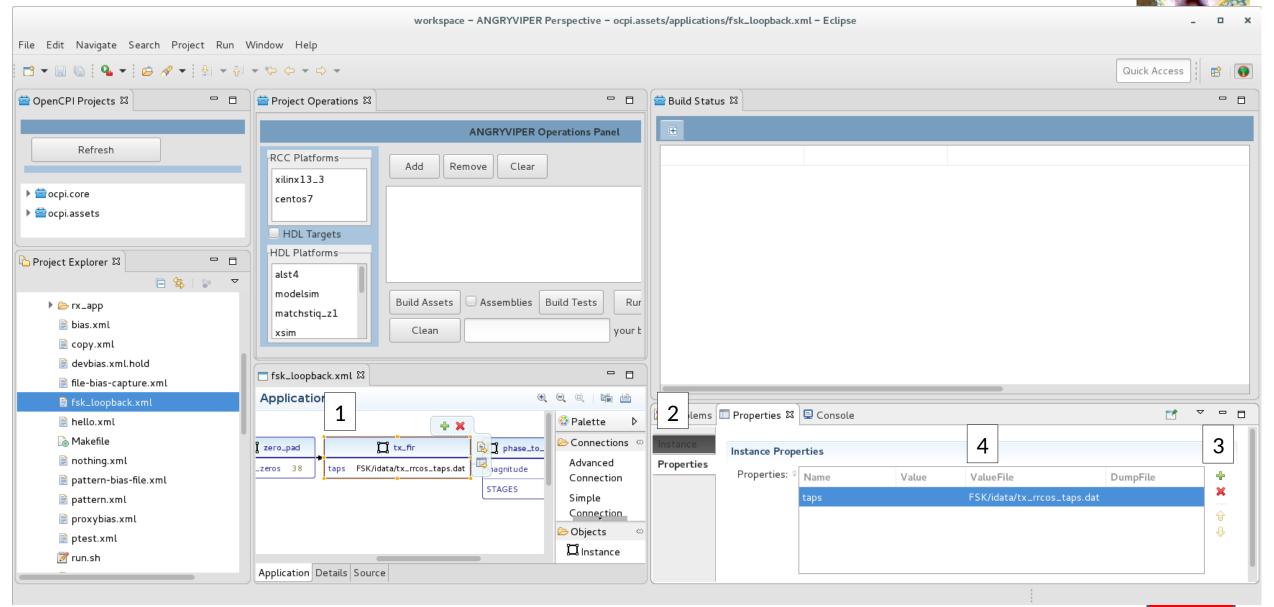
- Set property ValueFiles
- The fir_real_sse components used in this application have a property called 'taps' which are arrays of 64
- Instead of specifying all 64 values in the IDE, we can set an attribute called **ValueFile** which points to a file which contains the values
- To specify a property ValueFile (diagram on next slide)
- 1) Right click on instance -> 'Show in Properties View'
- 2) Click Properties Tab -> Properties
- 3) Click green plus sign on right side of tab -> Instance Property
- 4) Add 'Name' and 'ValueFile'



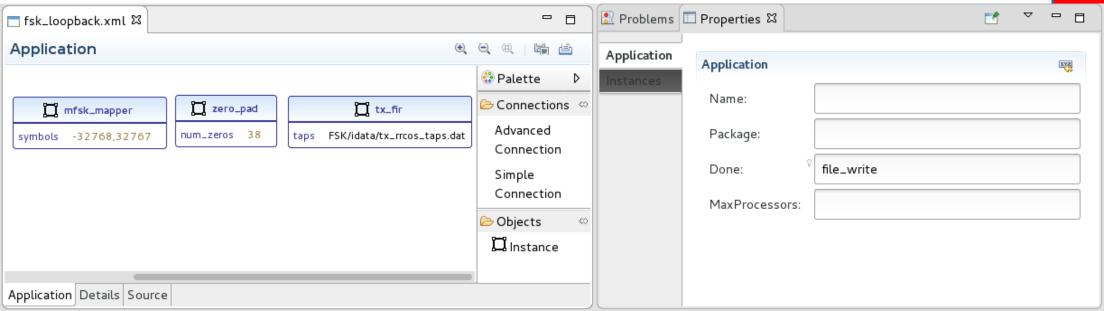




Specifying Property ValueFiles



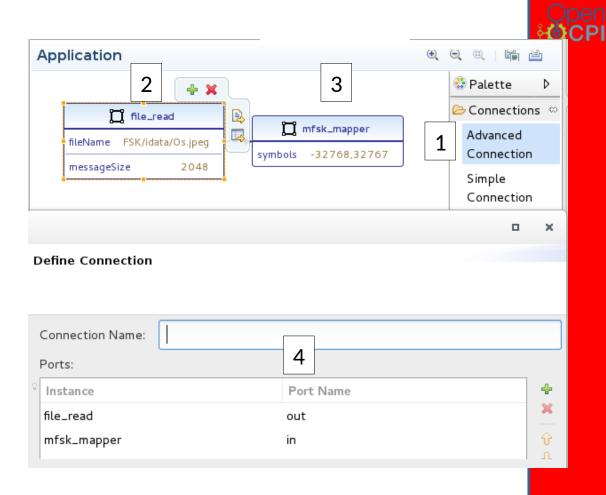
- Specifying Top Level Attributes in OAS
 - Configure the OAS to 'be done' when the file_write component received End-of-File. There is a "top-level" attribute for OAS XML called "Done" used for this purpose
- To set top level OAS attribute:
 - 1. Click on the white space in between instances so none are selected
 - 2. In the property tab, fill in the "Done" field with the desired worker name







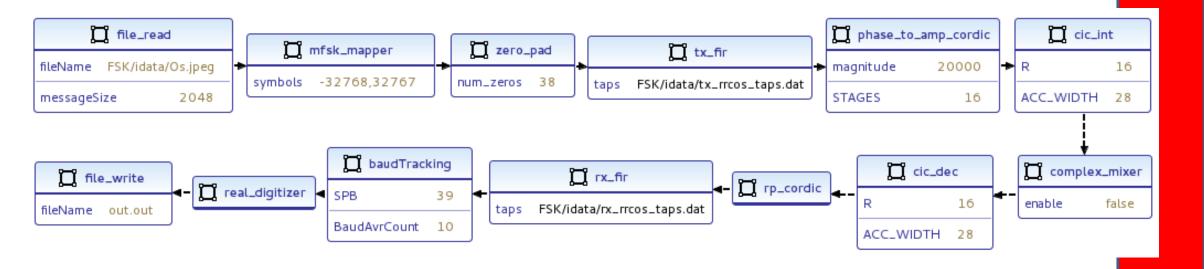
- Make connections
 - See next slide for diagram of required connections
- To make a connection
 - 1. Click "Advanced Connection" on Palette Menu
 - 2. Click originating instance
 - 3. Click destination instance
 - 4. Populate "Port Name" fields for connections
 - All workers in this lab use the default 'out' and 'in' Port Names



End Result







- Setup deployment platform
 - 1. Connect to serial port via USB on rear of Matchstiq Z1 using Host
 - 'screen /dev/ttyUSB0 115200'
 - 2. Boot and login into Petalinux
 - User/Password = root:root
 - 3. Verify Host and Matchstiq Z1 have valid IP addresses
 - For training, they should both be on the same subnet
 - 4. Run setup script on Matchstiq Z1
 - 'source /mnt/card/opencpi/mynetsetup.sh
 Host IP address>'

[screen O: ttyUSBO] File Edit View Search Terminal Help PetaLinux v2013.10 (Yocto 1.4) zyng ttyPS0 zyna login: root Password: login[781]: root login on `ttyPSO' root@zyng:~# . /mnt/card/opencpi/mynetsetup.sh 192.168.21.104 An IP address was detected. Setting the time from time server: time.nist.gov My IP address is: 192.168.21.113, and my hostname is: zynq Running login script. OCPI CDK DIR is now /mnt/net/cdk. Executing /home/root/.profile. No reserved DMA memory found on the linux boot command line. The mdev config has no OpenCPI rules. We will add them to /etc/mdev.conf NET: Registered protocol family 12 Driver loaded successfully. OpenCPI ready for zyng. Discovering available containers... Available containers: # Model Platform OS-Version Arch 0 hdl matchstiq z1 PL:0 xilinx13 3 linux x13 3 rcc0 mount: 192.168.21.104:/home/training/training project failed, reason given by se rver: No such file or directory mount: mounting 192.168.21.104:/home/training/training project on /mnt/training project failed: Bad file descriptor

More detail on this process can be found in the Matchstiq_Z1 Getting Started Guide document

- Open **;©CPI**

- Setup environment on Matchstiq Z1 using OCPI_LIBRARY_PATH
 - The OCPI_LIBRARY_PATH environment variable is used to locate deployable artifacts
 - To deploy this application, 5 artifacts are needed
 - 4 software worker .so's
 - 1. file_read.so
 - 2. file_write.so
 - 3. Baudtracking_simple.so
 - 4. real_digitizer.so
 - 1 HDL container .bitz
 - 1. fsk_filerw_matchstiq_base.bitz
- To set OCPI_LIBRARY_PATH on Matchstiq Z1
 - 'export OCPI_LIBRARY_PATH=/mnt/ocpi_core/exports:/mnt/ocpi_assets/exports'
 - These component instances were added from these component libraries.
 - The directories in this path are searched recursively, so this variable can be as specific or as broad as needed as long as the artifacts are in the path. Broader paths lead to longer search times when running an application
 - The exports directory at the top level of project contains links to artifacts contained in the project

- Run application on Matchstiq Z1 using ocpirun
 - ocpirun is a utility program provided with the Component Development Kit (CDK) for running applications described by OAS XML
- To run application on Matchstiq Z1:
 - 1. Navigate to OAS XML:
 - 'cd /mnt/ocpi_assets/applications'
 - 2. Pass OAS XML to ocpirun:
 - 'ocpirun -v fsk_loopback.xml'
 - ocpirun is a utility program provided with the CDK for running the application
 - Optional arguments to ocpirun:
 - -v : verbose
 - -d: dump property values for all workers before and after running application
 - View output image on Host
 - 'cd /home/training/ocpi_assets/applications'
 - 'eog out.out'



```
File Edit View Search Terminal Help
% cd /mnt/ocpi assets/applications/
% ocpirun -v fsk loopback.xml
Available containers are: 0: PL:0 [model: hdl os: platform: matchstiq z1], 1: rcc
0 [model: rcc os: linux platform: xilinx13 3]
Actual deployment is:
 Instance 0 file read (spec ocpi.core.file read) on rcc container 1: rcc0, using
file read in /mnt/ocpi core/exports/lib/components/rcc/linux-x13 3-arm/file read s
so dated Wed Feb 14 09:38:37 2018
  Instance 1 mfsk mapper (spec ocpi.assets.comms comps.mfsk mapper) on hdl contain
er O: PL:O, using mfsk_mapper/a/mfsk_mapper in /mnt/ocpi_assets/exports/lib/hdl/ass
emblies/fsk filerw matchstig z1 base.bitz dated Tue Feb 13 16:44:59 2018
  Instance 2 zero pad (spec ocpi.assets.util_comps.zero_pad) on hdl container 0: P
L:0, using zero pad-1/a/zero pad in /mnt/ocpi assets/exports/lib/hdl/assemblies/fsk
 filerw matchstig z1 base.bitz dated Tue Feb 13 16:44:59 2018
 Instance 3 tx_fir (spec ocpi.assets.dsp_comps.fir_real_sse) on hdl container 0:
PL:0, using fir real sse/a/tx fir real in /mnt/ocpi assets/exports/lib/hdl/assembli
es/fsk filerw matchstig z1 base.bitz dated Tue Feb 13 16:44:59 2018
  Instance 4 phase to amp cordic (spec ocpi.assets.dsp comps.phase to amp cordic)
on hdl container 0: PL:0, using phase to amp cordic-1/a/phase to amp cordic in /mnt
/ocpi assets/exports/lib/hdl/assemblies/fsk filerw matchstiq zl base bitz dated Tue
 Feb 13 16:44:59 2018
  Instance 5 cic int (spec ocpi.assets.dsp comps.cic int) on hdl container 0: PL:0
 using cic int-5/a/cic int in /mnt/ocpi assets/exports/lib/hdl/assemblies/fsk file
rw matchstig zl base.bitz dated Tue Feb 13 16:44:59 2018
  Instance 6 complex mixer (spec ocpi.assets.dsp comps.complex mixer) on hdl conta
iner 0: PL:0, using complex mixer/a/complex mixer in /mnt/ocpi assets/exports/lib/h
dl/assemblies/fsk filerw matchstig zl base.bitz dated Tue Feb 13 16:44:59 2018
  Instance 7 cic_dec (spec ocpi.assets.dsp_comps.cic_dec) on hdl container 0: PL:0
 using cic dec-5/a/cic dec in /mnt/ocpi assets/exports/lib/hdl/assemblies/fsk file
rw matchstig z1 base.bitz dated Tue Feb 13 16:44:59 2018
  Instance 8 rp cordic (spec ocpi.assets.dsp comps.rp cordic) on hdl container 0:
PL:0, using rp cordic/a/rp cordic in /mnt/ocpi assets/exports/lib/hdl/assemblies/fs
k filerw matchstiq z1 base bitz dated Tue Feb 13 16:44:59 2018
 Instance 9 rx fir (spec ocpi.assets.dsp_comps.fir_real_sse) on hdl container 0:
PL:0, using fir real sse/a/rx fir real in /mnt/ocpi assets/exports/lib/hdl/assembli
es/fsk_filerw_matchstiq_z1_base.bitz dated Tue Feb 13 16:44:59 2018
  Instance 10 baudTracking (spec ocpi.assets.dsp comps.baudTracking) on rcc contain
er 1: rcc0, using Baudtracking simple in /mnt/ocpi assets/exports/lib/dsp comps/rcc
/linux-x13 3-arm/Baudtracking simple s.so dated Tue Feb 13 15:16:23 2018
 Instance 11 real digitizer (spec ocpi.assets.dsp_comps.real_digitizer) on rcc con
tainer 1: rcc0, using real digitizer in /mnt/ocpi assets/exports/lib/dsp comps/rcc/
linux-x13 3-arm/real digitizer s.so dated Tue Feb 13 15:16:24 2018
  Instance 12 file write (spec ocpi.core.file write) on rcc container 1: rcc0, usin
g file write in /mnt/ocpi core/exports/lib/components/rcc/linux-x13 3-arm/file writ
e s.so dated Wed Feb 14 09:38:40 2018
Application XML parsed and deployments (containers and implementations) chosen
Application established: containers, workers, connections all created
Communication with the application established
Application started/running
Waiting for application to finish (no time limit)
Application finished
```