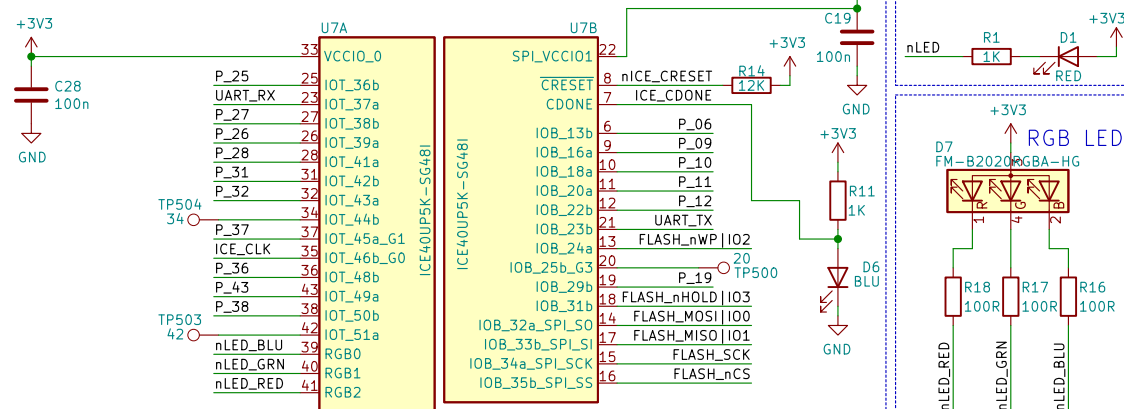
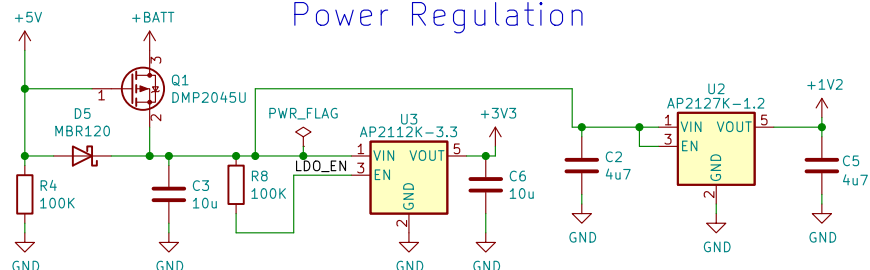


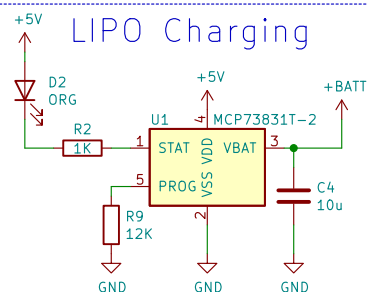
## iCE40UP5K FPGA IO



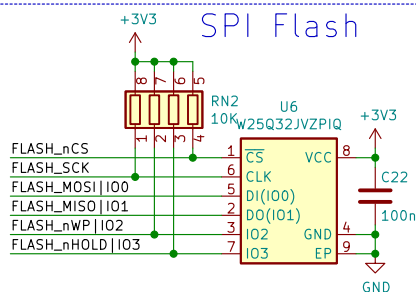
## Power Regulation



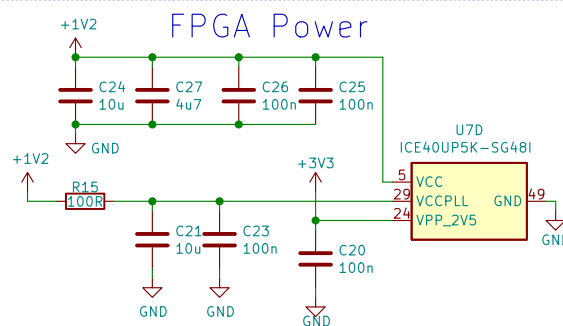
## LIPO Charging



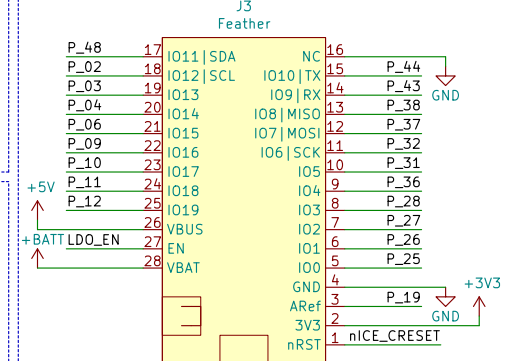
## SPI Flash



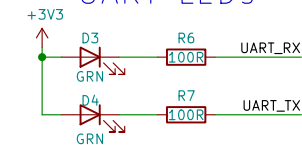
## FPGA Power



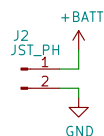
## Feather Footprint



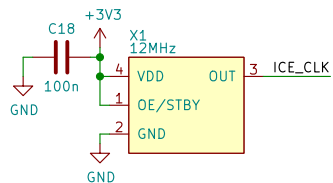
## UART LEDs



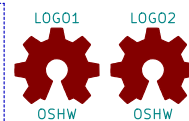
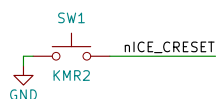
## LIPO JST Connector



## Oscillator



## Reset Button



LOG03  
Josh Name

- FID1  
Fiducial
- FID3  
Fiducial
- FID2  
Fiducial

**Josh Johnson**

Sheet: /  
File: iCE40-fpga.kicad\_sch

**Title: iCE40 Feather**

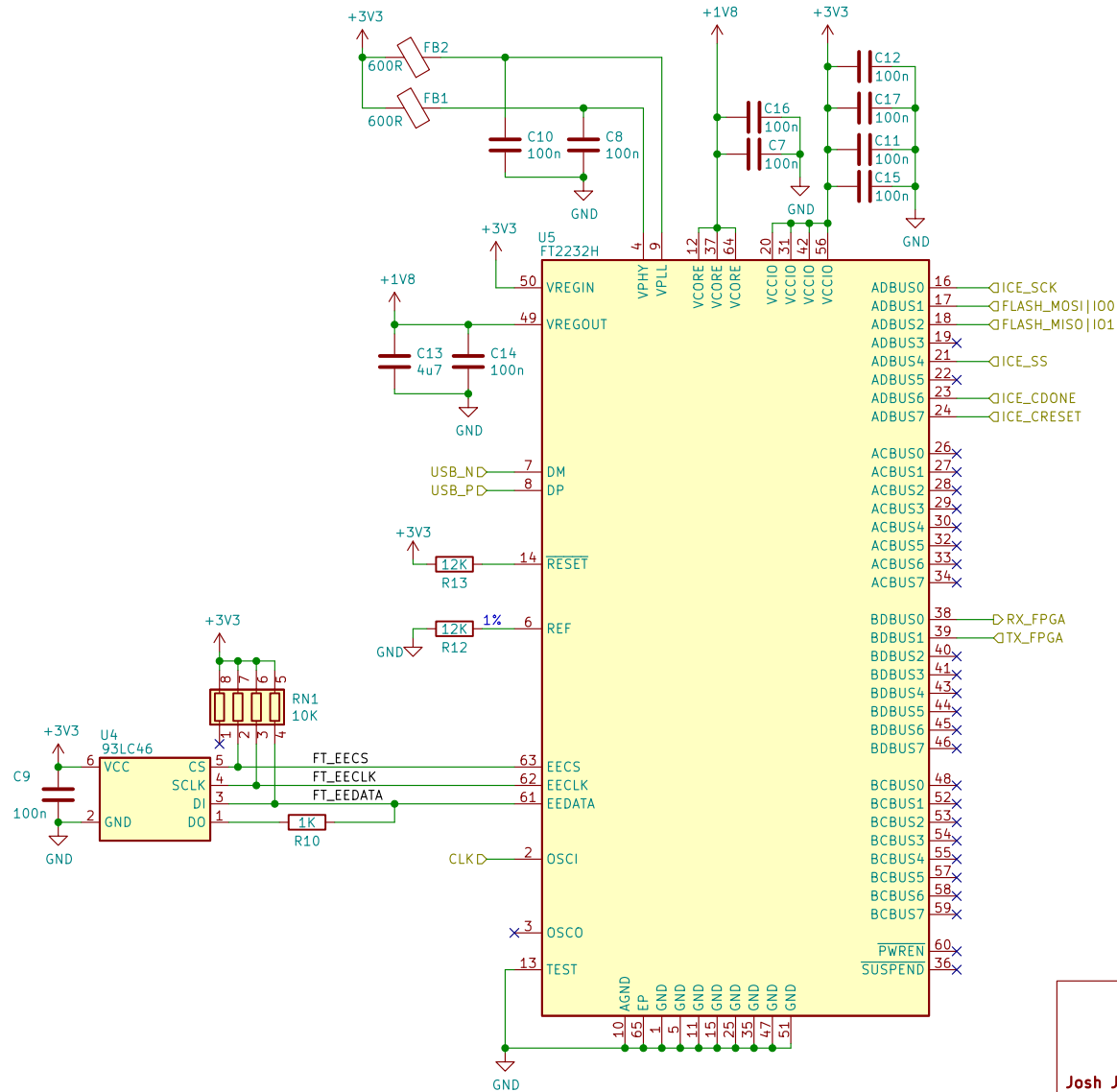
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Rev: 0.2

Id: 1/2

# FTDI Programmer / UART



Josh Johnson

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**Title: iCE40 Feather**

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