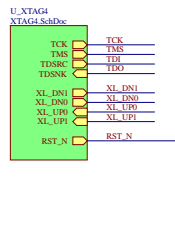
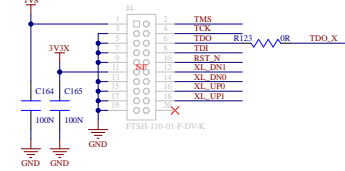


INTEGRATED XTAG4



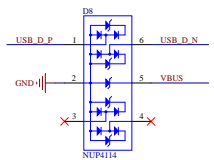
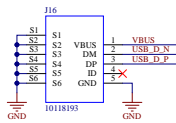
XSYS2 DEBUG TARGET CONNECTOR OPTION FOR USE WITH EXTERNAL XTAG4



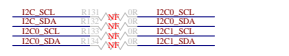
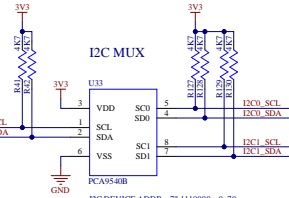
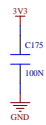
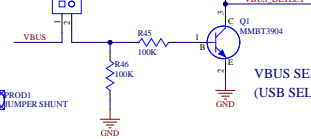
CURRENT SENSE

J15 68001-204HLF

USB CONNECTOR

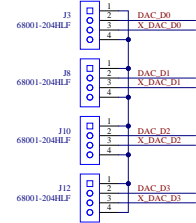


VBUS SENSE (USB SELF POWERED)



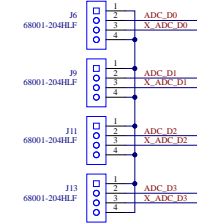
DAC I2S/TDM CONFIG

I2S MODE: SET ALL 2-3
TDM MODE: SET ALL 1-2, TDM SOURCE 3-4

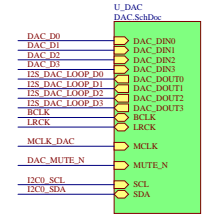


ADC I2S/TDM CONFIG

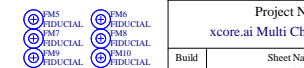
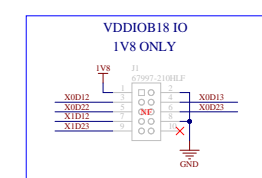
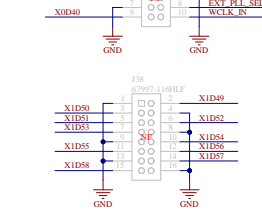
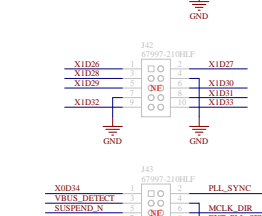
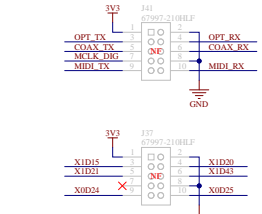
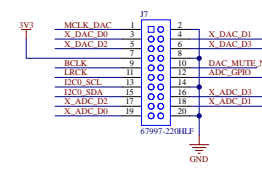
I2S MODE: SET ALL 2-3
TDM MODE: SET DATA 0 AND 2 1-2, TDM DESTINATION 3-4

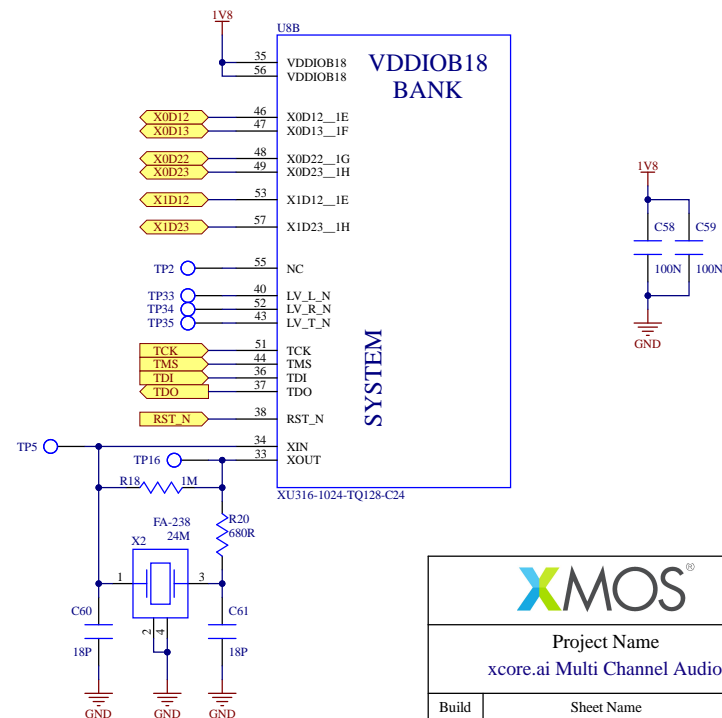
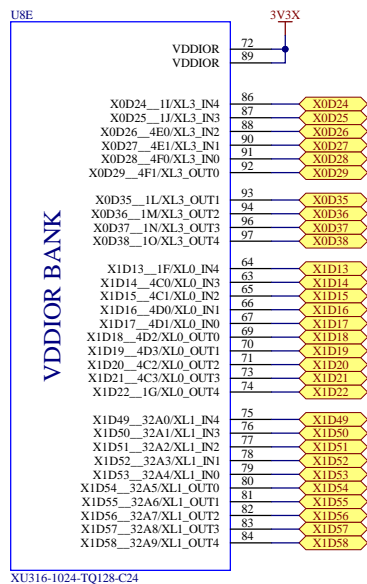
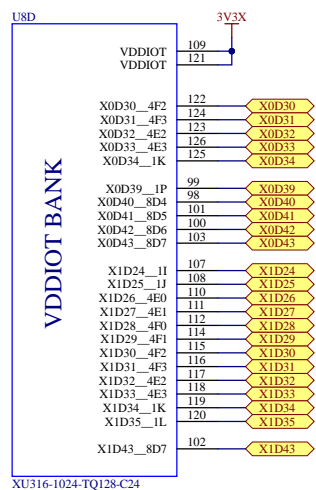
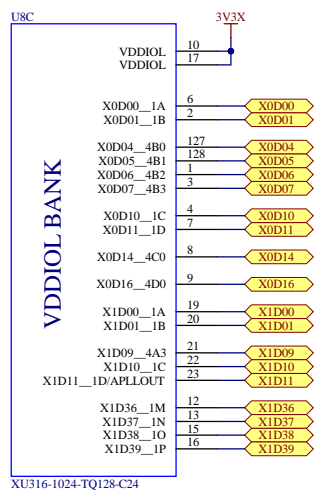
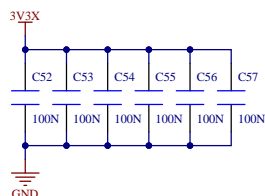
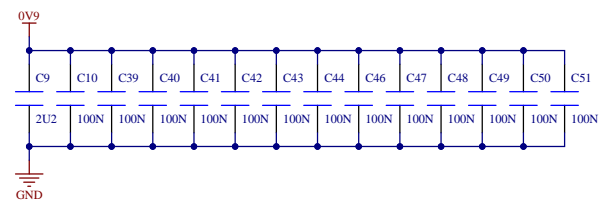
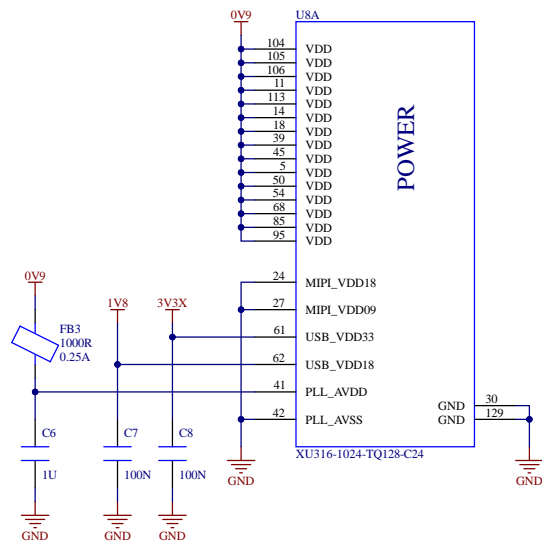
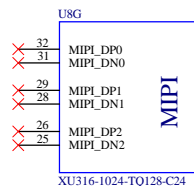


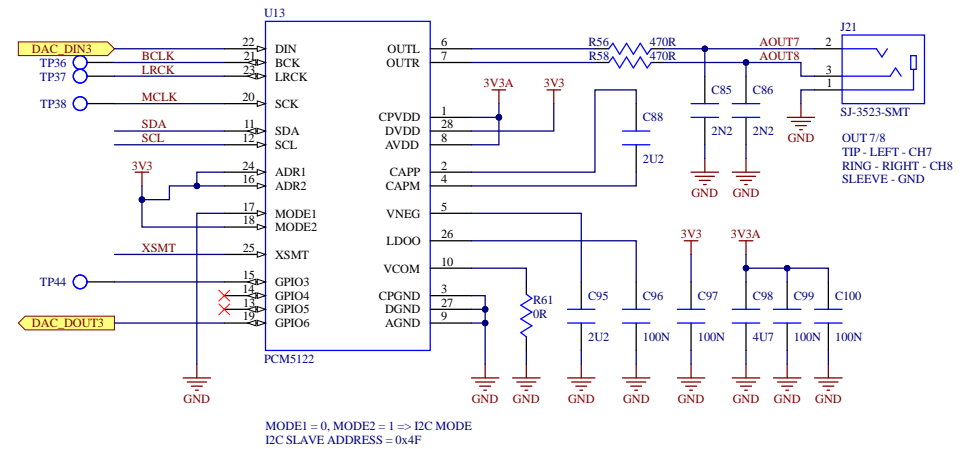
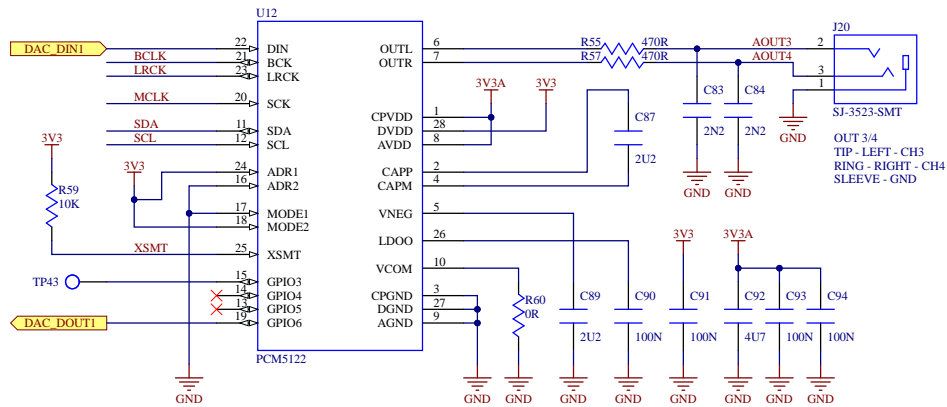
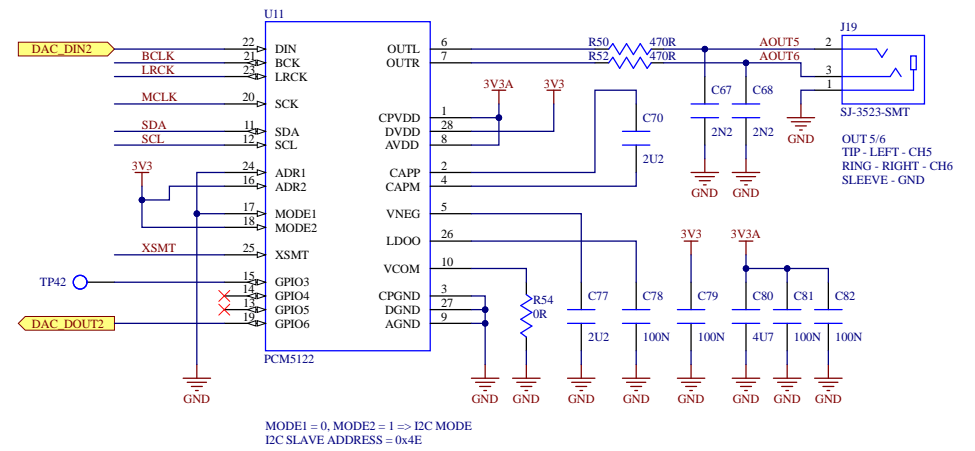
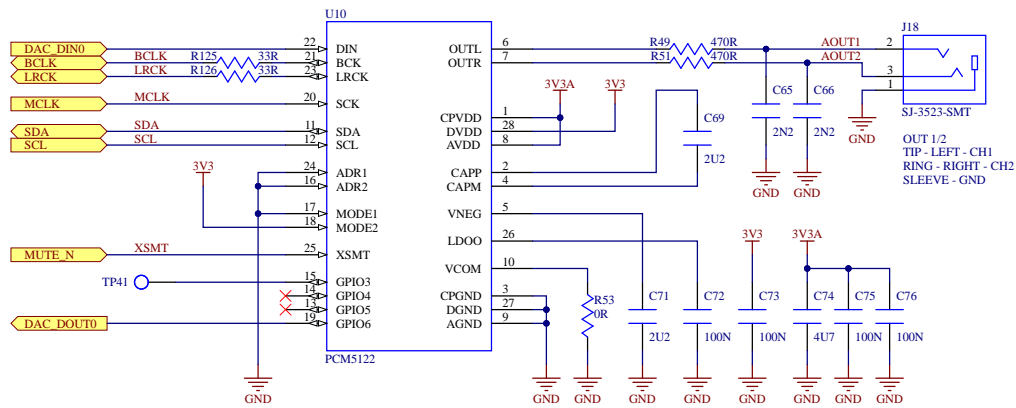
I2S DATA LOOPBACK FOR SOFTWARE TEST



EXPANSION/TESTPOINTS





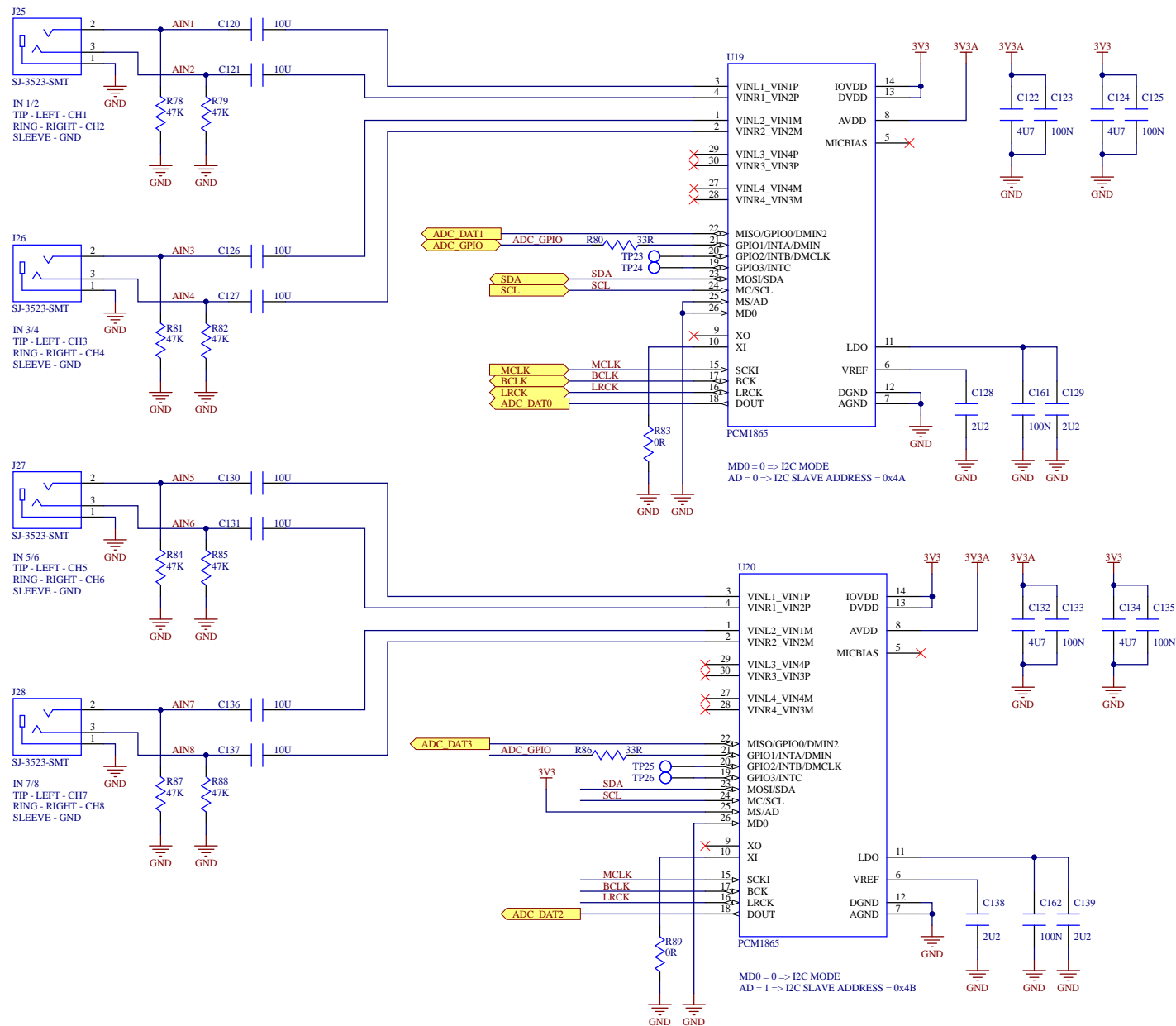


DAC DOUT IS LOOPBACK I2S DATA FOR TESTING
MUTE_N IS DIGITAL SOFT MUTE



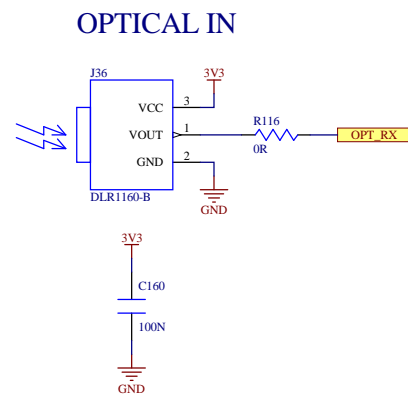
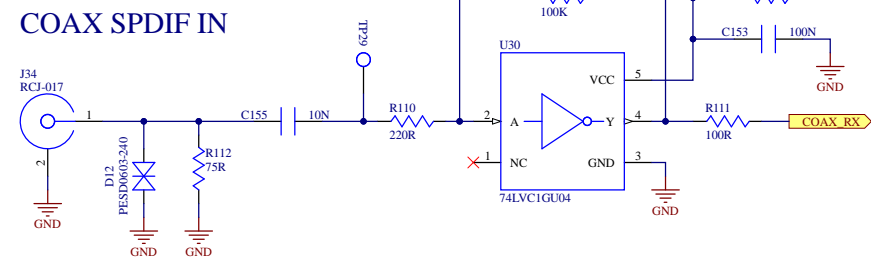
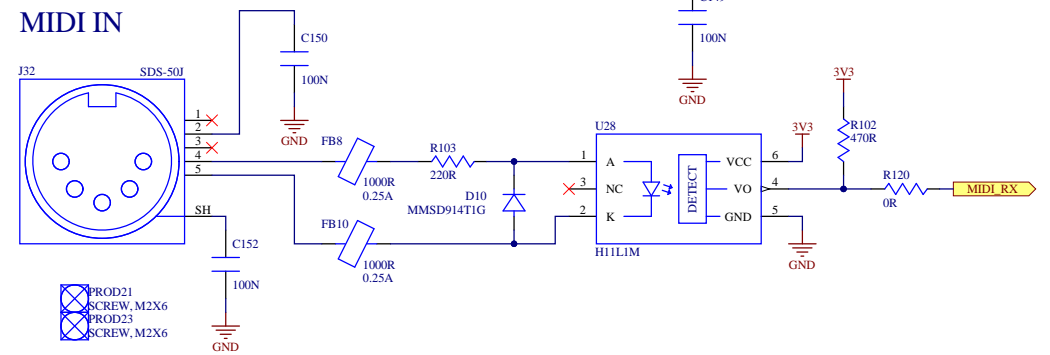
Project Name
xcore.ai Multi Channel Audio

Build A	Sheet Name DAC.SchDoc	Rev 1V1
Date 17/08/2022	Sheet 3 of 8	

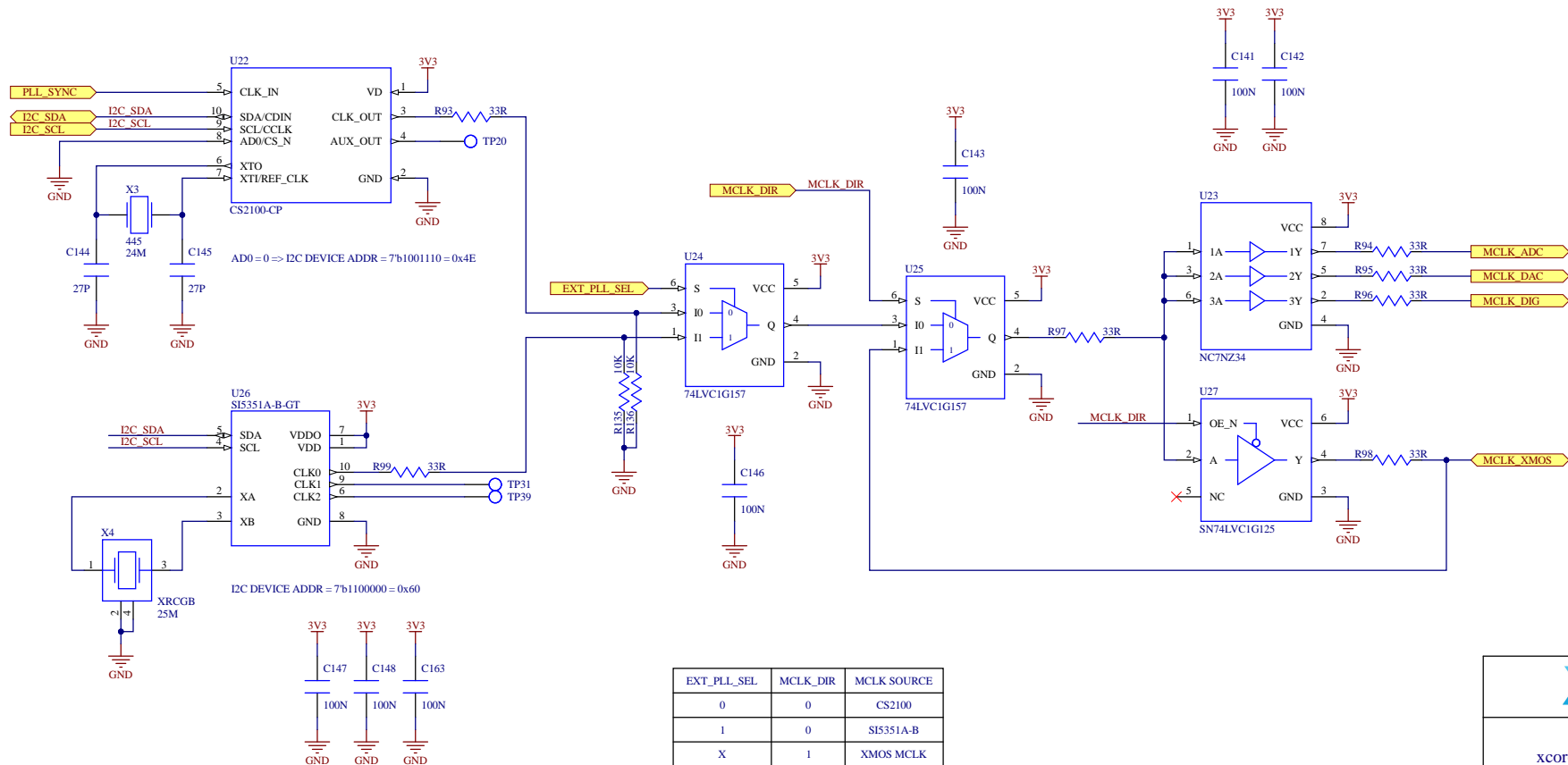
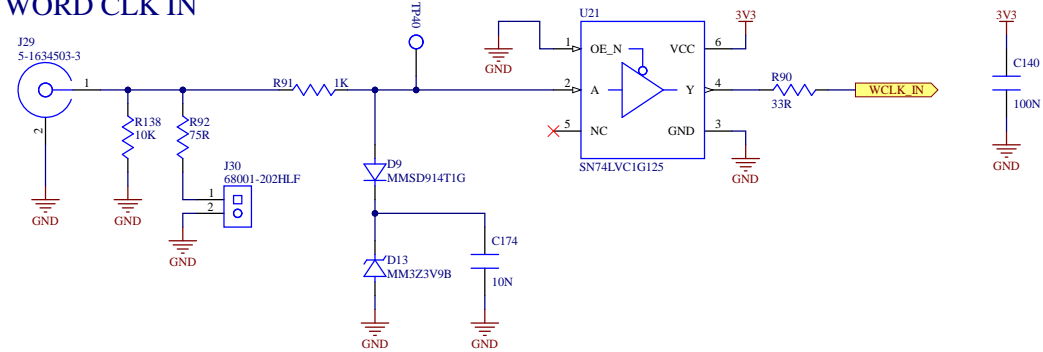


Project Name
xcore.ai Multi Channel Audio

Build A	Sheet Name ADC.SchDoc	Rev 1V1
Date 17/08/2022	Sheet 4 of 8	



WORD CLK IN

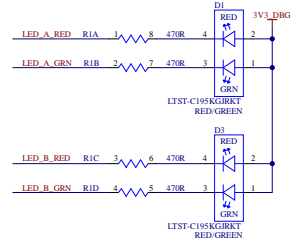
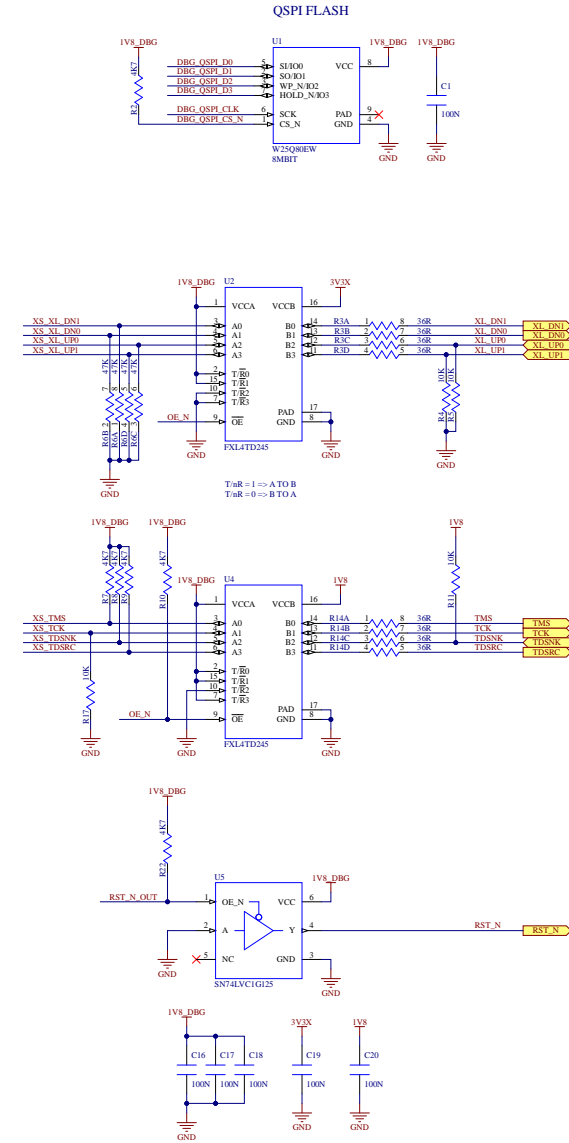
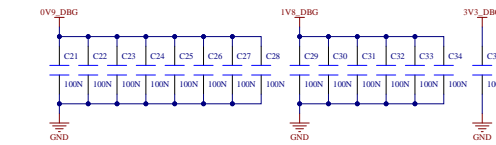
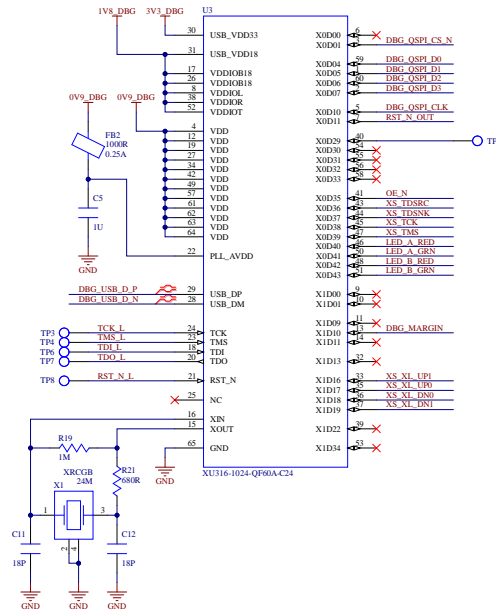
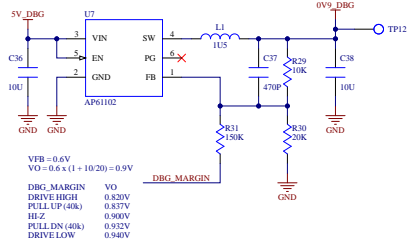
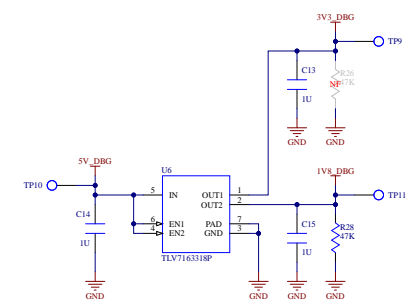
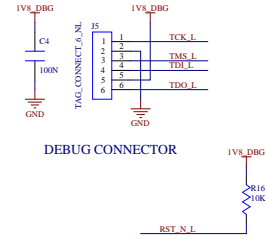
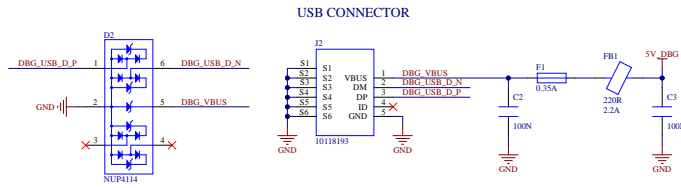


EXT_PLL_SEL	MCLK_DIR	MCLK SOURCE
0	0	CS2100
1	0	SI5351A-B
X	1	XMOS MCLK

XMOS®

Project Name
xcore.ai Multi Channel Audio

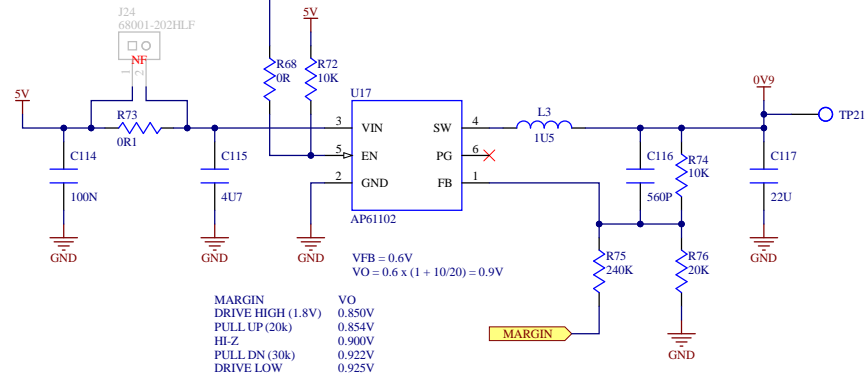
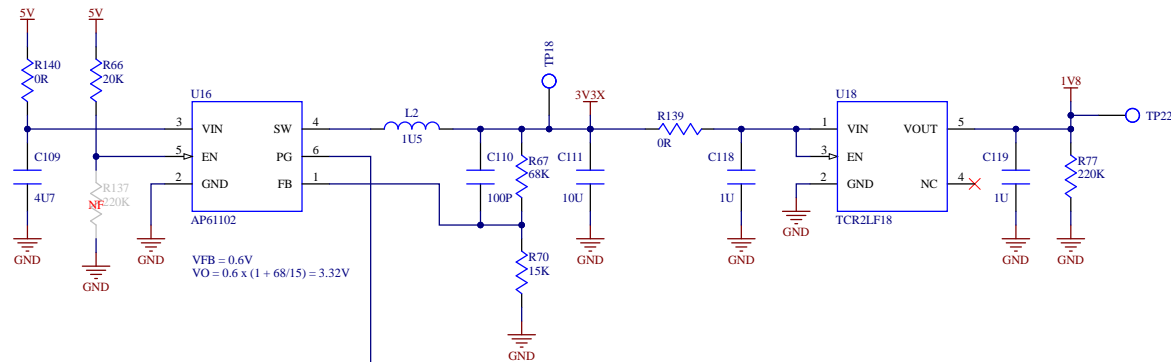
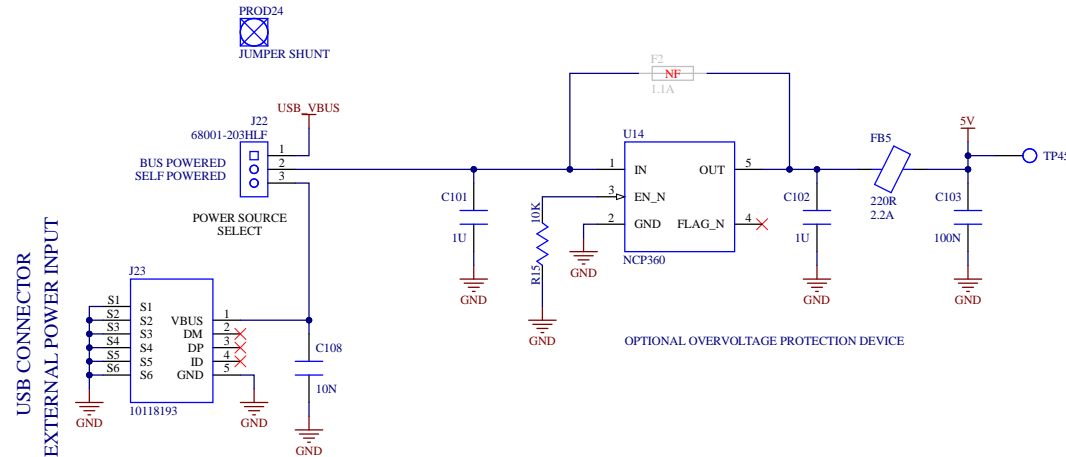
Build A	Sheet Name Clocks.SchDoc	Rev 1V1
Date 17/08/2022	Sheet 6 of 8	



XMOS

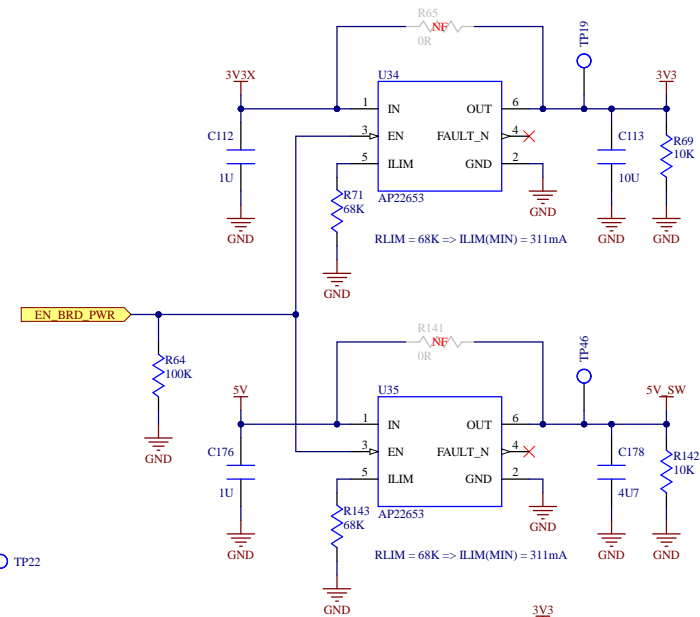
Project Name
xc0re.ai Multi Channel Audio

Build	Sheet Name	Rev
A	XTAG4.SchDoc	1V1
Date	17/08/2022	Sheet 7 of 8

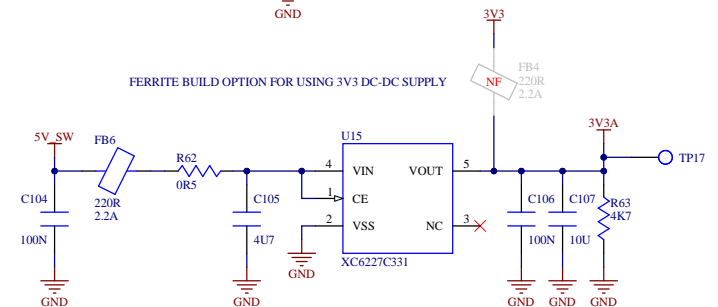


CORE SUPPLY CAN OPTIONALLY BE CONTROLLED
TO A LOWER VOLTAGE TO REDUCE POWER
CONSUMPTION IN USB SUSPEND

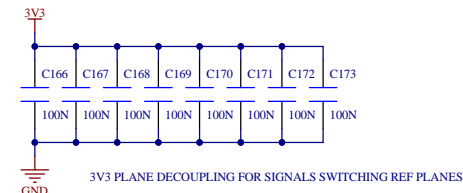
MAIN BOARD SUPPLIES SWITCHED TO REDUCE
POWER CONSUMPTION IN USB SUSPEND



FERRITE BUILD OPTION FOR USING 3V3 DC-DC SUPPLY



3V3A - ADCs 31mA typ. x2 + DACs 32mA x4 max = ~200mA
Pd = (5-3.3)*0.2 = 340mW



XMOS®

Project Name
xcore.ai Multi Channel Audio

Build A	Sheet Name Power.SchDoc	Rev 1V1
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