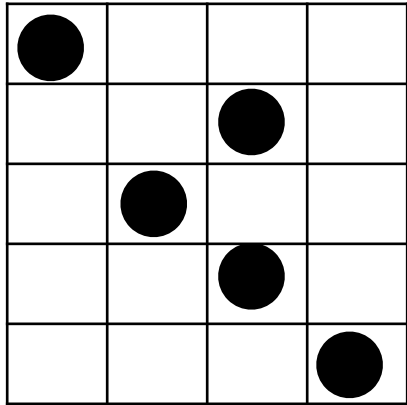


**Baseline
Working
Memory
Span Tasks**

Dots ↔ Letters



JBC

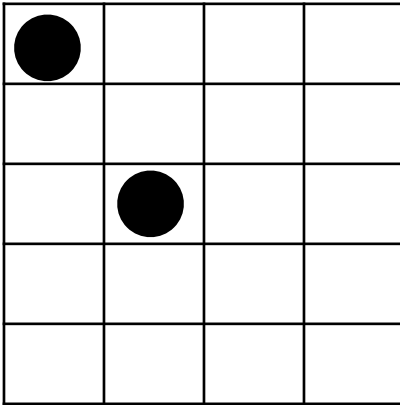
30 trials

30 trials

**Dual Task
Condition 1**

Low
Load

Dots ↔ Letters



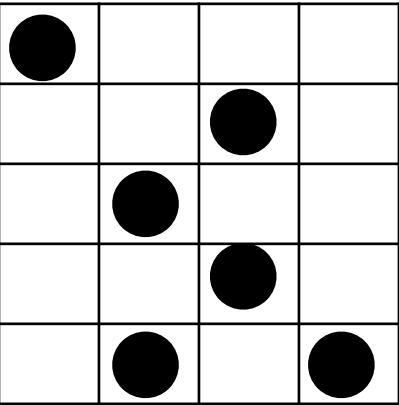
DG

15 trials

15 trials

High
Load

Dots ↔ Letters



BDJGKL

15 trials

15 trials

**Dual Task
Condition 2**

**Dual Task
Condition 3**

↔ Counterbalanced

Note: Dual task conditions differed by arithmetic expressions
(no-spacing, spacing-varying, spacing-varying with parenthesis)