A strategy for lower-bounding clique detection, combining Shannon's counting argument with clique covers of random hypergraphs

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October 6, 2019

Abstract

Shannon's function-counting argument [12] showed that some Boolean functions have exponential circuit complexity, but doesn't provide a specific example of such a hard-to-compute function. A simple modification of that argument shows that detecting a randomly-chosen subset of the k-vertex cliques in an n-vertex graph requires, on average, $\Omega(n^{k/2})$ NAND gates. This doesn't directly bound the complexity of detecting *all* of the cliques. However, we can view a random subset of cliques as a randomly-chosen k-regular hypergraph on n vertices. If we can cover such a hypergraph with large enough cliques, and we could detect cliques with few enough gates, we would contradict the average-case counting bound described above. Probablistic graph theory arguments [3] similar to [4] allow bounding the number of cliques needed to cover a random hypergraph. This provides a strategy for lower-bounding clique detection. We suggest several possible ways of covering hypergraphs with cliques, and numerically evaluate this bound for small graphs, finding that it barely gives a positive bound.

I am writing this up on the assumption that the modification to Shannon's counting argument (section 1) is not new. It doesn't seem to give a bound larger than 1. I'm hopeful that it is correct and/or that it has useful bits, or that someone can point me to similar lower-bound attempts; or, if it's wrong, that someone can point out the flaw(s).

Contents

1 A counting bound

The first argument is basically a function-counting argument.

1.1 Background: lower bounds from function counting

It's long been known that computing *some* function of a bit-string requires exponentially large circuits [12]. If there are m inputs to a circuit, then there are 2^{2^m} possible functions from the m-input bitstring to a one-bit output. Each of these functions, being different, must have a different circuit.

If we assume the circuit is made of NAND gates, and has g gates, then the circuit could have at most gm wires from inputs to gates, and $\binom{g}{2}$ wires from gates to gates. We can view the possible circuits as a bitmask, containing a 1 everywhere a gate is connected to an input (or another gate), and 0 everywhere else.

Theorem 1.1. *Consider functions from m bits to one bit of output. This means that, with g gates, we can represent at most $2^{gm+\binom{g}{2}}$ different boolean functions (with m bits of input, and one bit of output).*

Proof. The number of possible wires which are there, or not, is $gm + \binom{g}{2}$, which bounds how many possible circuits there are. Some of these circuits compute the same function. However, there can't be any more than this many circuits with this many wires. □

This means that if we have a large set of functions, and we know the size of the set of functions, then we know that at least *one* of them requires a large number of gates. (Knowing *which* function requires a lot, or many, gates is still an issue).

Consider functions from m bits to one bit of output. Let g be the number of gates, and w be the number of wires. Solving for the number of gates:

$$\begin{aligned} w &= mg + \binom{g}{2} \\ &= mg + g(g-1)/2 \\ &= mg + (g^2 - g)/2 \\ &= 0.5g^2 + (m - 0.5)g \\ 0 &= 0.5g^2 + (m - 0.5)g - w \end{aligned}$$

We solve the quadratic formula (writing $b = m - 0.5$ for simplicity), keeping only the non-imaginary root.

$$\begin{aligned} g &= -b \pm \sqrt{b^2 + 2w} \\ &= \sqrt{2w + b^2} - b \end{aligned}$$

Thus, given a set of functions, we know that at least one of them requires some number of gates.

1.2 Bounding the average number of gates

We can also count the total number of functions from m input bits to one output bit, using up to g NAND gates, as

$$\sum_{i=0}^{g-1} 2^{m+i} \quad = \quad 2^{m+g} - 2^m$$

If we're counting circuits with up to g gates, then some of the circuits have fewer than g gates. This somewhat complicates the book-keeping. However, *most* of the circuits have g gates. (Indeed, well over half, since each additional gate adds many potential wires). Because of this, I think that the average case bound is just one fewer gates than the worst-case bound.

1.3 Counting CLIQUE-like functions

We now consider NAND gate circuits (with any fan-in) which find k-cliques in n-vertex graphs.

We consider the set of "buggy" 6-clique finders. Maybe the circuit correctly finds all the cliques. Or maybe it finds all of the cliques except $K_{1..6}$, or it misses half the cliques, or finds none (and always outputs 0), or maybe it only successfully finds $K_{1,3,4,5,7,8}$, or whatever. More formally (and generally), we define a set of functions (*not* circuits):

Definition 1.1. BUGGY-k-CLIQUE(n) is the set of functions which recognize any set of K_ks. That is, for each set A of K_ks, BUGGY-k-CLIQUE(n) contains a function which is 1 if the input contains any $K_6 \in A$, and 0 otherwise.

This clearly includes HAS-k-CLIQUE (which finds all the cliques).

These functions are all distinct. If $f_1, f_2 \in$BUGGY-k-CLIQUE(n), then there's some K_k such that if y is the graph with *only* 1's in that K_k (and 0's elsewhere), $f_1(y) = 0$ and $f_2(y) = 1$.

Of course, many of these functions are quite similar (e.g. all but one of them output a 1 when you feed in all 1's). However, they're all slightly different.

Theorem 1.2. *BUGGY-k-CLIQUE(n) contains $2^{\binom{n}{k}}$ distinct functions.*

Proof. That's how many subsets of the K_ks there are. □

Although $2^{\binom{n}{k}}$ is a fairly large number, it's still comfortably less than $2^{2^{\binom{n}{2}}}$, the number of boolean functions on the $\binom{n}{2}$ input wires (one per edge).

1.3.1 But *which* function requires many gates?

So, there are $2^{\binom{n}{k}}$ different functions. How many NAND gates do these take? (We onsider NAND gate circuits (with any fan-in) which find k-cliques in n-vertex graphs, as a circuit with $\binom{n}{2}$ inputs)

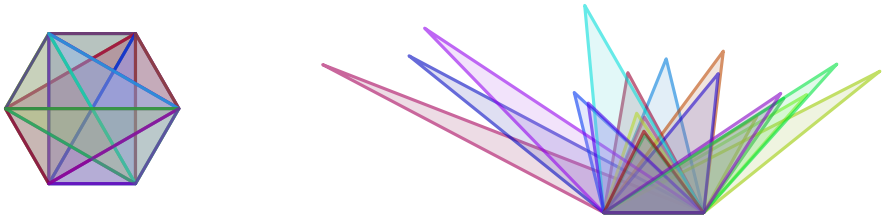
Applying Theorem 1.1, we know that at least one of the circuits requires $\sqrt{2\binom{n}{k/2} + b^2} - b = \Omega(n^{k/2})$ NAND gates (where $b = \binom{k}{2} - 0.5$).

Why doesn't this bound HAS-k-CLIQUE? Because we don't know that the circuit which finds *all* of the K_ks, is one of these larger circuits. As far as what I've shown thus far goes, it could be harder to find some weird subset of the K_ks.

Indeed, as far as what I've formally shown goes, the problem which needs the most NAND gates could be finding a single K_k! That's easily ruled out (because that only needs one NAND gate, plus the output gate).

1.4 Which sets of cliques are hard to find?

The hardness of these functions depends on how the cliques they find are laid out. For instance, here are two sets of 20 triangles ("K_3s"), arranged in different ways. Although we only show 20 triangles here, we can imagine similarly-structured graphs with more triangles.



Triangles can be detected using matrix multiplication [7], and there are fast algorithms known for matrix multiplication [13] [15], so the triangles on the left can be detected using fewer than one NAND gate per triangle (for large enough input graphs). [1]

On the other hand, if the triangles overlap less (as on the right), then to detect all of the triangles, we will definitely need at least one gate per triangle. To see this, note that if we feed in a 0 to the input for one of the edges unique to some triangle, then any gate connected to that edge will only output a 1. We can repeat this for each of the triangles, constructing a series of strictly smaller circuits (this is essentially what I think is called the "method of restrictions" FIXME CITE).

It seems intuitive that, in some sense, finding more cliques should be harder. Indeed, since we're using NAND gates, we know that finding any non-empty subset of cliques is strictly harder than finding *some* other smaller set of cliques (namely, the set you get after feeding in 0's to all the edges connected to some vertex). Unfortunately, this doesn't help much in the case of CLIQUE. If we have a circuit which finds 6-cliques on 100 vertices, and feed in 0's to all edges connected to one vertex, we end up with a strictly smaller circuit

[1] For smaller graphs, such as this with six vertices, it appears that 21 NAND gates are required [1], although this proof hasn't been published or rigorously checked.

which finds 6-cliques on 99 vertices! We still haven't connected the complexity of CLIQUE with the complexity of all those "buggy" circuits which find exactly half the cliques.

1.5 Counting slightly larger sets of functions

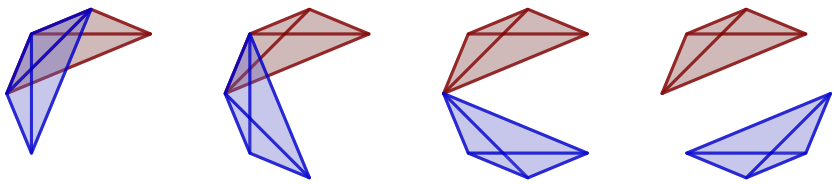
We can also construct somewhat larger sets of functions. For instance, suppose that, rather than detecting or ignoring each clique, we assign to each edge either 1, 0, or X, with this interpretation:

- 1: "If any of these cliques is present, then output 1..."
- 0: "...unless one of these cliques is present, in which case output 0."
- X: "(Ignore whether this clique is present)."

There are $3^{\binom{n}{k}}$ such strings. However, those consisting only of 0's and X's always output 0, and so are indistinguishable, so there are only $3^{\binom{n}{k}} - 2^{\binom{n}{k}}$ distinct functions.

The lower bound on the average hardness of computing these functions is slightly higher. However, this doesn't seem to help that much.

Note that distinguishing the functions requires that we be able to have at least two distinct cliques either be present or absent. With two cliques, we can do this by feeding in 1's to any edges shared by the cliques, and then feeding in 0's or 1's to the remaining edges:



However, it seems hard to push this to arbitrary functions of "which cliques are present", because they start to overlap.

2 Bounds based on covering hypergraphs with cliques

Suppose we consider all $2^{\binom{n}{k}}$ functions which find some subset of k-cliques in an n-vertex graph, and for each, find the circuit with the fewest NAND gates which computes it. If we add up the total number of gates in all of these circuits, it's a lot. Since they're all distinct functions, the function-counting bound gives a lower-bound on the total number of gates used (as from the above, we know how many functions from m inputs to one output we can implement using g NAND gates).

Note that, for a given set of k-cliques S, if $S = A \cup B$, and we have circuits to find the k-cliques in A and B, then by ORing them together, we obtain a circuit for S. (If we use NAND gates, we save a gate. Given circuits for A and B, to construct a circuit for $A \cup B$, we

can disconnect the wires from B's last gate, and connect them to A's last gate. A's output now computes $A \cup B$, and B's last gate is no longer needed). (FIXME add a figure?)

Suppose we had small circuits for detecting all of the k-cliques in an r-vertex graph, for $k \leq r \leq n$. Then, one way to generate a circuit which finds an arbitrary subset of k-cliques would be to decompose it into a set of cliques (of size between k and n), and OR them all together.

Suppose we do this, for *all* of the $2^{\binom{n}{k}}$ possible subsets of k-cliques. We then add up the total number of clique detectors of each size that we've used, and total up how many NAND gates are in each of these. We had better have used at least as many NAND gates as the Shannon counting bound says we needed, to implement all of those functions.

To be more concrete: suppose we devise a way of covering all the k-cliques in an n-vertex graph. Instead of counting across all possible sets of cliques, we will consider the average, for a set of cliques chosen uniformly at random. Let k be the size of cliques we're bounding the complexity of finding, choose some covering strategy, and let:

- A_{ij} = the average number of j-vertex hypercliques ("K_k^j"s) in an i-vertex graph
- x_j = the number of gates needed to find k-cliques on j vertices
- b_i = the Shannon bound on the number of gates needed (on average) to find any set of k-cliques on i vertices

We have

$$Ax \geq b$$

This gives a lower bound on x. The bound depends crucially on how efficiently we can cover the hyperedges with hypercliques – in order for x to be large, we need the entries of A to be as small as we can manage. (Presumably, we need as many of the cliques as possible to be large, and to not overlap much). We can choose as many different rows of this as we like. However, adjacent rows will presumably give very similar bounds, so it may be simpler to only use a subset of rows. It's not obvious that this even bounds x to be positive.

2.1 Anecdotal rephrasing

(I include the following hypothetical anecdotal explanation, partly because I think it's slightly funny, but also because I think it may provide at least some intuition).

In some alternate reality, it is the dawn of the era of TTL integrated circuits. Yoyodyne Corporation has developed a line of integrated circuits to detect any possible set of K_4s in a graph with nine vertices. These chips, the 74LSC series, are available in a convenient 40-pin DIP package, and are fabulously successful. (I have no idea why there would be huge demand for these, but let's say that there is). The only problem is that Yoyodyne's chip factories are having trouble producing enough chips.

Management contemplates how to re-envision productivity and enable synergy. (They are, after all, producing $2^{\binom{9}{4}} = 2^{126}$ different sorts of chips). They decide that they need to stop producing so many kinds of chips. After great deliberation, they decide to cut production to *just* the chips which find all possible K_4's on graphs with between four and nine vertices (namely, the 74LSC4 through 74LSC9). In order to meet the bizarre but humongous demand for finding arbitrary sets of K_4s, they will develop a line of tiny circuit boards, each containing several clique detectors, ORed together.

There is still the problem of designing these circuit boards. However, they only have to do this once for each possible set of K_4s. They hire a large team of software engineers and graph theorists. By renting many years of CPU time from AltaVista and MySpace, they are able to design the optimal circuit boards.

Note that, originally, Yoyodyne was using some number of NAND gates to cover all the $2^{\binom{9}{4}} = 2^{126}$ possible sets of K_4s. The Shannon counting argument gives a lower bound on how many NAND gates were used in all of these chips. (Specifically, it says that at least four NAND gates were used for at least one graph, since $36 + 37 + 38 + 39 = 150 \geq 126$).

After the redesign, if we count up the number of 74LSC4 through 74LSC9 gates which were used, and consider the number of NAND gates in each of these, the total had better *also* be larger than the bound from the Shannon counting argument.

2.2 How do we cover the graphs?

Thus, we are faced with the problem of covering all possible $2^{\binom{n}{k}}$ sets of cliques with a small number of large cliques. We can think of a set of possible k-cliques as a k-regular hypergraph on n vertices. Clique cover (for non-hypergraphs) is a well-known NP-complete problems [8]. In our case, we are covering hypergraphs with hypercliques. Also, we aren't concerned with the complexity of finding the covering. However, we do need to know how many hypercliques of various sizes are needed.

Here, we describe covering strategies of increasing simplicity. We then try to apply the simplest strategy in section 2.5.

(FIXME It's tempting to omit the "hyper" from "hypergraph", "hyperedge", etc. However, it might be good to include, to distinguish from the original clique-detecting problem).

2.3 Using the set-cover greedy algorithm

Suppose we're given a k-regular (hyper)graph, and are trying to cover it with a small number of (hyper)cliques. This is reminiscent of the set cover problem, in which we are trying to cover some elements with a small number of sets. There is a greedy strategy for set-cover, which consists of simply picking the set which covers the most elements, removing those elements, and repeating [5]. This has a guaranteed approximation ratio.

This suggests the following strategy. Pick the largest clique, remove it, and repeat. If we do this, then each time we remove a clique, we know exactly how much the density of the graph is reduced. If we know that a sufficiently-dense graph has a fairly large clique, then

we should be able to bound how many cliques of each size we decomposed the original graph into.

It's known that if a conventional graph (that is, a graph in which each edge has two vertices, or "2-regular graph") has many edges, then it must contain a clique of a certain size. Finding the densest graphs which lack some size of clique was an early problem in extremal graph theory. Since these maximally dense graphs (the Turán graphs) have a known structure, the size of the clique is known precisely [14].

Generalizing this to hypergraphs seems like a natural question. Based on the neatness of the solution for 2-regular graphs, we might guess that, if we know that a k-regular hypergraph is dense, then it must contain a fairly large clique, with a closed-form size. Unfortunately, for hypergraphs, there doesn't seem to be as neat an answer as there was for 2-regular graphs. If we choose a k-regular hypergraph uniformly at random, "most" of the subsets of cliques will have about half the possible number of hyperedges. But at that density, the known bounds can't guarantee that there will be many cliques for us to cover. There is a large gap in the density of graph having some size of clique [9]. We omit the details of this, but roughly speaking, the largest graph in a random hypergraph doesn't seem likely to be guaranteed to be "very large".

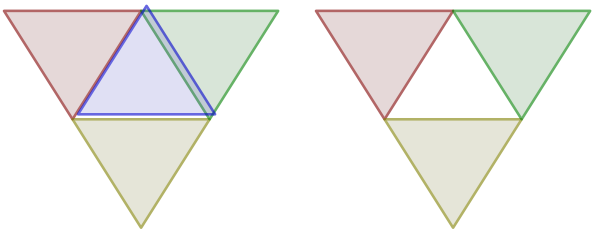
This strategy may be workable, but seems complicated, and we don't pursue it further here. However, it is funny to consider that if we *could* find a bound for k-regular graphs with $k \geq 3$ (for which Erdös offered \$1,000 [9]), *and* it turned out that the bound implied that sufficiently dense hypergraphs always had "large" hypercliques, then we also *might* have a lower bound on CLIQUE.

2.4 Non-constructively covering random hypergraphs

The lack of large hypergraph Turán bounds was initially discouraging. However, that problem (which possibly founded extremal graph theory?) is "extreme" in the sense that it looks for the densest graph which lacks a clique. It pays little attention to the average case.

Also, Keevah's review notes that when a hypergraph is dense enough to definitely contain some subgraph, it's apt to contain that subgraph "all over the place" [9], a phenomenon known as "supersaturation". This seems similar to what's described in [3], which suggests that most of the cliques in a random graph have a somewhat narrow range of sizes. This suggests that even if there isn't definitely a huge hyperclique, there may at least be many smallish hypercliques.

Thus, we try to use random graph theory to upper-bound how many r-vertex clique detecters (on average, or total). We will actually try to use all of the maximal cliques in a graph (that is, all of the cliques which are not part of a larger clique). This is presumably somewhat inefficient. For instance, in this toy example in which we're covering edges ("K_2"s), there are four maximal K_3s, but we could cover the edges with only three K_3s. Presumably similar things happen with hypergraphs.



One model of random graphs includes each edge with some fixed probability (FIXME CITE). Using this model, [3] counts the expected number of hypercliques in a hypergraph. Assuming all edges are chosen i.i.d. (in our case, with probability 1/2), there are $\binom{n}{r}$ possible r-vertex hypergraphs which might occur. Each of these is present if $\binom{r}{k}$ edges are present; as it were, if that many coins all come up heads. Then the total number of times we expect this to happen, as noted in [3], is

$$\binom{n}{r} \cdot 2^{-\binom{r}{k}}$$

Note that these could very well overlap. However, this isn't a problem for us in terms of correctness. If we cover some clique with more than one clique detector, we're ORing all the detectors together anyway. (This may be wasteful, and it's possible that we could manage better by only using some of the inputs to some clique detectors. For now, we ignore this possibility.)

2.4.1 How often do big hypercliques cover smaller ones?

The above bound counts hypercliques of different sizes, but has much overlap. Every time the graph contains a 15-vertex hyperclique, that includes 15 14-vertex hypercliques, $\binom{15}{13}$ 13-vertex hypercliques, tons of 8-vertex hypercliques, etc. We want to cover this with one 15-vertex clique detector.

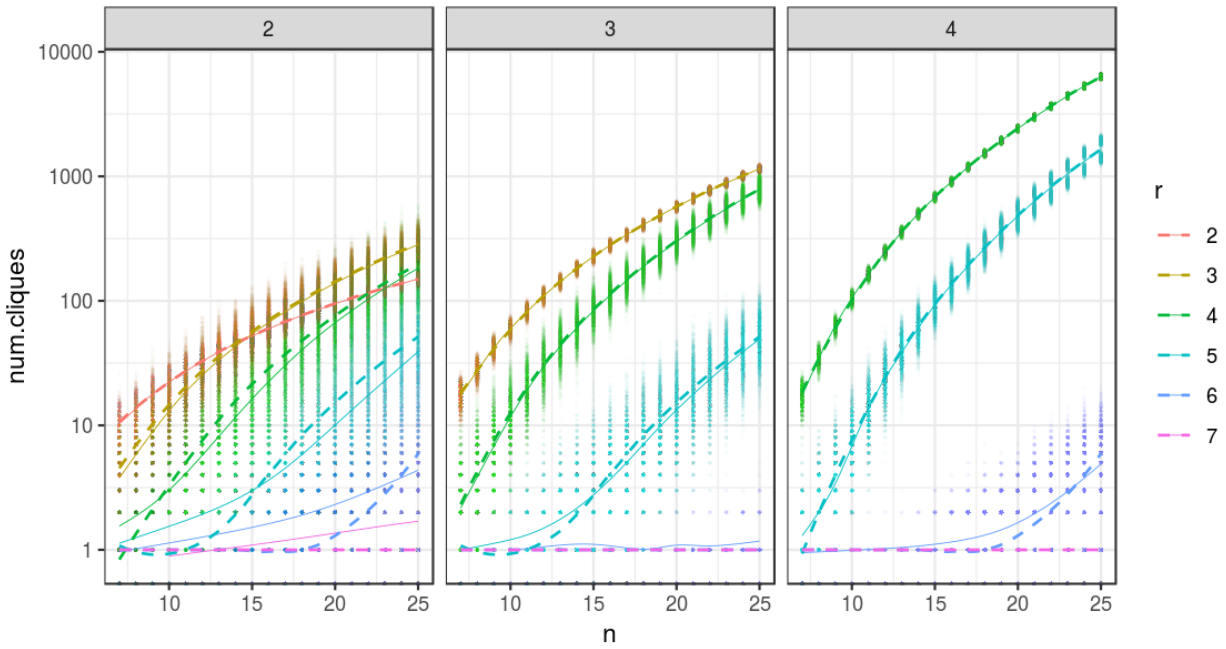
Instead, we consider the odds of a given r-vertex hyperclique A being completely covered by an $r+1$-vertex hyperclique. This requires, for a given vertex v, that all $\binom{r}{k-1}$ edges (consisting of v plus $k-1$ vertices of A) be present. v could be any of $n-r$ vertices; if at least one of those "completes" A, then A is covered. We're interested in counting the probability that this *doesn't* happen (as that's the number of smaller hypercliques we need to cover). That fraction is

$$\left[1 - 2^{-\binom{r}{k-1}}\right]^{n-r}$$

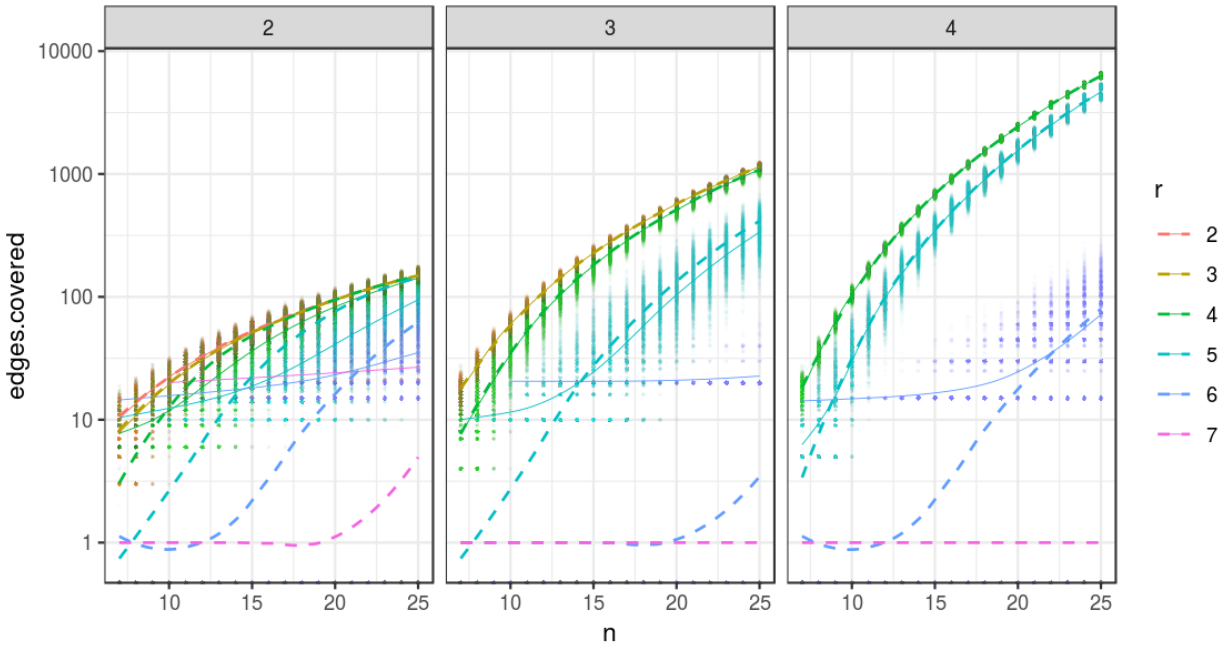
2.4.2 Checking the clique count estimates

These predictions made me slightly wary, as the events they were counting aren't independent. To check them (at least for *small* graphs), we used brute force. Suppose n, k are small enough that we can store the adjacency matrix of a k-regular graph as a bitvector, and precompute all possible hypercliques (with up to n vertices) as bitvectors. Then we can easily

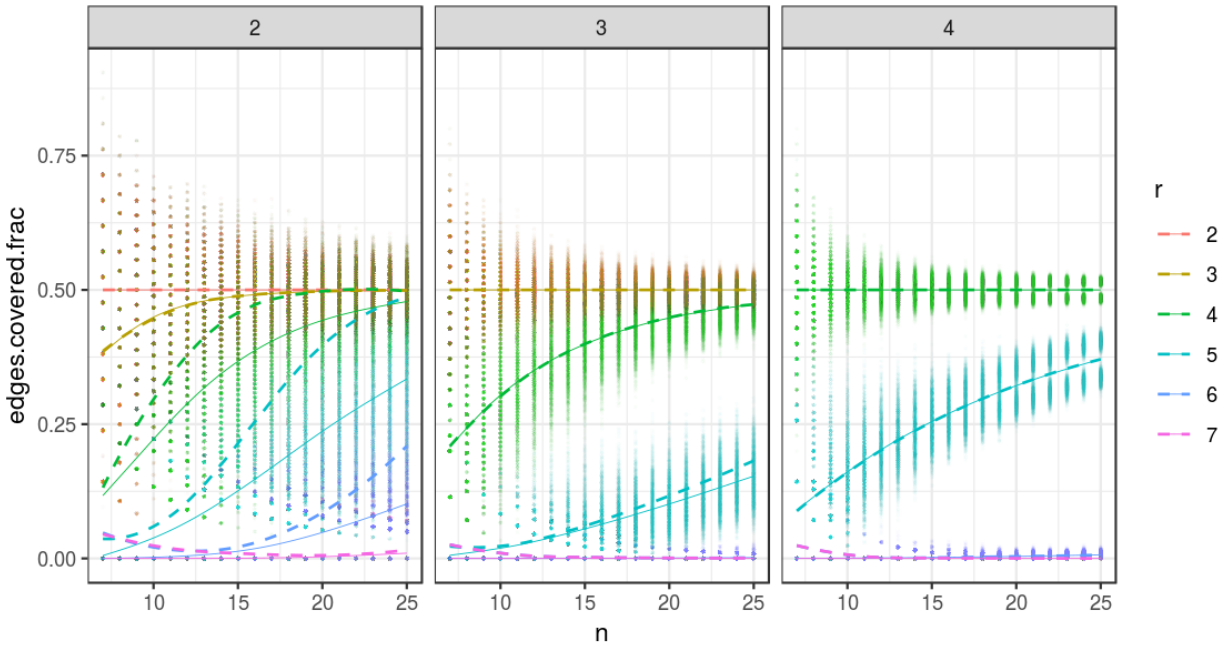
generate random graphs (as bitvectors), count the cliques, and compare that to the graph density. (Or possibly use the greedy set-cover strategy, and see how many cliques are need, for random graphs). We can then compare statistics about the cliques with predictions, based on [3]. (In the following graphs, the dotted line is the prediction, and the solid line is the average of samples).



We can also count the number of edges covered for different sizes of edge:



More relevant to this problem, is the fraction of edges covered by one of the r-cliques:



All of these estimates appear more accurate for larger edge sizes and graphs, which makes sense to me, because of the Central Limit Theorem.

2.4.3 The bound implied by this covering

We now combine the Shannon bound, together with the estimates of how many hypercliques are needed for an average random hypergraph. If we know the average number of gates needed to find many sets of cliques, and can do that efficiently using a "small" number of clique detectors, then we hopefully get a bound on the number of gates in each clique detector.

On the left side, we have the Shannon bound on how many gates are needed, for an average function chosen randomly from BUGGY-k-CLIQUE.

On the right side, we have the average number of gates which suffice for us to cover the hyperedges (which are K_ks in the original input graph) of an instance of BUGGY-k-CLIQUE. We will assume that we can detect k-cliques on r-vertex graphs using $h(r)$ NAND gates, for some function h. We then sum up the total cost (in NAND gates) of covering that many r-vertex hypergraphs in a larger n-vertex graph.

$$\underbrace{\sqrt{\binom{n}{k}} - \binom{n}{2}}_{\text{average number of gates needed}} \leq \sum_{r=k}^{n} \underbrace{h(r)}_{\text{cost, in gates}} \cdot \underbrace{\binom{n}{r} \cdot 2^{-\binom{r}{k}}}_{\text{expected number of } K_r^k\text{s}} \cdot \underbrace{\left[1 - 2^{-\binom{r}{k-1}}\right]^{n-r}}_{\text{fraction of } K_r^k\text{s not covered by a } K_{r+1}^k}$$

We then hope that if we set h to, say, a small enough function, we'll get a contradiction. Note that to bound $h(r)$, we need n to be much larger (so that much of the graph is covered with r-vertex hypercliques).

I tried to simplify this formula (e.g. by trying to change it into an integral), without much luck. However, even without simplifying this formula more, we could computing the number of graphs covering for a variety of n and k, and solving for the minimum values of $h(r)$ (a lower bound on the number of gates needed) using a linear program solver. I haven't done this so far.

2.5 Using just one size of hyperclique

As that expression is complicated, we seek a simpler (but presumably weaker) bound. As noted earlier, there is a strong incentive to use the largest hypercliques possible, as our assumption is that this will be less expensive. In theory, it seems useful to cover a graph with a variety of sizes of clique.

However, we don't expect large hypercliques to be frequent. Indeed, [4] covers random (non-hyper)-graphs with cliques of just one size. This suggests the simpler strategy of picking one intermediate size of hyperclique, and only use clique-finding for that size problems. For smaller problems, just use one NAND gate per hyperedge (clique). This strategy presumably is less efficient, but seems easier to analyze.

(In terms of the previous metaphor, perhaps Yoyodyne has abandoned trying to create a custom chip for every possible hypergraph. Instead, they have developed a CISC chip with a CLQ instruction, which checks for a clique of any given size. But then, a contingent of chip architects, fresh from reading [6], argue that they're better off building a chip with a CLQ instruction which only works for a fixed size of graph. Maybe this simplifies pipelining the clique detector...)

Suppose that we pick r such that $k < r < n$; in the given hypergraph, we will cover every K_r^k with some circuit; all remaining hyperedges (cliques) will simply be detected with one NAND gate each. In the bound below, the LHS is the Shannon bound on the average number of gates.

Assume that this subcircuit, for finding k-cliques in an r-vertex graph, requires $h(r)$ NAND gates, for some function h. Then, the cost to cover all edges in a K_r^k is simply $h(r)$ times the expected number of K_r^ks (as estimated in [3]). This is the second term in the RHS, below.

How many edges are left over? These are "expensive", as we're paying one gate for each. How likely is an edge to be "missed" by the larger cliques? There are k edges in the clique, so to form the larger clique, we need to pick $r-k$ additional vertices from the $n-k$ vertices not in the edge. Given that edge is chosen, there are another $\binom{r}{k}-1$ edges which all have to be included, each with probability 1/2. This is the first term in the RHS, below.

$$\underbrace{\sqrt{\binom{n}{k}} - \binom{n}{2}}_{\text{Shannon bound}} \leq \underbrace{\frac{1}{2}\binom{n}{r}\left(1-2^{1-\binom{r}{k}}\right)^{\binom{n-k}{r-k}}}_{\text{Gates for edges not covered by a clique}} + \underbrace{h(r)\cdot\binom{n}{r}2^{-\binom{r}{k}}}_{\text{Gates for edges covered by a clique}}$$

The nice thing about this sum is that it's only two terms (rather than the summation above).

2.5.1 Optimizing the bound

We now try to see what, if anything, this bound implies. Note again (somewhat confusingly) that in terms of the original clique-finding problem, this is bounding the number of NAND gates needed to find k-cliques in an r-vertex (non-hyper)-graph. We will be choosing n to be some much larger number (large enough to guarantee that many hyperedges are covered by hypercliques).

One problem that arises is numerical: we are multiplying huge numbers (the number of possible hypercliques) by infinitesimal numbers (the probability of an edge being present). Initially, I used R, and then used Julia's arbitrary-precision arithmetic.

When I attempted to optimize this for a tiny case, $k = 5$ and $r = 6$, searching for n up to 500, this did eventually get above zero. The maximum was when $n = 420$, at which point the bound was ... $3.1355 \cdot 10^{-6}$. Since there must be an integral number of NAND gates, we can then round up to "at least one NAND gate." (This is reminiscent of the "chipmunks in trees" problem described in [11], p. 344). That bound is uninspiring, but that was for a tiny case, which might be expected to be difficult to find a large lower bound for.

2.5.2 Dealing with numerical issues

When I tried maximizing for larger (presumably harder) problems, I started using up my laptop's memory (all 8 Gb of it). Also, computing the bound was fairly slow. I assume that the issue was that Julia's `Rational` type was spending much time reducing fractions to simplest form.

In order to work around this, I rewrote parts of the code, approximating based on the fact that

$$\lim_{a\to\infty} (1 - a^{-1})^a = e^{-1}$$

(In fact, e^{-1} is an upper bound). This implies that

$$\lim_{a\to\infty} [(1 - a^{-1})^a]^{\frac{1}{a}} = e^{-\frac{1}{a}}$$
$$\lim_{a\to\infty} (1 - a^{-1})^b = e^{-\frac{b}{a}}$$

In the actual code, we adapt this to work with log-transformed numbers. This bound seems closest when a is large, and b is not too large (data not shown).

The probability of a hyperedge not being covered by an r-hyperclique is

$$p = [1 - 2^{1-\binom{r}{k}}]^{\binom{n-k}{r-k}}$$

Letting $a = 2^{\binom{r}{k}-1}$ and $b = \binom{n-k}{r-k}$, we get

$$\log p \approx -\frac{\binom{n-k}{r-k}}{2^{[\binom{r}{k}-1]}}$$

... In any case, even after these numerical approximations, and looking at less trivial cases (e.g. finding cliques of size $n/2$ in n-vertex graphs), I wasn't able to get a bound above a tiny positive number.

FIXME write this up...

2.6 Covering with multiple sizes of hyperclique

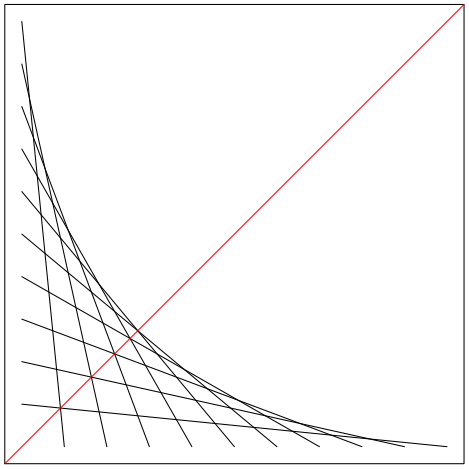
The previous failure suggests that covering with one size of hyperclique is too inefficient. The previous argument supposes that, for example, finding 6-vertex cliques in 10-vertex graphs requires some startlingly-small number g of gates. However, finding that size of cliques in 11-vertex graphs would require $11g$ gates, and in general, in n-vertex graphs, you'd need $\binom{n}{10}g$ gates. This isn't that startling an assumption.

Put another way, it would possibly be surprising if k-vertex cliques could be found with a small number of gates, for some particular size of input graph, but things didn't get much easier for larger sizes of input graph. A stronger (and more surprising) assumption would be that the number of gates needed to find k-vertex cliques grows *much* more slowly than the number of cliques, no matter how much n increases.

Therefore, after all, we attempt to use the inequality in section 2.

This system is overdetermined, in the sense that we can use as many rows of A as we like. It seems, intuitively, that there will be a variety of feasible solutions in a convex space. It's not obvious which of these to focus on.

We have another constraint: the number of gates to find k-cliques in $n+1$-vertex graphs is strictly larger than the number of gates needed in n-vertex graphs (as described in section 1.4). In symbols, $x_{i+1} > x_i$. The picture, intuitively, is vaguely reminscent of this (FIXME add 3-D variant?):



Therefore, we focus on minimizing $\sum_i x_i$. I think that this only gives a bound for the largest values of n. (FIXME make this qualification more accurate).

3 Related work

This bound relies heavily on a modification of Shannon's original function-counting argument [12].

Broadly speaking, the idea of using an upper bound to prove a lower bound is not new. Aaronson describes this as "ironic complexity theory" [2], and mentions several recent applications of it.

Probabilistic graph theory has often been used in lower bounds and algorithms. A well-known example is Razborov's lower bound on the complexity of detecting cliques using monotone circuits [10].

4 Future work

4.1 Improving the covering

We focused on using coverings based on maximal hypercliques, on the theory that it would simplify the accounting of the cost of the covering. However, as noted in section 2.4, this may not be the cheapest strategy to cover the hypergraph.

4.2 Related questions

Under the theory that this bound strategy works, what else does it apply to? (One small consequence would be [FIXME is this right?] that it "constructivizes" the Time Hierarchy theorem FIXME CITE, in that it shows that a natural problem, HAS-k-CLIQUE, requires increasing amounts of time to compute, for increasing values of k).

4.2.1 The complement of NP: co-NP

Although there's no obvious reduction from co-NP to NP, essentially the same argument seems to work. Suppose some family of circuits checks that there *isn't* any K_k in an n-vertex graph. Then we can check that an arbitrary graph is free of K_ks by ANDing together circuits which check that subsets of vertices are clear of K_ks. (ANDing them all together can be done with two additional NAND gates; there may be a cheaper way).

4.2.2 Quantum computation: BQP

This lower-bound strategy also seems potentially relevant to quantum computing, as the argument makes few restrictions on the sort of gates used. If it's the case that any function in BQP can be represented as a circuit made of discrete quantum gates, which can be ORed together, then clique detection isn't in BQP. However, I'm not sure what sort of quantum gates would be appropriate.

4.2.3 Counting the number of solutions to problems in NP: #P

Could we modify the counting argument to address the complexity of #P? (I'm not even sure that counting cliques is #P-complete FIXME check this?).

Suppose we have a family of circuits which count how many K_ks are in an n-vertex graph G; call this number $k(G)$. Then could we apply the previous hypergraph-covering strategy to count a subset of the cliques? In this case, if hypergraphs A and B don't overlap, then $k(A \cup B) = k(A) + k(B)$. But also, if $A \subset B$, then $k(B - A) = k(B) - k(A)$. This extra flexibility presumably means that fewer subcircuits are needed to cover an input graph, presumably leading to a sharper bound.

One question is how the function-counting argument changes. Also, I don't know how well-studied covering hypergraphs with unions and intersections of hypercliques is. (There's also the relatively minor problem of accounting for the bitwise complexity of doing arithmetic on counts of cliques, rather than just ORing sub-results together).

5 Conclusion

We first give a lower bound on finding *some* set of cliques. It is a modified form of Shannon's counting argument [12].

We also present a connection between this argument and the problem of covering hypergraphs with hypercliques. It seems that if an arbitrary hypergraph can be covered with "large" hypercliques, then this would give a lower bound on the NAND gates required to detect cliques.

Unfortunately, using one (relatively simple) strategy for doing this covering, the bound was, like, less than one. Possibly improving the covering strategy would help, but I'm not convinced it would help much.

6 Acknowledgements

The author would like to thank William Gasarch for introducing him to circuit complexity, probabilistic proofs about graphs, and lower bound strategies. He would also like to thank the maintainers of several entertaining and relevant blogs, including but not limited to: the Computational Complexity blog (Lance Fortnow and William Gasarch), Gödel's Lost Letter (Richard Lipton and Ken Regan), and Shtetl-Optimized (Scott Aaronson).

References

[1] Finding triangles in 6-vertex graphs requires 21 nand gates. Available online at `http://www.cs.umd.edu/~gasarch/BLOGPAPERS/burdick1.pdf` (part 1) and `http://www.cs.umd.edu/~gasarch/BLOGPAPERS/burdick2.pdf` (part 2).

[2] S. Aaronson. P ?= NP. Available online at `http://www.scottaaronson.com/papers/pnp.pdf`.

[3] B. Bollobás and P. Erdös. Cliques in random graphs. In *Mathematical Proceedings of the Cambridge Philosophical Society*, volume 80, pages 419–427. Cambridge University Press, 1976.

[4] B. Bollobás, P. Erdős, J. Spencer, and D. B. West. Clique coverings of the edges of a random graph. *Combinatorica*, 13(1):1–5, 1993.

[5] V. Chvatal. A greedy heuristic for the set-covering problem. *Mathematics of operations research*, 4(3):233–235, 1979.

[6] J. L. Hennessy and D. A. Patterson. *Computer architecture: a quantitative approach*. Elsevier, 2011.

[7] A. Itai and M. Rodeh. Finding a Minimum Circuit in a Graph. In *Proceedings of the Ninth Annual ACM Symposium on Theory of Computing*, STOC '77, pages 1–10, New York, NY, USA, 1977. ACM.

[8] R. M. Karp. Reducibility among combinatorial problems. In *Complexity of computer computations*, pages 85–103. Springer, 1972.

[9] P. Keevash. Hypergraph Turán problems. *Surveys in combinatorics*, 392:83–140, 2011.

[10] A. A. Razborov. Lower bounds on the monotone complexity of some boolean functions. Dokl. Akad, 1985.

[11] S. Ross. *A first course in probability*. Pearson, 2006.

[12] C. E. Shannon. The synthesis of two-terminal switching circuits. *Bell System Technical Journal*, 28(1):59–98.

[13] P. V. Strassen. Gaussian elimination is not optimal. *Numerische Mathematik*, 13(4):354–356, Aug. 1969.

[14] P. Turán. On an external problem in graph theory. *Mat. Fiz. Lapok*, 48:436–452, 1941.

[15] V. V. Williams. Multiplying matrices faster than Coppersmith-Winograd. In *In Proc. 44th ACM Symposium on Theory of Computation*, pages 887–898, 2012.