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Mercury Delay Line Emulation:
Reconstructing Memory for EDSAC

by

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June 12, 2017

A dissertation submitted in partial fulfilment of the degree of
MSc Embedded Systems

**Abstract**

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# List of Acronyms

**FPGA** field programmable gate array.

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# INTRODUCTION SC

Foobar [1] field programmable gate array (FPGA).

# Bibliography

[1] American Physical Society, "Invention of the first transistor," *APS News*, vol. 9, no. 10, p. 2, nov 2000.

# Appendix A
# Foo

Bar