Evolution of the MARGe Project

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The MARGe project has evolved from a simple reactive graph animation tool into an integrated environment for modeling, simulation, and formal verification of reconfigurable systems.

This evolution has surpassed the initial goals, incorporating dynamic modal logic and advanced semantic extensions.

Fundamental Axes of Contributions

1. Extension of the core formalism (guards and time)

2. Introduction of formal verification capabilities.

3. Formal mappings and interoperability with other tools.

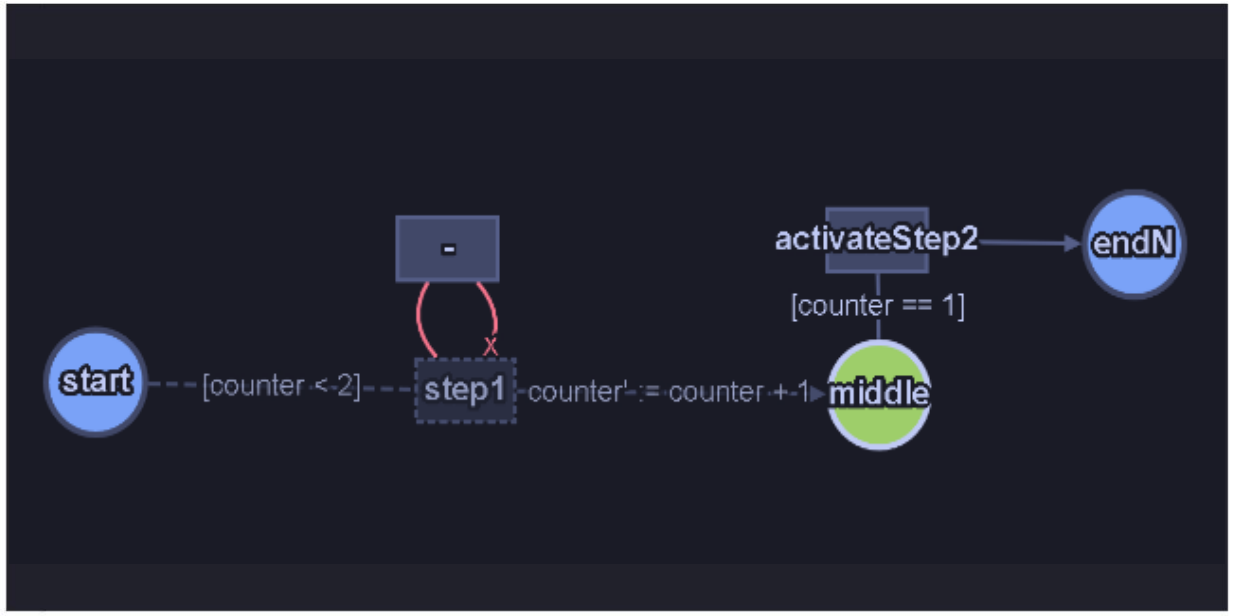
Extension of the Reactive Graph Formalism

- The Reactive Graphs formalism was expanded with guards and time, giving rise to two new formalisms:
  - Guarded Reactive Graphs (GRG): enable logical conditions and atomic updates.
  - Timed Reactive Graphs (TRG): introduce clocks and temporal invariants.

Guarded Reactive Graphs (GRG)

Input Reactive Graphs

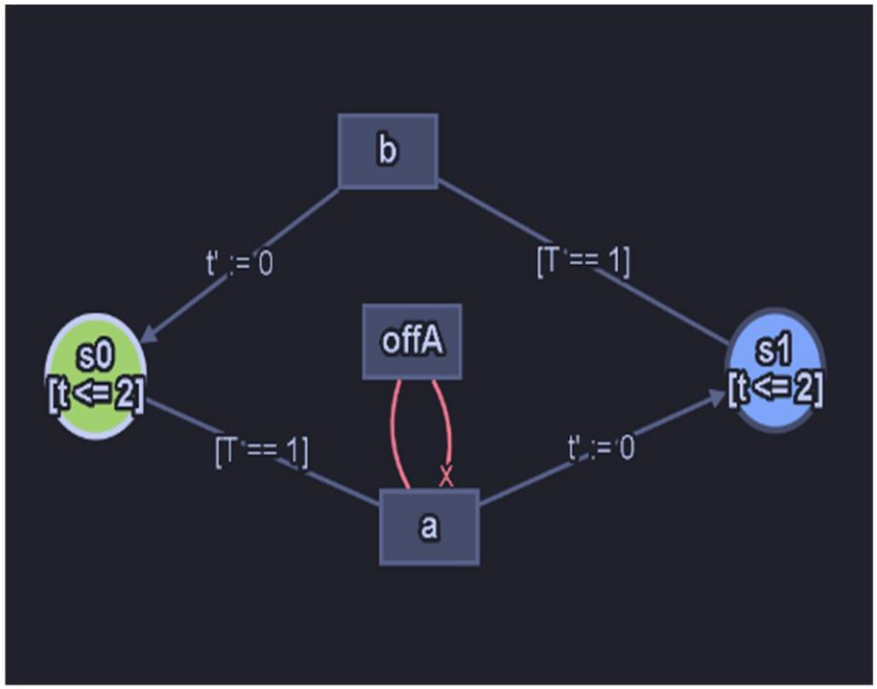
```
1 int counter = 0
2 init start
3 start --> middle: step1  if (counter < 2) then {
4   counter' := counter + 1
5 }
6 step1 --! step1
7 middle --> endN: activateStep2 if (counter == 1)
```



Timed Reactive Graphs (TRG)

Input Reactive Graphs

```
1  clock t
2  int T = 1
3
4  inv s0: t <= 2
5  inv s1: t <= 2
6
7  init s0
8
9  s0 --> s1: a if (T == 1) then {
10   t' := 0
11 }
12 s1 --> s0: b if (T == 1) then {
13   t' := 0
14 }
15 a  --! a: offA
```



2. Formal Analysis and Property Verification

- Implementation of a verifier based on Propositional Dynamic Logic (PDL).

Input Reactive Graphs

```
1 init s0
2 s0 --> s0: act
3 act --! act: offAct disabled
4 act ->> offAct: on1 disabled
5 act ->> on1
```

PDL Evaluation Result

```
From state: s0
Formula: DiamondP(Seq(Seq(Act(act),Act(act)),Act(act)),StateProp(s0))
Result: true
```

Thank you!

Questions?