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Homework 3 Problems 6.28, 6.30, 6.32 *Work shown on pages below*

6.28 -> refer to practice problem 6.12

A. List all of the hex memory addresses that will hit in set 2.

None

B. List all of the hex memory addresses that will hit in set 4.

0x18F0 0x00B0 0x18F1 0x00B1 0x18F2 0x00B2 0x18F3 0x00B3

C. List all of the hex memory addresses that will hit in set 5.

0x0E34

0x0E35

0x0E36

0x0E37

D. List all of the hex memory addresses that will hit in set 7.

0x1BDC

0x1BDD

0x1BDE

0x1BDF

6.30

A. What is the size (C) of this cache in bytes?

$$C = S \times E \times B = 8 * 4 * 4 = 128$$
 bytes

B. The box that follows shows the format of an address (1 bit per box). Indicate (by labeling the diagram) the fields that would be used to determine the following:

CO. The cache block offset

CI. The cache set index

CT. The cache tag

СТ	СТ	СТ	CT	CT	СТ	СТ	СТ	CI	CI	CI	CO	CO
12	11	10	9	8	7	6	5	4	3	2	1	0

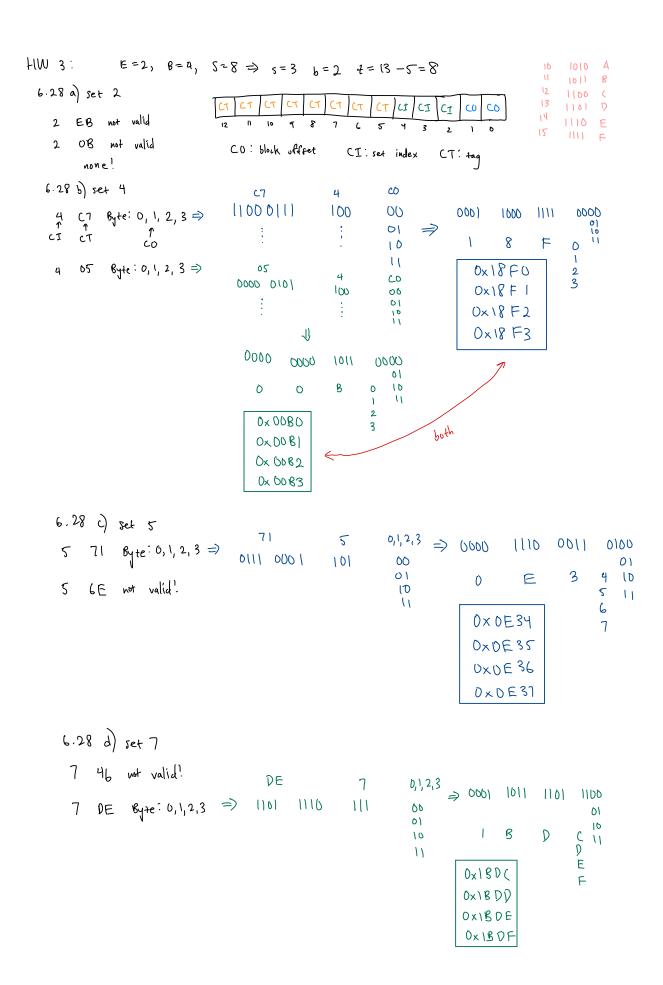
<u>6.32 – Repeat Problem 6.31 for memory address 0x16E8</u>

A. Address format (1 bit per box):

1	0	1	1	0	1	1	1	0	1	0	0	0
12	11	10	9	8	7	6	5	4	3	2	1	0

B. Memory Reference:

<u>Parameter</u>	<u>Value</u>
Block offset (CO)	0x0
Index (CI)	0x2
Cache tag (CT)	0xB7
Cache hit? (Y / N)	N
Cache byte returned	



1010 A

1100 (1101 D 1110 E