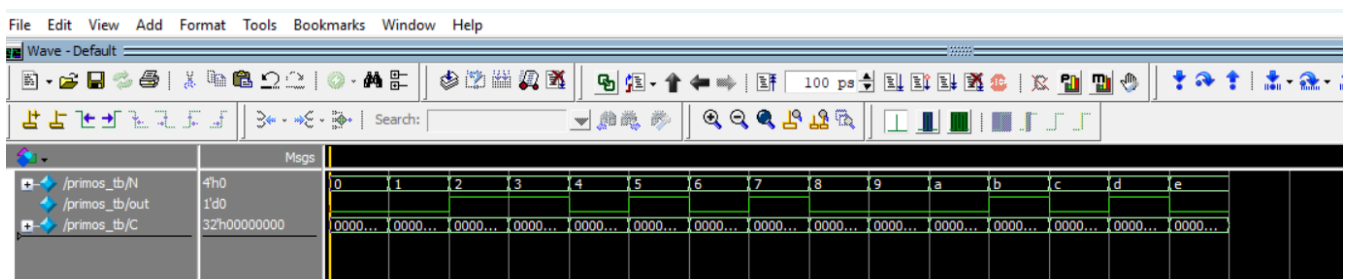


```

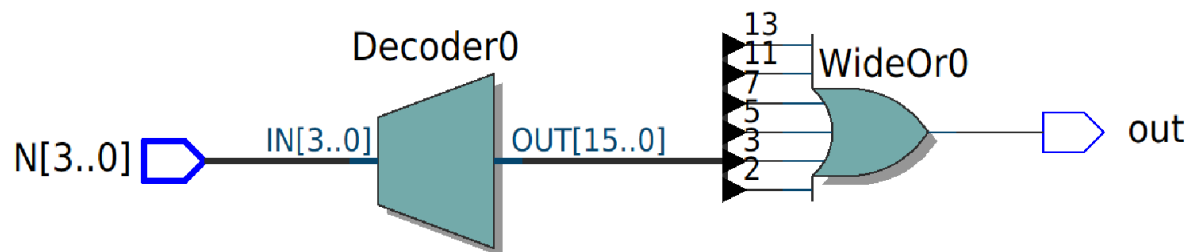
# simulacion iniciada
# N=0000, out0 0
# N=0001, out0 0
# N=0010, out0 1
# N=0011, out0 1
# N=0100, out0 0
# N=0101, out0 1
# N=0110, out0 0
# N=0111, out0 1
# N=1000, out0 0
# N=1001, out0 0
# N=1010, out0 0
# N=1011, out0 1
# N=1100, out0 0
# N=1101, out0 1
# N=1110, out0 0
# simulaci3n finalizada
# ** Note: $stop      : C:/quartus_codes/practicas/practical/primos_tb.v(27)
#   Time: 150 ps   Iteration: 0   Instance: /primos_tb
# Break in Module primos_tb at C:/quartus_codes/practicas/practical/primos_tb.v line 27

```

Imagen de lo que corre en la terminal



RTL SIMULATION para corroborar que las se1ales se emiten correctamente en la placa.



RTL viewer

Video con el funcionamiento de la pr1ctica:

<https://drive.google.com/file/d/1G1JBe6cCYyS61fDxSfHJl6Od92UFTB-7/view?usp=sharing>