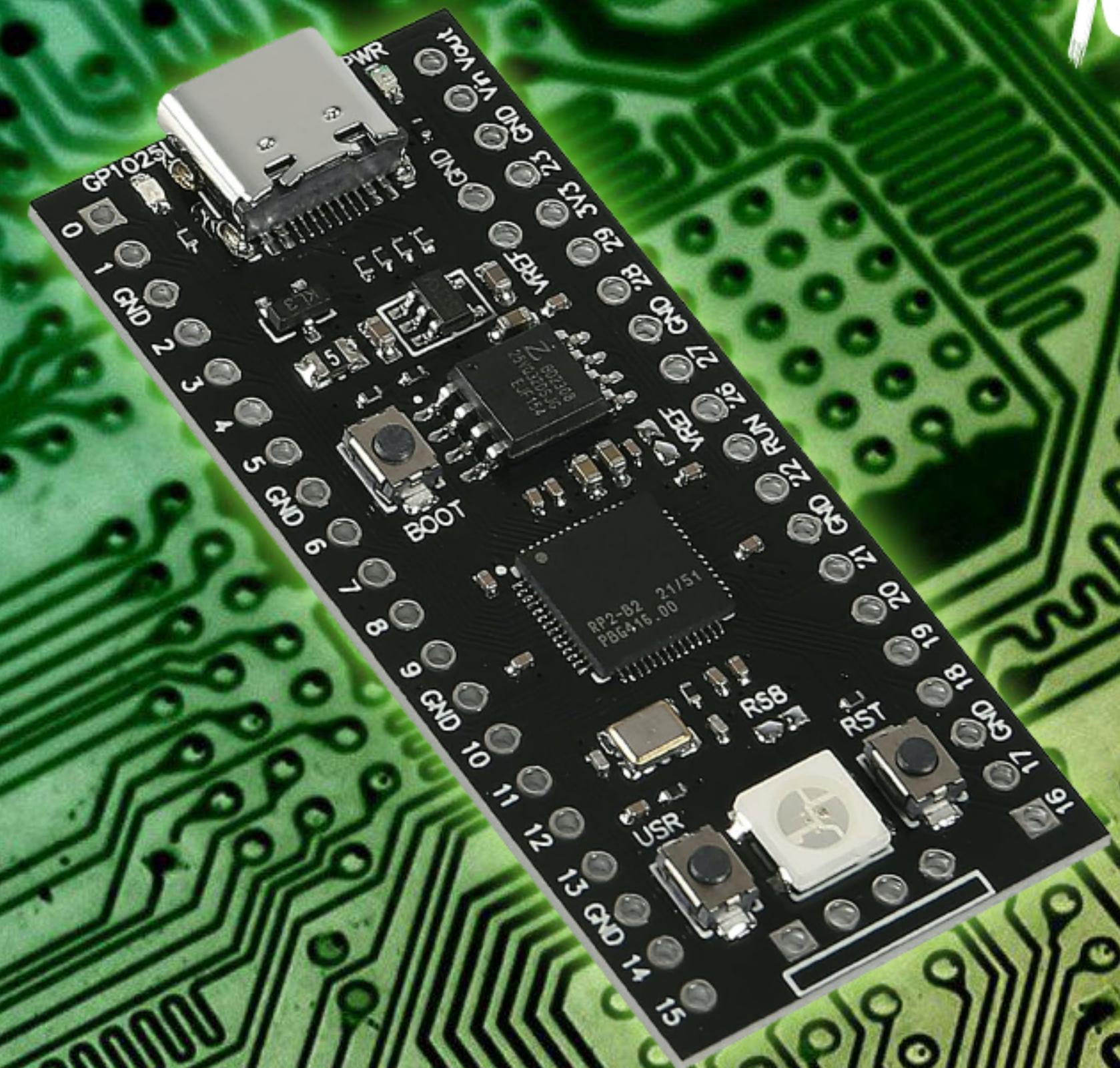
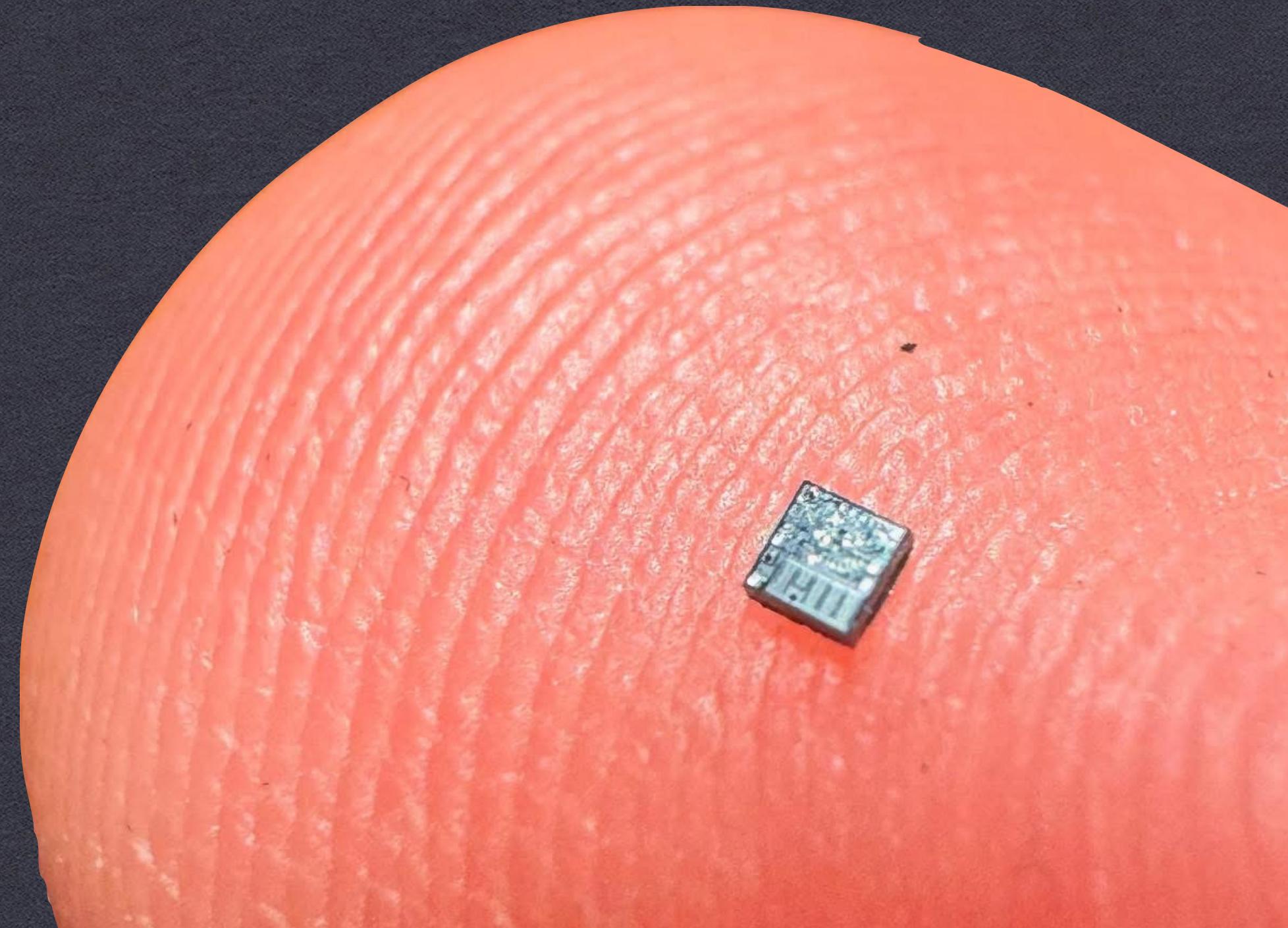


MICROCONTROLLER MASTERY



01 START

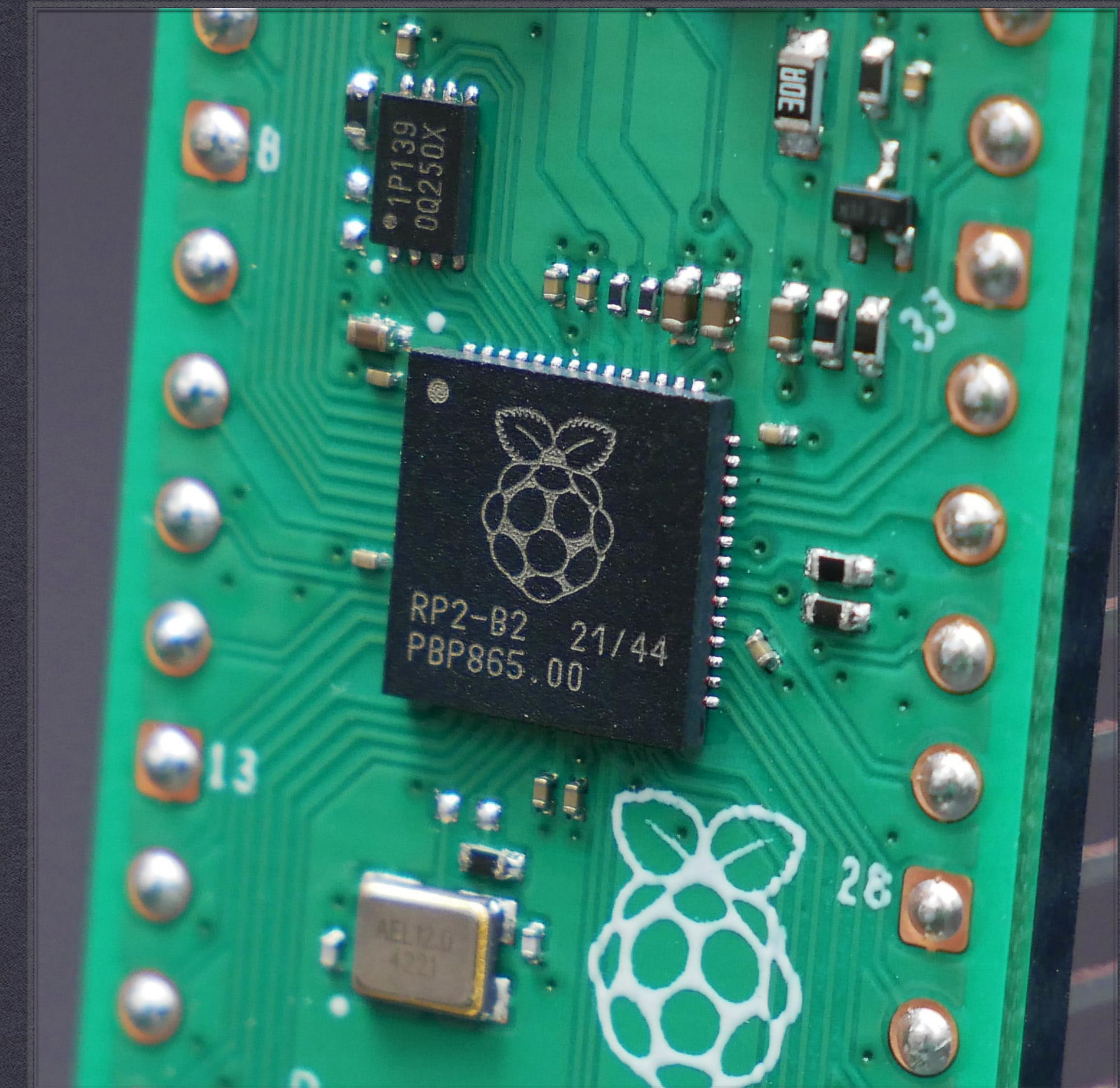
WHAT IS A MICROCONTROLLER?



WHAT IS A MICROCONTROLLER?

A SMALL, SELF-CONTAINED COMPUTER THAT ACTS AS THE "BRAIN" INSIDE MANY ELECTRONIC DEVICES. IT'S A CHIP THAT CAN PROCESS DATA AND CONTROL OTHER PARTS OF A SYSTEM.

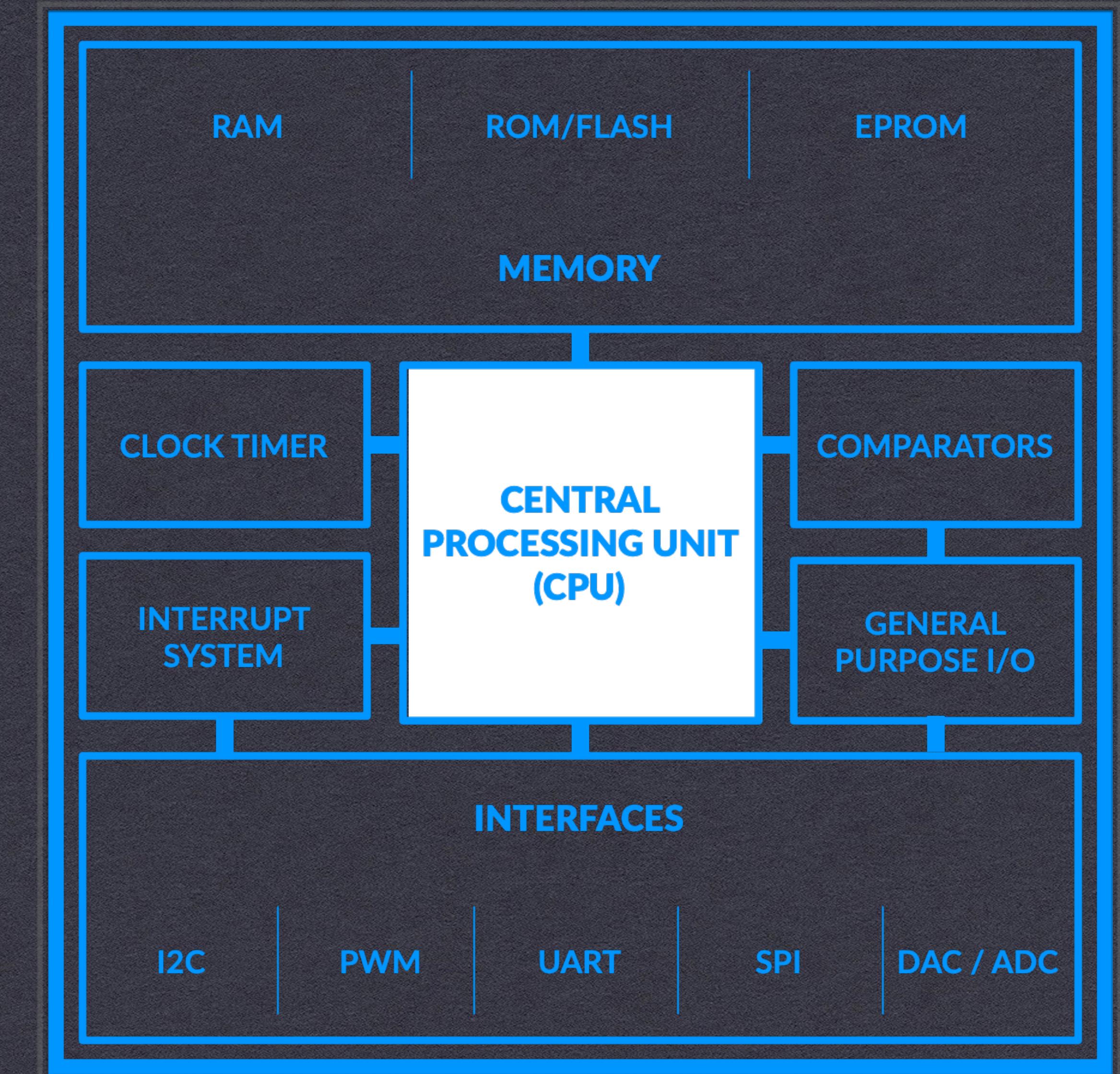
IT IS A SPECIAL TYPE OF COMPUTER DESIGNED FOR SPECIFIC TASKS, LIKE TURNING ON A LIGHT, CONTROLLING A ROBOT, OR READING DATA FROM A SENSOR.



WHAT IS A MICROCONTROLLER?

PROCESSOR (CPU)

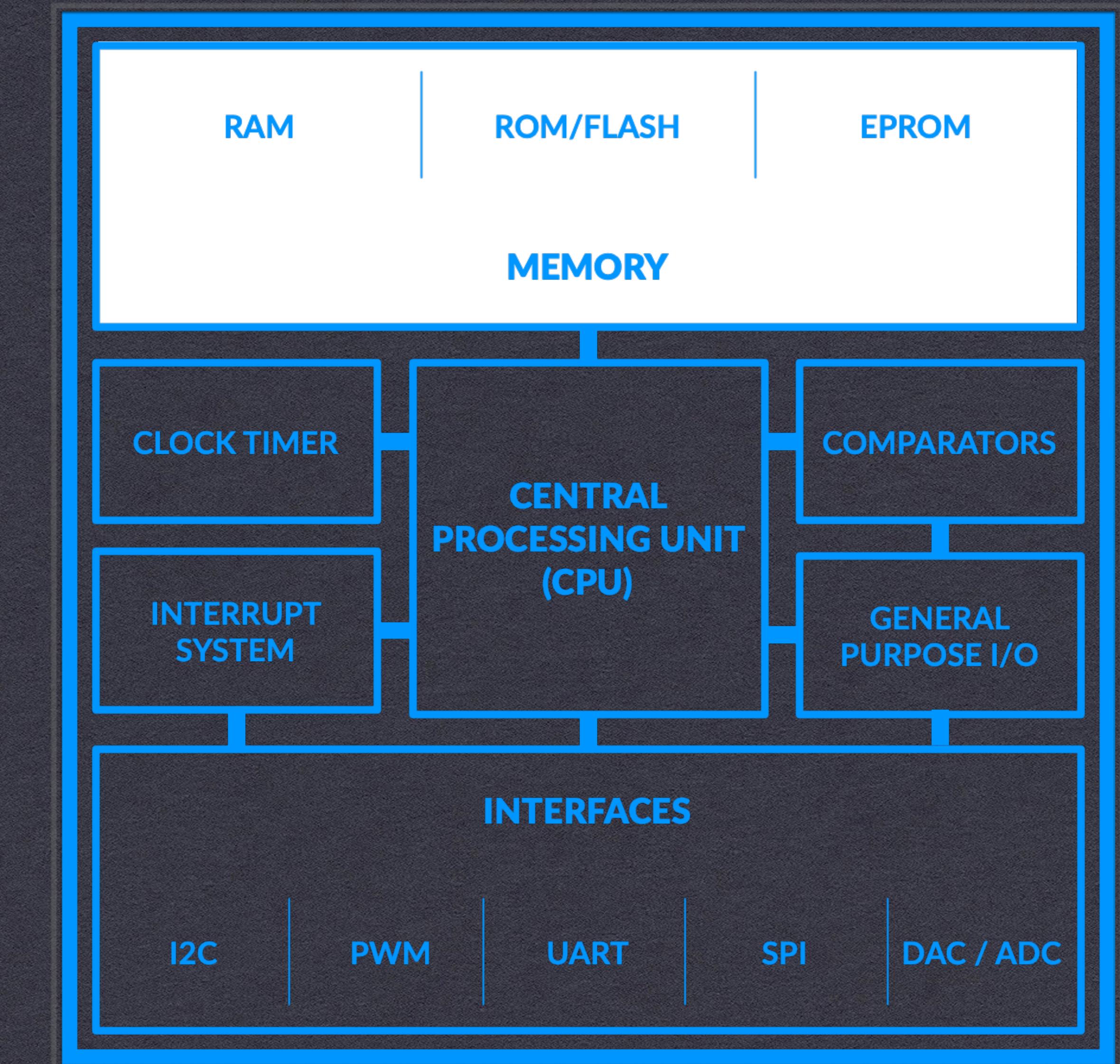
ALSO KNOWN AS THE CENTRAL PROCESSING UNIT, IT IS THE PART THAT PERFORMS CALCULATIONS AND MAKES DECISIONS.



WHAT IS A MICROCONTROLLER?

MEMORY

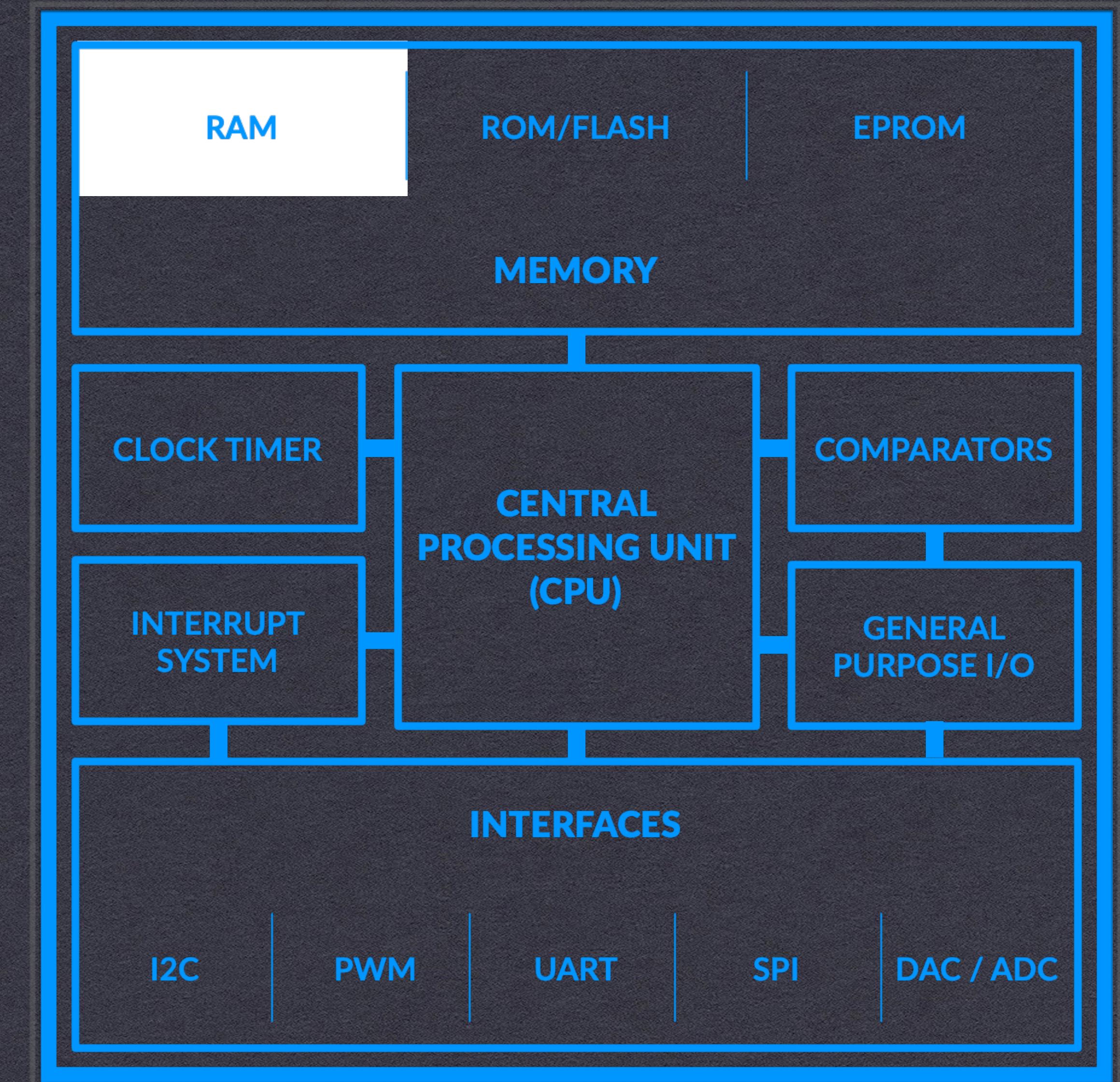
WHERE DATA AND INSTRUCTIONS ARE STORED FOR LATER USE. DIFFERENT TYPES ARE FASTER AND STORE INFORMATION LONGER.



WHAT IS A MICROCONTROLLER?

MEMORY - RAM

ALSO KNOWN AS RANDOM ACCESS MEMORY, THIS MEMORY IS VERY FAST AND “VOLATILE”, ONLY KEEPING DATA WHILE POWERED ON.

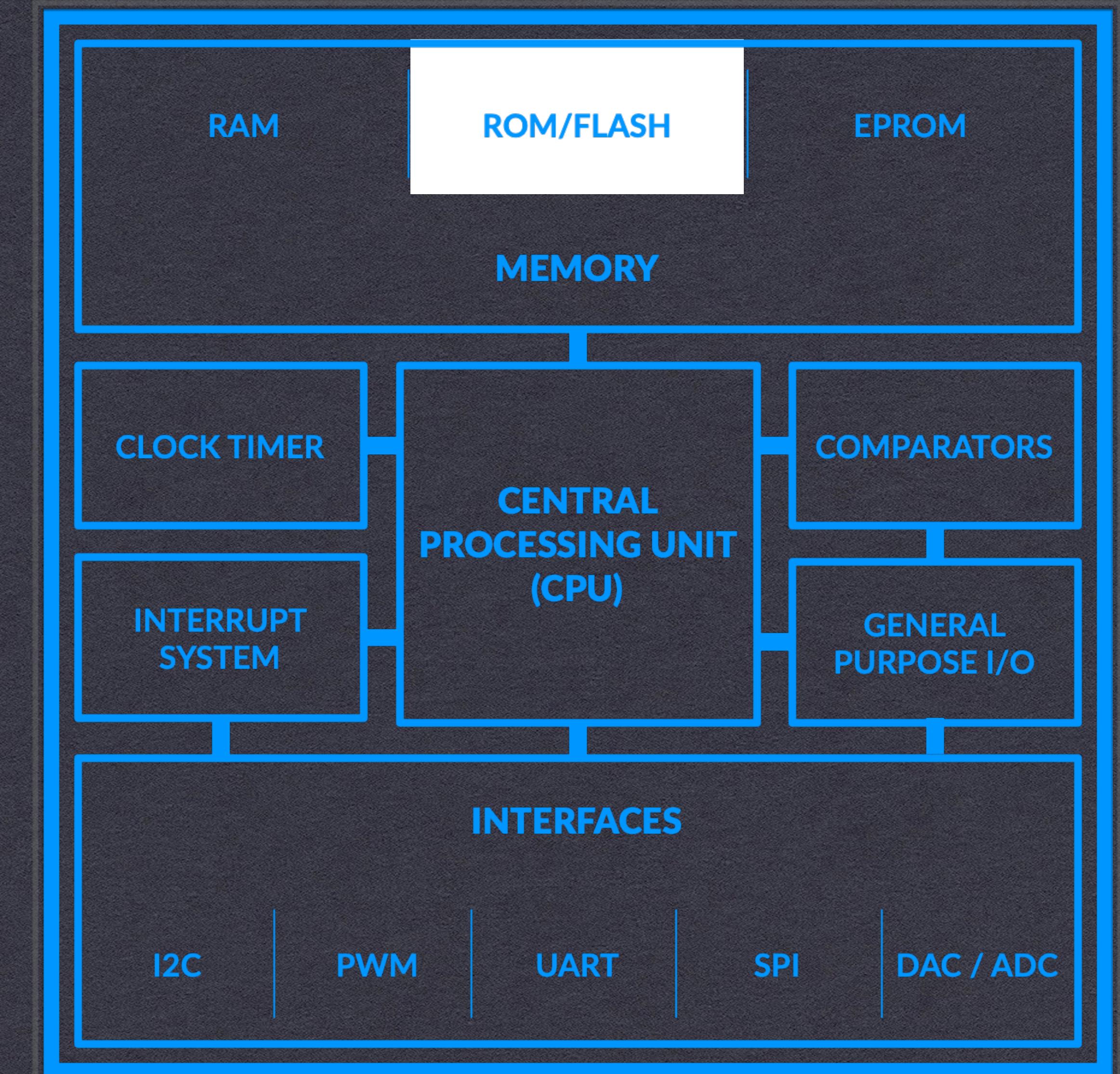


WHAT IS A MICROCONTROLLER?

MEMORY - ROM/FLASH

ALSO KNOWN AS READ ONLY MEMORY,
THIS MEMORY IS SLOWER THAN RAM, AND
USED TO HOLD PROGRAM CODE EVEN
WHEN THE DEVICE IS OFF.

ROM CANNOT BE CHANGED WITHOUT
SPECIAL TOOLS OR SOFTWARE, IF AT ALL.

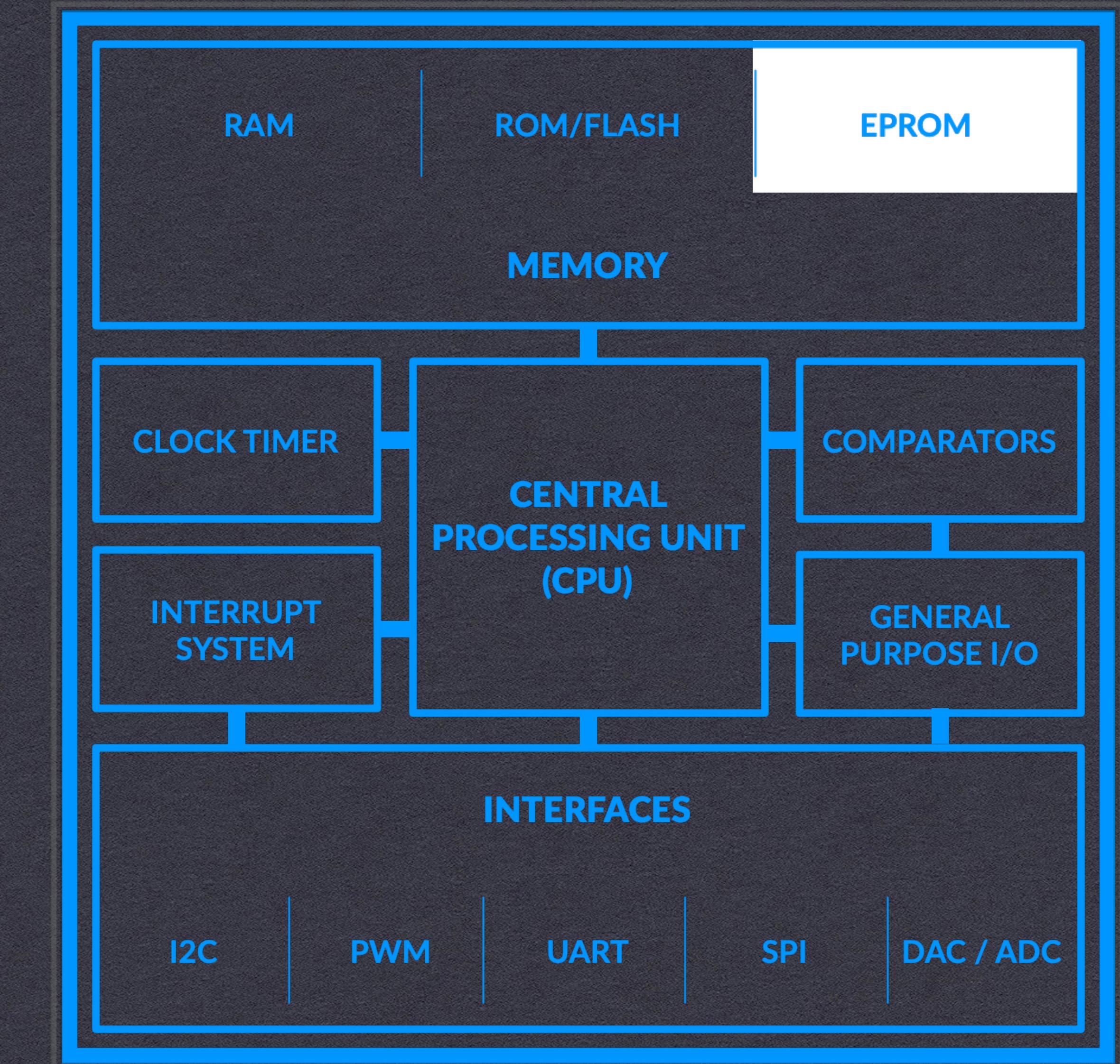


WHAT IS A MICROCONTROLLER?

MEMORY - EPROM

NOT IN EVERY MICROCONTROLLER, ERASABLE PROGRAMMABLE READ-ONLY MEMORY IS GENERALLY SLOWER THAN ROM AND CAN STORE DATA EVEN WHEN THE DEVICE IS OFF.

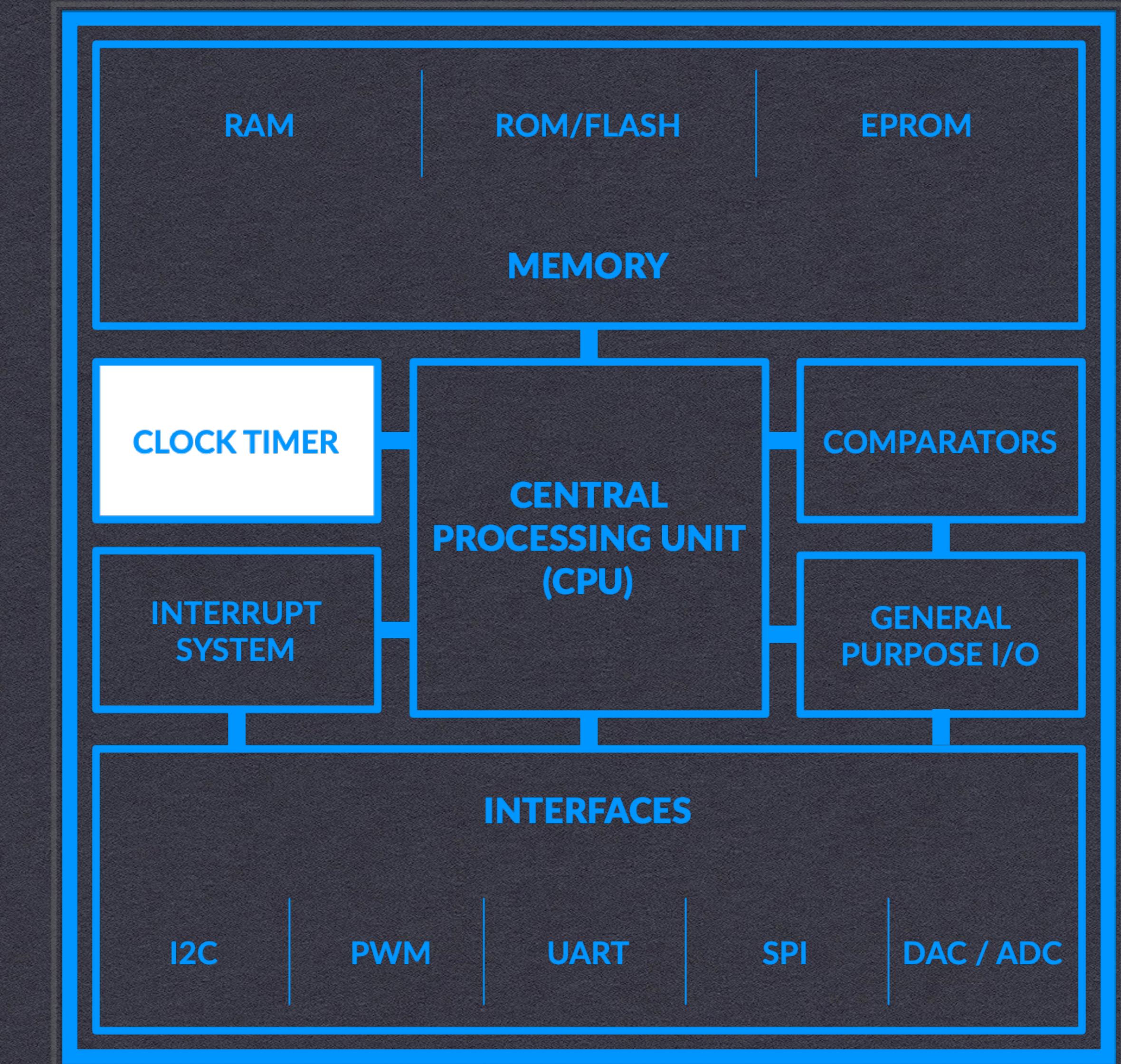
AN EPROM CAN BE MODIFIED USING SPECIALIZED TOOLS.



WHAT IS A MICROCONTROLLER?

CLOCK TIMER

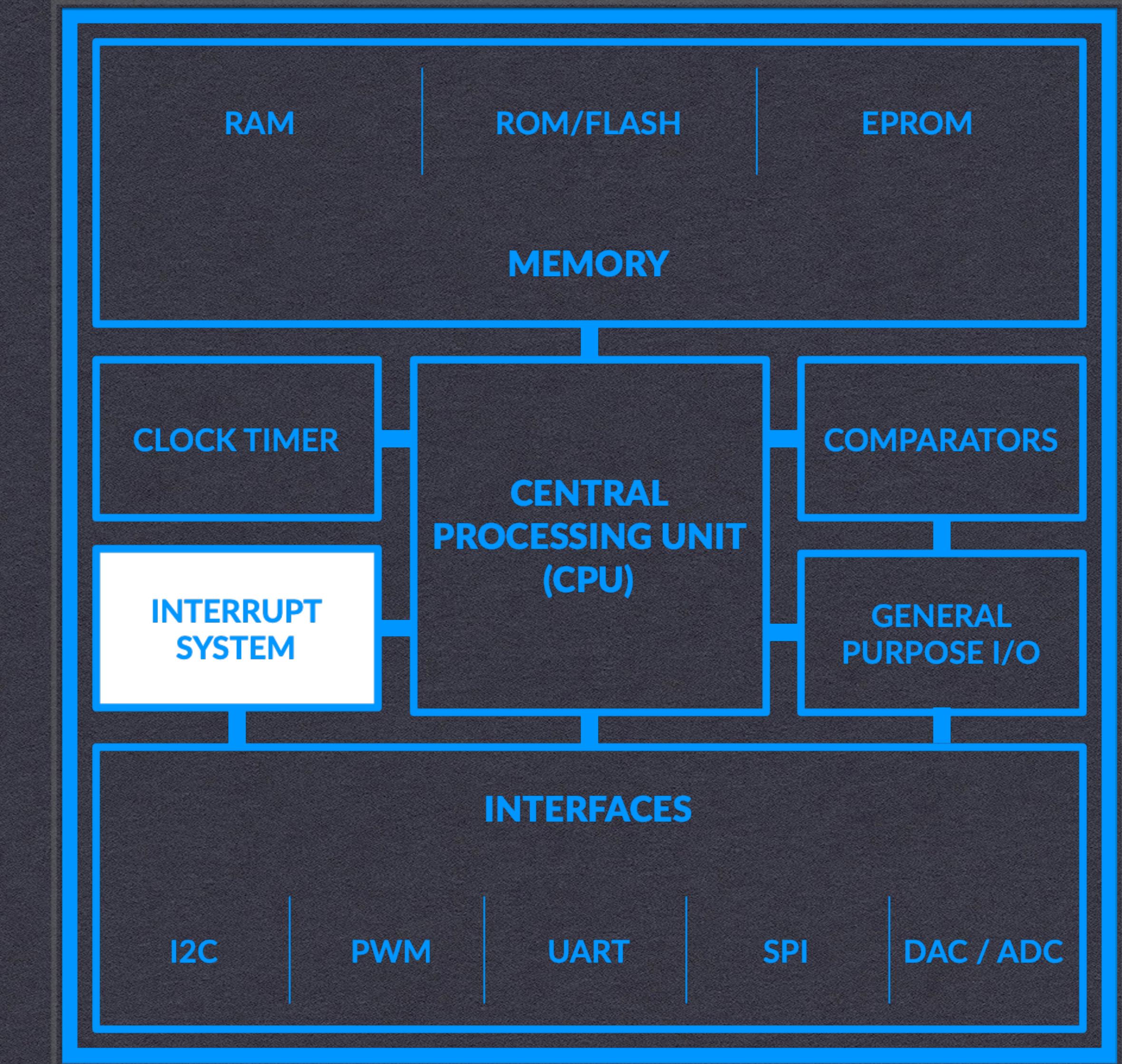
PROVIDES TIMING TO THE
MICROCONTROLLER AND ALLOWS FOR
MULTIPLE JOBS TO BE DONE AT ONCE



WHAT IS A MICROCONTROLLER?

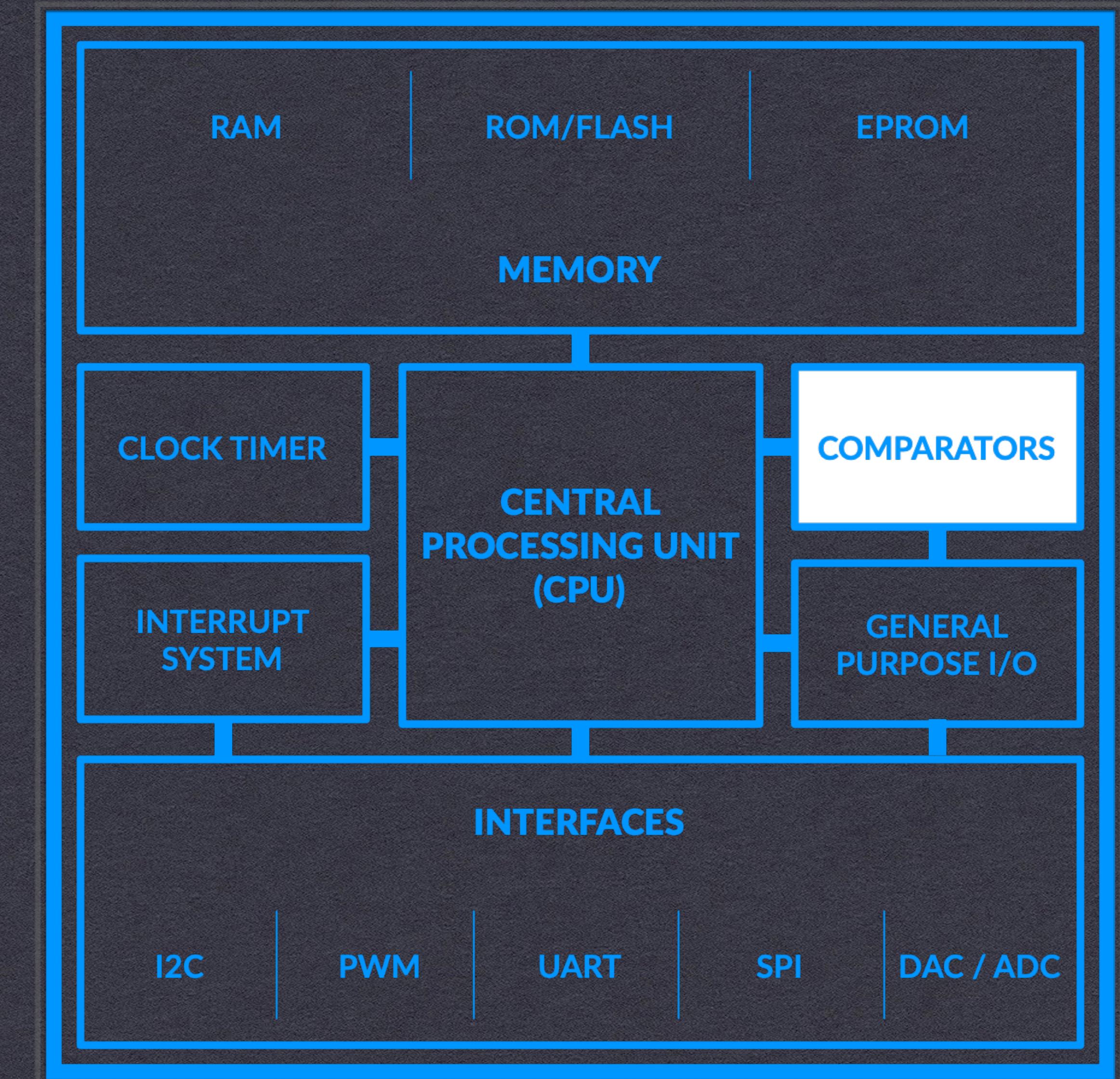
INTERRUPT SYSTEM

ALLOWS MICROCONTROLLER TO STOP A
CURRENT TASK TO DO SOMETHING ELSE



WHAT IS A MICROCONTROLLER?

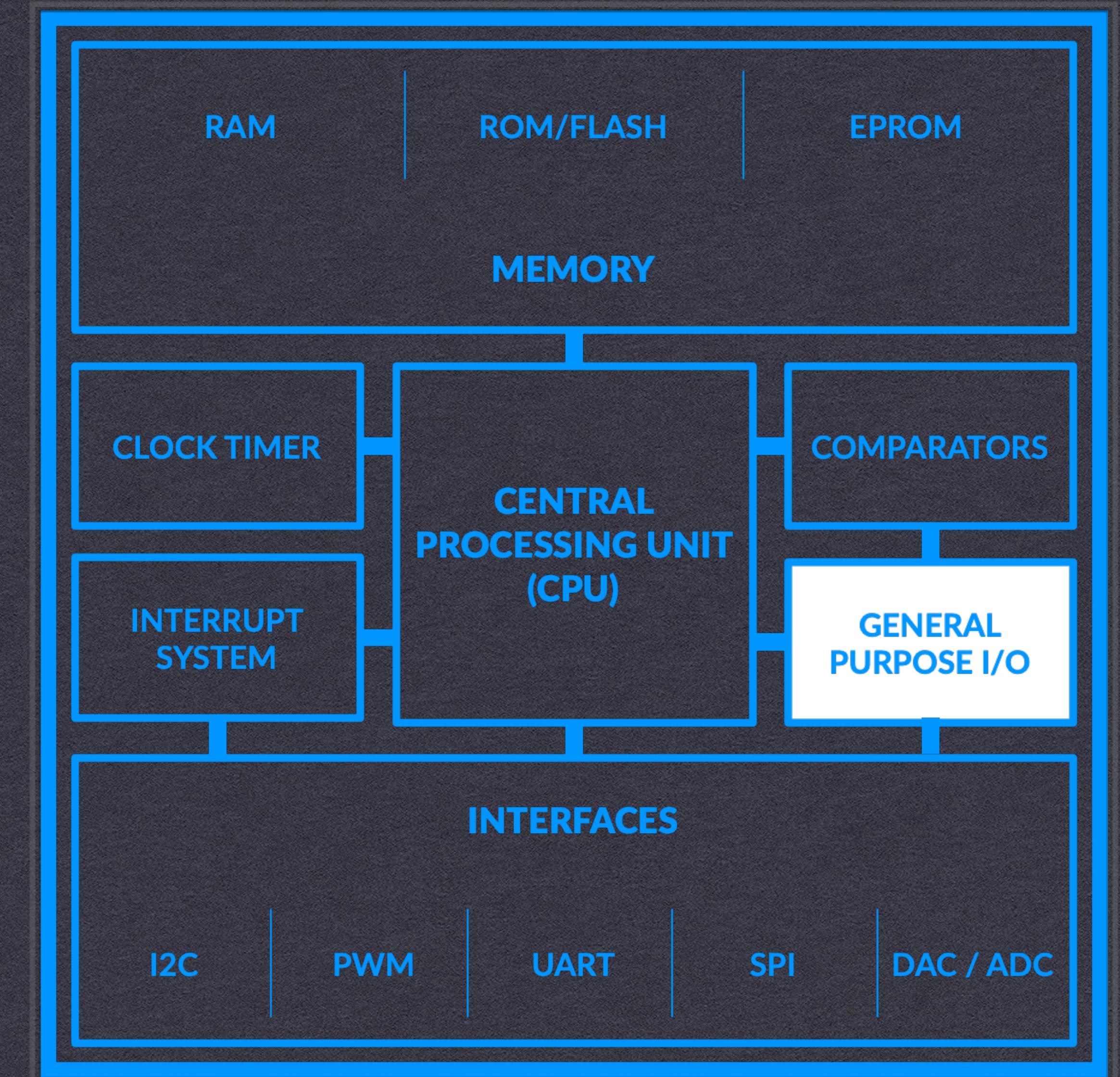
COMPARATORS
CHECKS DIFFERENT VOLTAGES



WHAT IS A MICROCONTROLLER?

GENERAL PURPOSE I/O

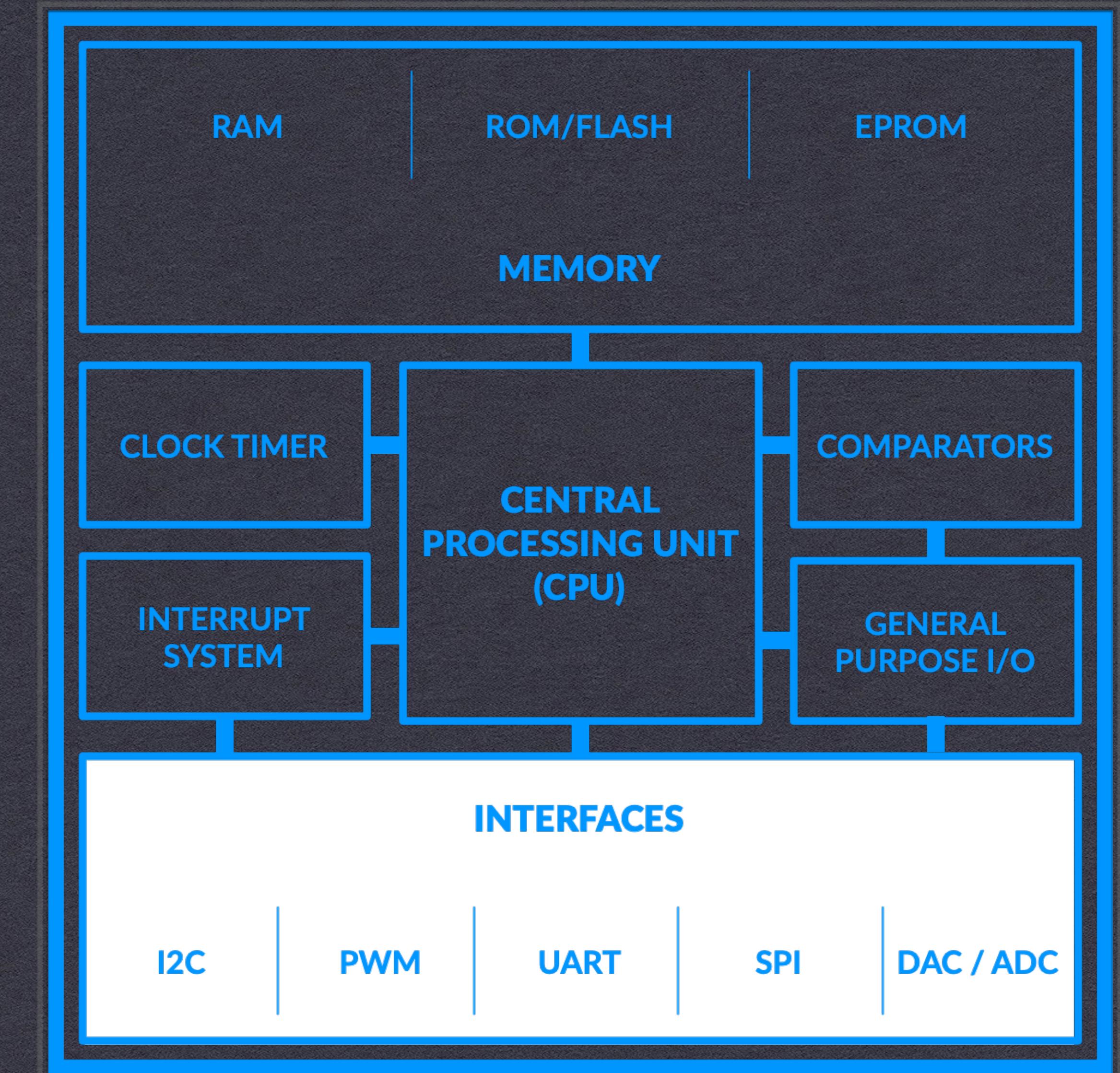
ALLOWS MICROCONTROLLER TO
INTERACT WITH THE OUTSIDE WORLD
GETTING INPUT AND SENDING OUTPUT
USING THE MICROCONTROLLER'S
DIFFERENT PINS



WHAT IS A MICROCONTROLLER?

INTERFACES

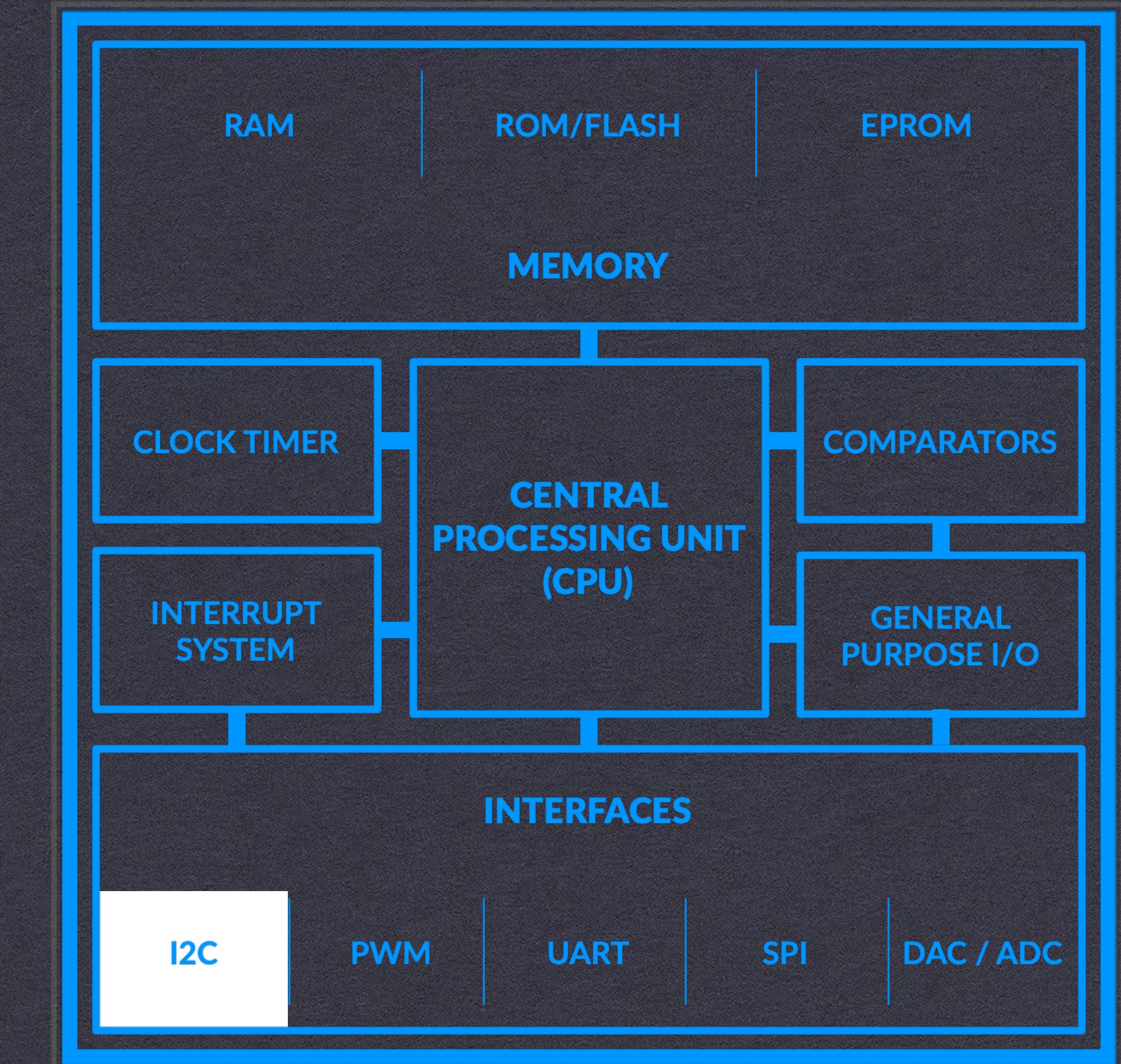
DIFFERENT WAYS TO “TALK” TO DEVICES (PERIPHERALS) USING AGREED UPON METHODS. DEPENDING ON THE INTERFACE, THE WAY TO COMMUNICATE MIGHT TAKE MORE OR LESS WIRES.



WHAT IS A MICROCONTROLLER?

INTERFACES - I²C

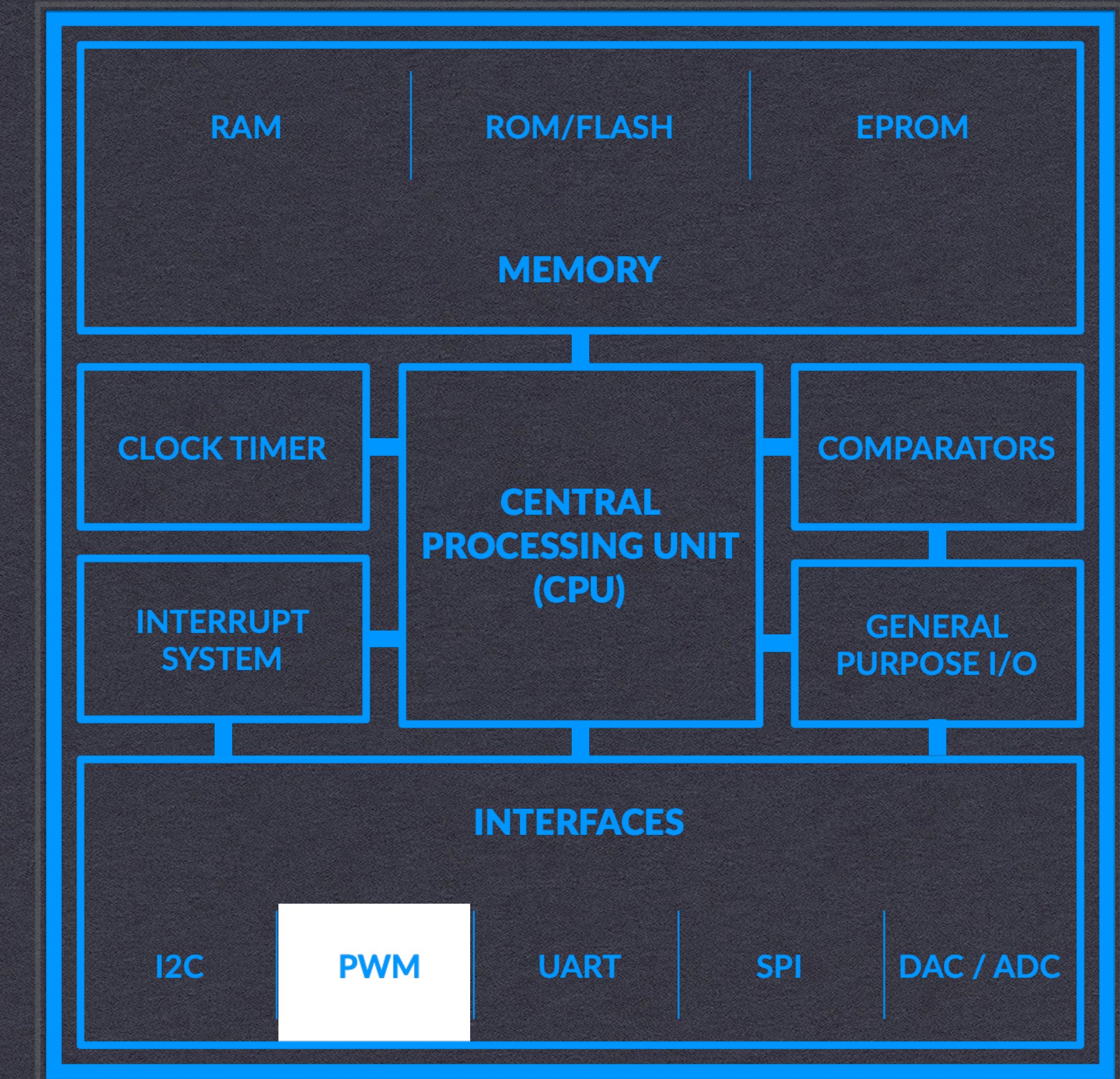
ALSO KNOWN AS I²C OR INTER-INTEGRATED CIRCUIT. THIS INTERFACE ALLOWS CONNECTING MULTIPLE DEVICES.



WHAT IS A MICROCONTROLLER?

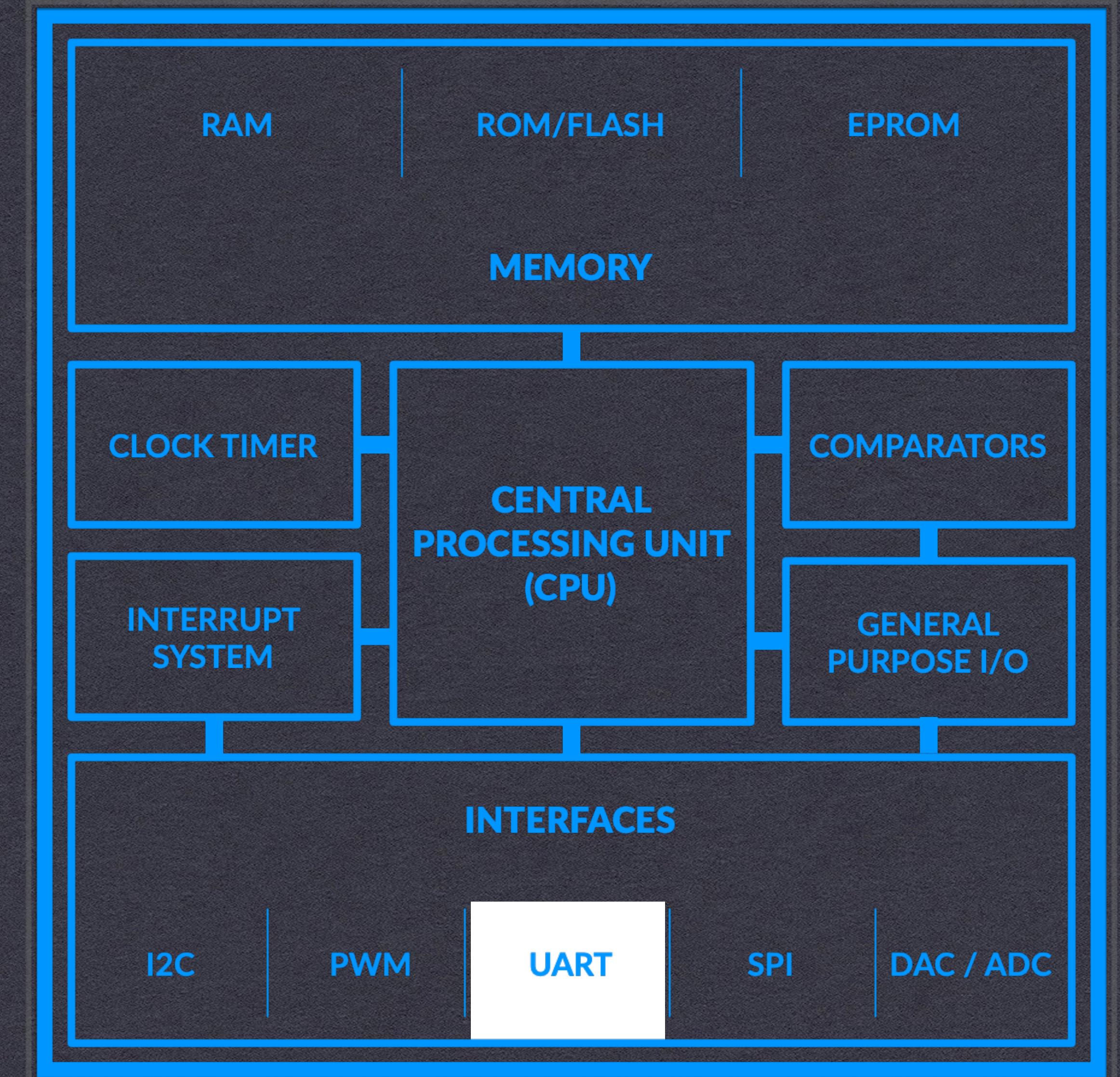
INTERFACES - PWM

PULSE-WIDTH MODULATION IS AN INTERFACE VERY COMMONLY USED WITH MOTORS.



WHAT IS A MICROCONTROLLER?

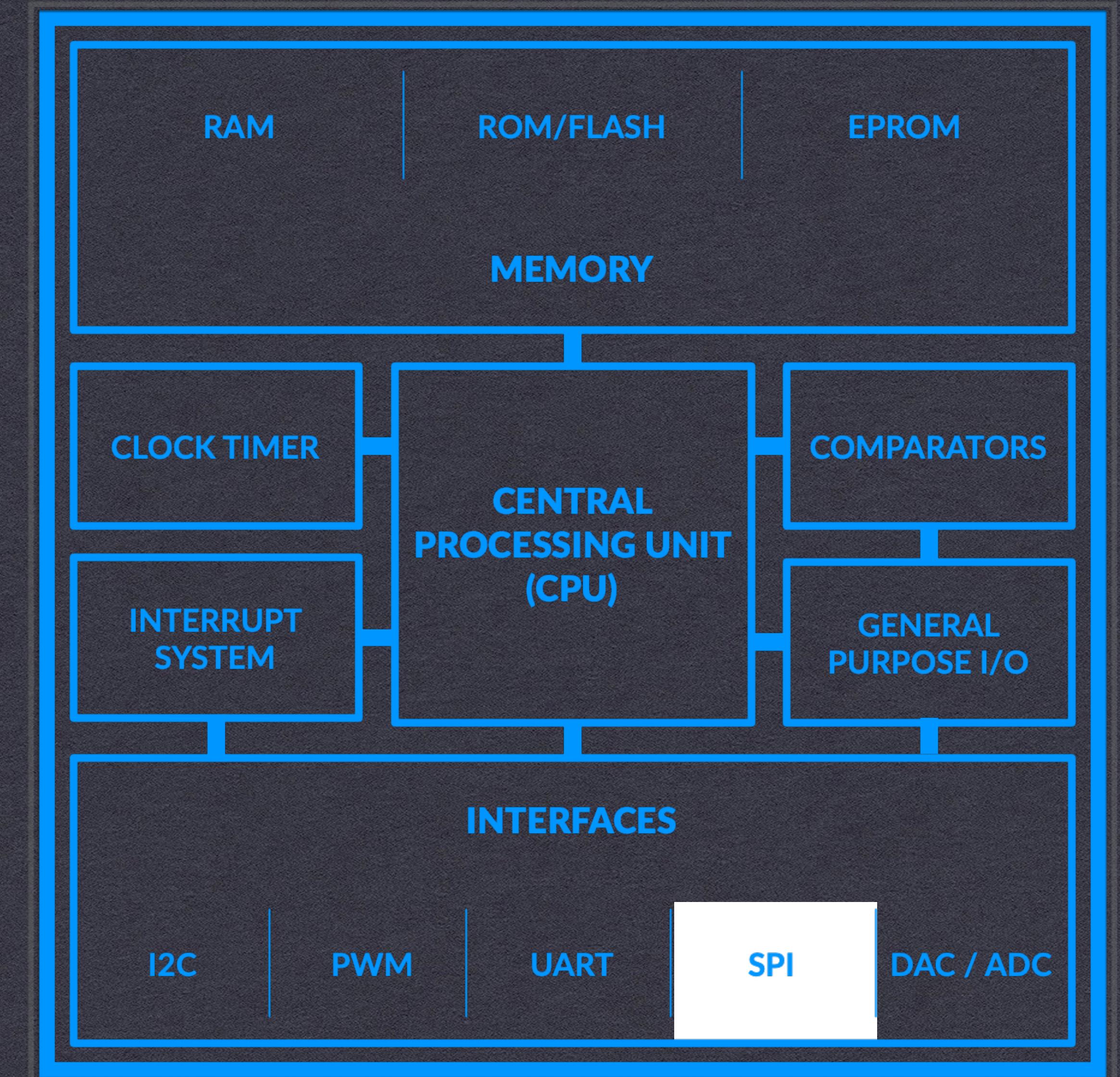
INTERFACES - UART
UNIVERSAL ASYNCHRONOUS RECEIVER-
TRANSMITTER IS USED FOR SENDING
DATA ONE PIECE AT A TIME (SERIALLY).



WHAT IS A MICROCONTROLLER?

INTERFACES - SPI

SERIAL PERIPHERAL INTERFACE IS USED FOR SENDING DATA ONE PIECE AT A TIME (SERIALLY) USING A CLOCK SIGNAL.

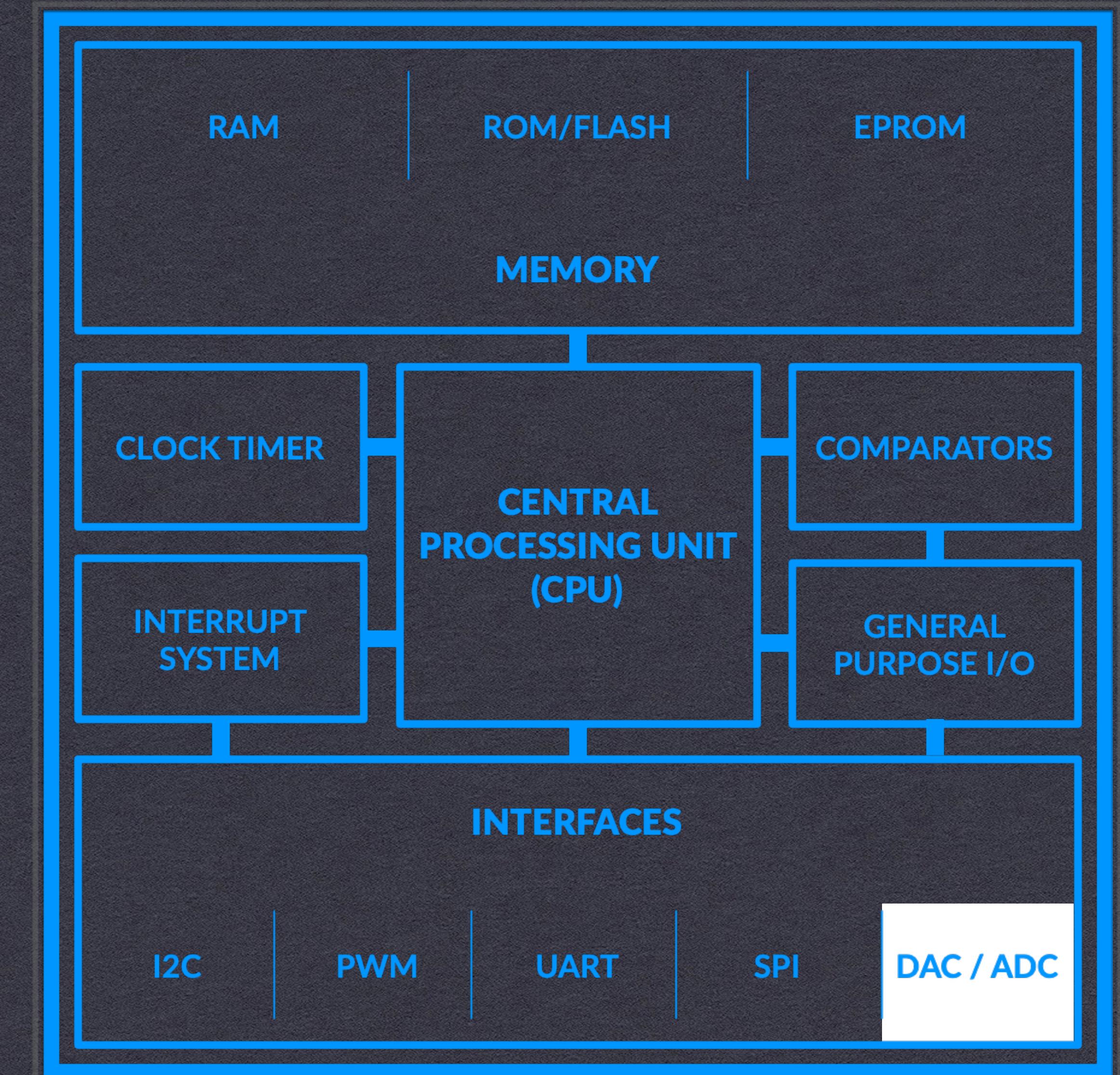


WHAT IS A MICROCONTROLLER?

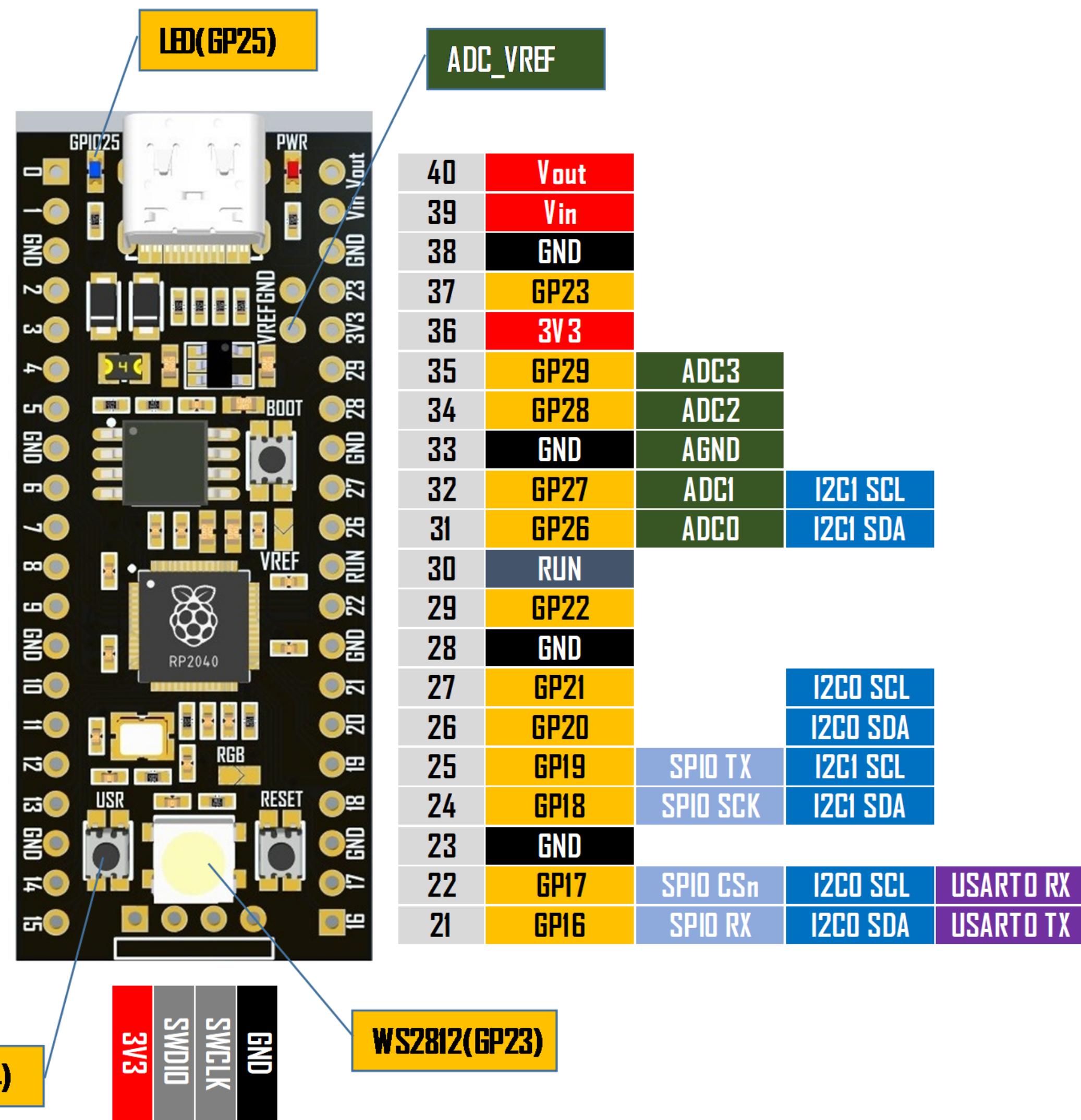
INTERFACES - DAC/ADC

A DIGITAL-TO-ANALOG CONVERTER (DAC)
TAKES DIGITAL DATA AND CONVERTS IT TO
ANALOG SIGNALS (LIKE A SPEAKER).

AN ANALOG-TO-DIGITAL CONVERTOR (ADC)
TAKES ANALOG DATA AND CONVERTS IT TO
DIGITAL SIGNALS (LIKE A MICROPHONE).



USART0 TX	I2C0 SDA	SPI0 RX	GPIO	1
USART0 RX	I2C0 SCL	SPI0 CS _n	GPIO	2
			GND	3
	I2C1 SDA	SPI0 SCK	GPIO	4
	I2C1 SCL	SPI0 TX	GPIO	5
USART1 TX	I2C0 SDA	SPI0 RX	GPIO	6
USART1 RX	I2C0 SCL	SPI0 CS _n	GPIO	7
			GND	8
	I2C1 SDA	SPI0 SCK	GPIO	9
	I2C1 SCL	SPI0 TX	GPIO	10
USART1 TX	I2C0 SDA	SPI1 RX	GPIO	11
USART1 RX	I2C0 SCL	SPI1 CS _n	GPIO	12
			GND	13
	I2C1 SDA	SPI1 SCK	GPIO	14
	I2C1 SCL	SPI1 TX	GPIO	15
USART0 TX	I2C0 SDA	SPI1 RX	GPIO	16
USART0 RX	I2C0 SCL	SPI1 CS _n	GPIO	17
			GND	18
	I2C1 SDA	SPI1 SCK	GPIO	19
	I2C1 SCL	SPI1 TX	GPIO	20



VCC-GND® Studio
源地®工作室

Power
Ground
USART
USART (default)
GPIO and PIO
ADC
SPI
I2C
SYS Control
Debugging



PROJECT

POWERING THE MICROCONTROLLER

LAB

1

MATERIALS

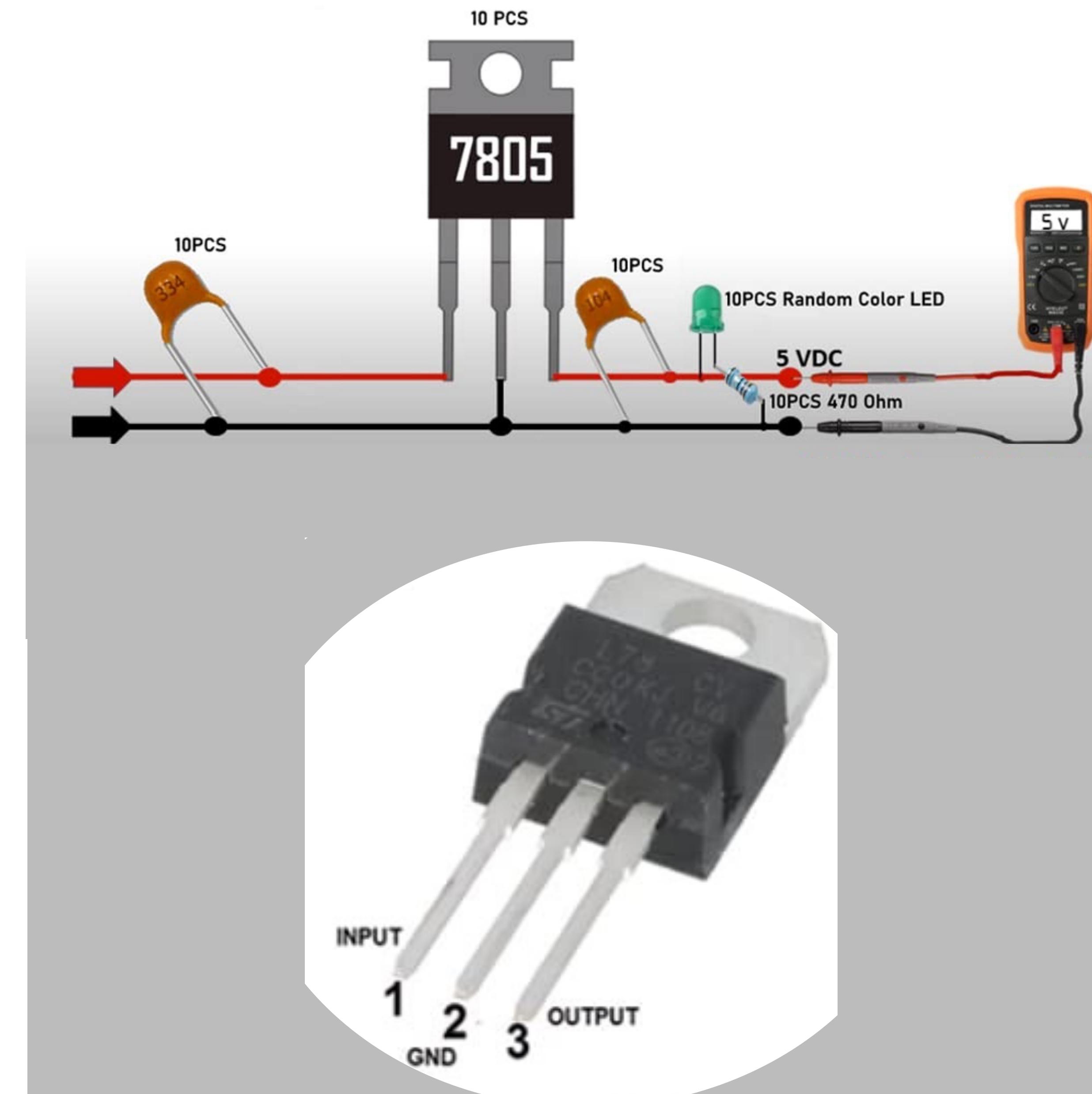
VOLTAGE REGULATOR, 3x WIRE

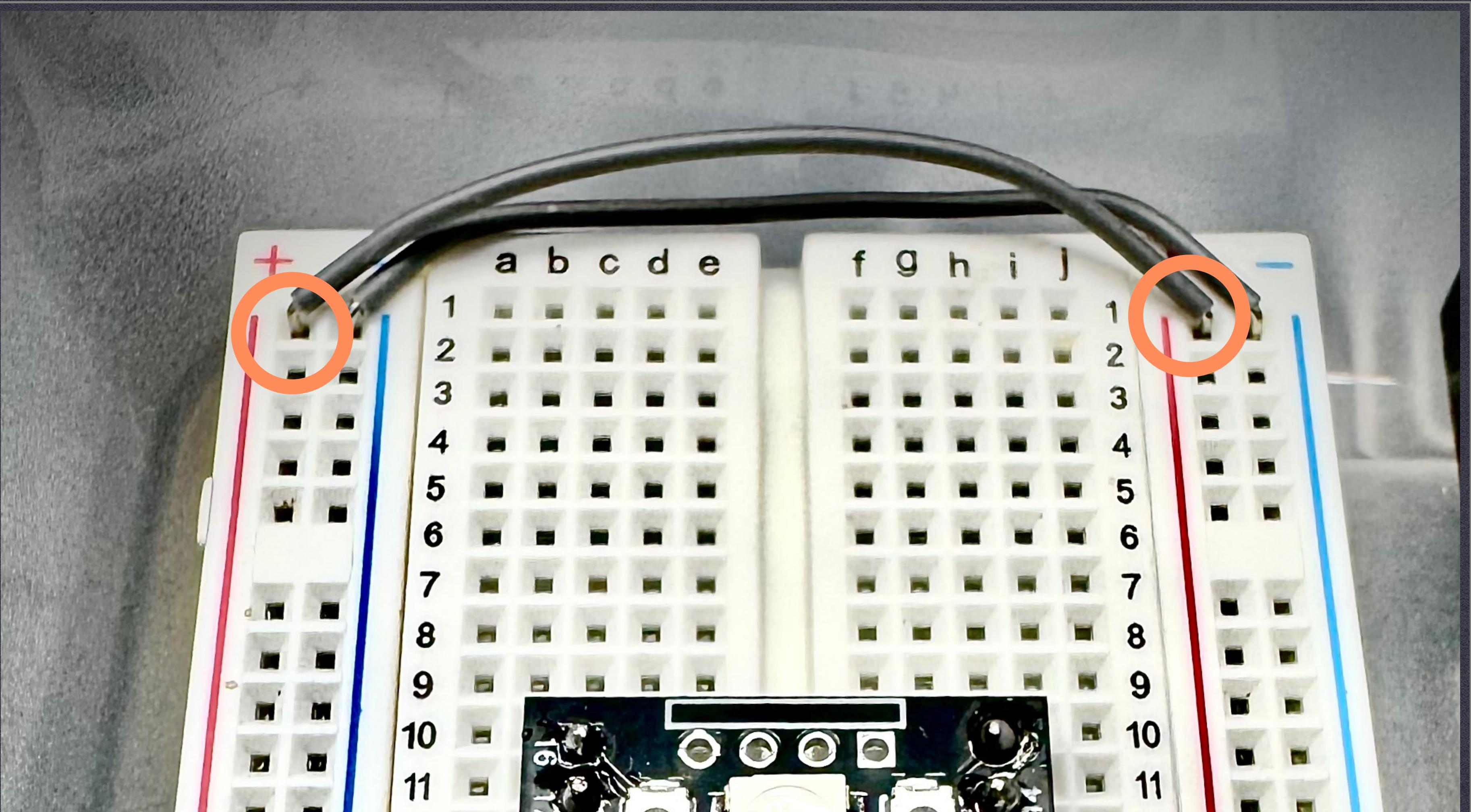
$V_I = 11 \text{ V}$, $I_O = 1 \text{ A}$, $T_J = 0 \text{ to } 125^\circ\text{C}$ (L7806AC), $T_J = -40 \text{ to } 125^\circ\text{C}$ (L7806AB), unless otherwise specified^(b).

Table 4. Electrical characteristics of L7806A

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	$T_J = 25^\circ\text{C}$	5.88	6	6.12	V
V_O	Output voltage	$I_O = 5 \text{ mA to } 1 \text{ A}$, $V_I = 8.6 \text{ to } 19 \text{ V}$	5.76	6	6.24	V
V_O	Output voltage	$I_O = 1 \text{ A}$, $V_I = 19 \text{ to } 21 \text{ V}$, $T_J = 25^\circ\text{C}$	5.76	6	6.24	V
$\Delta V_O^{(1)}$	Line regulation	$V_I = 8.6 \text{ to } 25 \text{ V}$, $I_O = 500 \text{ mA}$, $T_J = 25^\circ\text{C}$		9	60	mV
		$V_I = 9 \text{ to } 13 \text{ V}$		11	60	mV
		$V_I = 9 \text{ to } 13 \text{ V}$, $T_J = 25^\circ\text{C}$		3	30	mV
		$V_I = 8.3 \text{ to } 21 \text{ V}$, $T_J = 25^\circ\text{C}$		9	60	mV
$\Delta V_O^{(1)}$	Load regulation	$I_O = 5 \text{ mA to } 1 \text{ A}$		25	100	mV
		$I_O = 5 \text{ mA to } 1.5 \text{ A}$, $T_J = 25^\circ\text{C}$		30	100	
		$I_O = 250 \text{ to } 750 \text{ mA}$		10	50	
I_q	Quiescent current	$T_J = 25^\circ\text{C}$		4.3	6	mA
					6	mA
ΔI_q	Quiescent current change	$V_I = 9 \text{ to } 24 \text{ V}$, $I_O = 500 \text{ mA}$			0.8	mA
		$V_I = 8.6 \text{ to } 21 \text{ V}$, $T_J = 25^\circ\text{C}$			0.8	mA
		$I_O = 5 \text{ mA to } 1 \text{ A}$			0.5	mA
SVR	Supply voltage rejection	$V_I = 9 \text{ to } 19 \text{ V}$, $f = 120 \text{ Hz}$, $I_O = 500 \text{ mA}$		65		dB
V_d	Dropout voltage	$I_O = 1 \text{ A}$, $T_J = 25^\circ\text{C}$		2		V
eN	Output noise voltage	$T_A = 25^\circ\text{C}$, $B = 10 \text{ Hz to } 100 \text{ kHz}$		10		$\mu\text{V}/V_O$
R_O	Output resistance	$f = 1 \text{ kHz}$		17		$\text{m}\Omega$
I_{sc}	Short circuit current	$V_I = 35 \text{ V}$, $T_A = 25^\circ\text{C}$		0.2		A
I_{scp}	Short circuit peak current	$T_J = 25^\circ\text{C}$		2.2		A
$\Delta V_O/\Delta T$	Output voltage drift			-0.8		$\text{mV}/^\circ\text{C}$

1. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.





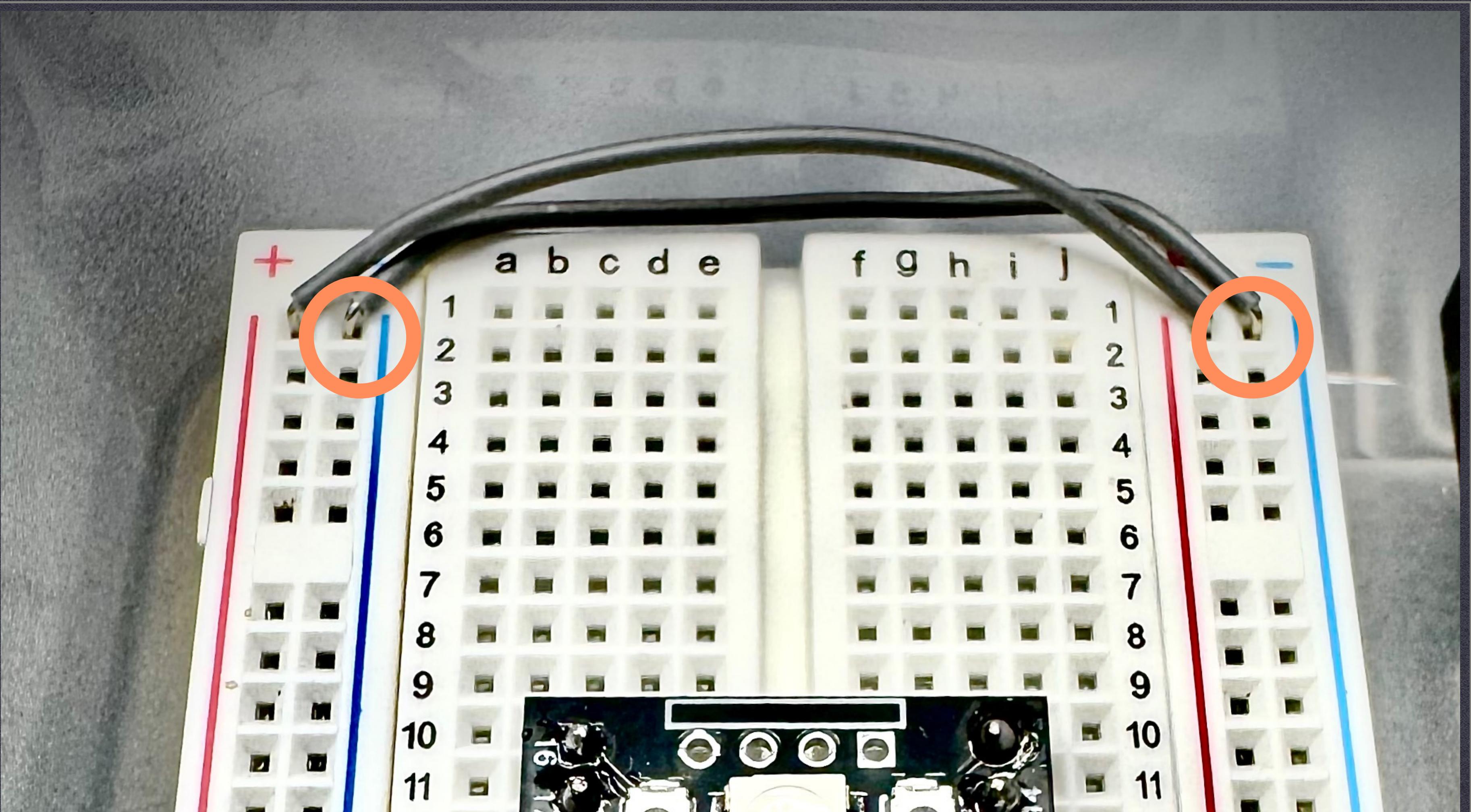
TASK

**CONNECT POSITIVE RAILS TOGETHER
(+ TO +)**

STEP
1

MATERIALS

2x WIRE (MEDIUM)



TASK

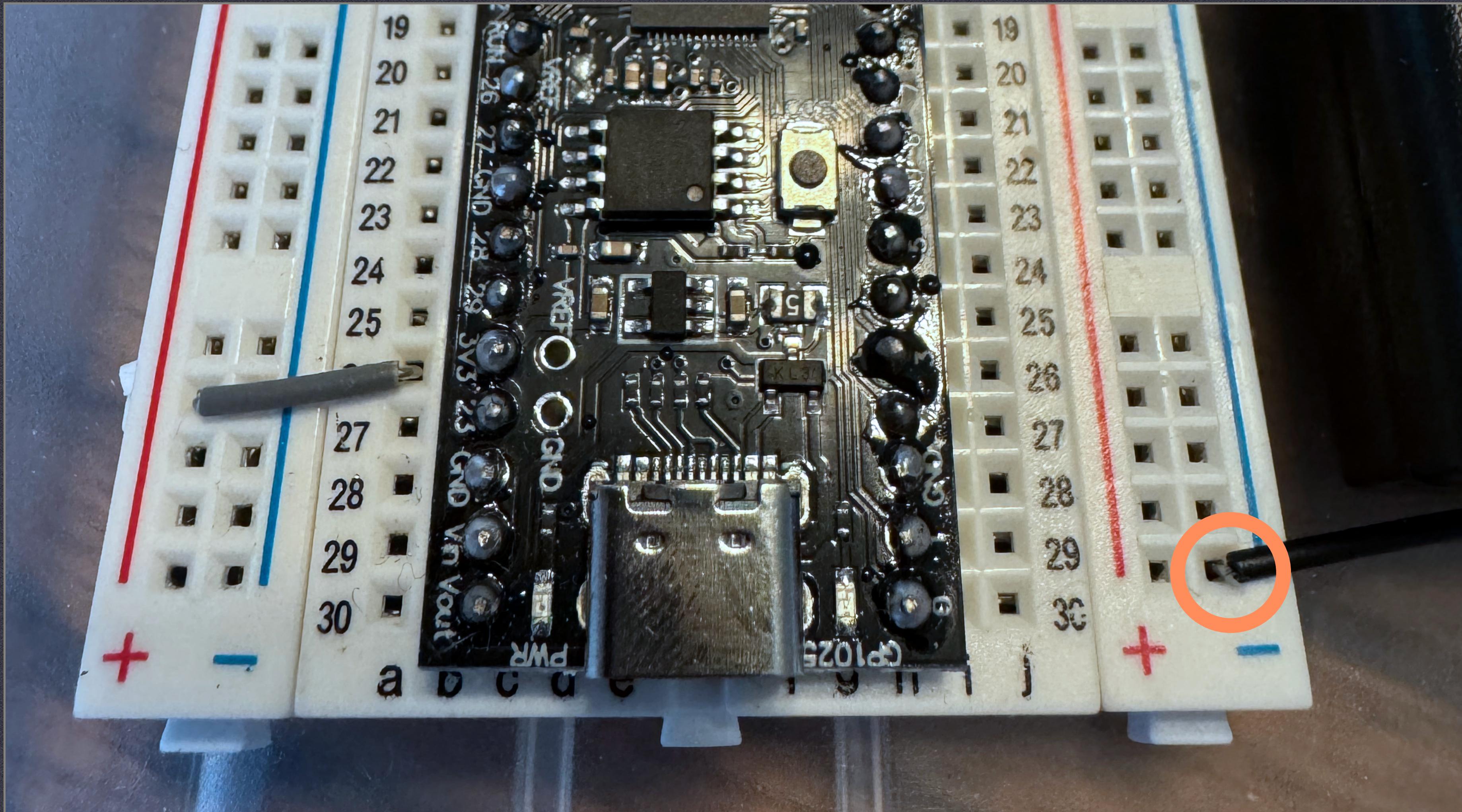
CONNECT NEGATIVE RAILS TOGETHER

(- TO -)

STEP
2

MATERIALS

2x WIRE (MEDIUM)



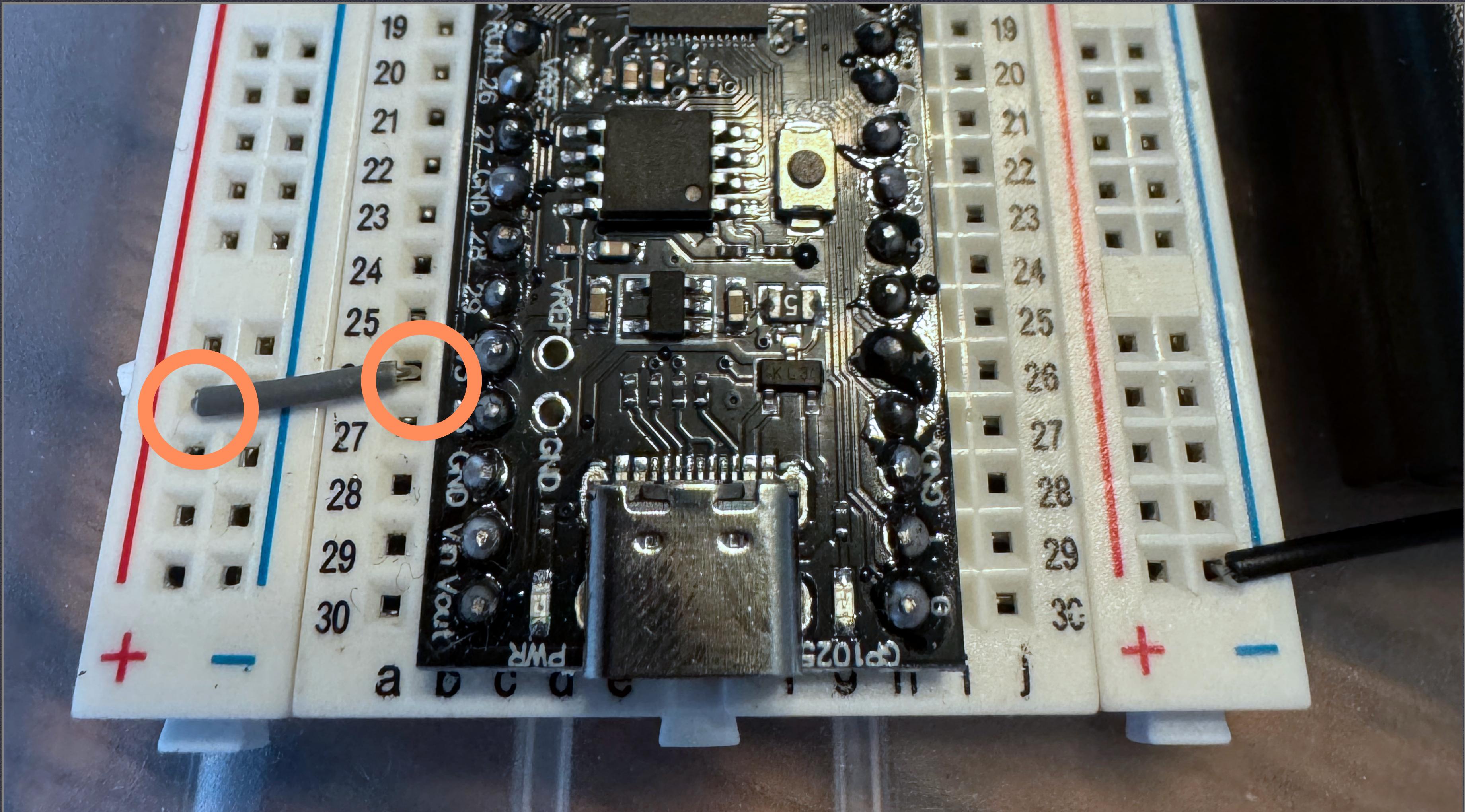
TASK

ATTACH NEGATIVE WIRE (- TO - BATTERY WIRE)

STEP
3

MATERIALS

1x WIRE (TINY)



TASK

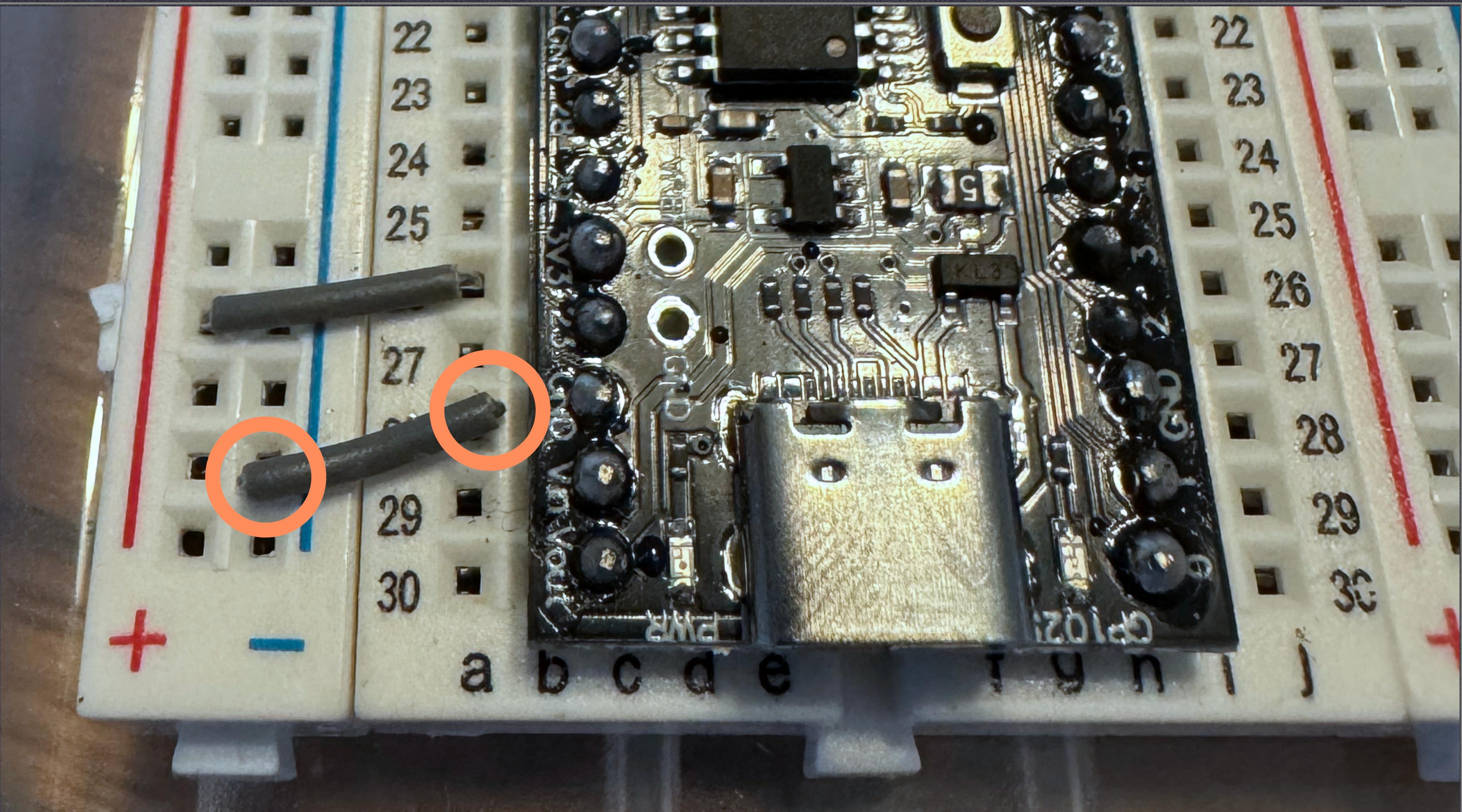
ATTACH POSITIVE WIRE (+ TO 26a)

STEP

4

MATERIALS

1x WIRE (TINY)



TASK

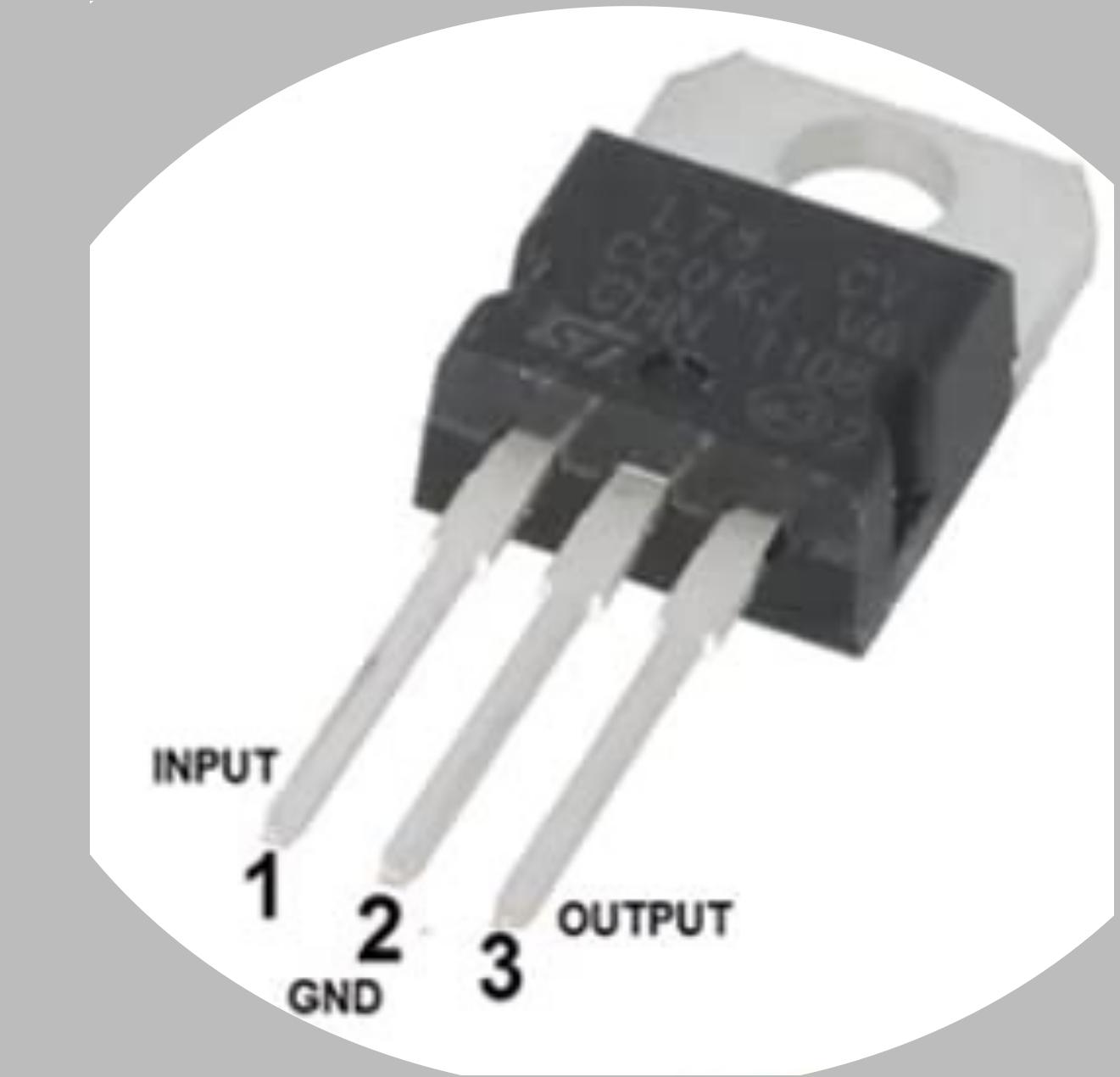
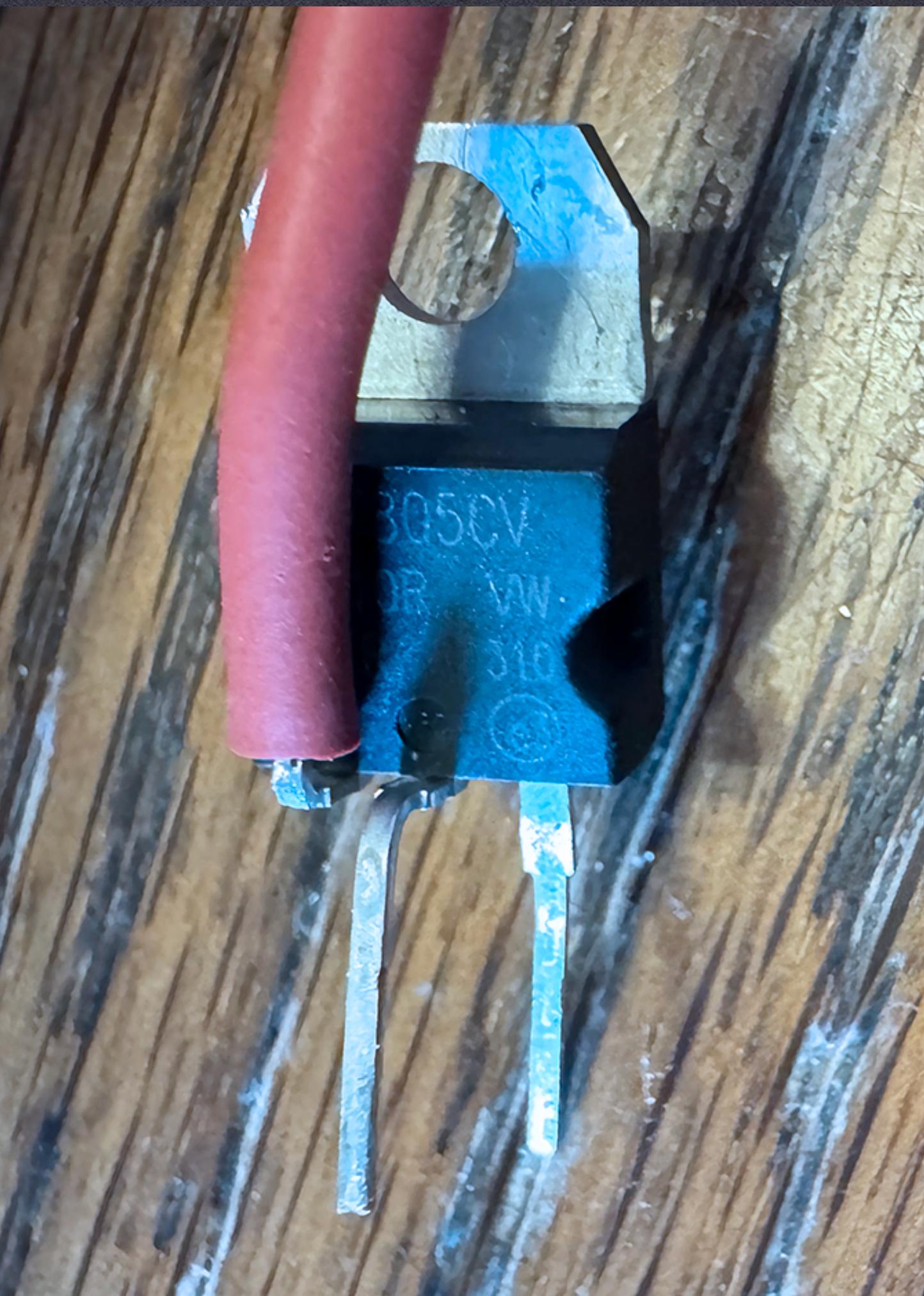
ATTACH NEGATIVE WIRE (- TO 28a)

STEP

5

MATERIALS

1x WIRE (TINY)



TASK

CONNECT REGULATOR TO BATTERY

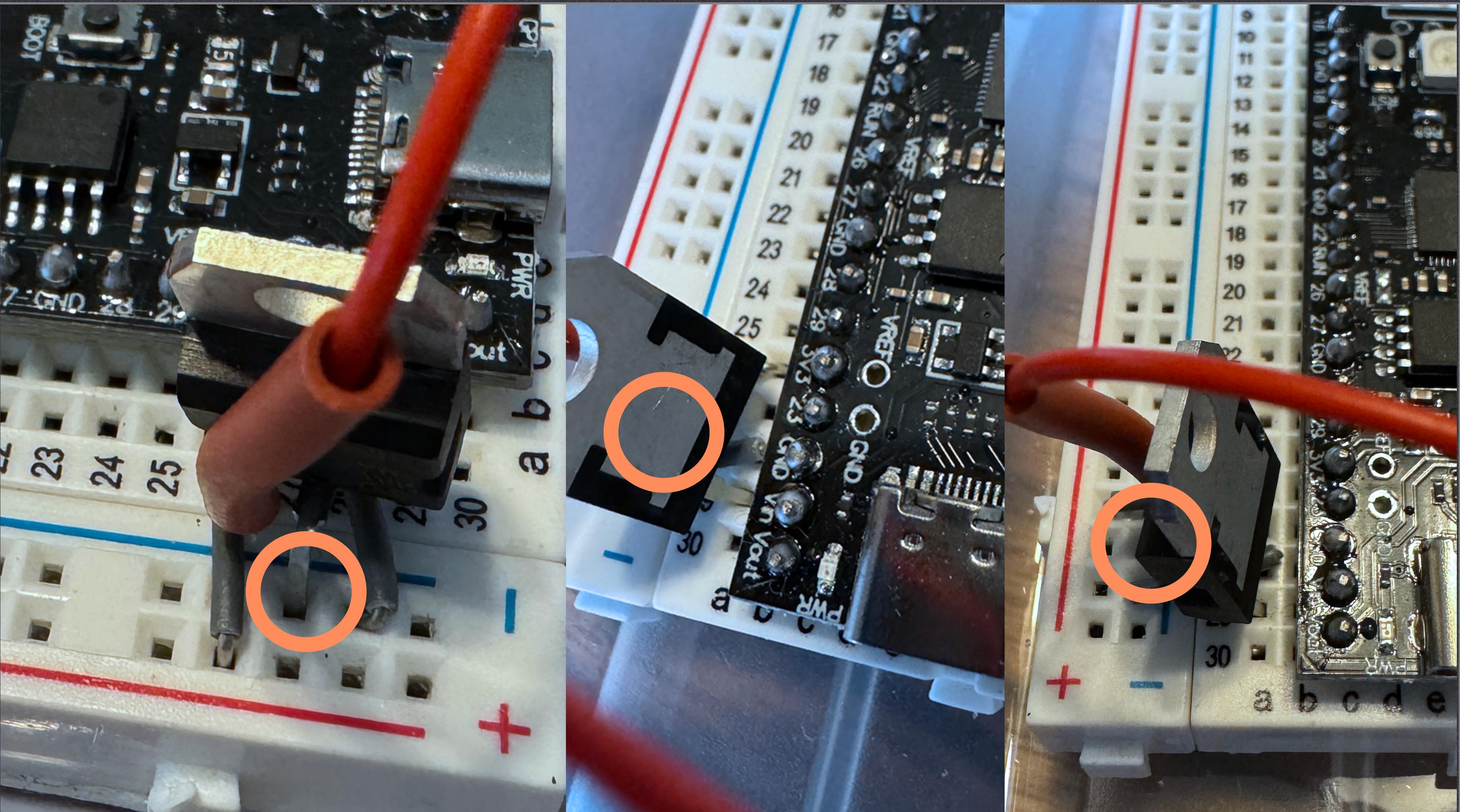
“INPUT” PIN HOOKS TO + BATTERY WIRE

STEP

6

MATERIALS

VOLTAGE REGULATOR (LM7805)



TASK

ATTACH REGULATOR TO BOARD

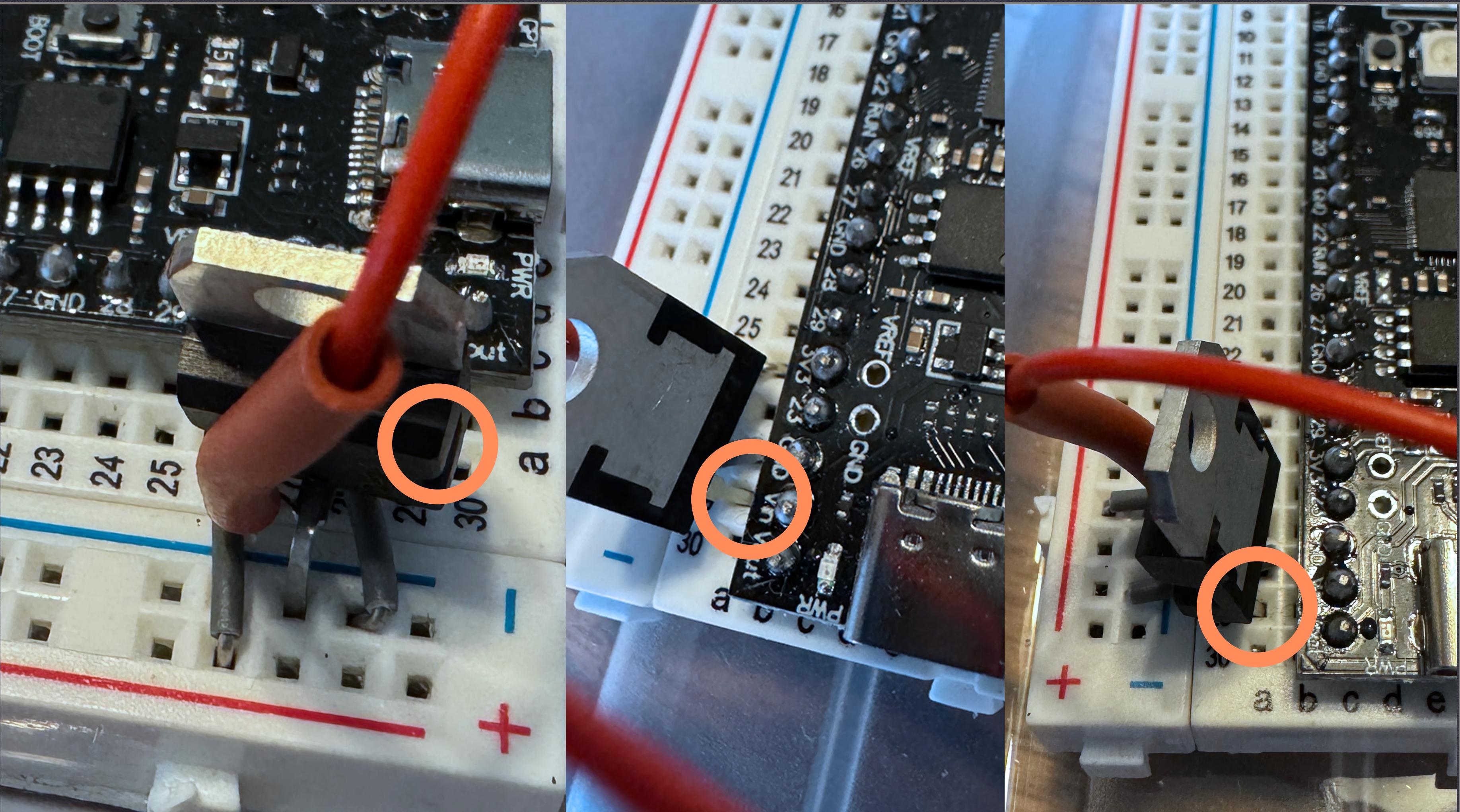
“GND” PIN HOOKS TO -

STEP

7

MATERIALS

VOLTAGE REGULATOR (LM7805)



TASK

ATTACH REGULATOR TO BOARD “OUTPUT” PIN HOOKS TO 29a

STEP

7

MATERIALS

VOLTAGE REGULATOR (LM7805)



TASK

**TURN ON BATTERY PACK
(IF THERE IS NOT AN IMMEDIATE RESPONSE, TURN OFF)**

STEP

9

MATERIALS

BATTERY PACK