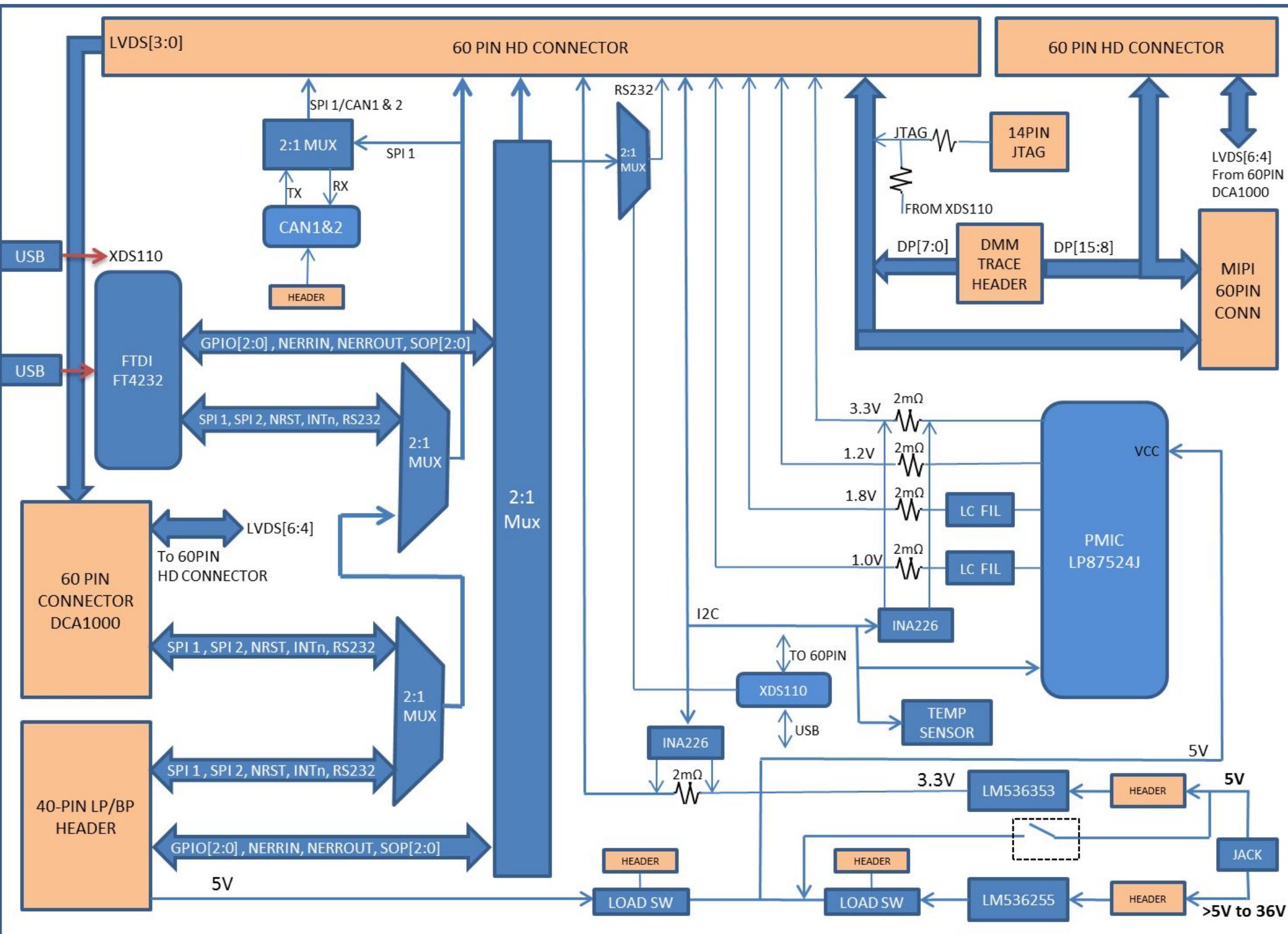
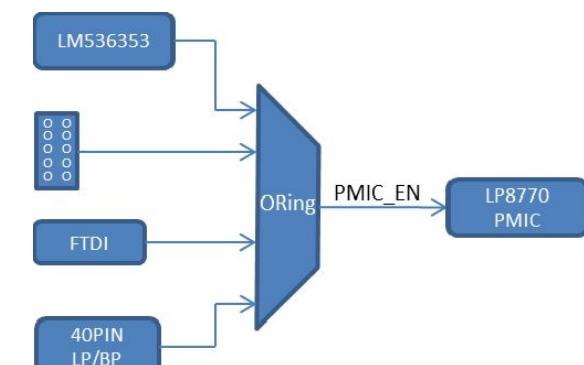


Revision History

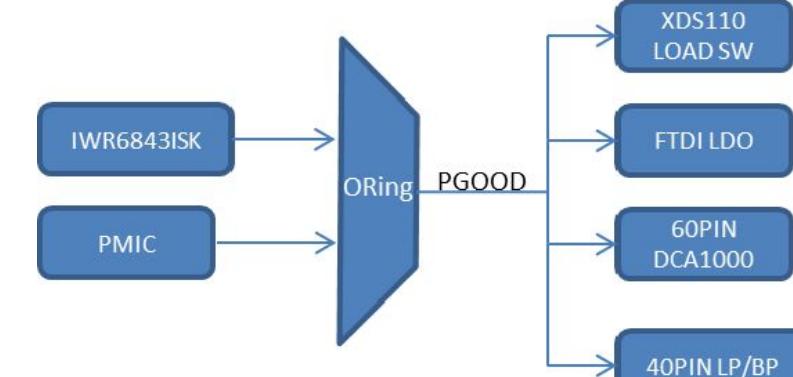
Rev	ECN #	Approved Date	Approved by	Notes
B	1	29/11/18	Chehan Kumar Y.B	J17 connector nets modified
B	2	29/11/18	Chehan Kumar Y.B	Mounted R76,R79,R81,R92 resistors
B	3	29/11/18	Chehan Kumar Y.B	Made R43 and R205 as DNP
B	4	29/11/18	Chehan Kumar Y.B	Changed the R120 and C24 part number



PMIC EN ARCHITECTURE



PGOOD ARCHITECTURE



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Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 11/29/2018
TID #:	N/A	Project Title: MMWAVEICBOOST
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 22
Drawn By: Chethan Kumar Y.B	File: PROC074B_Block_diagram.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

MUX SETTINGS

Switch Refdes	Default position	Position for STAND ALONE mode*	Position for DCA1000 mode	Position for 40Pin LP/BP
S1	OFF (SPI-1)	OFF	OFF	OFF
S2	ON (40pin)	Default position	Default position	ON
S3	ON (FTDI)	ON	Default position	OFF
S4	ON (FTDI/40pin/J16)	ON	OFF	ON
S5 ¹	ON (XDS110)	Default position	Default position	Default position
S6	ON (FTDI)	ON	OFF	OFF
S7	ON (FTDI)	ON	OFF	OFF
S8	OFF (60pin)	Default position	OFF	ON
S9	OFF (60pin)	Default position	OFF	ON
S10	ON (FTDI)	ON	Default position	OFF
S11	ON (FTDI)	ON	Default position	OFF
S12	ON (XDS110)	Default position	Default position	Default position

Table 1 : Switch settings for different sources

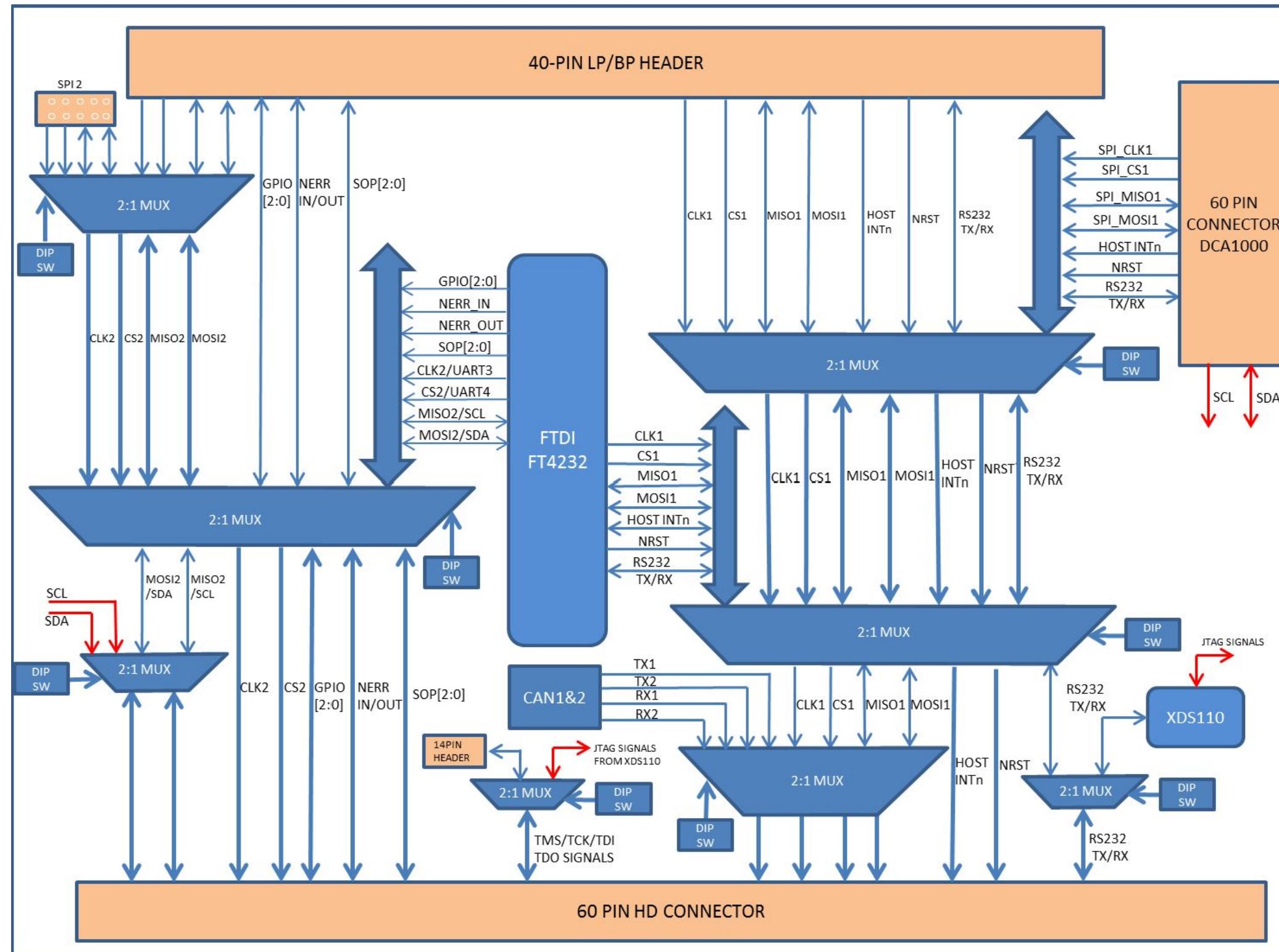
*Standalone mode means the combination of Starter kit and MMWAVEICBOOST

(1)S5 has RS232 connections from 40pin/FTDI/60pin/XDS110. Default position will be XDS110.

I2C DEVICES

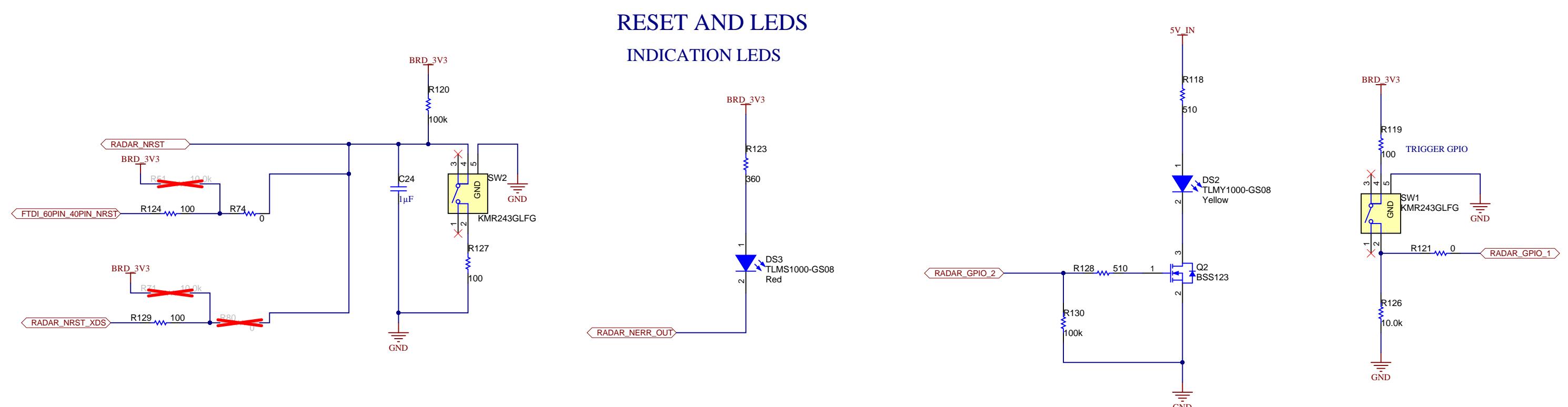
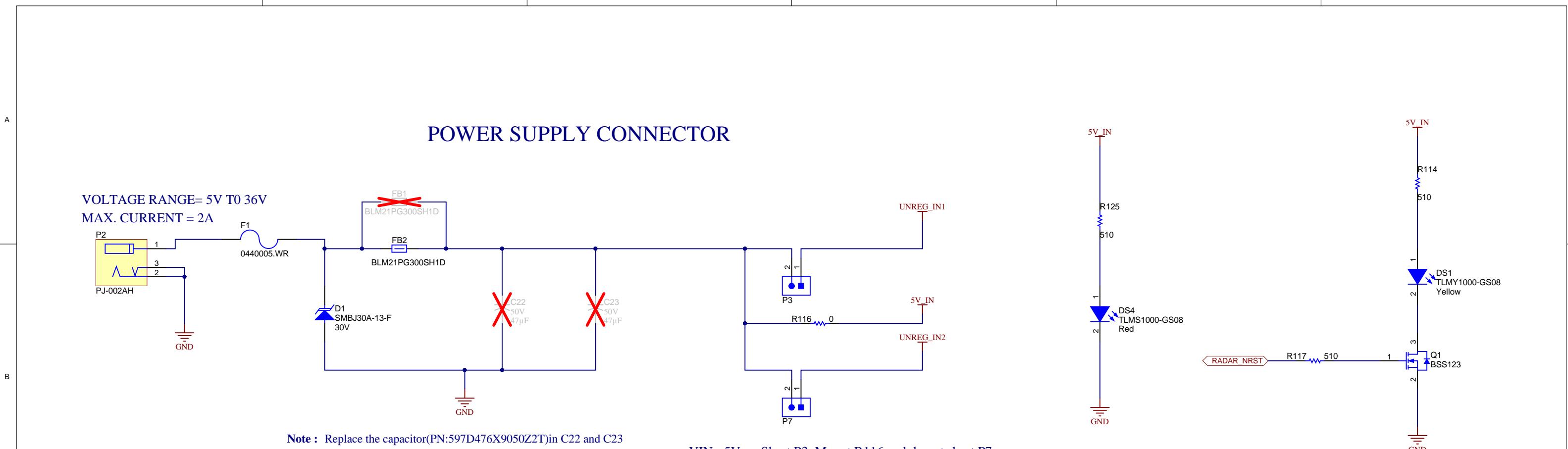
S.No	DESCRIPTION	I2C ADDRESS
1	PMIC	110 0000
2	CURRENT SENSOR 3.3V	100 0010
3	CURRENT SENSOR 3.3V (PMIC)	100 0011
4	CURRENT SENSOR 1.8V	100 0110
5	CURRENT SENSOR 1.2V	100 0111
6	CURRENT SENSOR 1.0V	100 1100
7	TEMPERATURE SENSOR1	100 1001
8	TEMPERATURE SENSOR2	100 1000

MUXING BLOCK DIAGRAM



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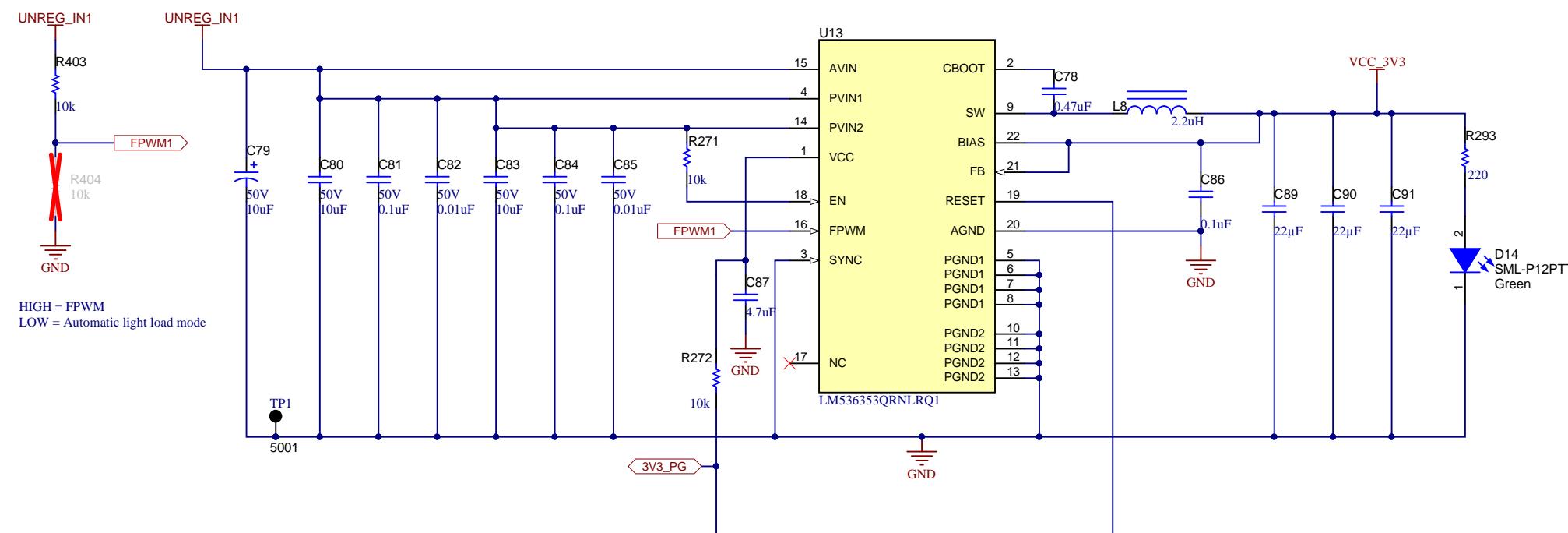
Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 10/11/2018
TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 22
Drawn By:	File: PROC074B_Muxing_Block_Diagram.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018



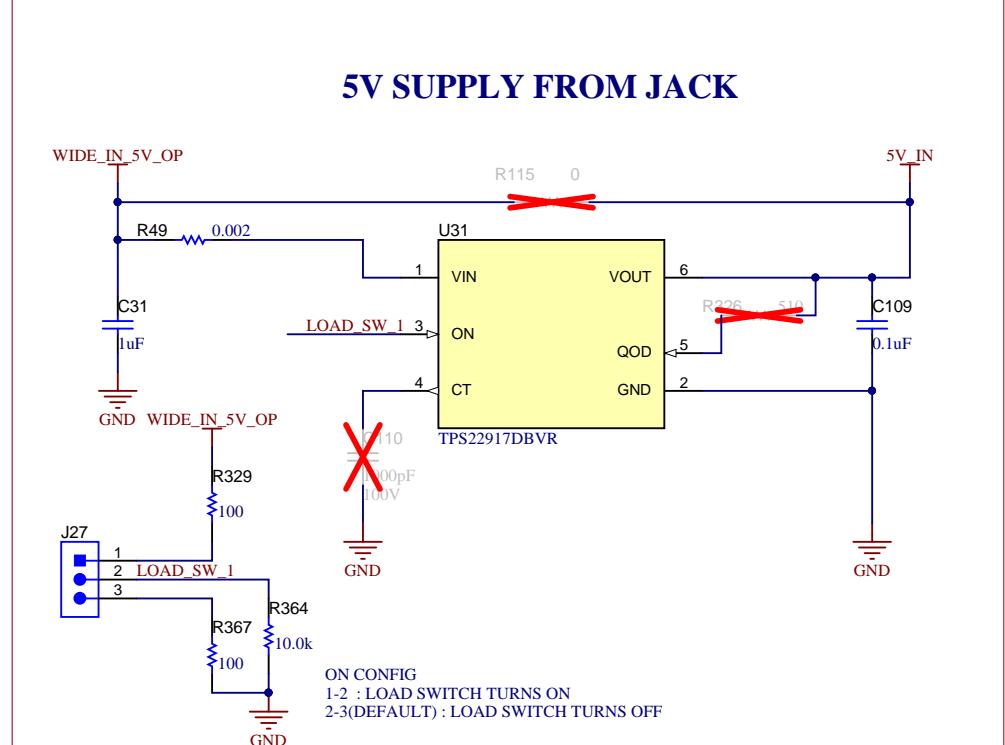
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TID #:	N/A	Project Title: MMWAVEICBOOST
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 22
Drawn By:	File: PROC074B_PWR_RST_LEDs.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

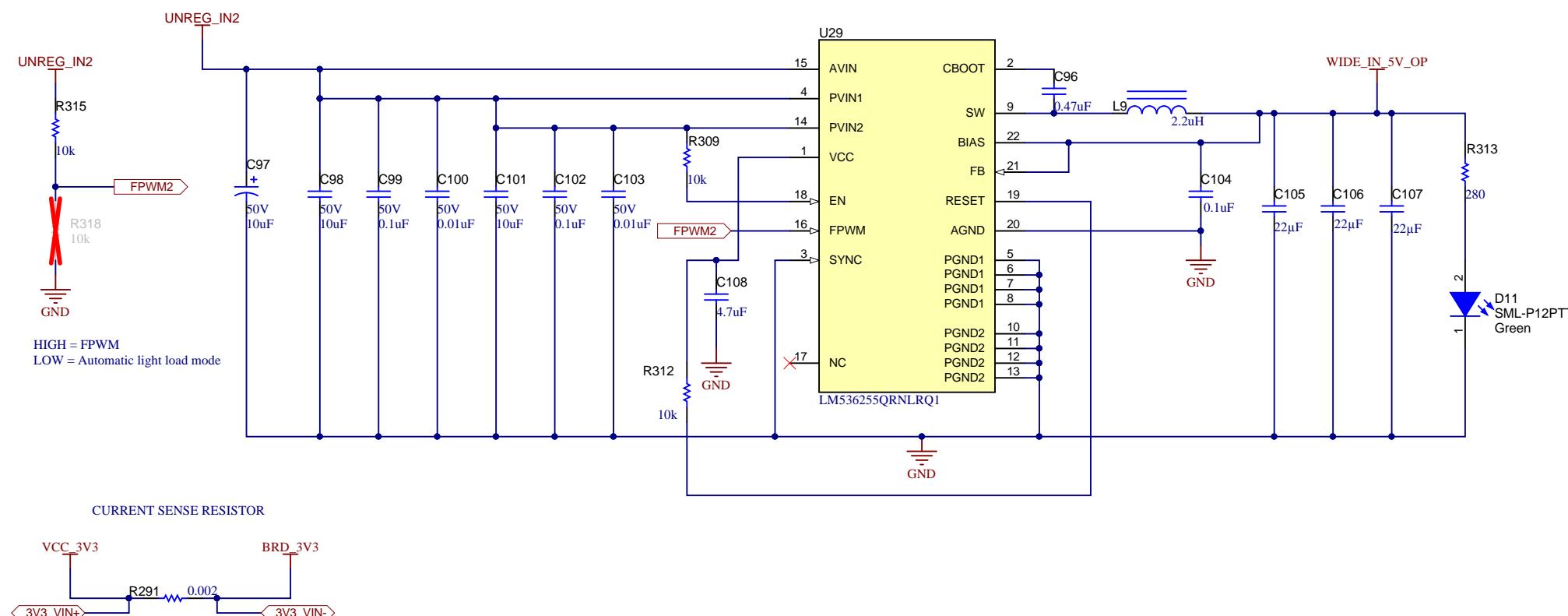
POWER SUPPLY INPUT 5V TO 3.3V OUTPUT



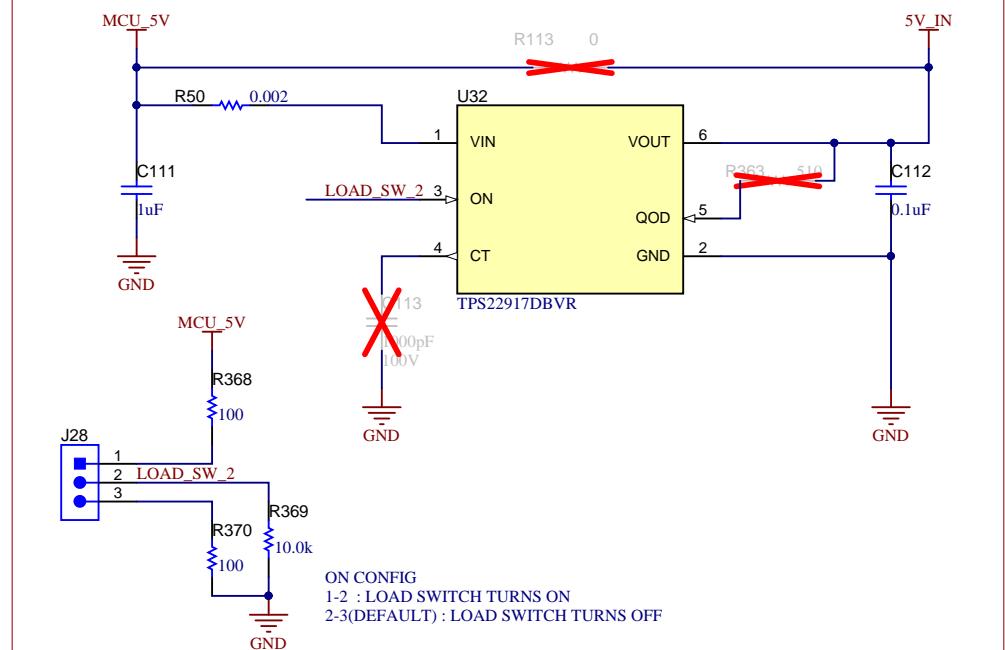
5V SUPPLY FROM JACK



POWER SUPPLY INPUT(6V-36V) TO 5V OUTPUT



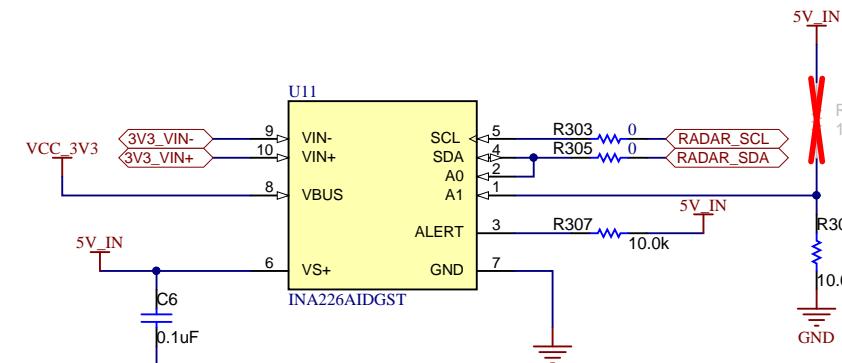
5V SUPPLY FROM THE 40PIN LP/BP CONNECTOR



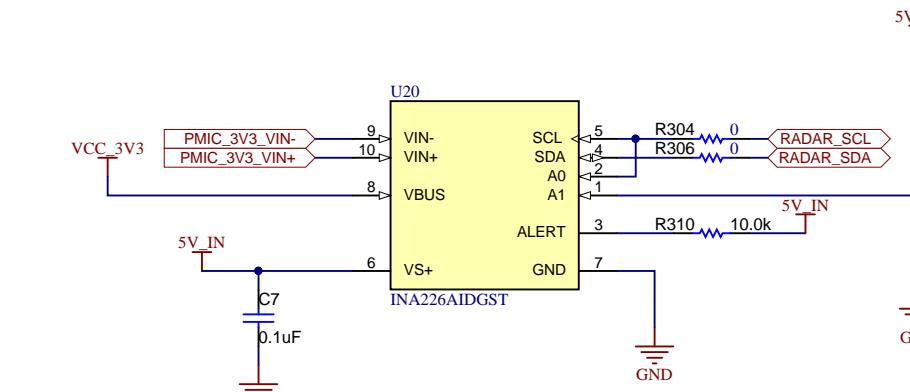
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Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 10/11/2018
TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 5 of 22
Drawn By:	File: PROC074B_PWR_INPUT_REG.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

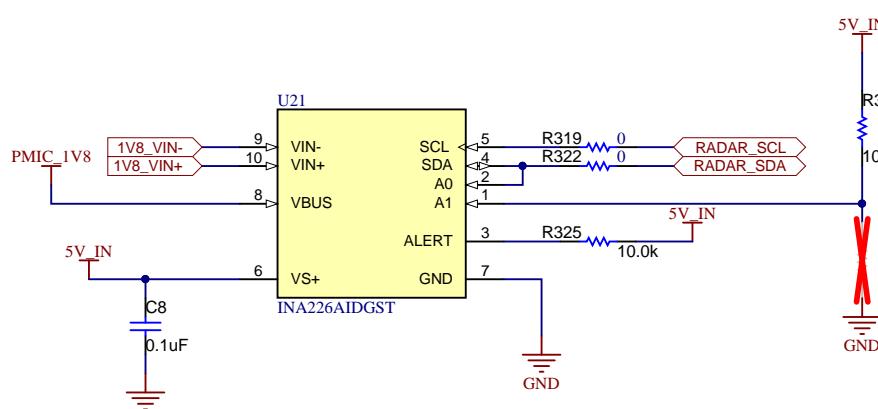
Current Sensors



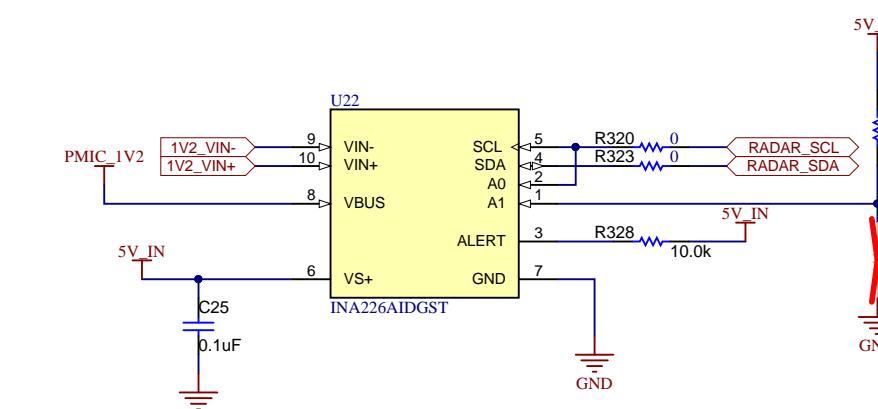
I2C Address : 100 0010



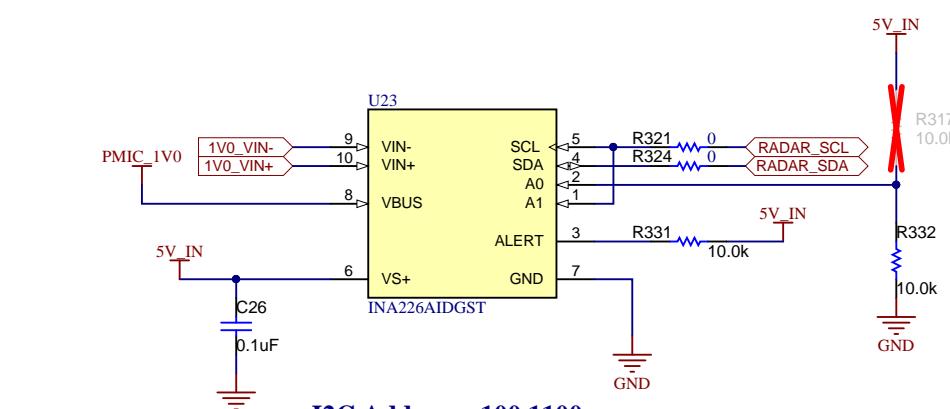
I2C Address : 100 0011



I2C Address : 100 0110



I2C Address : 100 0111

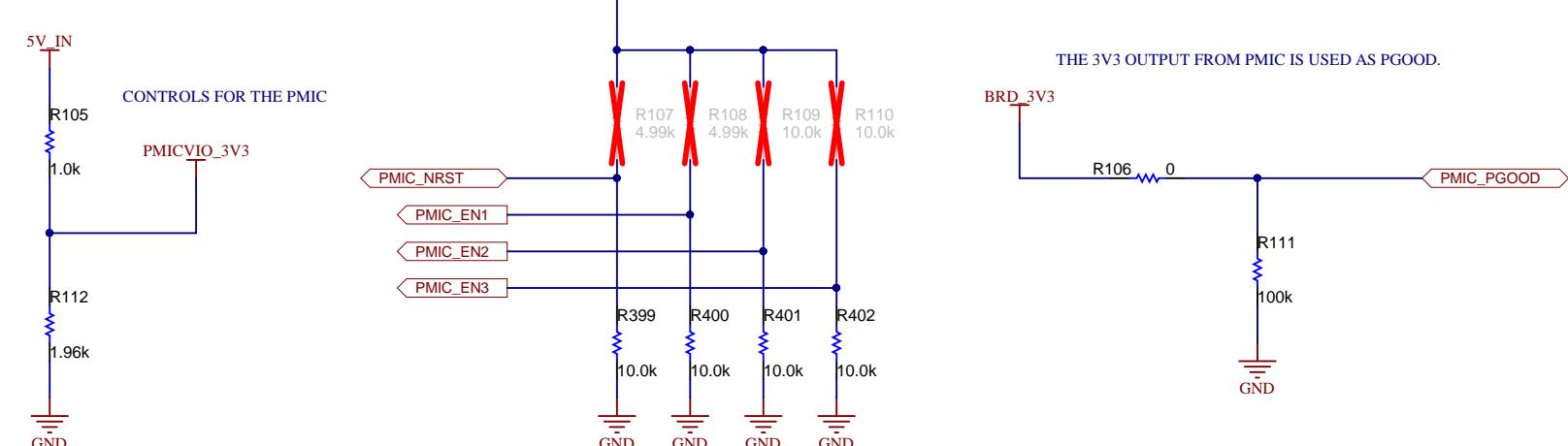
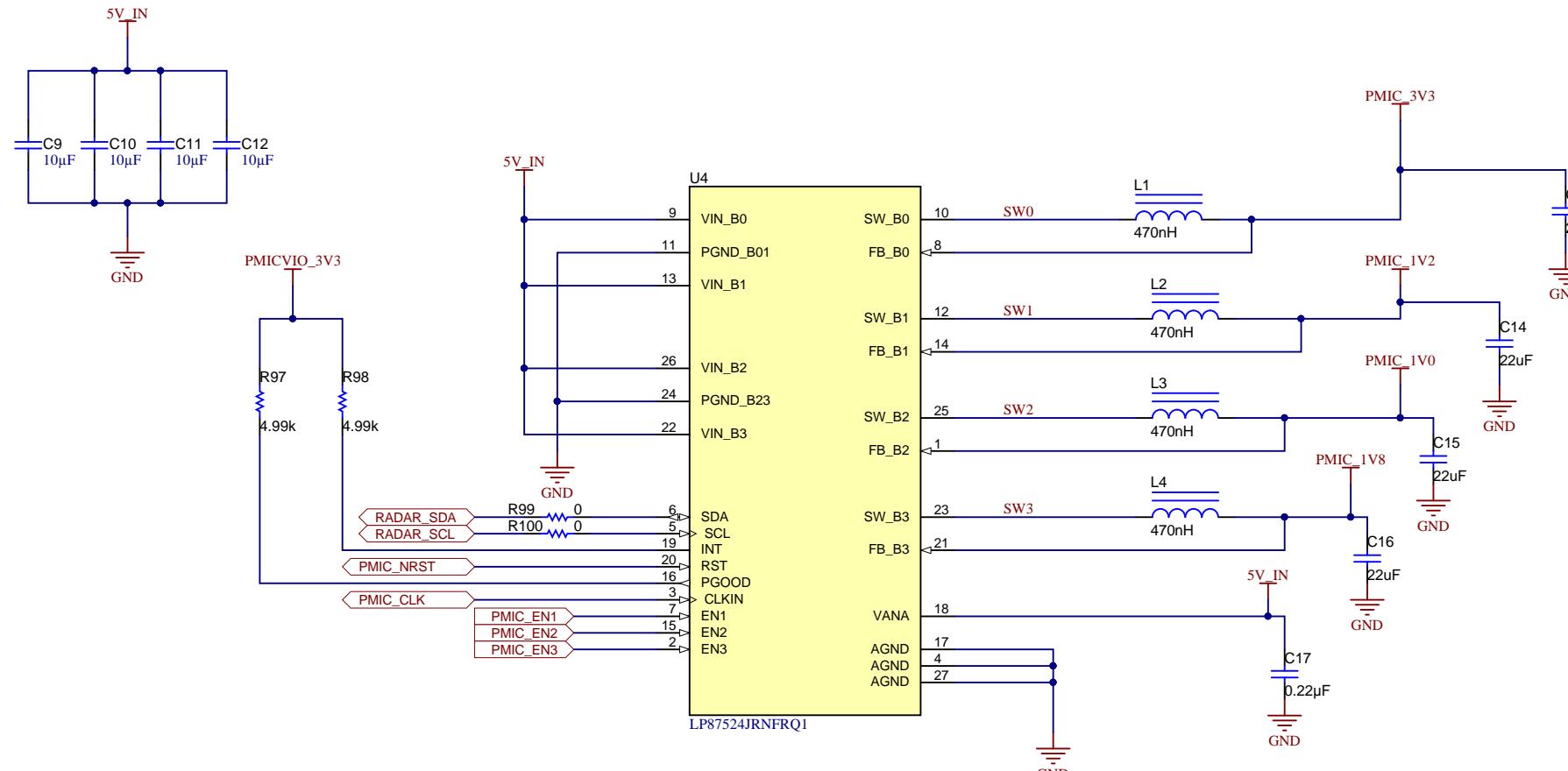


I2C Address : 100 1100

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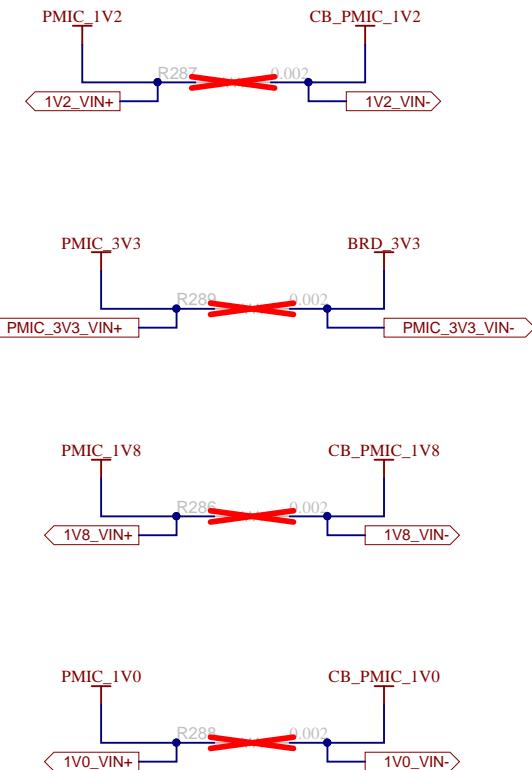
Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 10/11/2018
TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 6 of 22
Drawn By:	File: PROC074B_Current_Sensors.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

PMIC (3.3V, 1.2V, 1.8V, 1.0V OUTPUTS)

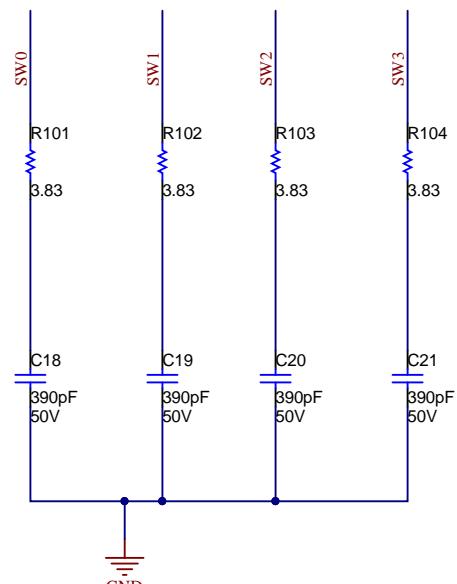


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CURRENT SENSE RESISTORS

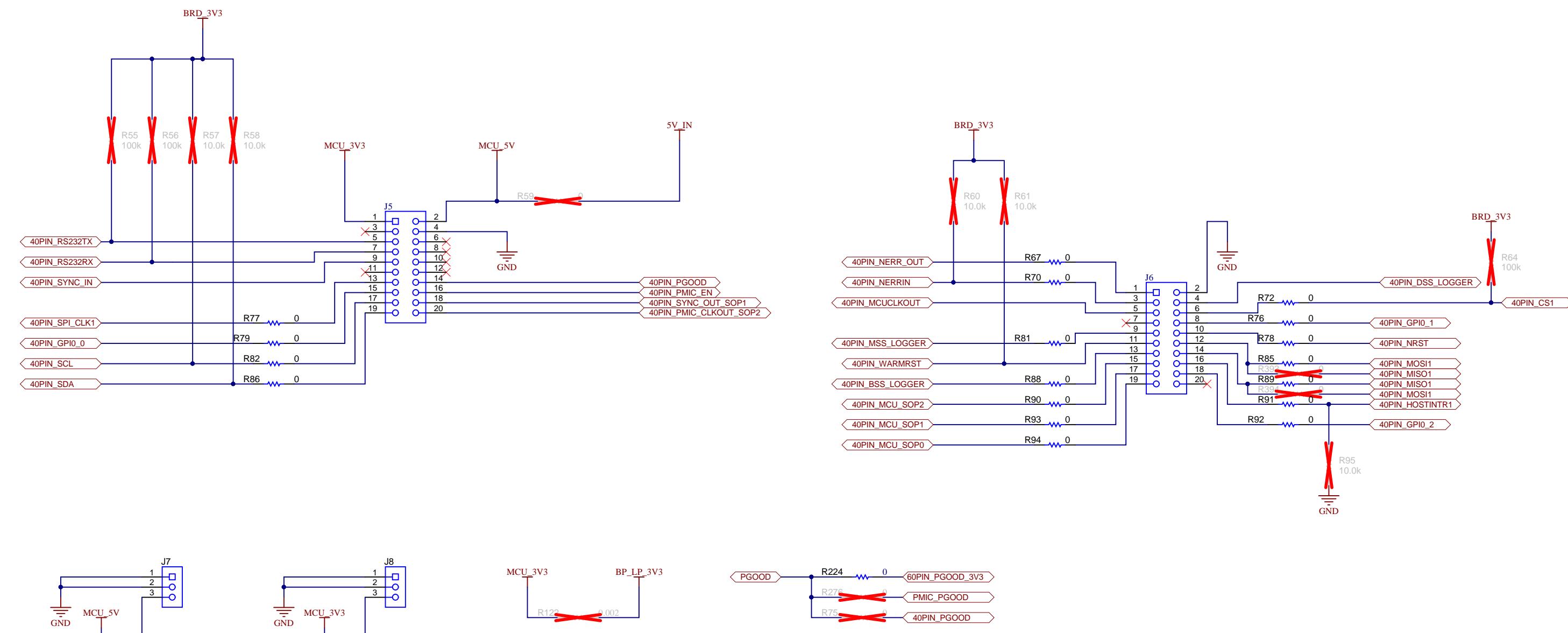


SNUBBER ON SWITCHING NODES



Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 11/28/2018
TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 7 of 22
Drawn By:	File: PROC074B_LP8752J_PMIC.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

BP/LP CONNECTOR



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TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 8 of 22
Drawn By:	File: PROC074B_LP_Connector.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

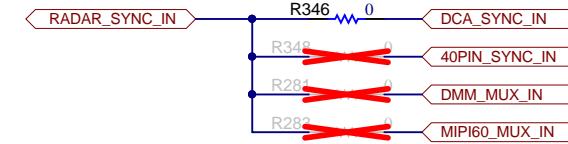
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BP/LP RNR OPTIONS

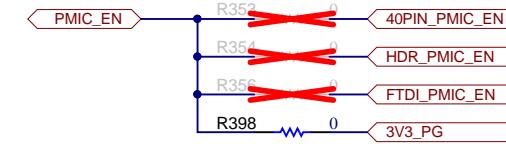
RNR FOR MCUCLKOUT



RNR FOR SYNC IN



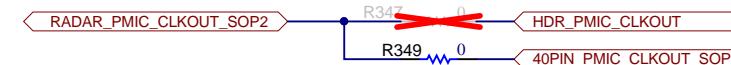
RNR FOR PMIC ENABLE



RNR FOR WARMRST



RNR FOR PMIC CLKOUT



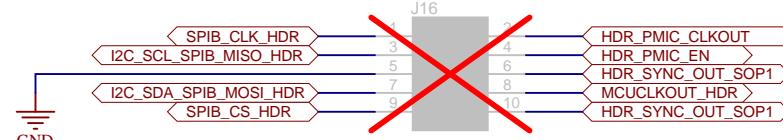
RNR FOR DSS LOGGER



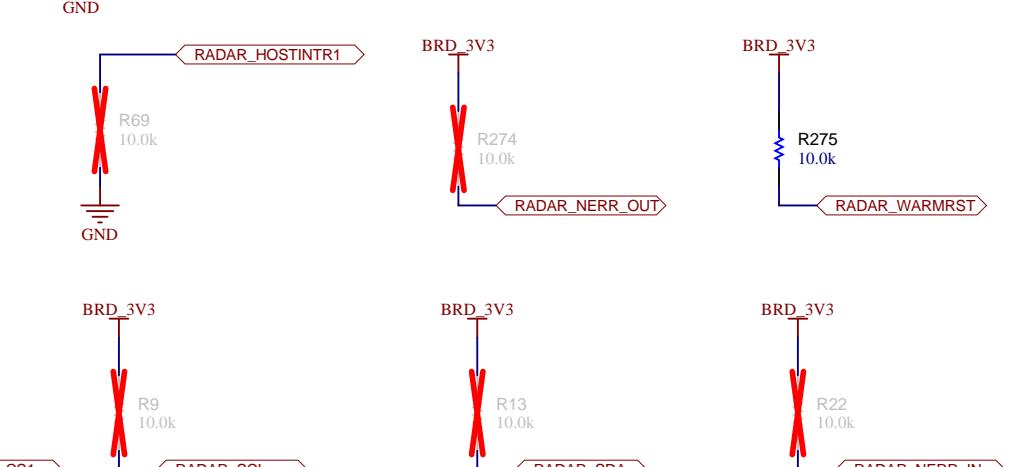
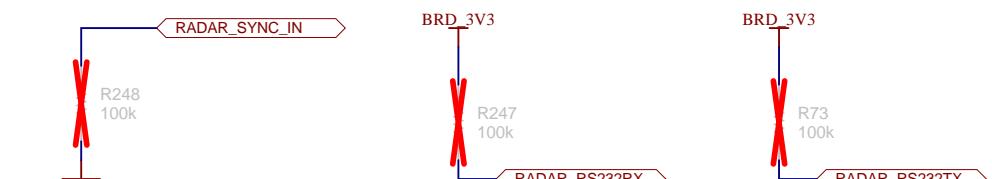
PULL UPS/DOWNs RESISTORS FOR

I2C,WARMRST,NERROUT, NERRIN, RS232, SYNC_IN & HOST_INTn

LP/BP SPARE PINS HEADER 1



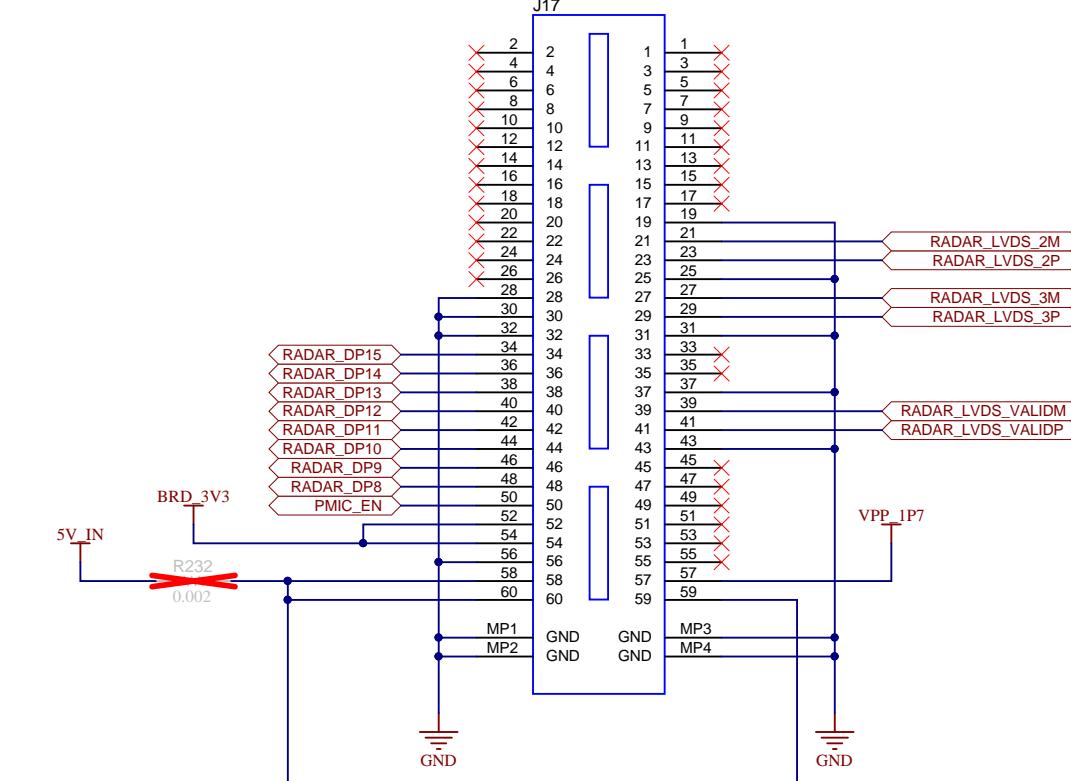
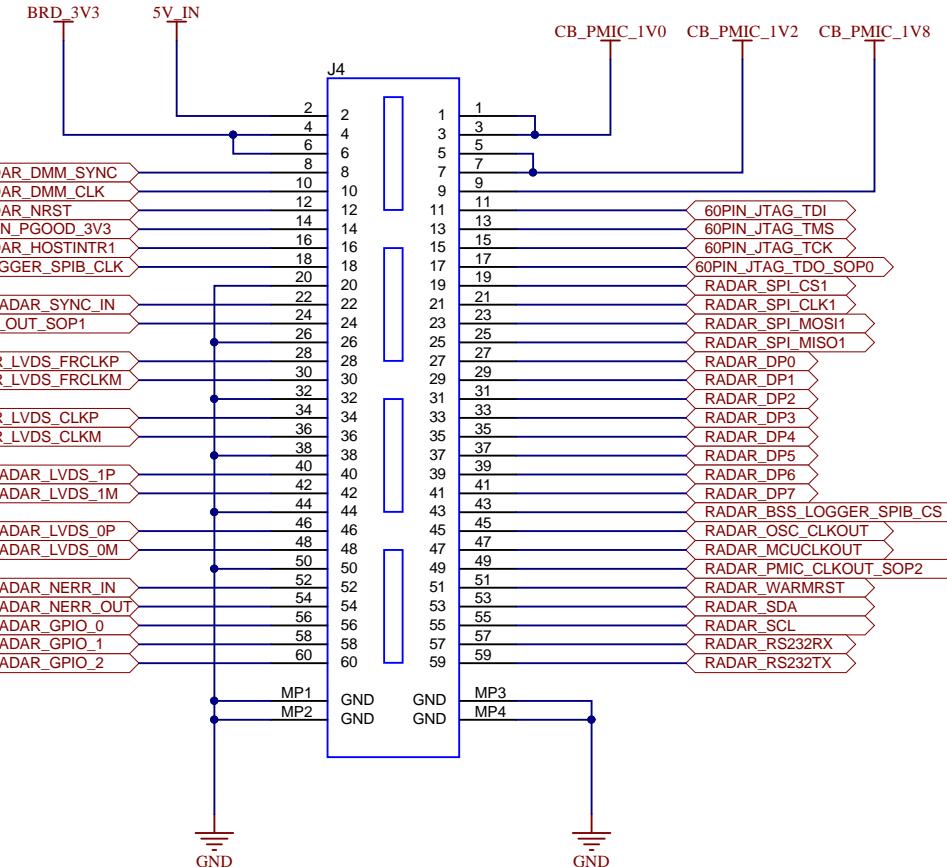
RNR FOR SYNC OUT



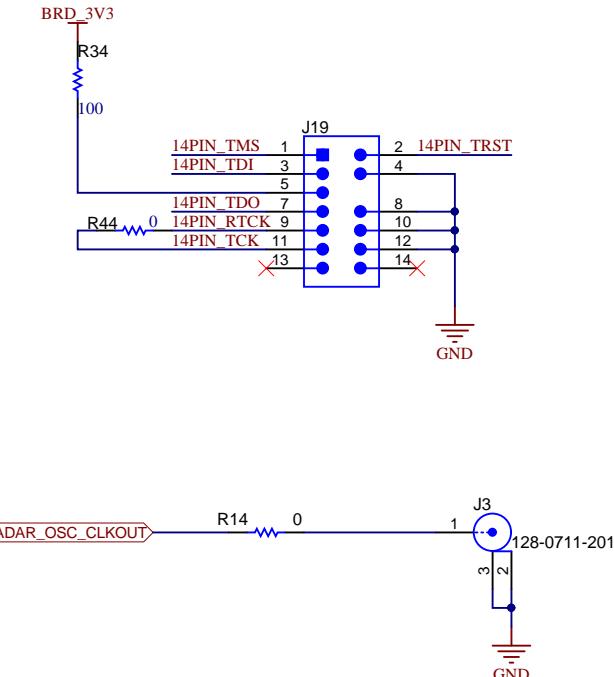
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TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 9 of 22
Drawn By:	File: PROC074B_BP_LP_RNR_Options.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

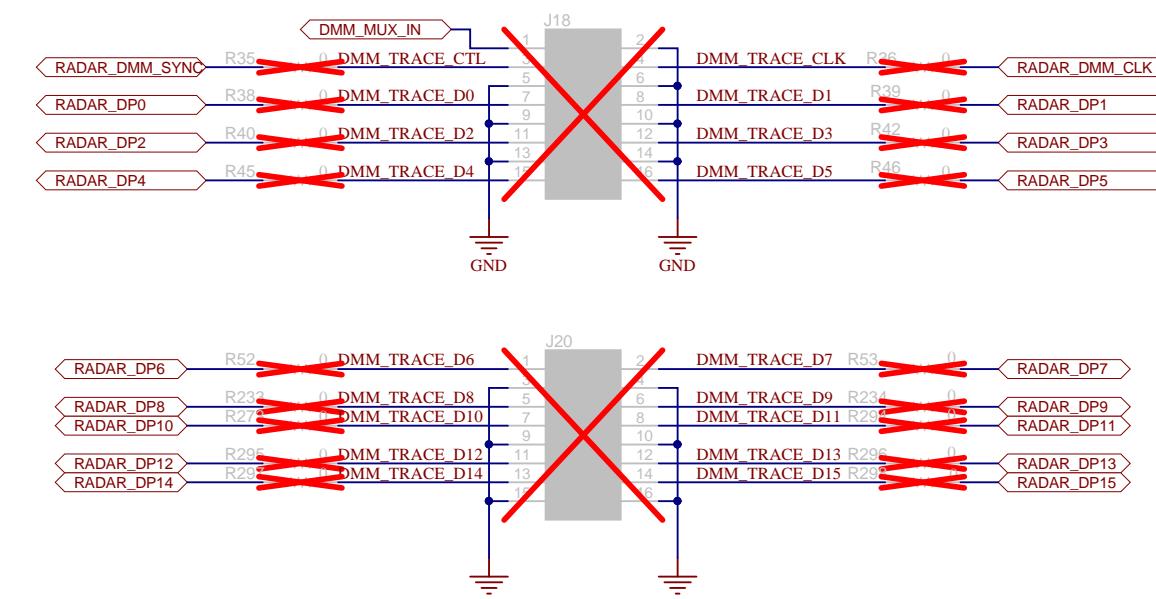
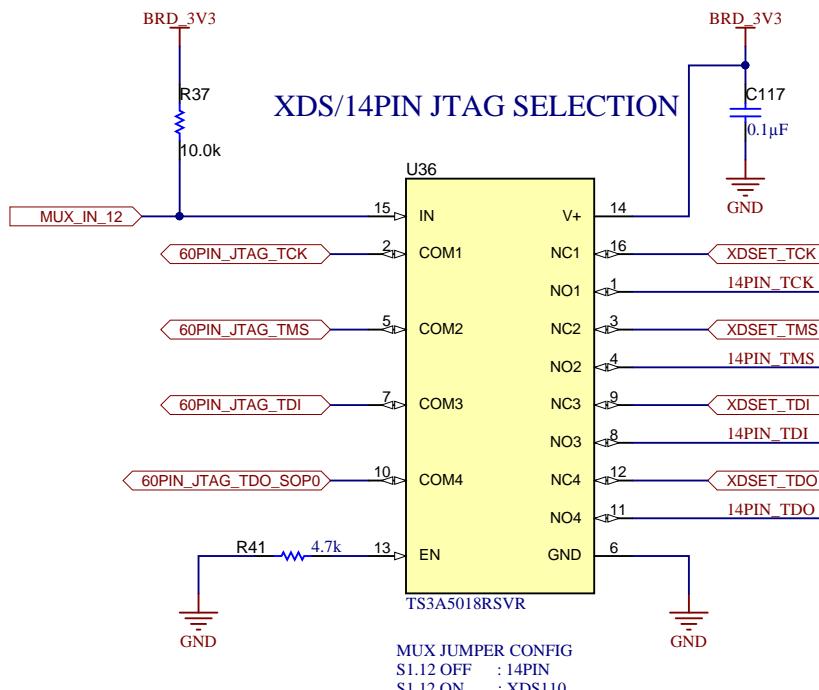
HD CONNECTOR FOR LVDS AND JTAG



JTAG DEBUG CONNECTOR



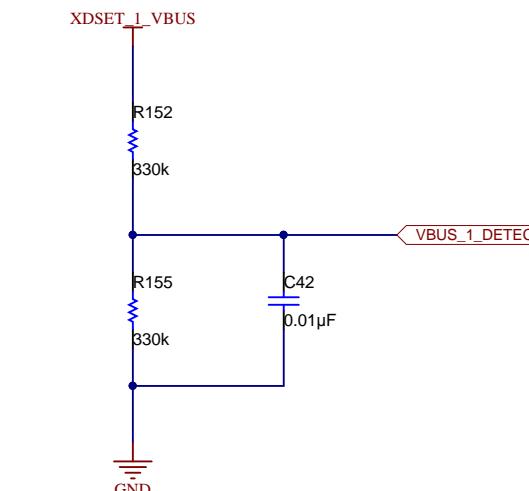
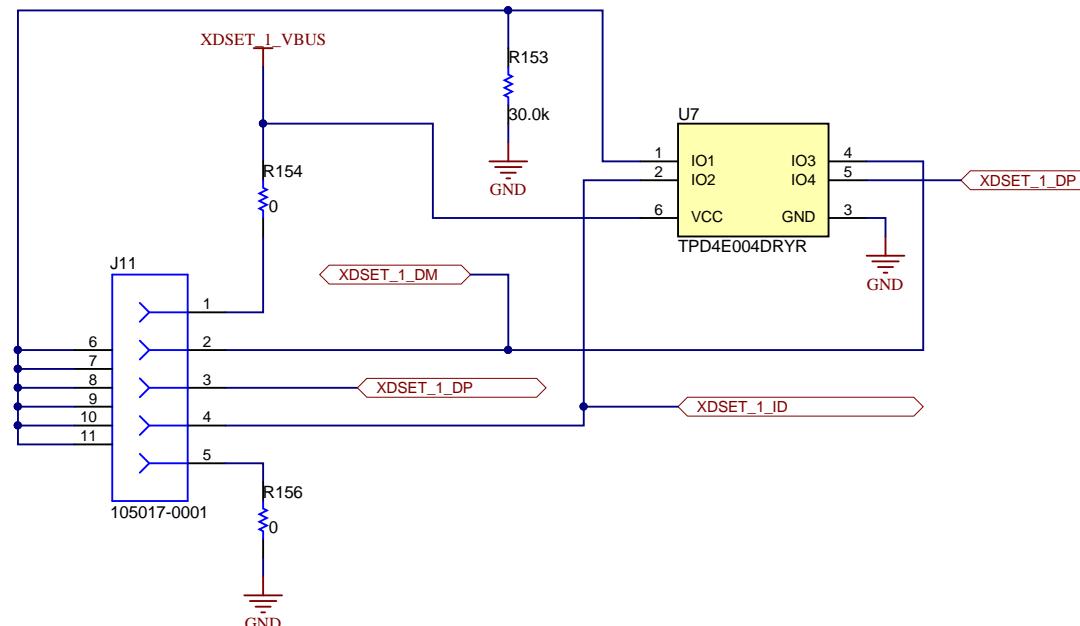
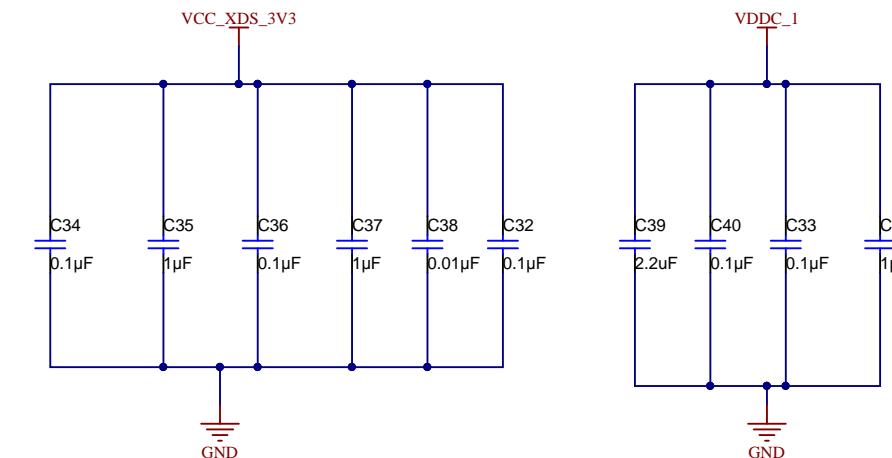
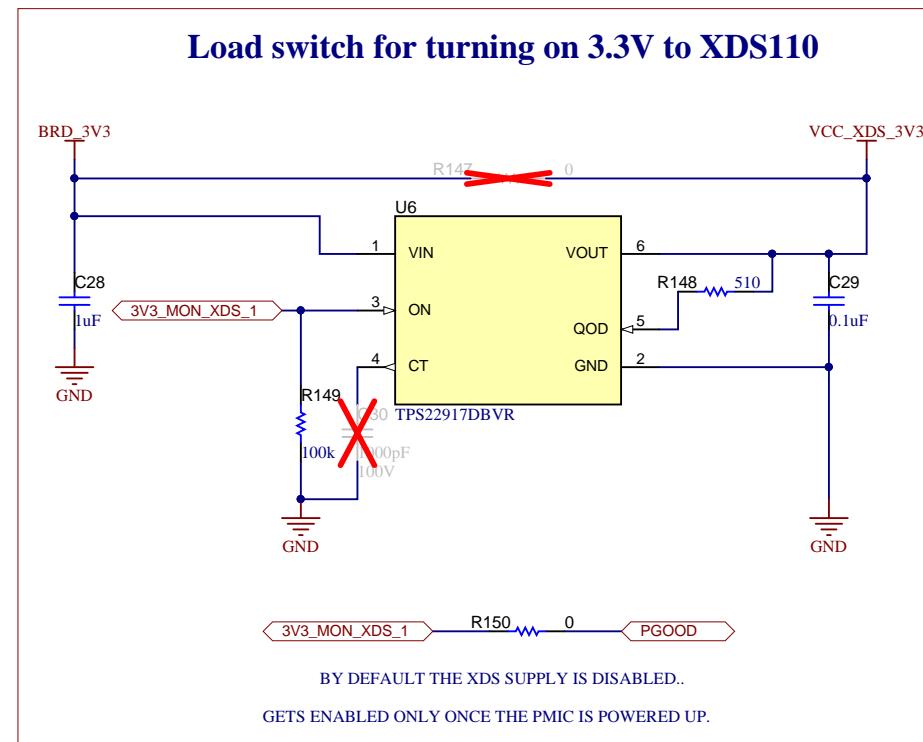
JTAG TRACE DMM INTERFACE HEADER



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Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 11/15/2018
TID #:	N/A	Project Title: MMWAVEICBOOST
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 10 of 22
Drawn By: Chethan Kumar Y.B	File: PROC074B_HD_Conn_JTAG_HDR.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

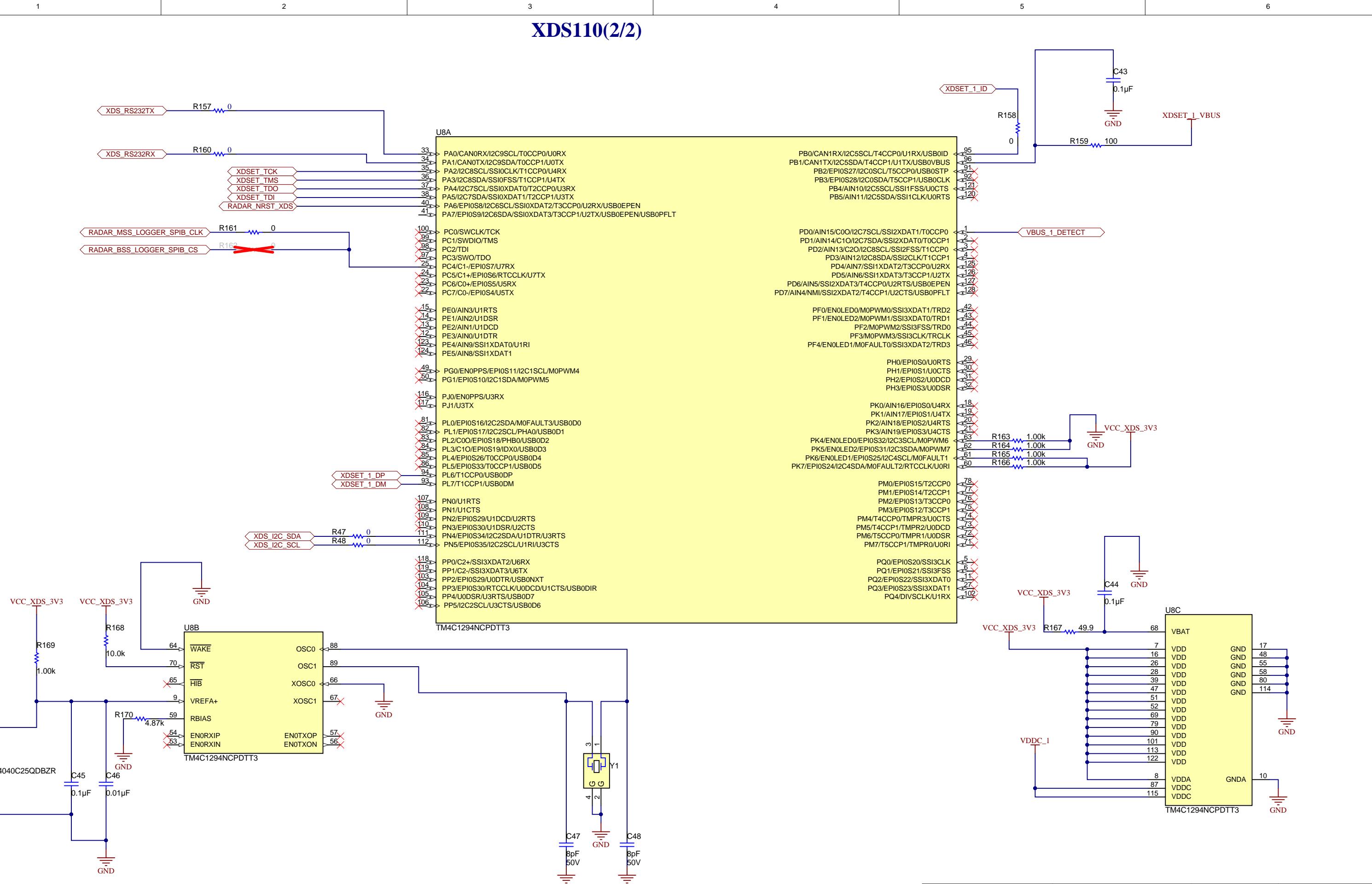
XDS110(1/2)



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TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 11 of 22
Drawn By: Chethan Kumar Y.B	File: PROC074B_XDS110_Interface_1A.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

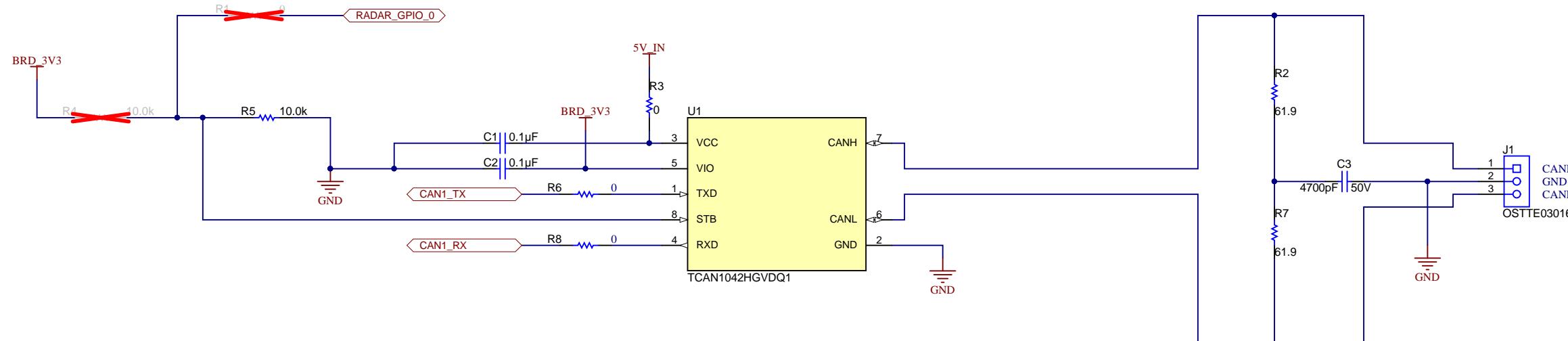
XDS110(2/2)



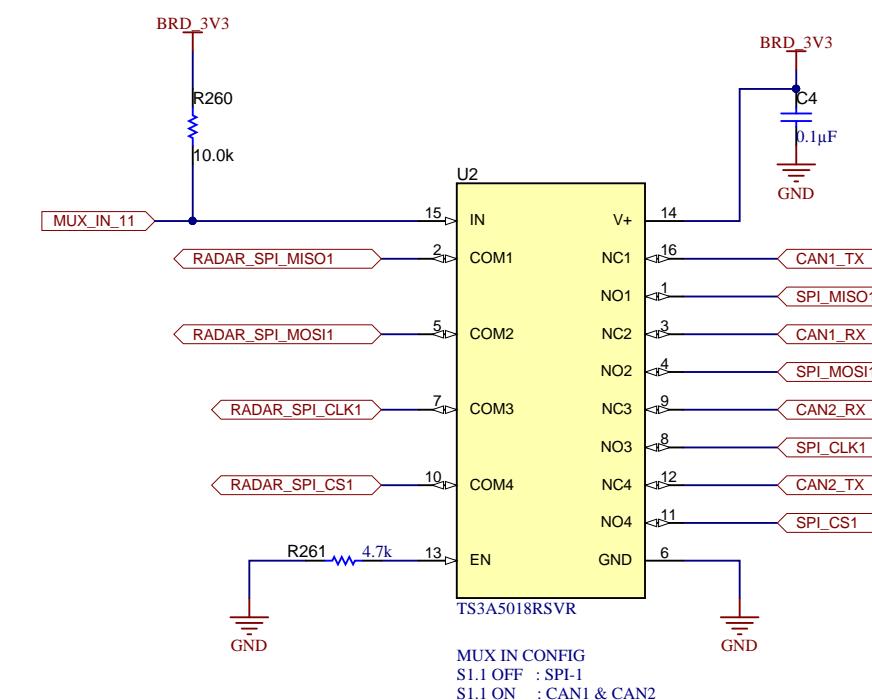
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TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 12 of 22
Drawn By: Chethan Kumar Y.B	File: PROC074B_XDS110_Interface_1B.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

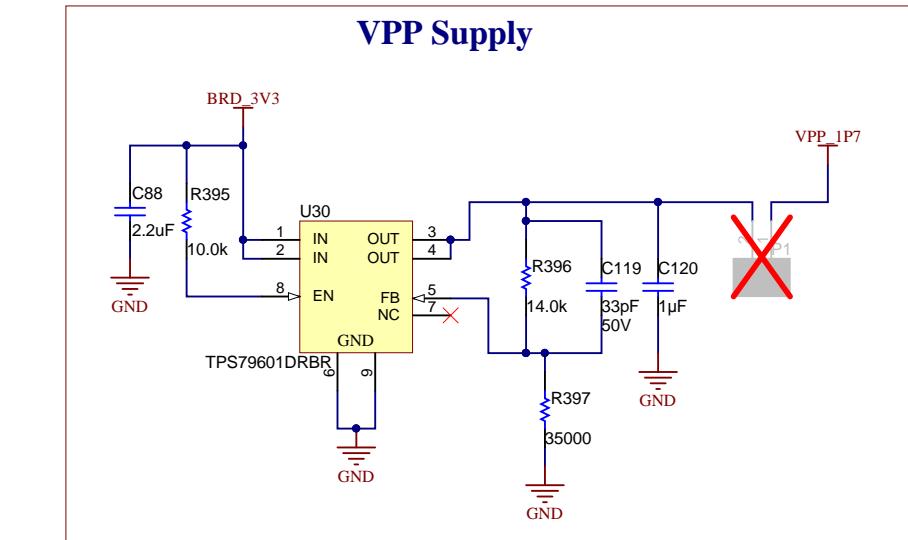
CAN_FD TRANSCEIVER



CAN/SPI SELECTION



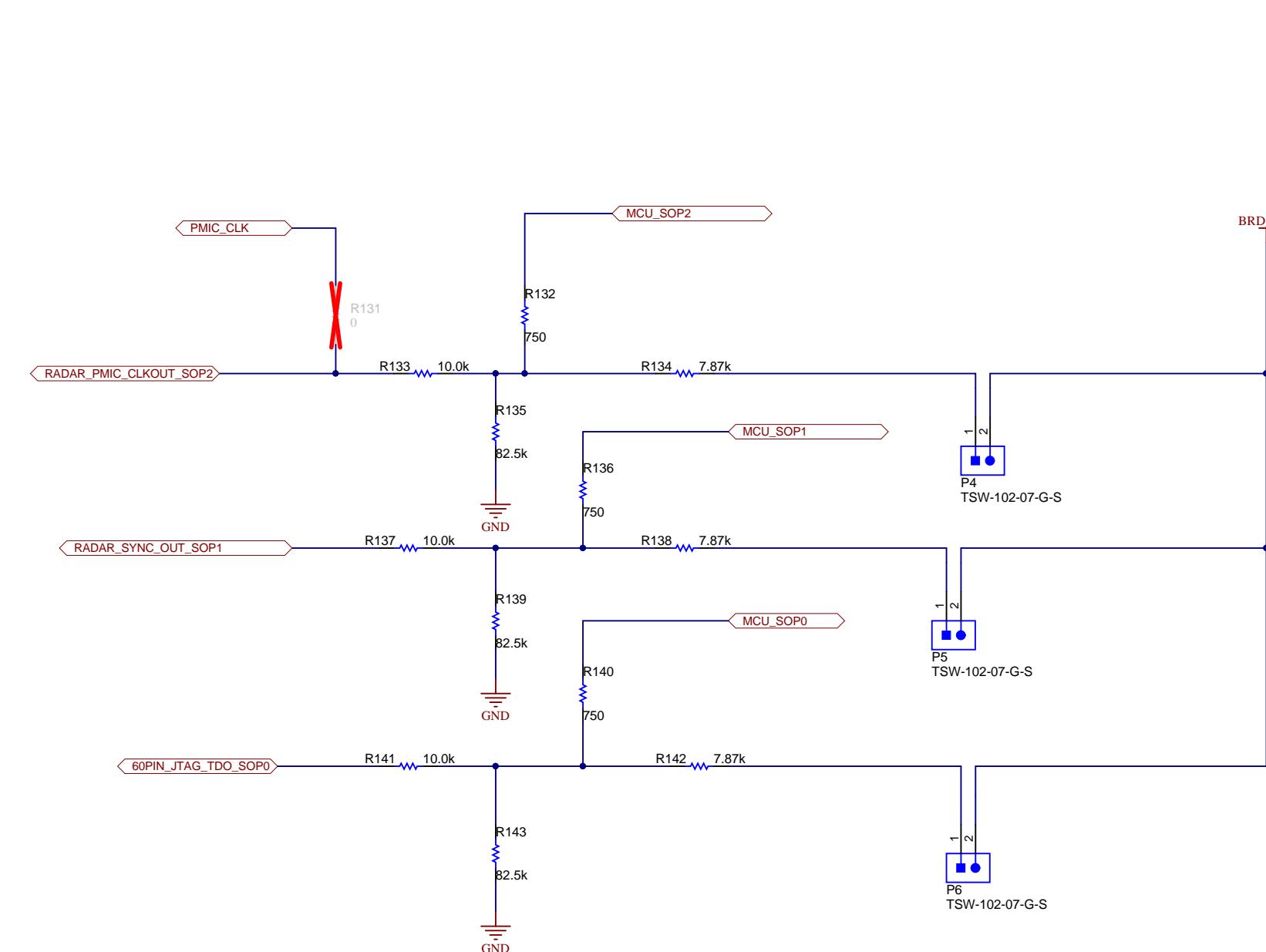
VPP Supply



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Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 10/11/2018
TID #:	N/A	Project Title: MMWAVEICBOOST
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 13 of 22
Drawn By:	File: PROC074B_CAN_Interface.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

SOP HEADERS



SOP_MODE2	"011"	DEV/FLED
SOP_MODE4	"001"	FUNC -> DEFAULT VALUE FOR OUTPUTS
SOP_MODE5	"101"	DEV MANAGEMENT -> FOR FLASHING

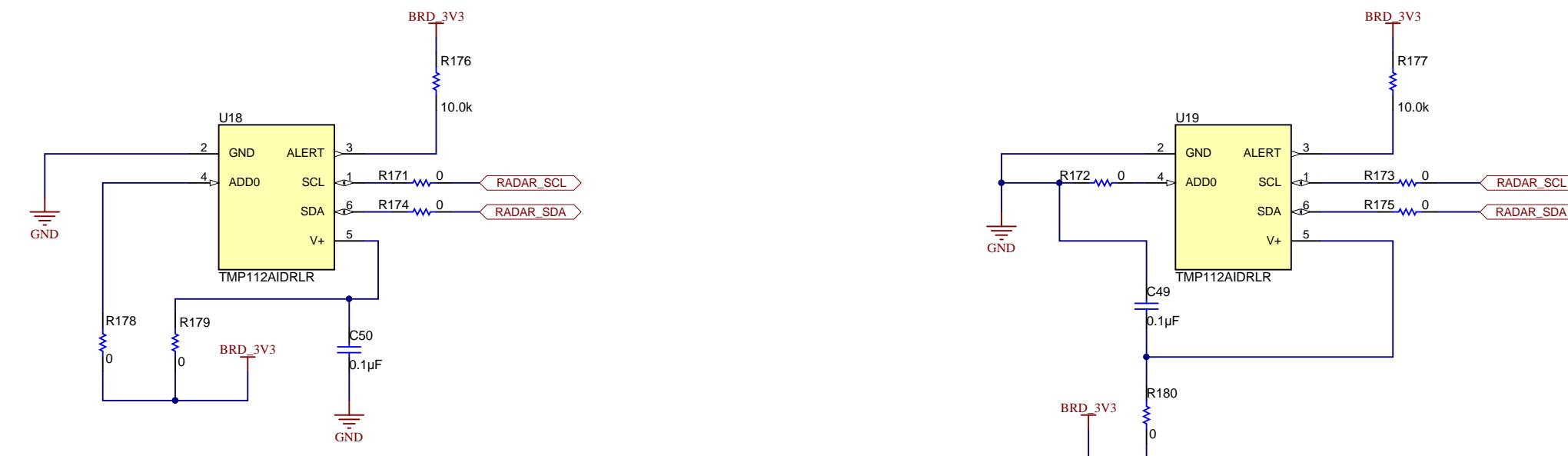
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Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 10/11/2018
TID #:	N/A	Project Title: MMWAVEICBOOST
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 14 of 22
Drawn By:	File: PROC074B_SOP_Selection.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

A

A

ONBOARD TEMP SENSORS



I2C ADDRESS: 100 1001
TEMP SENSOR AWAY FROM PMIC

I2C Address : 100 1000
TEMP SENSOR CLOSE TO PMIC

C

C

D

D

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TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 15 of 22
Drawn By:	File: PROC074B_Temp_Sensor.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

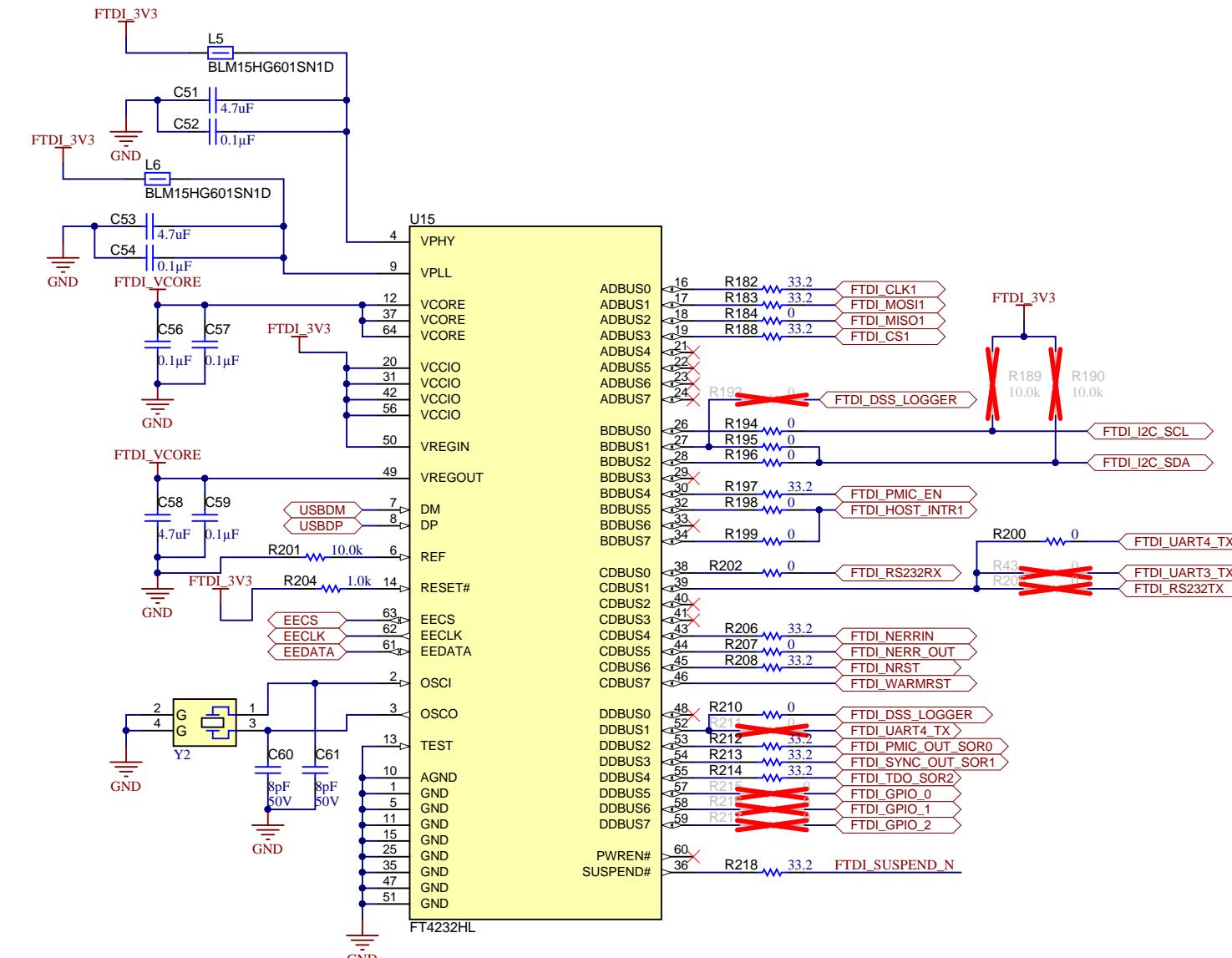
A

A

FTDI INTERFACE

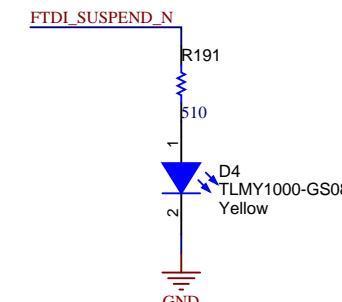
B

B



C

C



D

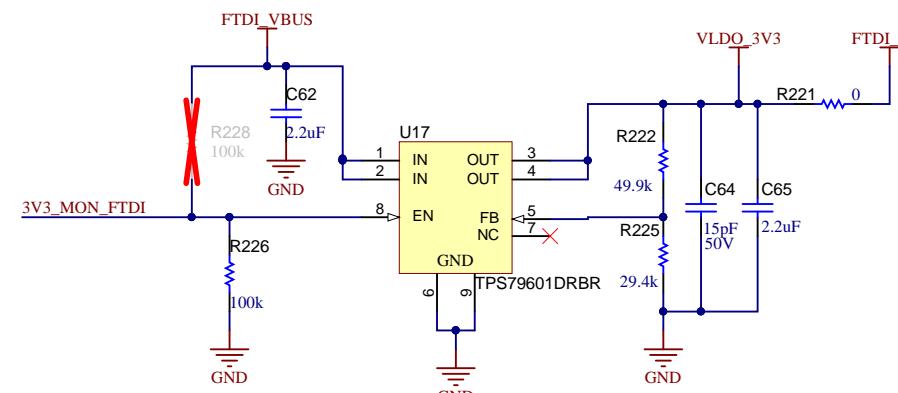
D

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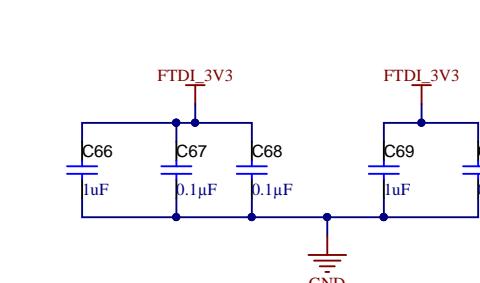
Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 10/11/2018
TID #:	N/A	Project Title: MMWAVEICBOOST
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 16 of 22
Drawn By:	File: PROC074B_FTDI_Interface.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

FTDI POWER SECTION

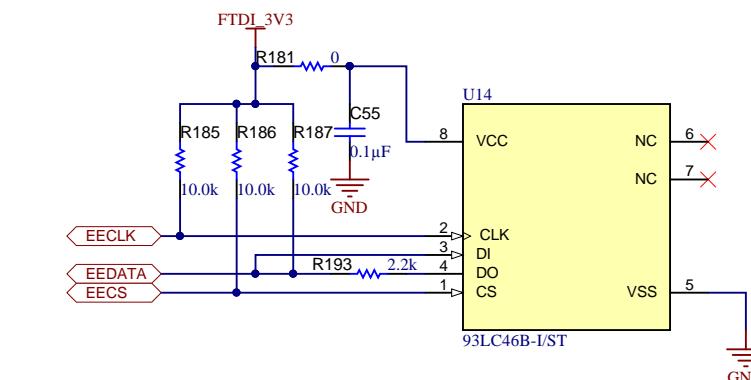
3.3V LDO FOR FTDI



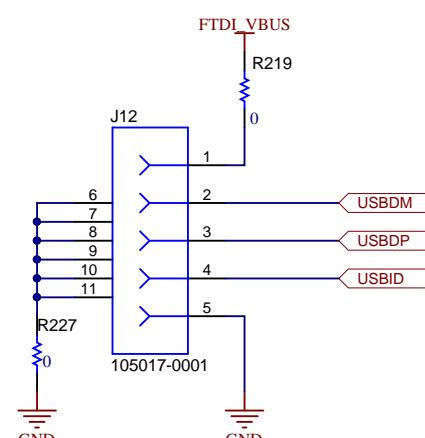
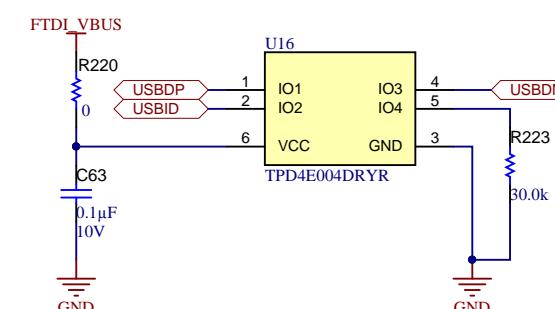
THIS 3.3V LDO WILL TURN ON
EITHER FOR IWR6XXX MODULE BOARD
OR CARRIER BOARD IS POWERED UP



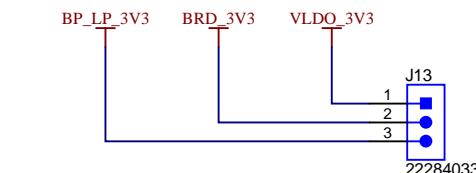
FTDI EEPROM



3V3_MON_FTDI R290 0 PGOOD



3.3V SUPPLY SELECTION JUMPER

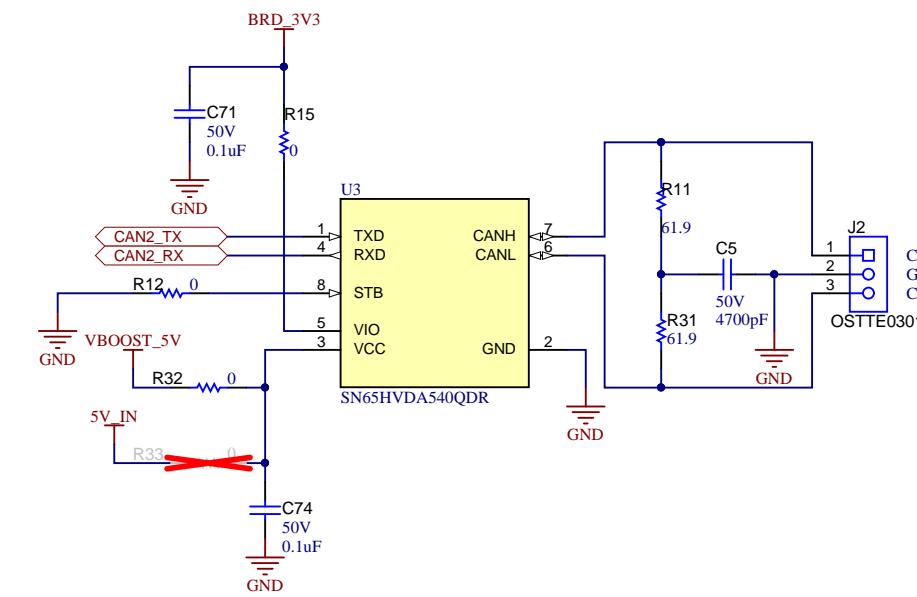


POS 1-2: FROM FTDI LDO
POS 2-3: FROM 40PIN LP/BP CONNECTOR

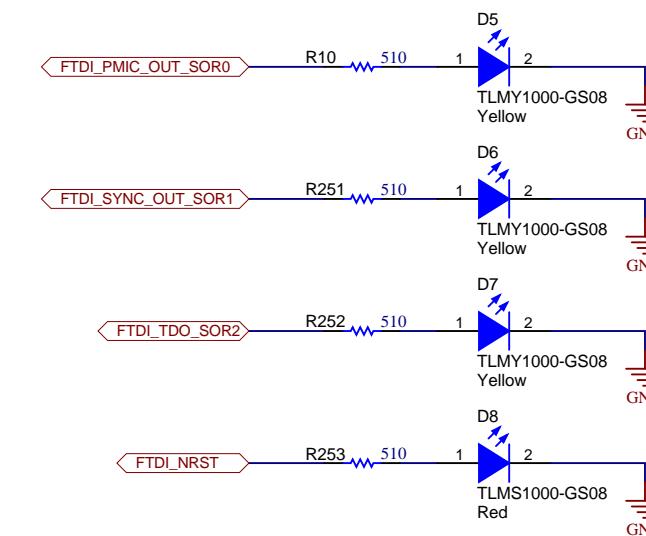
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TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 17 of 22
Drawn By:	File: PROC074B_FTDI_Interface_PWR.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

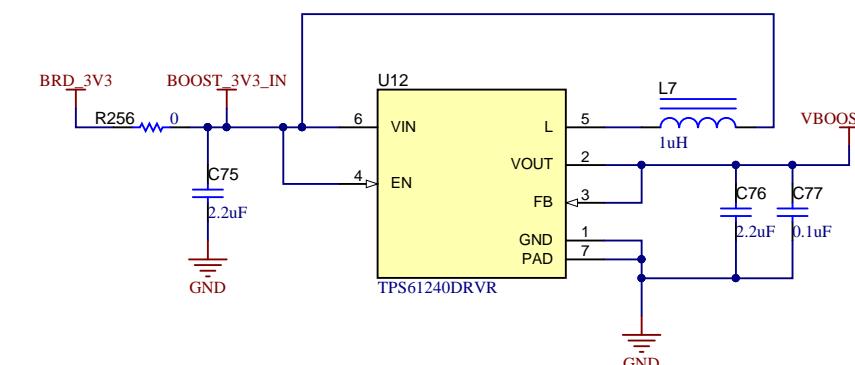
CAN TRANSCEIVER



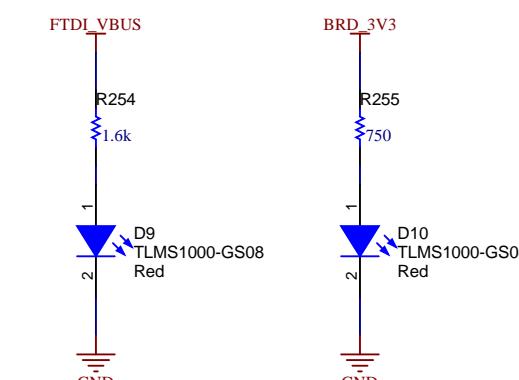
RESET & SOP STATUS INDICATION



3.3V TO 5V BOOST CONVERTER



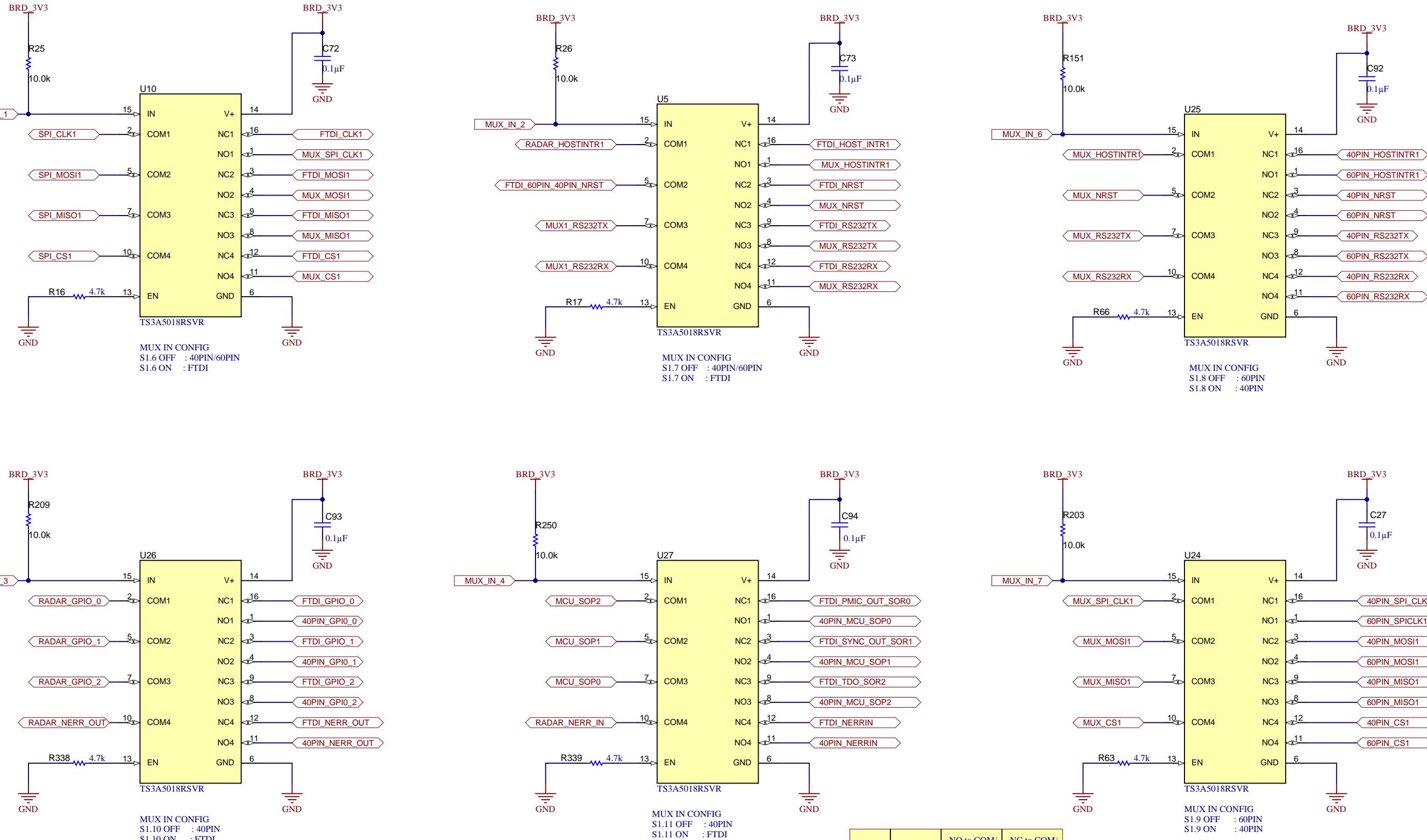
5V & 3.3V SUPPLY INDICATION



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TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 18 of 22
Drawn By: Chethan Kumar Y.B	File: PROC074B_CAN_Interface_LEDs.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

ANALOG MUX BETWEEN FTDI , DCA & 40PIN HDR 1/2

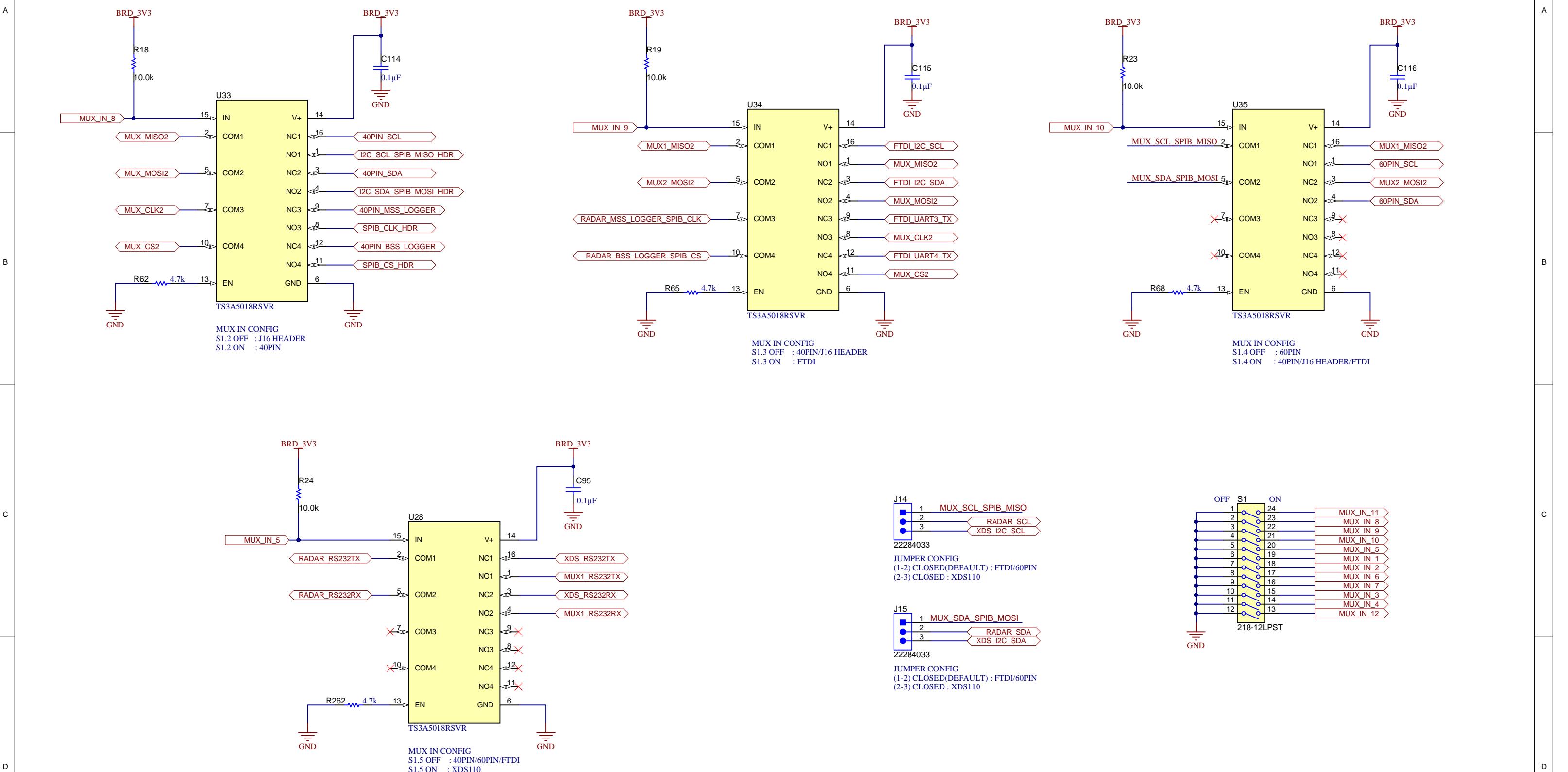


EN	IN	NO to COM/ COM to NO	NC to COM/ COM to NC
L	L	OFF	ON
L	H	ON	OFF
H	X	OFF	OFF

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TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 19 of 22
Drawn By: Chethan Kumar Y.B	File: PROC074B_Analog_Mux_1A.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	

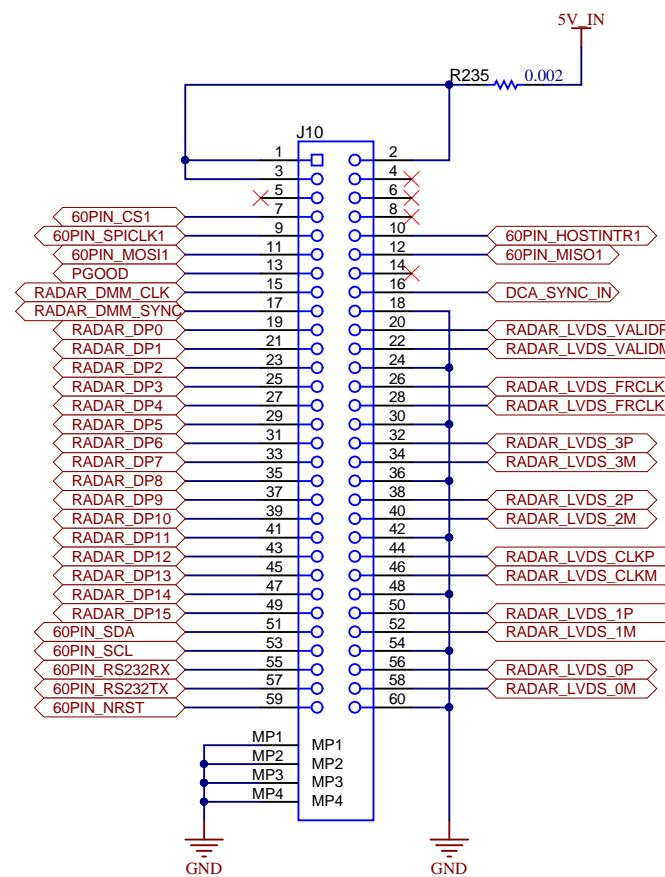
ANALOG MUX BETWEEN FTDI , DCA & 40PIN HDR (2/2)



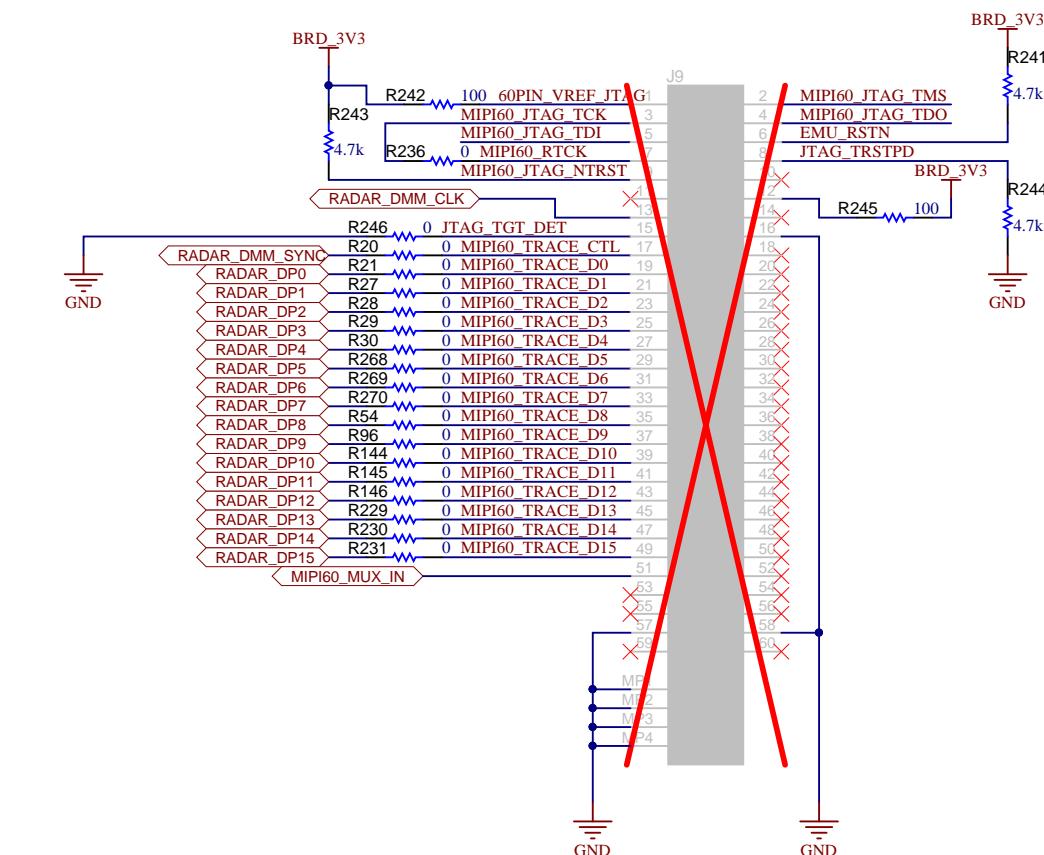
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TID #:	N/A	Project Title: MMWAVEICBOOST
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 20 of 22
Drawn By:	File: PROC074B_Analog_Mux_1B.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018

60PIN HD CONNECTOR FOR DCA1000



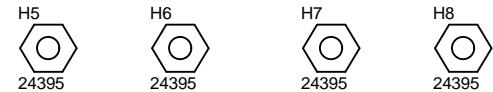
MIPI-60 DEBUG TRACE CONNECTOR



60PIN_JTAG_TMS → R237 → 0 → MIPI60_JTAG_TMS
 60PIN_JTAG_TDI → R238 → 0 → MIPI60_JTAG_TDI
 60PIN_JTAG_TDO_SOP0 → R239 → 0 → MIPI60_JTAG_TDO
 60PIN_JTAG_TCK → R240 → 0 → MIPI60_JTAG_TCK

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TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 21 of 22
Drawn By:	File: PROC074B_HD_Connector_DCA.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	© Texas Instruments 2018



PCB Number: PROC074
PCB Rev: B

PCB
LOGO
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PCB
LOGO
FCC disclaimer



Open Top Jumper Sockets



Variant/Label Table	
Variant	Label Text
001	MMWAVEICBOOST

LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Orderable: MMWAVEICBOOST	Designed for: Public Release	Mod. Date: 11/15/2018
TID #: N/A	Project Title: MMWAVEICBOOST	
Number: PROC074	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 22 of 22
Drawn By:	File: PROC074B_EVM_Hardware.SchDoc	Size: B
Engineer: Chethan Kumar Y.B	Contact: http://www.ti.com/support	

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