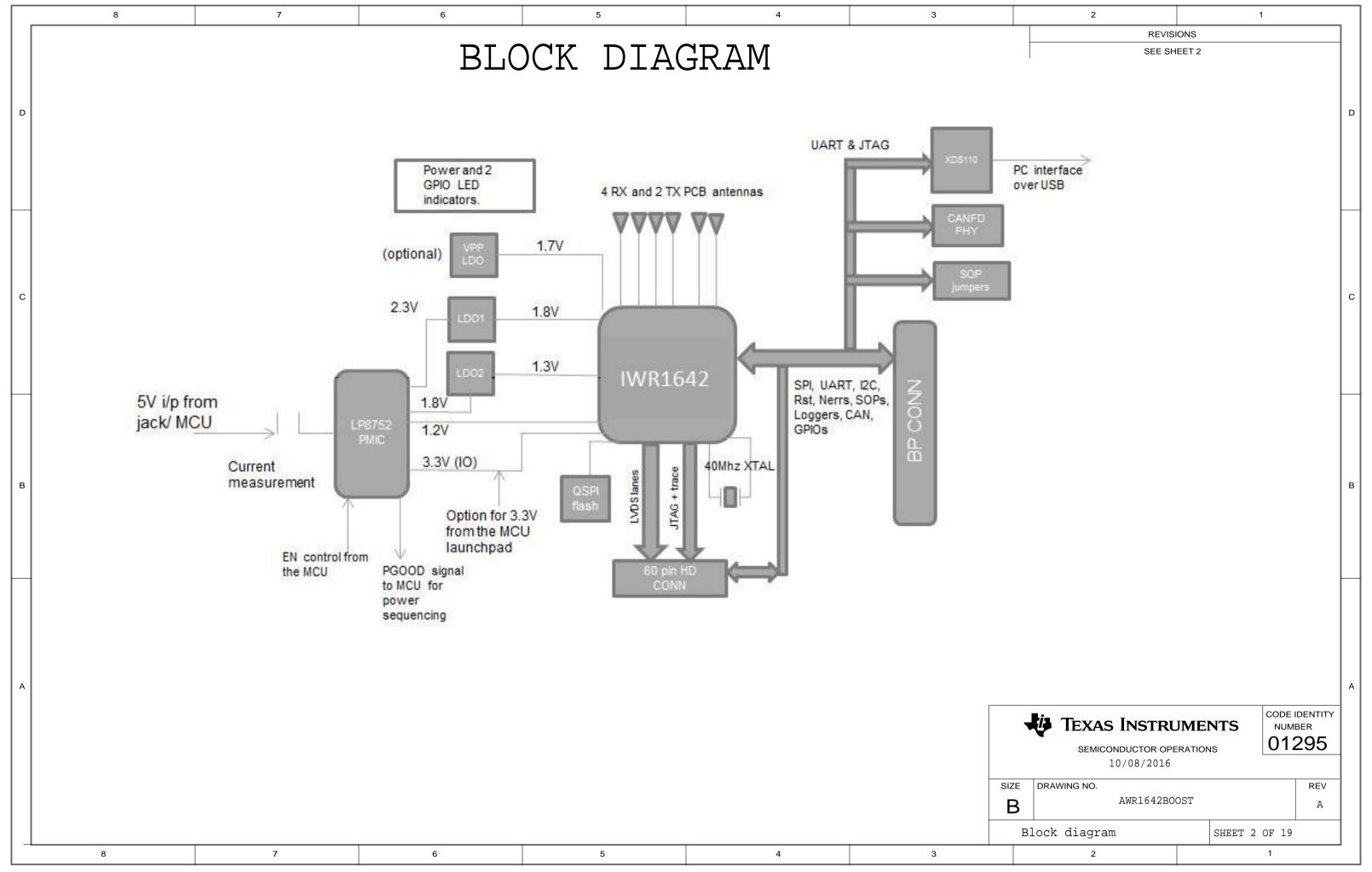
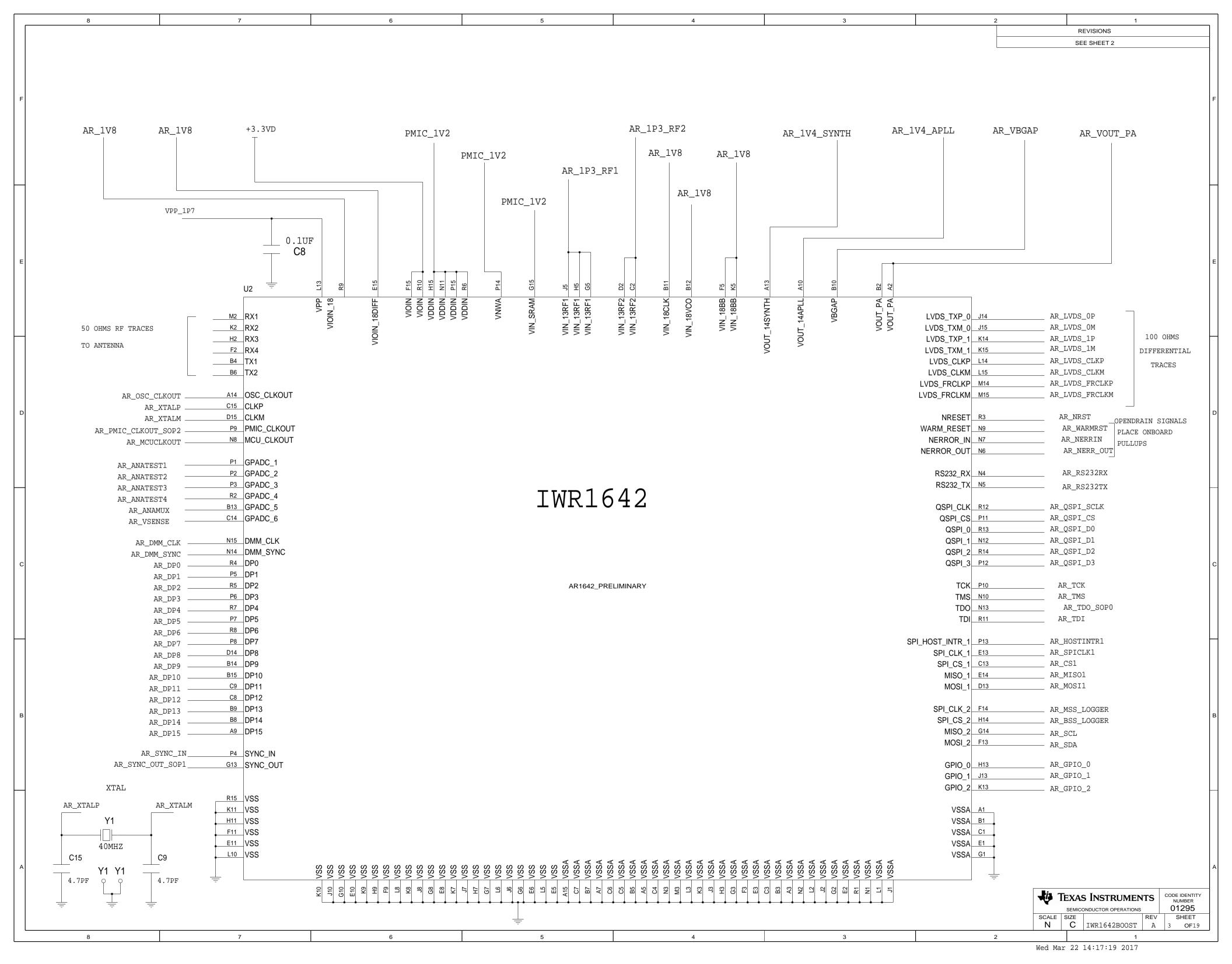
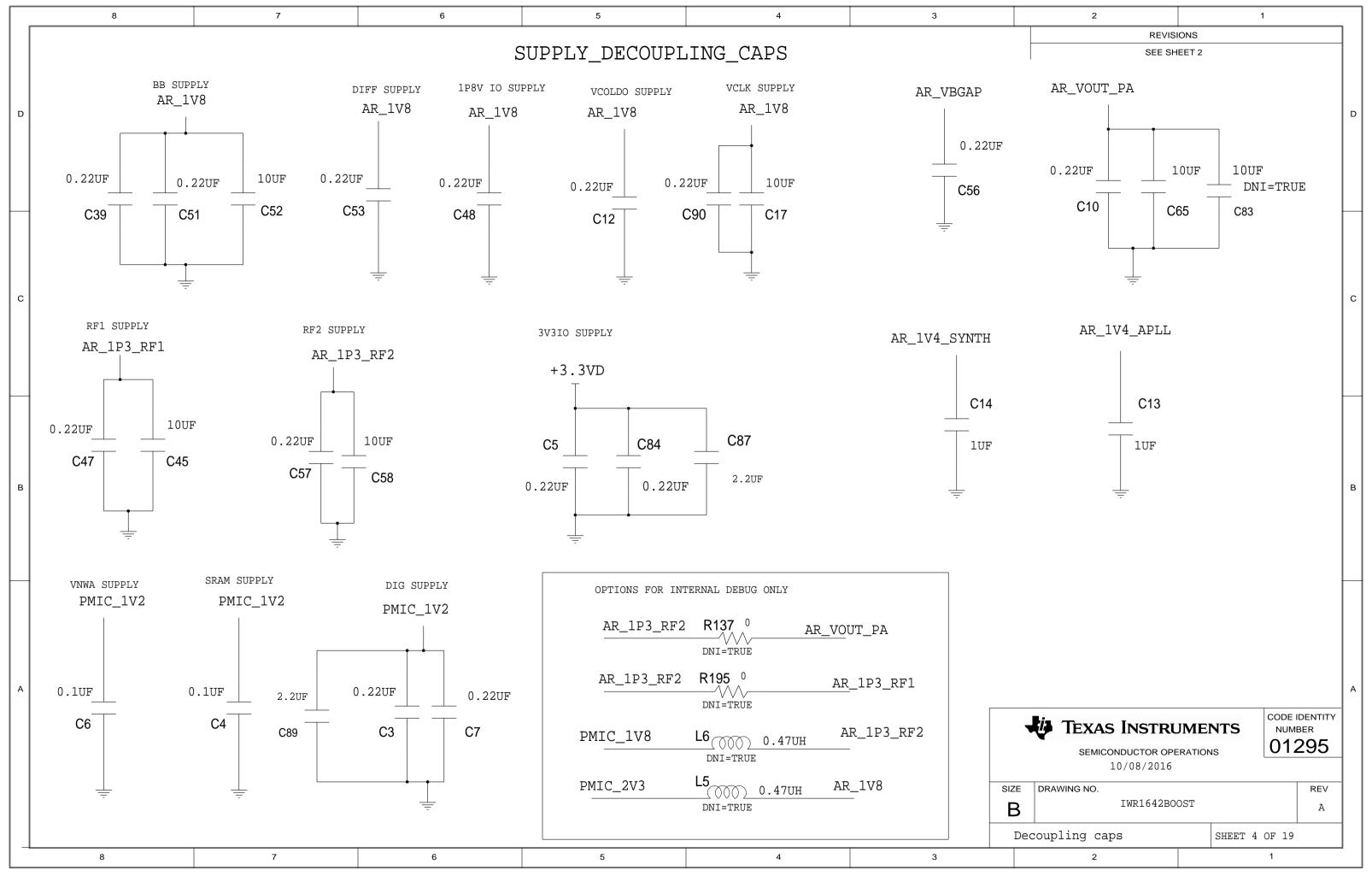
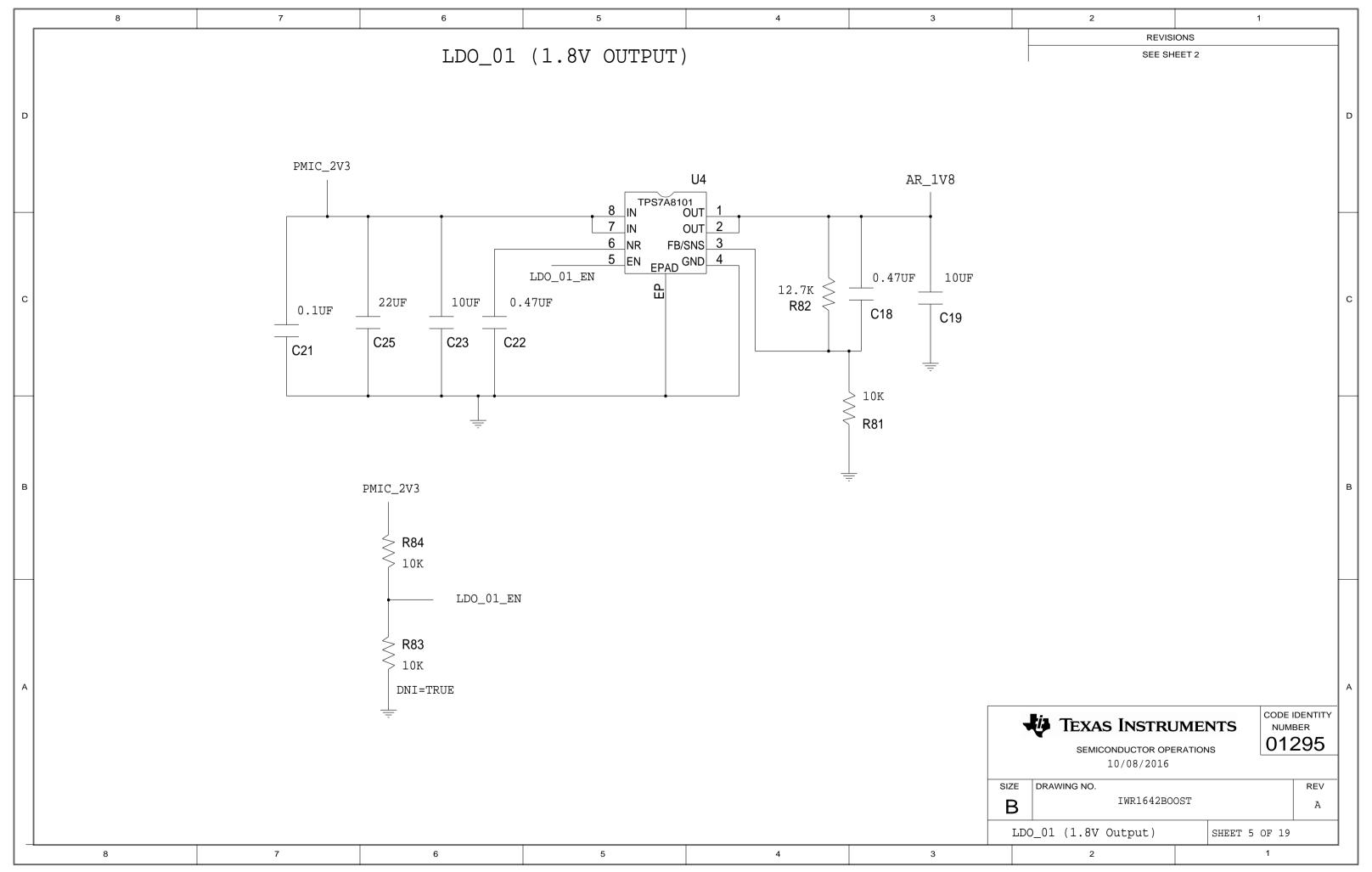
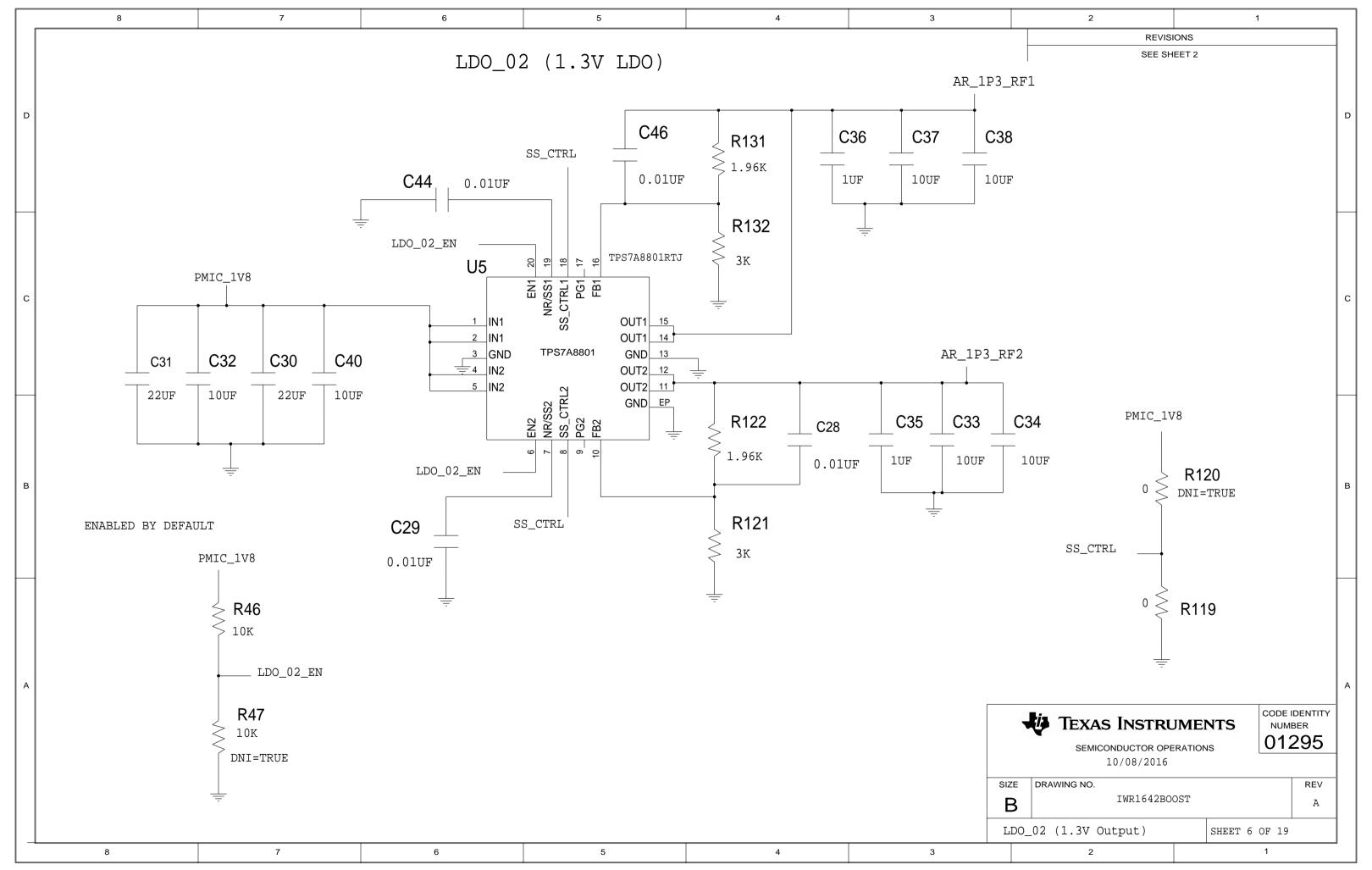
2 REVISIONS IWR1642BOOST SEE SHEET 2 PROC011 TABLE OF CONTENTS SHEET NO. SHEET NAME Contents Block diagram DUT Decoupling caps LDO\_01 (1.8V Output) LDO\_02 (1.3V Output) VPP\_Supply Pwr\_RST\_LEDs PMIC 10 QSPI flash section 11 LP Connector 12 HD Connector 13 XDS110 Interface\_1A 14 XDS110 Interface\_1B 15 CAN Interface SOP selection 16 17 Tempsensor 18 Revision History 19 Hardware ASSEMBLY NOTES: ASSEMBLIES MUST BE CLEAN AND FREE FROM FLUX AND ALL CONTAMINANTS. USE OF NO CLEAN FLUX IS NOT ACCEPTABLE ASSEMBLIES MUST COMPLY WITH WORKMANSHIP STANDARDS IPC-A-610 CLASS 2, UNLESS OTHERWISE SPECIFIED COMPONENTS MARKED "DNI = TRUE" WILL NOT BE ASSEMBLED CODE IDENTITY TEXAS INSTRUMENTS NUMBER 01295 SEMICONDUCTOR OPERATIONS 10/08/2016 SIZE DRAWING NO. REV IWR1642BOOST В Contents SHEET 1 OF 19

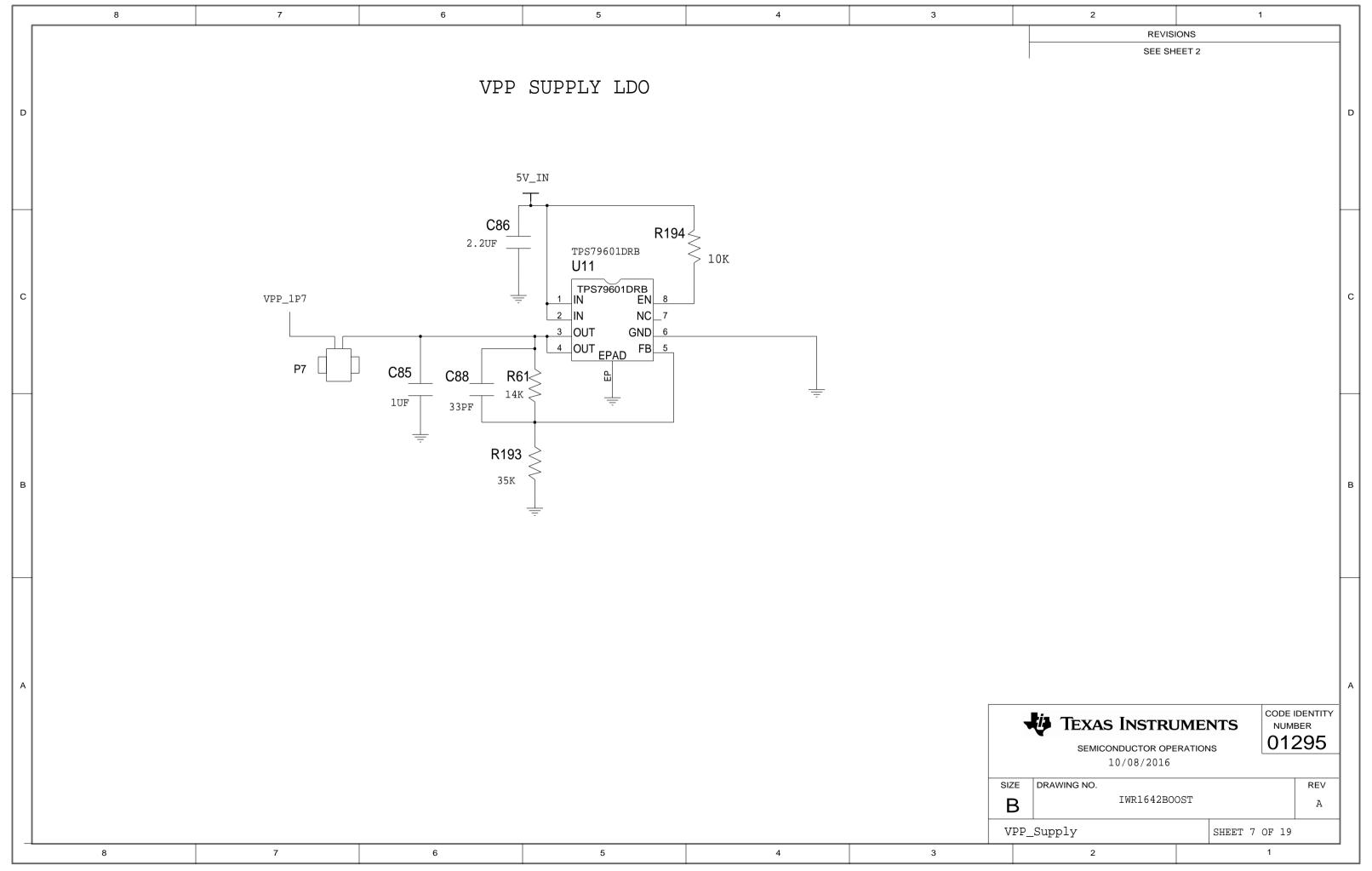


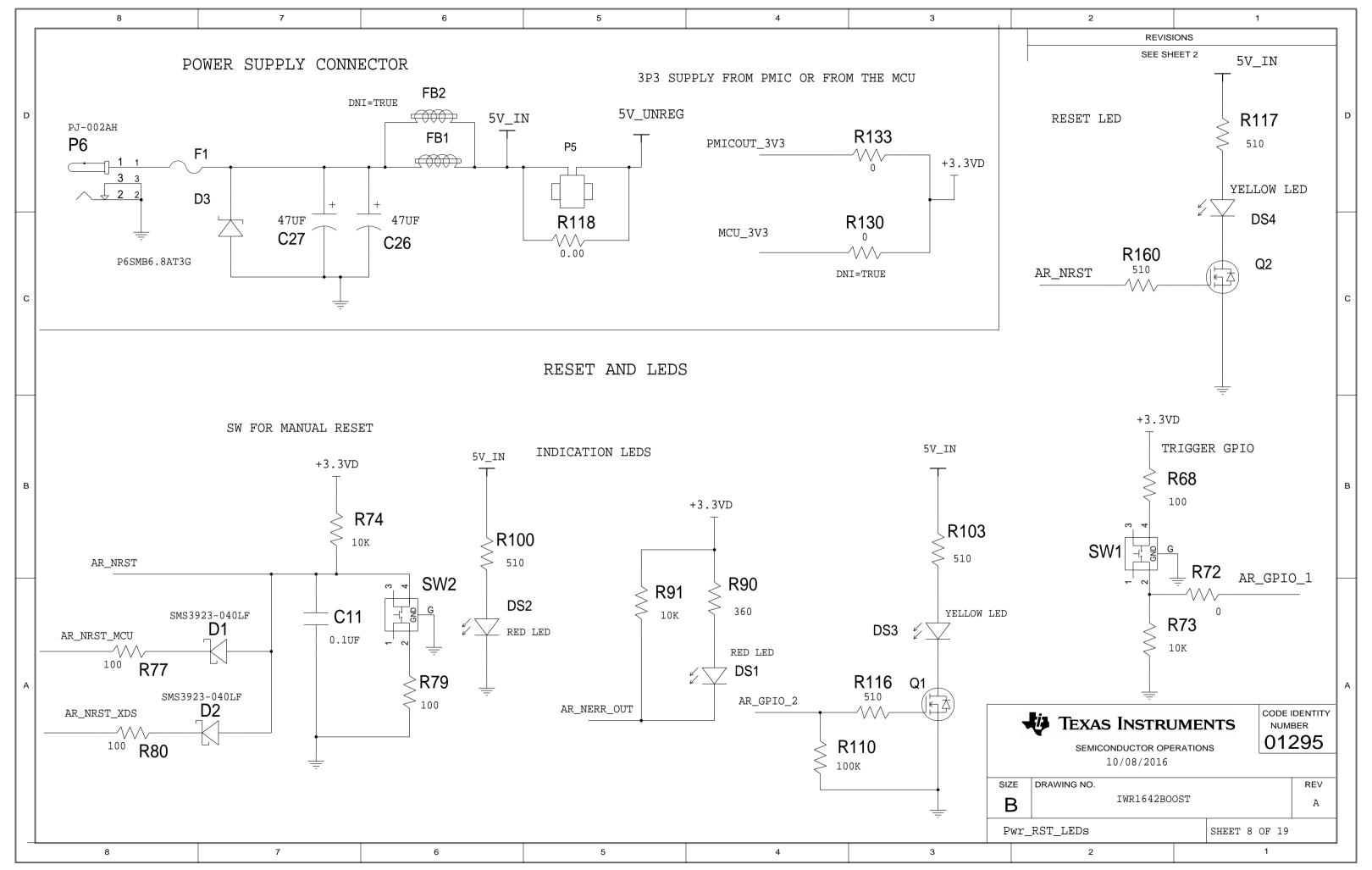


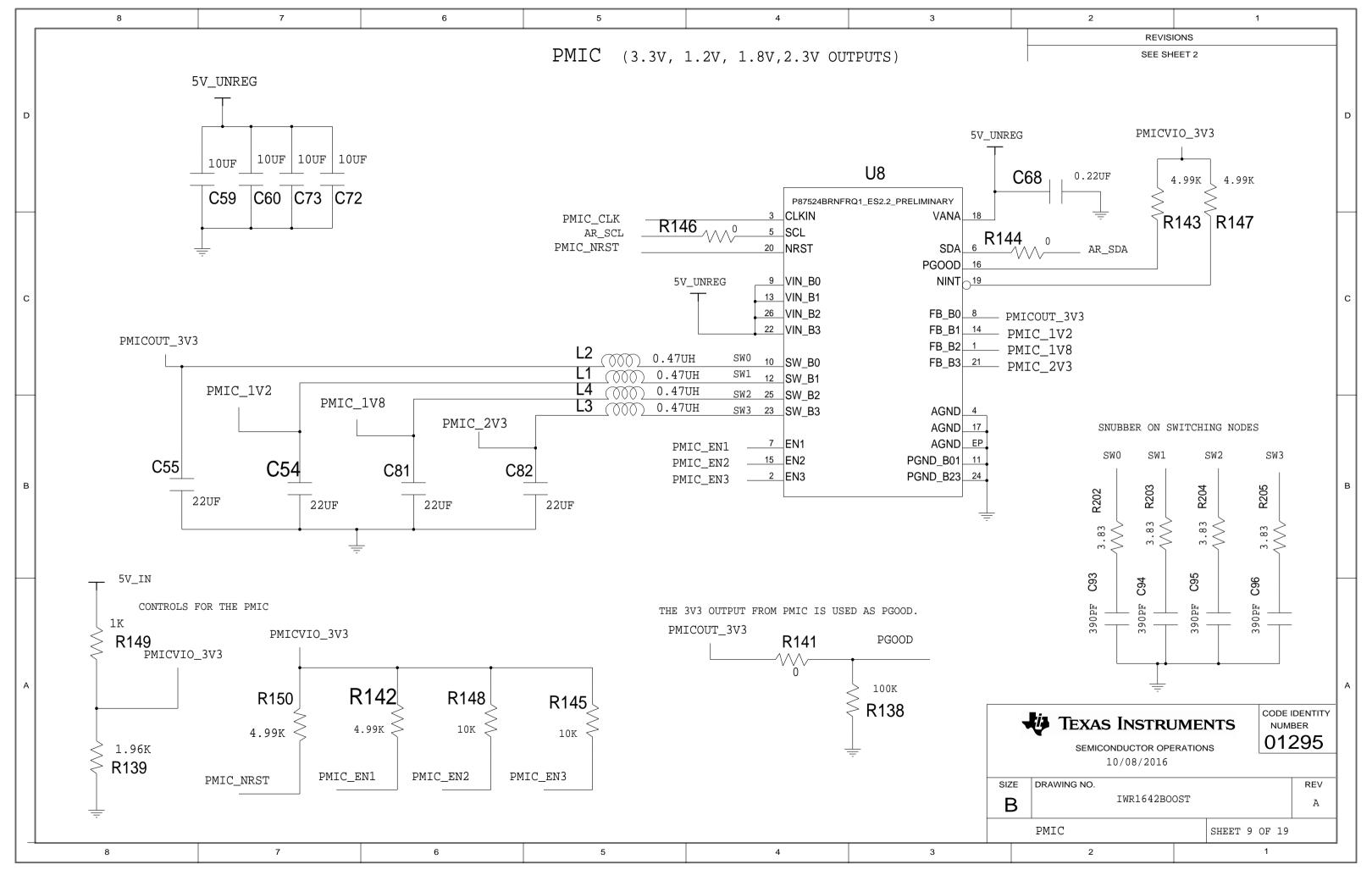


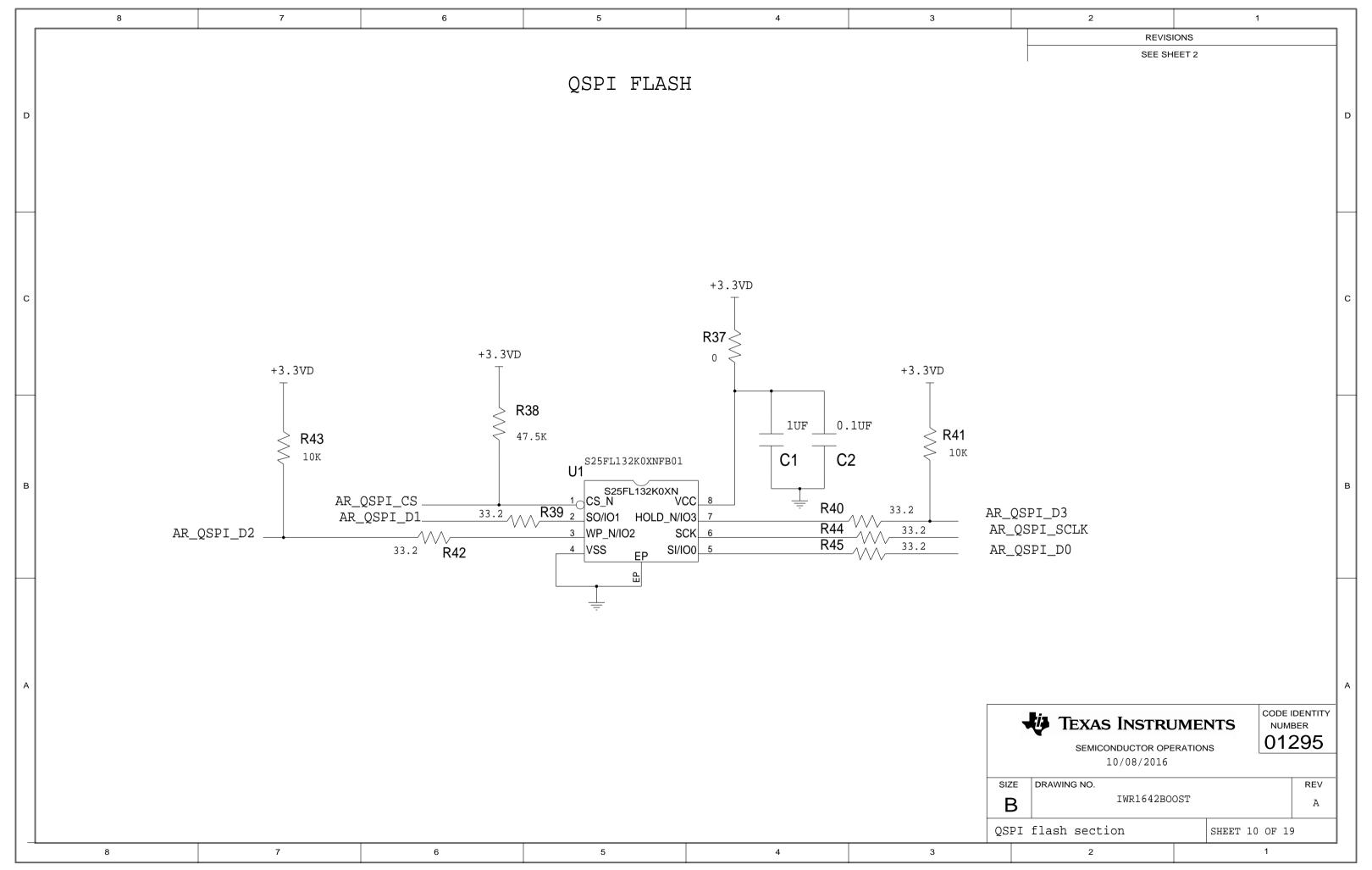


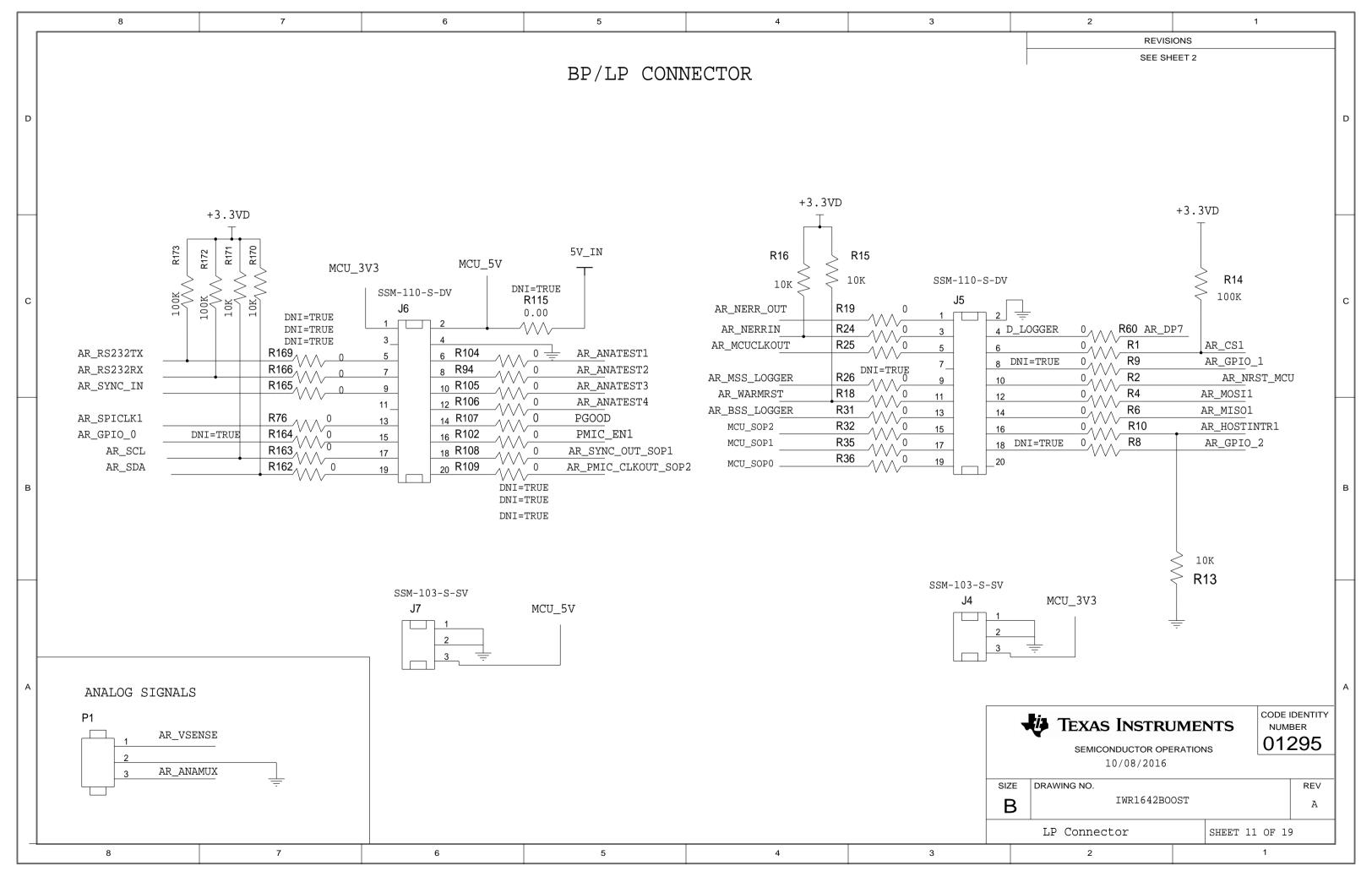


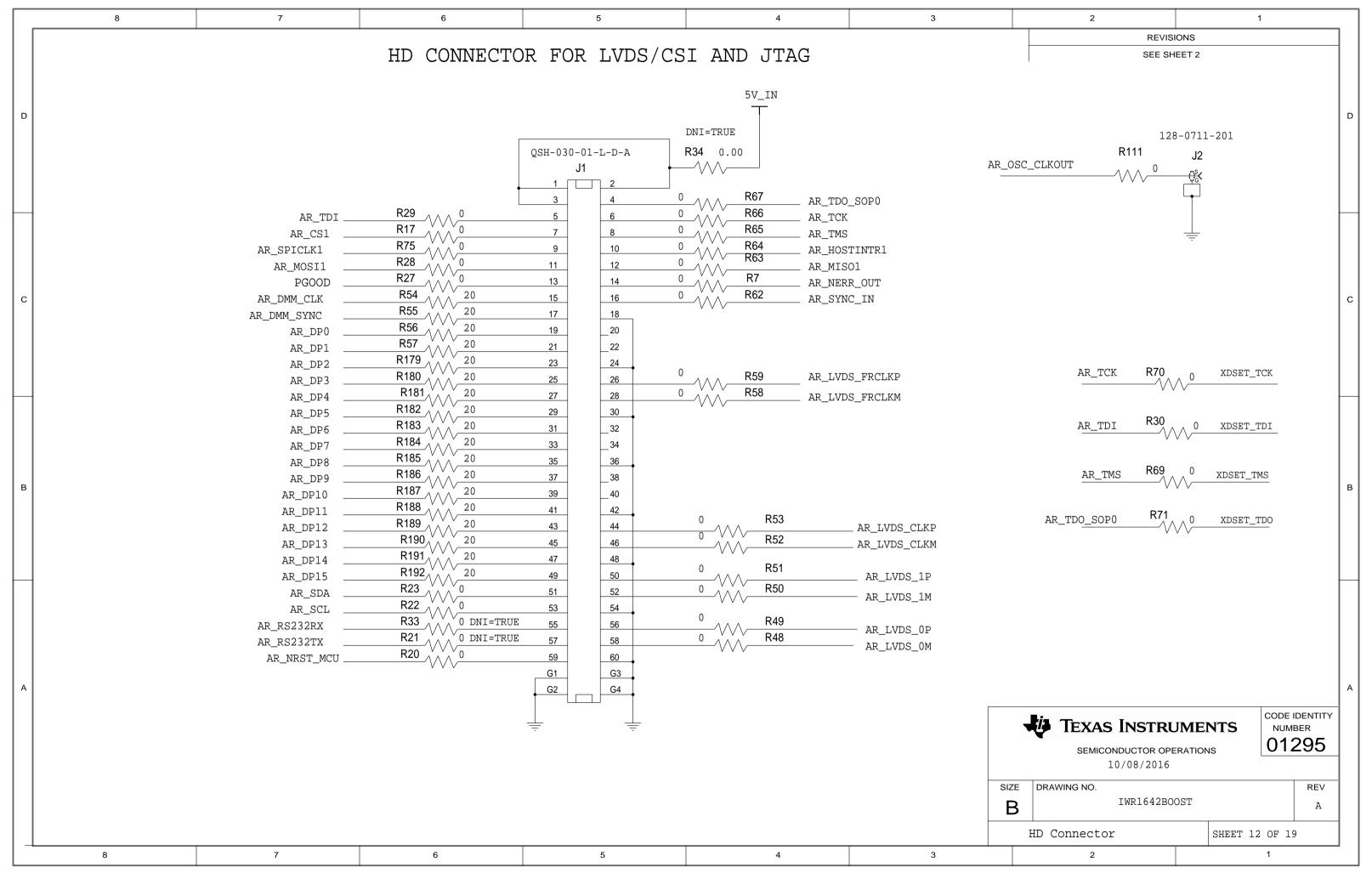


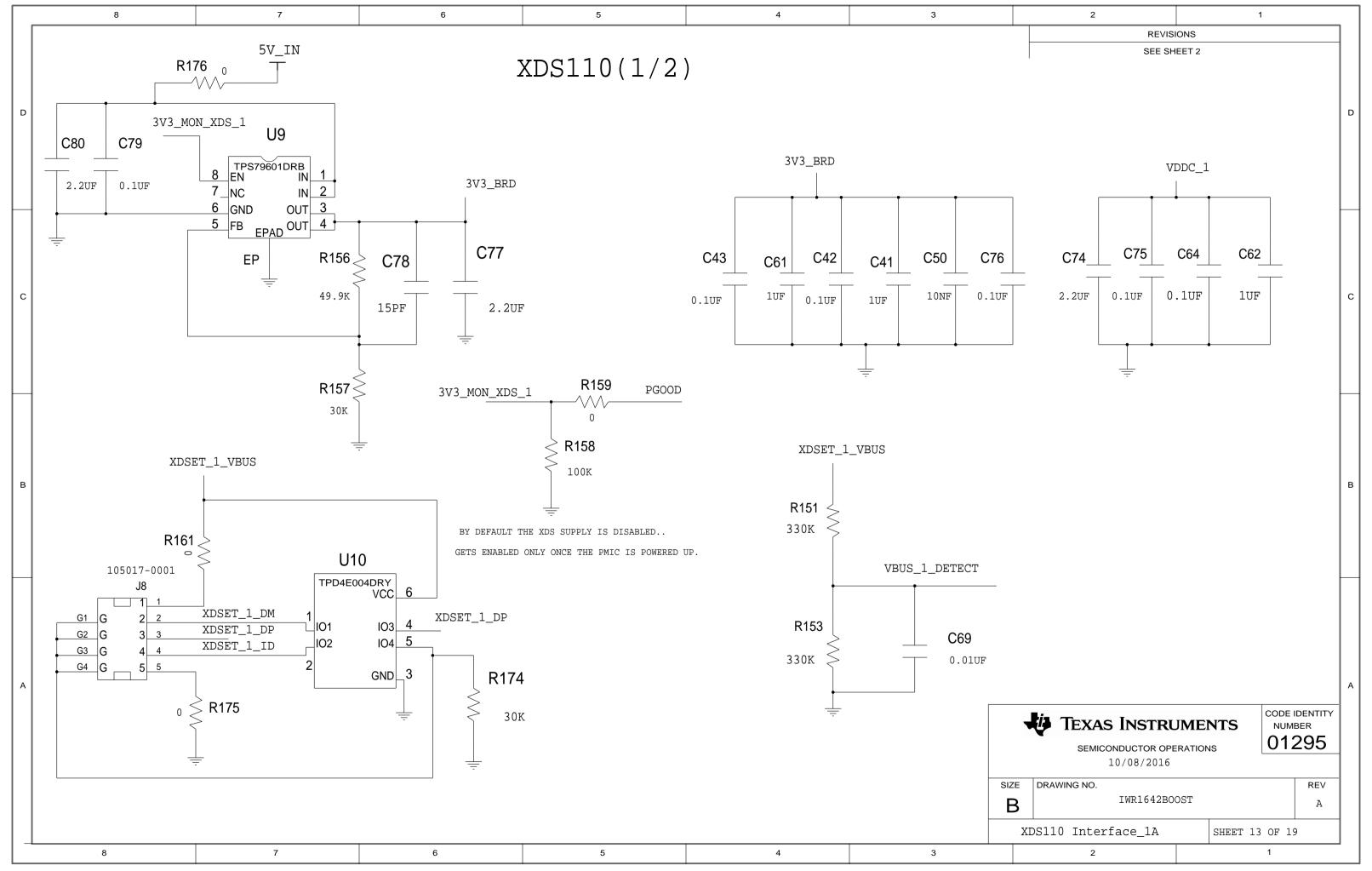


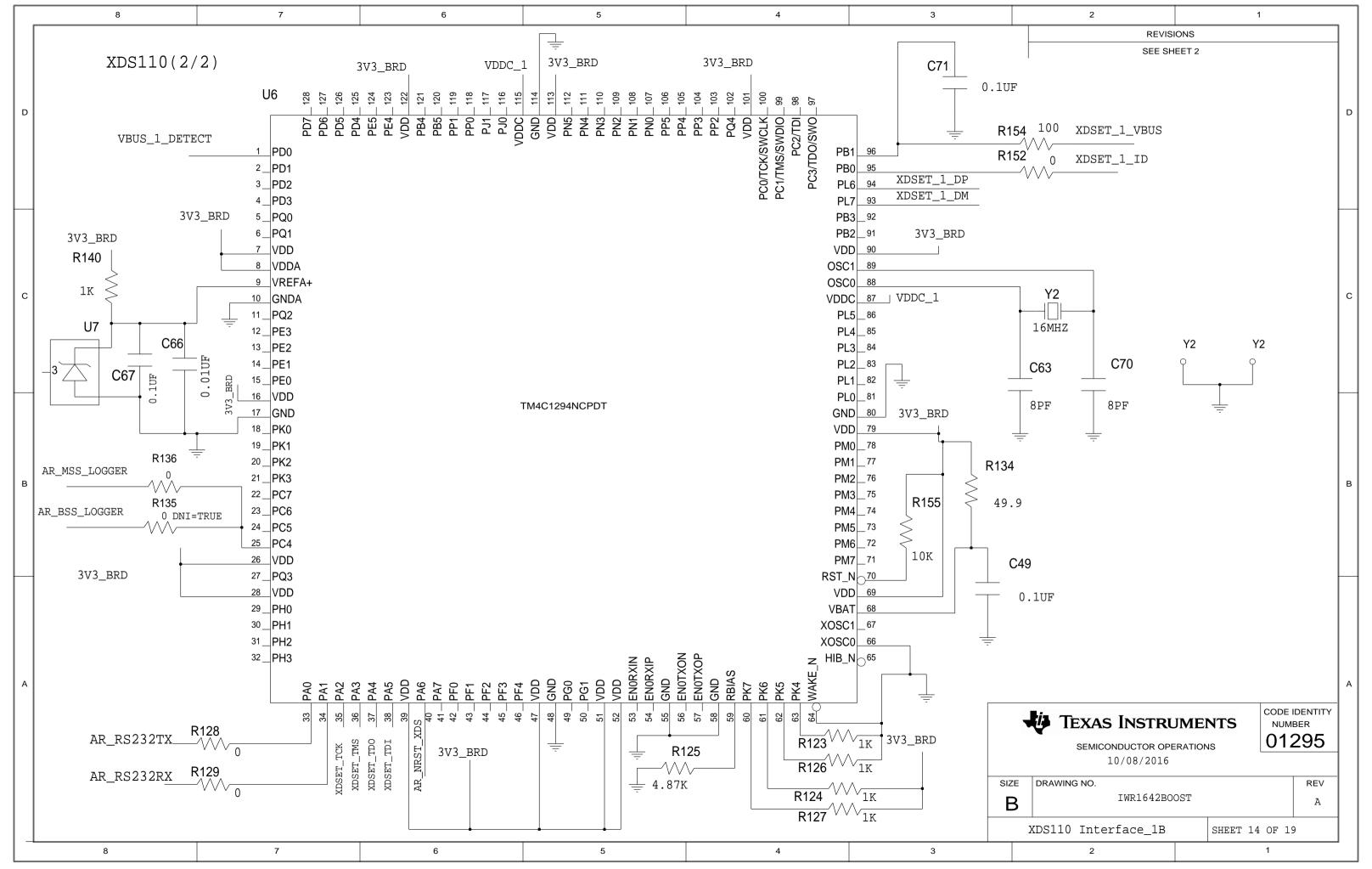


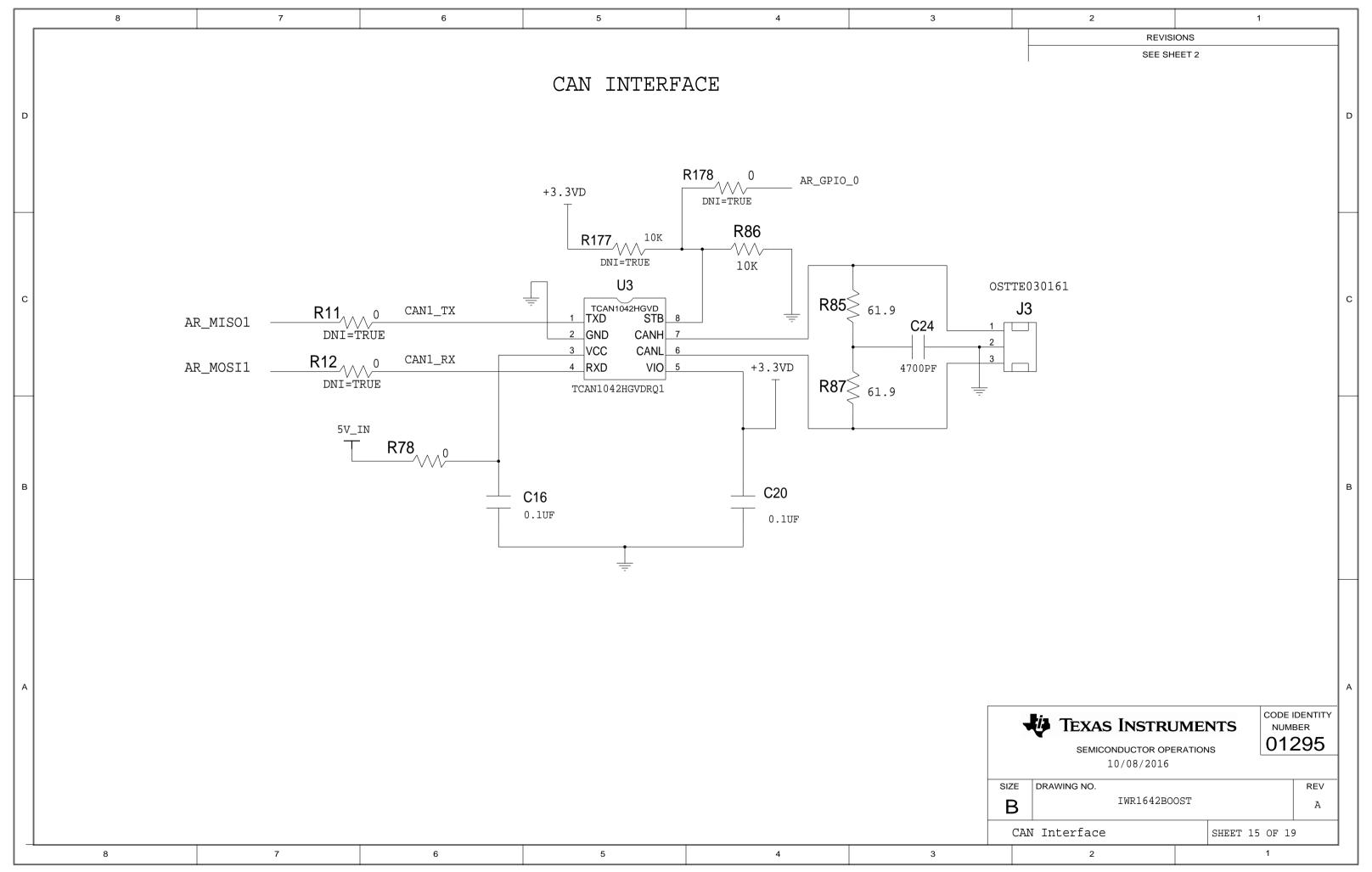


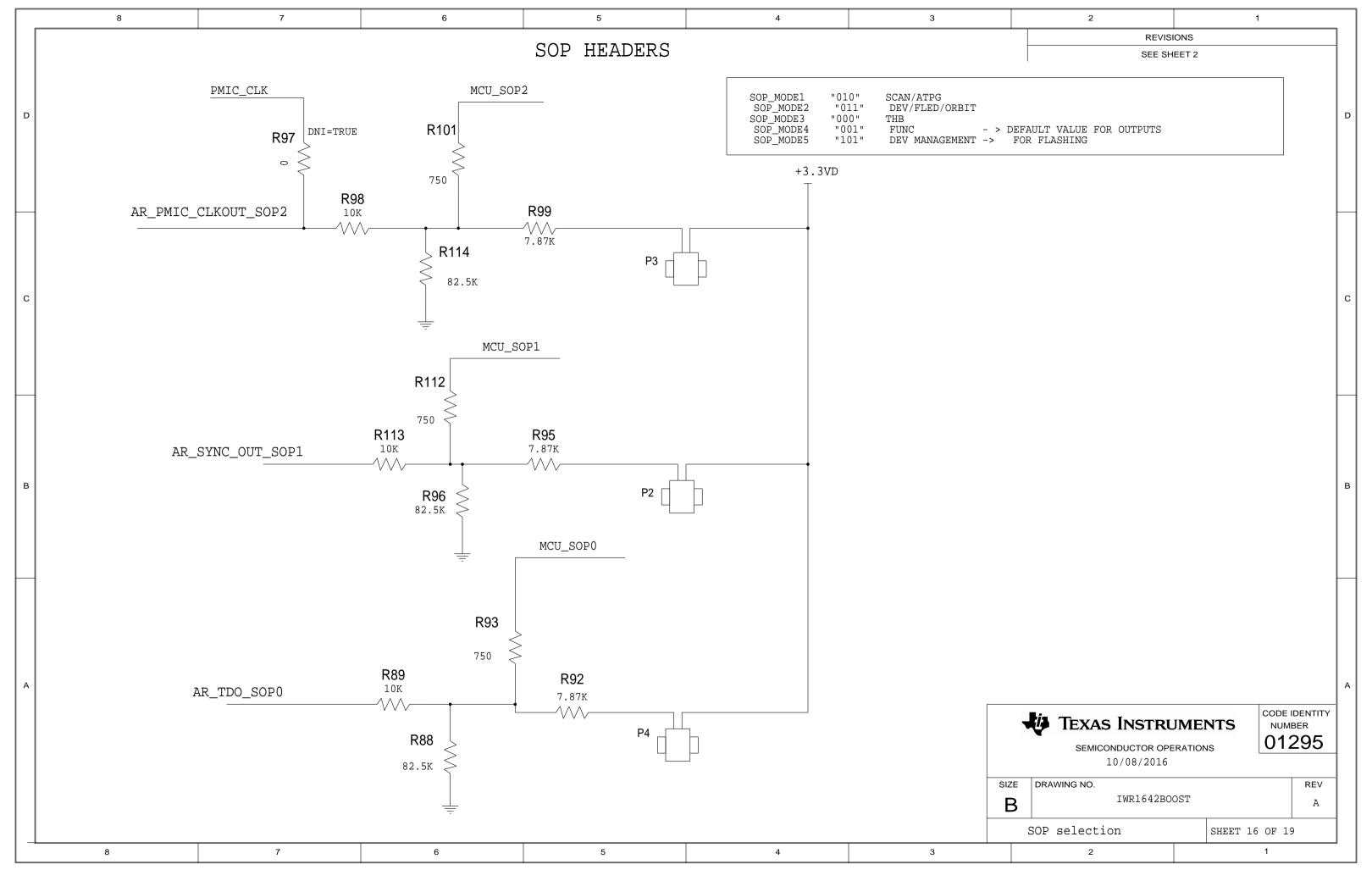


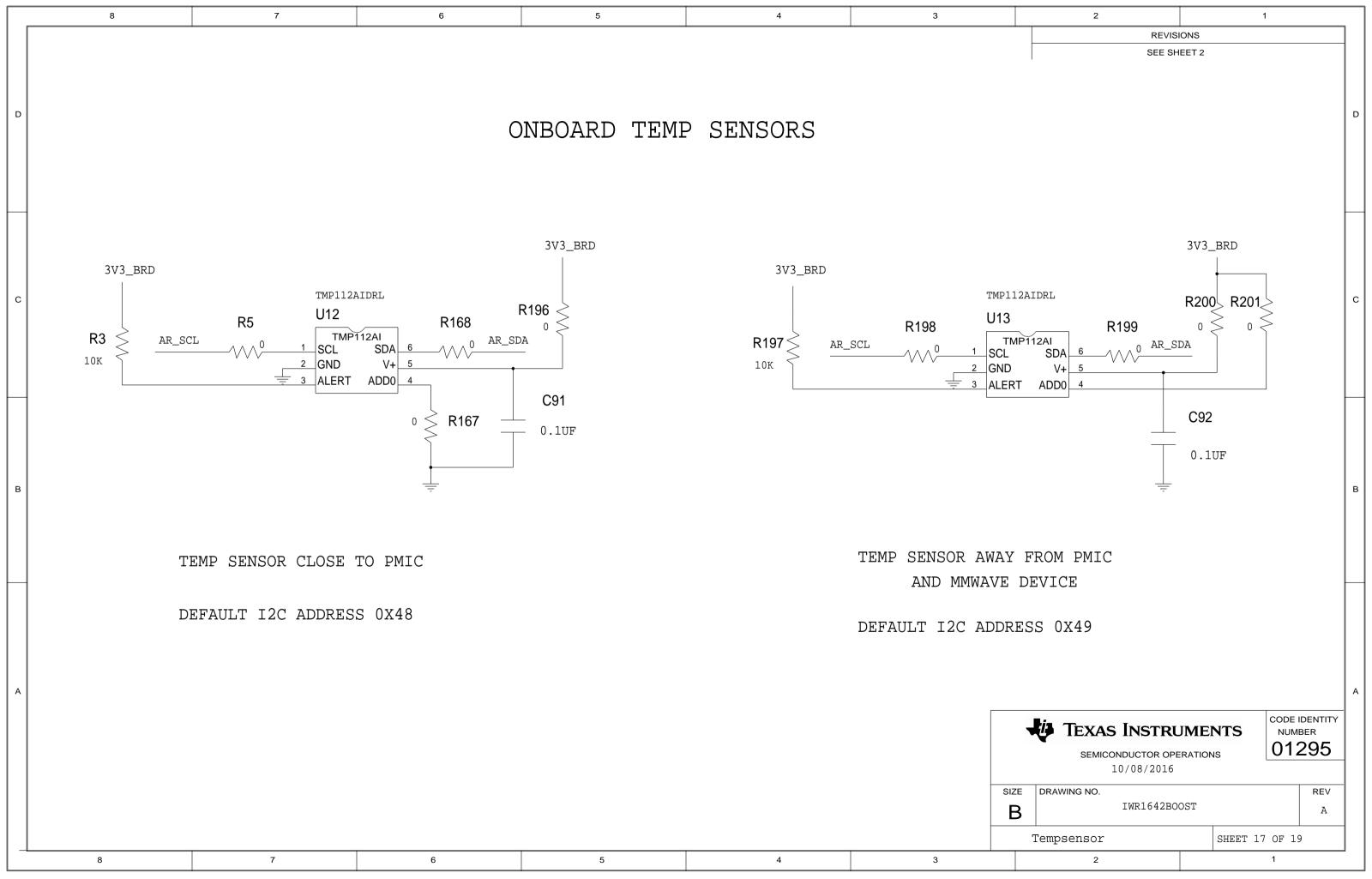


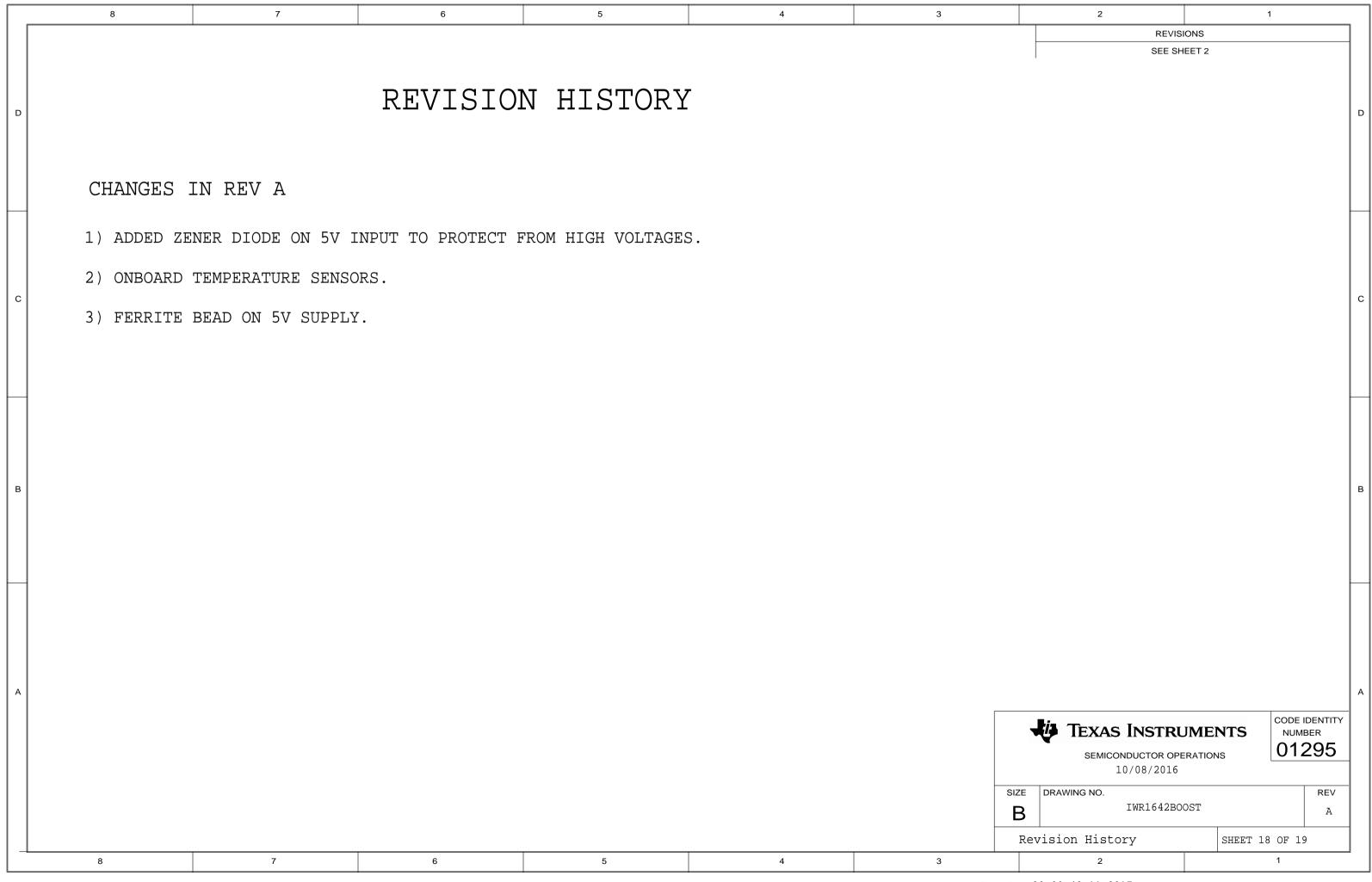












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D		FD1	O FD4				SEE 5	HEET 2				
		FDI										
		FD2	O FD5							D		
		TD 3	O FD6									
		FD3	C PD0									
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		D∕ī	B LOGO	PCB LOGO	PCB LOGO	P	CB LOGO					
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	PCB LABELS	: THESE LABELS N	IEED TO BE PUT	ON THE ASSEMBLED PCB								
	1) TOP SIDE OF TH	HE PCB -> IW	VR1642BOOST									
		⊤ <i>ъ</i> л	R1642BOOST							$\vdash$		
	2) BOTTOM SIDE OF	F THE PCB ->	REV A									
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