DAVICOM

聯傑國際股份有限公司

To: _____   Date: _____

CC: _____

From: _____

Part No. / Version : <u>DM9051A-E1_</u> to <u>DM9051A-E2_</u>
Design Change Report

Content:
1. Summary
2. Detail Description
3. Proposal modification : ☐Y ■N
4. ☐Data sheet modification : ☐Y ■N

Reviewer	Signature
Designer	
PM	
SA(HW)	
SA(SW)	
TM	
PE / TE / 品質單位	
其它	

Initiator: _____   Director: _____

Project Leader: _____   研發最高主管: _____

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1. Summary

Serial No.	Title	Type
0	主旨：E1 shuttle to E2 MP 變更前: E1 V0~V3 變更後: MP version	Specification
1	主旨：fix E-fuse functions 變更前: 1.1 address 0x12 TX trim fail 　　　　1.2 address 0x10 loading error 　　　　1.3 address 0x17 bit 0 error 　　　　1.4 fail when all effuse data are 0xff 　　　　1.5 no 10M code option in e-fuse address 0x12 bit 7 變更後: 1.1 address 0x12 TX trim fixed 　　　　1.2 address 0x10 loading OK 　　　　1.3 address 0x17 bit 0 OK 　　　　1.4 all effuse data can be any value 　　　　1.5 add 10M code option in e-fuse address 0x12 bit 7	Fix Bug
2	主旨：fix Fiber SD source logic 變更前: Fiber SD source from LNKLED error 變更後: Fiber SD source from LNKLED OK	Fix Bug
3	主旨：fix chksum offload in en/decryption function 變更前: chksum offload function error 變更後: chksum offload function OK	Fix Bug
4	主旨：Update IC version 變更前: MAC REG_5CH is 0xe1 變更後: MAC REG_5CH is 0xe2	Specification
5	主旨：Improve ESD 變更前:ESD upto 6K 變更後::ESD expect to 8K	Enhancement

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