

English Language Test Description

MIPR # M9545012MP24797
CDRL F001

for

Unit Under Test

CDA Control Logic A CCA
P/N 16101157-021

From

Light Armored Vehicle - 25A2
(LAV-25A2)

ATE SYSTEM

AN/USM-657B – Third Echelon Test System (TETS-B)
AN/USM-717 – Virtual Instrument Portable Equipment Repair/Test (VIPER/T)

Developed by

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ELTD REVISION SUMMARY

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1.0 Reference Documents

1.1 Virtual Instrument Portable Equipment Repair/Test (VIPER/T)

IEEE Std 716-1989	IEEE Standard Common Abbreviated Test Language for All Systems
TM TBD-CD	VIPER/T IETM (Interactive Electronic Technical Manual)
System Design Document Doc # 7992008	VIPER/T AN/USM-717
VIPER/T P/N 7992021	VIPER/T CPM (Computer Programming Manual)

1.2 Third Echelon Test System (TETS-B)

TM 10530A-CD	TETS IETM (Interactive Electronic Technical Manual)
System Design Description Doc # 93006A0018	Third Echelon Test System (TETS) AN/USM-657
TETS P/N 93006A0026	TETS CPM (Computer Programming Manual)

1.3 Unit Under Test

UUT P/N: 16101157-021
 UUT Nomenclature: Control Logic A CCA
 UUT Type: Shop Replaceable Unit (SRU)

<u>DESCRIPTION</u>	<u>NUMBER</u>	<u>REVISION</u>	<u>DATE</u>
Parts List	16101157-011	H	07-Jan-2003
LRU QA Specification	ES13456	None	None
Circuit Card Assy, Control Logic A Schematic Diagram,	16101157-011	H	07-Jan-2003
Control Logic A	16101160-001	None	None

1.4 Reference Drawings

Refer to the following schematics when diagnosing connection paths.

ID Schematic



13020A0001
(SYSTEM INTERCON)

W1 Schematic



13020A7101 (CABLE,
W1, SCHEMATIC).pdf

2.0 English Language Test Description Steps

2.1 Common Procedures

The following connections are common throughout the entire test.

2.1.1 UUT POWER

Description:

15V Power is applied to J3-30 (HI) and J1-10 (LO) using DC2.

-15V Power is applied to J3-29 (HI) and J1-10 (LO) using DC6.

28V Power is applied to ID BUS2 (HI) and J3-10 (LO) using DC4.

CONNECTION PATH IS AS FOLLOWS:

FROM ID P1-4 (DC2-HI)	TO ID A1P1.2
FROM ID A1J1.2	TO ID A1J2.23
FROM ID A1P2.23	TO ID P10-22 (S101-1)
FROM ID P10-86 (S101-2)	TO ID A1P2.15
FROM ID A1J2.15	TO ID J1A-14E
FROM W1 P1A-14E	TO W1 P3-30 (UUT J3-30)
FROM ID A1J2.15	TO ID A1J10.14
FROM ID A1P10.14	TO ID P11-140 (S301-161)
FROM ID P11-206 (S301-162)	TO ID A1P10.16
FROM ID A1J10.16	TO ID J1A-13E
FROM W1 P1A-13E	TO W1 P2-10 (UUT J1-10)
FROM ID P1-16 (DC6-HI)	TO ID A1P1.15
FROM ID A1J1.15	TO ID A1J2.20
FROM ID A1P2.20	TO ID P10-25 (S101-13)

FROM ID P10-89 (S101-14)	TO ID A1P2.33
FROM ID A1J2.33	TO ID J1A-14D
FROM W1 P1A-14D	TO W1 P3-29 (UUT J3-29)
FROM ID P1-10 (DC4-HI)	TO ID A1P1.3
FROM ID A1J1.3	TO ID A1J8.2
FROM ID A1P8.2	TO ID P10-141 (S301-70)
FROM ID P10-44 (S301-69)	TO ID A1P8.31
FROM ID A1J8.31	TO ID BUS 2
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P3-6 (UUT J3-6)	TO W1 P1B-5B
FROM ID J1B-5B	TO ID A1J12.14
FROM ID A1P12.14	TO ID P12-27 (S202-24)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2

2.1.2 APPLY IC

Description:

Connect CT-RTN to BUS8
Connect CT-RTN to INSTR-GND

CONNECTION PATH IS AS FOLLOWS:

FROM CT-RTN	TO ID A1J6.9
FROM ID A1P6.9	TO ID P10-94 (S402-1)
FROM ID P10-253 (S402-5)	TO ID A1P6.22
FROM ID A1J6.22	TO ID BUS8
FROM INSTR-RTN	TO ID A1J6.11
FROM ID A1P6.11	TO ID P10-166(S301-26)
FROM ID P10-102 (S301-25)	TO ID A1P7.34
FROM ID A1J7.34	TO GROUND

2.2 INTERFACE ID

STEP 1

Date: 04 March 2016

DESCRIPTION:

This step verifies the correct ID is installed. The DMM is used to measure the resistance across ID resistors R111 (324 ohms) and R109 (698 ohms). The DMM should measure between 971 and 1073 Ohms.

CONNECTION PATH IS AS FOLLOWS:

FROM ID BUS 5	TO ID A1J8.47
FROM ID A1P8.47	TO ID P10-73 (S301-48)
FROM ID P10-7 (S301-47)	TO ID A1P7.23
FROM ID A1J7.23	TO ID A1J4.15
FROM ID A1P4.15	TO ID R111.1
FROM ID R111.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-137 (S503-7)	TO ID A1P6.47
FROM ID A1J6.47	TO ID BUS 5
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J8.26
FROM ID A1P8.26	TO ID P10-139 (S503-2)
FROM ID P10-170 (S503-8)	TO ID A1P6.38
FROM ID A1J6.38	TO ID BUS 6

2.3 UUT ID

STEP 2

DESCRIPTION:

This step verifies the correct UUT is installed. The DMM is used to measure continuity between UUT pins J1.32 and J1.67. The DMM should measure less than 10 ohms.

CONNECTION PATH IS AS FOLLOWS:

FROM W1 P2-32 (UUT J1-32)	TO W1 P1B-6B
FROM ID J1B-6B	TO ID A1J12.11
FROM ID A1P12.11	TO ID P12-22 (S201-37)
FROM ID P12-16 (S201-1)	TO ID A1P12.42
FROM ID A1J12.42	TO ID A1J10.6

FROM ID A1P10.6	TO ID P11-203 (S508-1)
FROM ID P11-77 (S508-3)	TO ID A1P9.15
FROM ID A1J9.15	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J8.26
FROM ID A1P8.26	TO ID P10-139 (S503-2)
FROM ID P10-12 (S503-4)	TO ID A1P6.23
FROM ID A1J6.23	TO ID BUS 2

2.4 SAFE TO TURN ON

Refer to [1.4 Reference Drawings](#) when diagnosing connection paths.

STEP 3

DESCRIPTION:

THIS VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J3.30 TO UUT J1.45. THE DMM SHOULD MEASURE GREATER THAN 10 KOHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J7.42
FROM ID A1P7.42	TO ID P10-34 (S301-2)
FROM ID P10-65 (S301-1)	TO ID A1P7.3
FROM ID A1J7.3	TO ID A1J2.15
FROM ID A1P2.15	TO ID P10-86 (S101-2)
FROM ID P10-22 (S101-1)	TO ID A1P2.23
FROM ID A1J2.23	TO ID A1J1.2
FROM ID A1P1.2	TO ID P1-4 (DC2-HI J3-30)
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 4

DESCRIPTION:

THIS VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.10 TO UUT J1.45. THE DMM SHOULD MEASURE GREATER THAN 10 KOHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM W1 P2-10 (UUT J1-10)	TO W1 P1A-13E
FROM ID J1A-13E	TO ID A1J10.16
FROM ID A1P10.16	TO ID P11-206 (S301-162)
FROM ID P11-140 (S301-161)	TO ID A1P10.14
FROM ID A1J10.14	TO ID A1J2.15
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J7.42
FROM ID A1P7.42	TO ID P10-34 (S301-2)
FROM ID P10-65 (S301-1)	TO ID A1P7.3
FROM ID A1J7.3	TO ID A1J2.15
FROM ID A1P2.15	TO ID P10-86 (S101-2)
FROM ID P10-22 (S101-1)	TO ID A1P2.23
FROM ID A1J2.23	TO ID A1J1.2
FROM ID A1P1.2	TO ID P1-4 (DC2-HI J1-10)
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 5

DESCRIPTION:

THIS VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J3.29 TO UUT J1.45. THE DMM SHOULD MEASURE GREATER THAN 500 OHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM ID A1J1.15 (DC2-HI J3-29)	TO ID A1J2.20
FROM ID A1P2.20	TO ID P10-25 (S101-13)
FROM ID P10-89 (S101-14)	TO ID A1P2.33
FROM ID A1J2.33	TO ID A1J7.5
FROM ID A1P7.5	TO ID P10-97 (S301-9)
FROM ID P10-2 (S301-10)	TO ID A1P7.48
FROM ID A1J7.48	TO ID A1J15.49
FROM ID A1P15.49	TO ID P20-2 (DMM-HI)
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 6

DESCRIPTION:

THIS VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.67 TO UUT J1.45. THE DMM SHOULD MEASURE GREATER THAN 10 KOHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 7

DESCRIPTION:

THIS VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J3.6 TO UUT J1.45. THE DMM SHOULD MEASURE GREATER THAN 10 KOHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM W1 P3-6 (UUT J3-6)	TO W1 P1B-5B
FROM ID J1B-5B	TO ID A1J12.14
FROM ID A1P12.14	TO ID P12-27 (S202-24)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13

FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 8

DESCRIPTION:

THIS VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.43 TO UUT J1.45. THE DMM SHOULD MEASURE BETWEEN 500 AND 850 OHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM W1 P2-43 (UUT J1-43)	TO W1 P1B-9D
FROM ID J1B-9D	TO ID A1J13.17
FROM ID A1P13.17	TO ID P12-41 (S701-38)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

2.5 MODULE 1: DISCRETE OUTPUTS

Refer to [1.4 Reference Drawings](#) when diagnosing connection paths.

STEP 101

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J3.4 SHOULD BE BETWEEN 27.2 AND 27.6VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

Date: 04 March 2016

SEE "APPLY IC"

FROM W1 P3-4 (UUT J3-4)	TO W1 P1A-3B
FROM ID J1A-3B	TO ID A1J15.2
FROM ID A1P15.2	TO ID P13-6 (S701-8)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 102

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J1.10 SHOULD BE BETWEEN 14.5 AND 15.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J7.42
FROM ID A1P7.42	TO ID P10-34 (S301-2)
FROM ID P10-65 (S301-1)	TO ID A1P7.3
FROM ID A1J7.3	TO ID A1J2.15
FROM ID A1P2.15	TO ID P10-86 (S101-2)
FROM ID P10-22 (S101-1)	TO ID A1P2.23
FROM ID A1J2.23	TO ID A1J1.2
FROM ID A1P1.2	TO ID P1-4 (DC2-HI J1-10)
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 103

Date: 04 March 2016

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J3.29 SHOULD BE BETWEEN -14.5 AND -15.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J7.48
FROM ID A1P7.48	TO ID P10-2 (S301-10)
FROM ID P10-97 (S301-9)	TO ID A1P7.5
FROM ID A1J7.5	TO ID A1J2.33
FROM ID A1P2.33	TO ID P10-89 (S101-14)
FROM ID P10-25 (S101-13)	TO ID A1P2.20
FROM ID A1J2.20	TO ID A1J1.15
FROM ID A1P1.15	TO ID P1-16 (DC6-HI)
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 104

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J1.41 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.36
FROM ID A1P8.36	TO ID P10-14 (S301-73)
FROM ID P10-15 (S301-74)	TO ID A1P8.3
FROM ID A1J8.3	TO ID R2.1
FROM ID R2.2	TO GROUND
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E

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FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 105

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J3.17 SHOULD BE BETWEEN 12.0 AND 12.4VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P3-17 (UUT J3-17)	TO W1 P1B-13F
FROM ID J1B-13F	TO ID A1J13.12
FROM ID A1P13.12	TO ID P12-5 (S701-12)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 106

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT PIN J1.39 TO GROUND. THE OUTPUT MEASURED AT PIN J1.41 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.36
FROM ID A1P8.36	TO ID P10-14 (S301-73)
FROM ID P10-15 (S301-74)	TO ID A1P8.3
FROM ID A1J8.3	TO ID R2.1
FROM ID R2.2	TO GROUND
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

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FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 107

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 TO GROUND. THE OUTPUT MEASURED AT PIN J3.17 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P3-17 (UUT J3-17)	TO W1 P1B-13F
FROM ID J1B-13F	TO ID A1J13.12
FROM ID A1P13.12	TO ID P12-5 (S701-12)

FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 108

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.4. SHORT J1.39 TO GROUND. THE OUTPUT MEASURED AT PIN J1.41 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-4 (UUT J1-4)	TO W1 P1B-13A
FROM ID J1B-13A	TO ID A1J13.2
FROM ID A1P13.2	TO ID P12-47 (S201-6)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.36
FROM ID A1P8.36	TO ID P10-14 (S301-73)
FROM ID P10-15 (S301-74)	TO ID A1P8.3
FROM ID A1J8.3	TO ID R2.1
FROM ID R2.2	TO GROUND
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1

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FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 109

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 TO GROUND. THE OUTPUT MEASURED AT PIN J1.41 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.36
FROM ID A1P8.36	TO ID P10-14 (S301-73)
FROM ID P10-15 (S301-74)	TO ID A1P8.3
FROM ID A1J8.3	TO ID R2.1
FROM ID R2.2	TO GROUND

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FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 110

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 TO GROUND. THE OUTPUT MEASURED AT PIN J3.18 SHOULD BE BETWEEN 12.0 AND 12.4VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-18 (UUT J3-18)	TO W1 P1B-12E
FROM ID J1B-12E	TO ID A1J13.14
FROM ID A1P13.14	TO ID P12-37 (S701-14)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

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FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 111

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 AND J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J1.41 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.36
FROM ID A1P8.36	TO ID P10-14 (S301-73)
FROM ID P10-15 (S301-74)	TO ID A1P8.3
FROM ID A1J8.3	TO ID R2.1
FROM ID R2.2	TO GROUND

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FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 112

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 AND J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J3.18 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-18 (UUT J3-18)	TO W1 P1B-12E
FROM ID J1B-12E	TO ID A1J13.14
FROM ID A1P13.14	TO ID P12-37 (S701-14)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1

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FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 113

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39, J1.40 AND J3.22 TO GROUND. THE OUTPUT MEASURED AT PIN J1.41 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-129 (S506-8)	TO ID A1P9.30

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FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.36
FROM ID A1P8.36	TO ID P10-14 (S301-73)
FROM ID P10-15 (S301-74)	TO ID A1P8.3
FROM ID A1J8.3	TO ID R2.1
FROM ID R2.2	TO GROUND
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 114

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J1.41 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E

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FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.36
FROM ID A1P8.36	TO ID P10-14 (S301-73)
FROM ID P10-15 (S301-74)	TO ID A1P8.3
FROM ID A1J8.3	TO ID R2.1
FROM ID R2.2	TO GROUND
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 115

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40 AND J3.22 TO GROUND. THE OUTPUT MEASURED AT PIN J1.41 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F

FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.36
FROM ID A1P8.36	TO ID P10-14 (S301-73)
FROM ID P10-15 (S301-74)	TO ID A1P8.3
FROM ID A1J8.3	TO ID R2.1
FROM ID R2.2	TO GROUND
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

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STEP 116

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40 AND J3.22 TO GROUND. 28.0VDC IS APPLIED TO J1.5. THE OUTPUT MEASURED AT PIN J1.41 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.36
FROM ID A1P8.36	TO ID P10-14 (S301-73)
FROM ID P10-15 (S301-74)	TO ID A1P8.3
FROM ID A1J8.3	TO ID R2.1
FROM ID R2.2	TO GROUND
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20

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FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 117

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J1.41 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.36

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FROM ID A1P8.36	TO ID P10-14 (S301-73)
FROM ID P10-15 (S301-74)	TO ID A1P8.3
FROM ID A1J8.3	TO ID R2.1
FROM ID R2.2	TO GROUND
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 118

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40 AND J3.22 TO GROUND. THE OUTPUT MEASURED AT PIN J1.41 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E

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FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.36
FROM ID A1P8.36	TO ID P10-14 (S301-73)
FROM ID P10-15 (S301-74)	TO ID A1P8.3
FROM ID A1J8.3	TO ID R2.1
FROM ID R2.2	TO GROUND
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 119

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40 AND J3.22 TO GROUND. APPLY 28.0VDC TO J3.16. THE OUTPUT MEASURED AT PIN J1.41 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P3-16 (UUT J3-16)	TO W1 P1B-6C
FROM ID J1B-6C	TO ID A1J12.12
FROM ID A1P12.12	TO ID P12-53 (S201-39)
FROM ID P12-20 (S201-3)	TO ID A1P12.46

FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.36
FROM ID A1P8.36	TO ID P10-14 (S301-73)
FROM ID P10-15 (S301-74)	TO ID A1P8.3
FROM ID A1J8.3	TO ID R2.1
FROM ID R2.2	TO GROUND
FROM W1 P2-41 (UUT J1-41)	TO W1 P1B-10E
FROM ID J1B-10E	TO ID A1J13.20
FROM ID A1P13.20	TO ID P12-9 (S701-36)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)

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FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 120

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.46, J1.40, J3.22 AND J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.9 (**SEE NOTE BELOW**) J1.8 AND J1.47. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

NOTE: 28.0VDC IS APPLIED TO J1.9 FOR 10MS, AND THEN IT IS RETURNED TO THE OPENED CONDITION.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"
SEE "APPLY IC"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)

FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)

FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2

FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
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FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 121

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.46, J1.40, J3.22 AND J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.9 (**SEE NOTE BELOW**) J1.8 AND J1.47. MOMENTARILY LIFT J1.16 FROM GROUND, CONNECT TO J1.10 AND RECONNECT TO GROUND. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

Date: 04 March 2016

NOTE: 28.0VDC IS APPLIED TO J1.9 FOR 10MS, AND THEN IT IS RETURNED TO THE OPENED CONDITION.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-16 (UUT J1-16)	TO W1 P1A-12D
FROM ID J1A-12D	TO ID A1J11.9
FROM ID A1P11.9	TO ID P11-43 (S301-154)
FROM ID P11-233 (S301-153)	TO ID A1P11.11
FROM ID A1J11.11	TO ID A1J2.15 (DC2-HI J3-30)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11

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FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 122

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.46, J1.40, J3.22, J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.9 (**SEE NOTE BELOW**) AND J1.8. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

NOTE: 28.0VDC IS APPLIED TO J1.9 FOR 10MS, AND THEN IT IS RETURNED TO THE OPENED CONDITION.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)

FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)

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FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 123

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.46, J1.40, J3.22 AND J1.39 TO GROUND. 28.0VDC IS APPLIED TO PINS J1.29 AND J1.13. MOMENTARILY LIFT J1.16 FROM GROUND AND CONNECT TO J1.10. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P2-13 (UUT J1-13)	TO W1 P1B-14B
FROM ID J1B-14B	TO ID A1J13.3
FROM ID A1P13.3	TO ID P12-46 (S201-7)

FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)

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FROM W1 P2-16 (UUT J1-16)	TO W1 P1A-12D
FROM ID J1A-12D	TO ID A1J11.9
FROM ID A1P11.9	TO ID P11-43 (S301-154)
FROM ID P11-233 (S301-153)	TO ID A1P11.11
FROM ID A1J11.11	TO ID A1J2.15 (DC2-HI J3-30)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 124

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.46, J1.40, J3.22 AND J1.39 TO GROUND. 28.0VDC IS APPLIED TO PINS J1.29, J1.8 AND J1.13. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-13 (UUT J1-13)	TO W1 P1B-14B
FROM ID J1B-14B	TO ID A1J13.3
FROM ID A1P13.3	TO ID P12-46 (S201-7)
FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

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FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 125

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.12, J1.40, J3.22 AND J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29 AND J1.47. MOMENTARILY CONNECT J1.16 TO J1.44. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)

FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)

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FROM W1 P2-16 (UUT J1-16)	TO W1 P1A-12D
FROM ID J1A-12D	TO ID A1J11.9
FROM ID A1P11.9	TO ID P11-43 (S301-154)
FROM ID P11-233 (S301-153)	TO ID A1P11.11
FROM ID A1J11.11	TO ID A1J2.15 (DC2-HI J3-30)
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 126

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.12, J1.40, J3.22 AND J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.47 AND J1.9. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 127

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40, J3.22 AND J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29 AND J1.13. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P2-13 (UUT J1-13)	TO W1 P1B-14B
FROM ID J1B-14B	TO ID A1J13.3
FROM ID A1P13.3	TO ID P12-46 (S201-7)

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)

FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)

FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23

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FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 128

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40, J3.22 AND J1.39 TO GROUND. 28.0VDC IS APPLIED TO PINS J1.29 AND J1.13. MOMENTARILY CONNECT J1.16 TO J1.44. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-16 (UUT J1-16)	TO W1 P1A-12D
FROM ID J1A-12D	TO ID A1J11.9
FROM ID A1P11.9	TO ID P11-43 (S301-154)
FROM ID P11-233 (S301-153)	TO ID A1P11.11
FROM ID A1J11.11	TO ID A1J2.15 (DC2-HI J3-30)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-13 (UUT J1-13)	TO W1 P1B-14B
FROM ID J1B-14B	TO ID A1J13.3
FROM ID A1P13.3	TO ID P12-46 (S201-7)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)

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FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 129

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40, J3.22 AND J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.13 AND J1.17. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-17 (UUT J1-17)	TO W1 P1A-2B
FROM ID J1A-2B	TO ID A1J14.4
FROM ID A1P14.4	TO ID P13-48 (S201-12)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27

FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-13 (UUT J1-13)	TO W1 P1B-14B
FROM ID J1B-14B	TO ID A1J13.3
FROM ID A1P13.3	TO ID P12-46 (S201-7)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 130

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40, J3.22 AND J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29 AND J1.13. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-13 (UUT J1-13)	TO W1 P1B-14B
FROM ID J1B-14B	TO ID A1J13.3
FROM ID A1P13.3	TO ID P12-46 (S201-7)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

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FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 131

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40 TO GROUND. 28.0VDC IS APPLIED TO PINS J1.29 AND J1.47. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

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FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 132

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40 AND J3.22 TO GROUND. 28.0VDC IS APPLIED TO PINS J1.29, J1.47 AND J1.5. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F

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FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 133

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40 AND J3.22 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.47 AND J1.5. MOMENTARILY CONNECT J1.16 TO J1.44. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

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FROM W1 P2-16 (UUT J1-16)	TO W1 P1A-12D
FROM ID J1A-12D	TO ID A1J11.9
FROM ID A1P11.9	TO ID P11-43 (S301-154)
FROM ID P11-233 (S301-153)	TO ID A1P11.11
FROM ID A1J11.11	TO ID A1J2.15 (DC2-HI J3-30)
FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

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FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 134

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40 AND J3.22 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.47, J1.5 AND J1.51. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-51 (UUT J1-51)	TO W1 P1B-9B
FROM ID J1B-9B	TO ID A1J12.2
FROM ID A1P12.2	TO ID P12-15 (S201-16)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F

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FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J7.36	TO GROUND

STEP 135

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40, J3.22. 28.0VDC IS APPLIED TO PIN J1.29, J1.47 AND J1.5. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 136

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.29 AND J1.13. MOMENTARILY CONNECT J1.16 TO J1.44. SHORT J1.39 TO GROUND. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-16 (UUT J1-16)	TO W1 P1A-12D
FROM ID J1A-12D	TO ID A1J11.9
FROM ID A1P11.9	TO ID P11-43 (S301-154)
FROM ID P11-233 (S301-153)	TO ID A1P11.11
FROM ID A1J11.11	TO ID A1J2.15 (DC2-HI J3-30)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 137

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.29, J1.13 AND J1.38. SHORT J1.39 TO GROUND. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 138

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.9 (FOR 10MSEC AND THEN OPEN), J1.13, J1.5 AND J1.38. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)

FROM W1 P2-13 (UUT J1-13)	TO W1 P1B-14B
FROM ID J1B-14B	TO ID A1J13.3
FROM ID A1P13.3	TO ID P12-46 (S201-7)

FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)

FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)

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FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 139

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.13, J1.5, J1.38 AND J1.8. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)

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FROM W1 P2-13 (UUT J1-13)	TO W1 P1B-14B
FROM ID J1B-14B	TO ID A1J13.3
FROM ID A1P13.3	TO ID P12-46 (S201-7)
FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 140

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 AND J1.46 TO GROUND. 8.0VDC IS APPLIED TO PIN J1.29, J1.13, J1.5, J1.38 AND J1.8. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

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CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)
FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM W1 P2-13 (UUT J1-13)	TO W1 P1B-14B
FROM ID J1B-14B	TO ID A1J13.3
FROM ID A1P13.3	TO ID P12-46 (S201-7)
FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E

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FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 141

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.19 (FOR 2 SECONDS AND THEN OPEN), J1.5 AND J1.38. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-19 (UUT J1-19)	TO W1 P1B-13C
FROM ID J1B-13C	TO ID A1J13.6
FROM ID A1P13.6	TO ID P12-78 (S201-14)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

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FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 142

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.5, J1.38 AND J1.9. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1

FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 143

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 AND J1.12 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.5, J1.38 AND J1.9. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)
FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48

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FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 144

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 AND J1.12 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.5, J1.38 AND J1.9. MOMENTARILY CONNECT J1.16 TO J1.44. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-16 (UUT J1-16)	TO W1 P1A-12D
FROM ID J1A-12D	TO ID A1J11.9
FROM ID A1P11.9	TO ID P11-43 (S301-154)
FROM ID P11-233 (S301-153)	TO ID A1P11.11
FROM ID A1J11.11	TO ID A1J2.15 (DC2-HI J3-30)
FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)
FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

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FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 145

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39, J1.12 AND J1.56 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.5, J1.38 AND J1.9. INITIATE COUNTER/TIMER TO MEASURE THE TIME INTERVAL BETWEEN PINS J1.56 AND J1.6. WHEN J1.56 DECREASES PAST 5 VOLTS, THE OUTPUT AT PIN J1.6 SHOULD DROP TO LESS THAN 0.5VDC RELATIVE TO GROUND IN BETWEEN 190 AND 725 MSECS.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-115 (S510-7)	TO ID A1P9.38
FROM ID A1J9.38	TO ID BUS 5
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-161 (S501-7)	TO ID A1P7.45
FROM ID A1J7.45	TO ID BUS 5
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-193 (S506-6)	TO ID A1P9.50
FROM ID A1J9.50	TO ID BUS 4
FROM ID P19-19 (CT-IN2)	TO ID A1P22.1
FROM ID A1J22.1	TO ID A1J6.10
FROM ID A1P6.10	TO ID P10-71 (S502-2)
FROM ID P10-38 (S502-6)	TO ID A1P7.43
FROM ID A1J7.43	TO ID BUS 4
FROM W1 P2-30 (UUT J1-30)	TO W1 P1B-4B
FROM ID J1B-4B	TO ID A1J12.17
FROM ID A1P12.17	TO ID P12-96 (S202-47)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)
FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27

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FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 146

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.19 (FOR 2 SECONDS AND THEN OPEN), J1.47 AND J1.8. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

Date: 04 March 2016

SEE "APPLY IC"

FROM W1 P2-29 (UUT J1-29)
FROM ID J1A-1C
FROM ID A1P14.5

TO W1 P1A-1C
TO ID A1J14.5
TO ID P13-49 (S201-17)

FROM ID P12-20 (S201-3)
FROM ID A1J12.46
FROM ID A1P10.2
FROM ID P11-72 (S507-4)
FROM ID A1J9.27

TO ID A1P12.46
TO ID A1J10.2
TO ID P11-39 (S507-1)
TO ID A1P9.27
TO ID BUS 2

FROM W1 P2-19 (UUT J1-19)
FROM ID J1B-13C
FROM ID A1P13.6

TO W1 P1B-13C
TO ID A1J13.6
TO ID P12-78 (S201-14)

FROM ID P12-52 (S201-4)
FROM ID A1J12.44
FROM ID A1P10.4
FROM ID P11-72 (S507-4)
FROM ID A1J9.27

TO ID A1P12.44
TO ID A1J10.4
TO ID P11-71 (S507-2)
TO ID A1P9.27
TO ID BUS 2

FROM W1 P2-47 (UUT J1-47)
FROM ID J1A-3F
FROM ID A1P14.14

TO W1 P1A-3F
TO ID A1J14.14
TO ID P13-18 (S201-35)

FROM W1 P2-8 (UUT J1-8)
FROM ID J1A-5E
FROM ID A1P14.17

TO W1 P1A-5E
TO ID A1J14.17
TO ID P13-87 (S202-10)

FROM ID P13-29 (S202-4)
FROM ID A1J14.50
FROM ID A1P10.50
FROM ID P11-147 (S510-4)
FROM ID A1J9.31

TO ID A1P14.50
TO ID A1J10.50
TO ID P11-244 (S510-2)
TO ID A1P9.31
TO ID BUS 2

FROM W1 P2-39 (UUT J1-39)
FROM ID J1A-1E
FROM ID A1P14.9

TO W1 P1A-1E
TO ID A1J14.9
TO ID P13-17 (S201-26)

FROM ID P12-80 (S201-2)
FROM ID A1J12.40
FROM ID A1P10.8
FROM ID P11-205 (S508-10)
FROM ID A1J9.2

TO ID A1P12.40
TO ID A1J10.8
TO ID P11-139 (S508-2)
TO ID A1P9.2
TO ID BUS 8

FROM W1 P2-6 (UUT J1-6)
FROM ID J1B-13E
FROM ID A1P13.10

TO W1 P1B-13E
TO ID A1J13.10
TO ID P12-3 (S701-6)

FROM ID P12-44 (S701-2)
FROM ID A1J12.48
FROM ID A1P10.1
FROM ID P11-164 (S506-3)

TO ID A1P12.48
TO ID A1J10.1
TO ID P11-162 (S506-2)
TO ID A1P9.23

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FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 147

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.47, J1.8 AND J1.38. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2

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FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 148

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.8 AND J1.38. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2

FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 149

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 AND J1.56 TO GROUND. 28.0VDC IS APPLIED TO PIN J1.29, J1.8 AND J1.38. INITIATE COUNTER/TIMER TO MEASURE THE TIME INTERVAL BETWEEN PINS J1.56 AND J1.6. WHEN J1.56 DECREASES PAST 5 VOLTS, THE OUTPUT AT PIN J1.6 SHOULD DROP TO LESS THAN 0.5VDC RELATIVE TO GROUND IN BETWEEN 190 AND 725 MSEC.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-115 (S510-7)	TO ID A1P9.38
FROM ID A1J9.38	TO ID BUS 5
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-161 (S501-7)	TO ID A1P7.45
FROM ID A1J7.45	TO ID BUS 5
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-193 (S506-6)	TO ID A1P9.50
FROM ID A1J9.50	TO ID BUS 4
FROM ID P19-19 (CT-IN2)	TO ID A1P22.1
FROM ID A1J22.1	TO ID A1J6.10
FROM ID A1P6.10	TO ID P10-71 (S502-2)
FROM ID P10-38 (S502-6)	TO ID A1P7.43
FROM ID A1J7.43	TO ID BUS 4
FROM W1 P2-30 (UUT J1-30)	TO W1 P1B-4B
FROM ID J1B-4B	TO ID A1J12.17
FROM ID A1P12.17	TO ID P12-96 (S202-47)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B

FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 150

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.29, J1.47 AND J1.17. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)

FROM W1 P2-17 (UUT J1-17)	TO W1 P1A-2B
FROM ID J1A-2B	TO ID A1J14.4
FROM ID A1P14.4	TO ID P13-48 (S201-12)

FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)

FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
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FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 151

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.29, J1.47, J1.17 AND J3.16. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P3-16 (UUT J3-16)	TO W1 P1B-6C
FROM ID J1B-6C	TO ID A1J12.12
FROM ID A1P12.12	TO ID P12-53 (S201-39)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM W1 P2-17 (UUT J1-17)	TO W1 P1A-2B
FROM ID J1A-2B	TO ID A1J14.4
FROM ID A1P14.4	TO ID P13-48 (S201-12)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

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FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 152

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.29, J1.47, J1.17 AND J3.16. SHORT J1.50 TO GROUND. THE OUTPUT MEASURED AT PIN J1.6 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-50 (UUT J1-50)	TO W1 P1B-6A
FROM ID J1B-6A	TO ID A1J12.10
FROM ID A1P12.10	TO ID P12-83 (S201-32)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-16 (UUT J3-16)	TO W1 P1B-6C
FROM ID J1B-6C	TO ID A1J12.12
FROM ID A1P12.12	TO ID P12-53 (S201-39)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM W1 P2-17 (UUT J1-17)	TO W1 P1A-2B
FROM ID J1A-2B	TO ID A1J14.4

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FROM ID A1P14.4	TO ID P13-48 (S201-12)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-6 (UUT J1-6)	TO W1 P1B-13E
FROM ID J1B-13E	TO ID A1J13.10
FROM ID A1P13.10	TO ID P12-3 (S701-6)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 153

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J1.26 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-26 (UUT J1-26)	TO W1 P1A-6C
FROM ID J1A-6C	TO ID A1J15.12
FROM ID A1P15.12	TO ID P13-74 (S701-34)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)

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FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 154

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60. THE OUTPUT MEASURED AT PIN J1.26 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-26 (UUT J1-26)	TO W1 P1A-6C
FROM ID J1A-6C	TO ID A1J15.12
FROM ID A1P15.12	TO ID P13-74 (S701-34)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 155

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60 AND J1.61. THE OUTPUT MEASURED AT PIN J1.26 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-61 (UUT J1-61)	TO W1 P1B-7A
FROM ID J1B-7A	TO ID A1J12.7
FROM ID A1P12.7	TO ID P12-82 (S201-29)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-26 (UUT J1-26)	TO W1 P1A-6C
FROM ID J1A-6C	TO ID A1J15.12
FROM ID A1P15.12	TO ID P13-74 (S701-34)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 156

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60. THE OUTPUT MEASURED AT PIN J1.26 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)

FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P2-26 (UUT J1-26)	TO W1 P1A-6C
FROM ID J1A-6C	TO ID A1J15.12
FROM ID A1P15.12	TO ID P13-74 (S701-34)

FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 157

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60 AND J1.27. THE OUTPUT MEASURED AT PIN J1.26 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

Date: 04 March 2016

FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-26 (UUT J1-26)	TO W1 P1A-6C
FROM ID J1A-6C	TO ID A1J15.12
FROM ID A1P15.12	TO ID P13-74 (S701-34)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 158

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-23 (UUT J1-23)	TO W1 P1A-6A
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Date: 04 March 2016

FROM ID J1A-6A	TO ID A1J15.10
FROM ID A1P15.10	TO ID P13-43 (S701-32)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 159

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-23 (UUT J1-23)	TO W1 P1A-6A
FROM ID J1A-6A	TO ID A1J15.10
FROM ID A1P15.10	TO ID P13-43 (S701-32)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28

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FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 160

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60 AND J1.61. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-61 (UUT J1-61)	TO W1 P1B-7A
FROM ID J1B-7A	TO ID A1J12.7
FROM ID A1P12.7	TO ID P12-82 (S201-29)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-23 (UUT J1-23)	TO W1 P1A-6A
FROM ID J1A-6A	TO ID A1J15.10
FROM ID A1P15.10	TO ID P13-43 (S701-32)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)

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FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 161

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-23 (UUT J1-23)	TO W1 P1A-6A
FROM ID J1A-6A	TO ID A1J15.10
FROM ID A1P15.10	TO ID P13-43 (S701-32)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 162

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60 AND J1.27. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-23 (UUT J1-23)	TO W1 P1A-6A
FROM ID J1A-6A	TO ID A1J15.10
FROM ID A1P15.10	TO ID P13-43 (S701-32)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 163

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-23 (UUT J1-23)	TO W1 P1A-6A
FROM ID J1A-6A	TO ID A1J15.10
FROM ID A1P15.10	TO ID P13-43 (S701-32)

FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 164

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)

FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

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FROM W1 P2-23 (UUT J1-23)	TO W1 P1A-6A
FROM ID J1A-6A	TO ID A1J15.10
FROM ID A1P15.10	TO ID P13-43 (S701-32)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 165

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60 AND J1.28. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-23 (UUT J1-23)	TO W1 P1A-6A

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FROM ID J1A-6A	TO ID A1J15.10
FROM ID A1P15.10	TO ID P13-43 (S701-32)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 166

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-23 (UUT J1-23)	TO W1 P1A-6A
FROM ID J1A-6A	TO ID A1J15.10
FROM ID A1P15.10	TO ID P13-43 (S701-32)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28

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FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 167

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60 AND J1.62. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-62 (UUT J1-62)	TO W1 P1B-7C
FROM ID J1B-7C	TO ID A1J12.9
FROM ID A1P12.9	TO ID P12-19 (S201-31)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-23 (UUT J1-23)	TO W1 P1A-6A
FROM ID J1A-6A	TO ID A1J15.10
FROM ID A1P15.10	TO ID P13-43 (S701-32)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)

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FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 168

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J1.57 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-57 (UUT J1-57)	TO W1 P1A-7B
FROM ID J1A-7B	TO ID A1J15.14
FROM ID A1P15.14	TO ID P13-12 (S701-40)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 169

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60. THE OUTPUT MEASURED AT PIN J1.57 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
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FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-57 (UUT J1-57)	TO W1 P1A-7B
FROM ID J1A-7B	TO ID A1J15.14
FROM ID A1P15.14	TO ID P13-12 (S701-40)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 170

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60 AND J1.28. THE OUTPUT MEASURED AT PIN J1.57 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7

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FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-57 (UUT J1-57)	TO W1 P1A-7B
FROM ID J1A-7B	TO ID A1J15.14
FROM ID A1P15.14	TO ID P13-12 (S701-40)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 171

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60. THE OUTPUT MEASURED AT PIN J1.57 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-57 (UUT J1-57)	TO W1 P1A-7B
FROM ID J1A-7B	TO ID A1J15.14
FROM ID A1P15.14	TO ID P13-12 (S701-40)

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FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 172

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60 AND J1.62. THE OUTPUT MEASURED AT PIN J1.57 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-62 (UUT J1-62)	TO W1 P1B-7C
FROM ID J1B-7C	TO ID A1J12.9
FROM ID A1P12.9	TO ID P12-19 (S201-31)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-57 (UUT J1-57)	TO W1 P1A-7B
FROM ID J1A-7B	TO ID A1J15.14
FROM ID A1P15.14	TO ID P13-12 (S701-40)
FROM ID P12-44 (S701-2)	TO ID A1P12.48

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FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 173

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.60, J1.62 AND J1.61. THE OUTPUT MEASURED AT PIN J1.57 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-61 (UUT J1-61)	TO W1 P1B-7A
FROM ID J1B-7A	TO ID A1J12.7
FROM ID A1P12.7	TO ID P12-82 (S201-29)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-62 (UUT J1-62)	TO W1 P1B-7C
FROM ID J1B-7C	TO ID A1J12.9
FROM ID A1P12.9	TO ID P12-19 (S201-31)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-60 (UUT J1-60)	TO W1 P1A-1D
FROM ID J1A-1D	TO ID A1J14.7
FROM ID A1P14.7	TO ID P13-79 (S201-20)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4

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FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-57 (UUT J1-57)	TO W1 P1A-7B
FROM ID J1A-7B	TO ID A1J15.14
FROM ID A1P15.14	TO ID P13-12 (S701-40)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 174

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J1.1 SHOULD BE LESS THAN .5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-1 (UUT J1-1)	TO W1 P1B-13D
FROM ID J1B-13D	TO ID A1J13.8
FROM ID A1P13.8	TO ID P12-68 (S701-4)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28

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FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 175

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.4. THE OUTPUT MEASURED AT PIN J1.1 SHOULD BE GREATER THAN 13VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-4 (UUT J1-4)	TO W1 P1B-13A
FROM ID J1B-13A	TO ID A1J13.2
FROM ID A1P13.2	TO ID P12-47 (S201-6)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-1 (UUT J1-1)	TO W1 P1B-13D
FROM ID J1B-13D	TO ID A1J13.8
FROM ID A1P13.8	TO ID P12-68 (S701-4)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 176

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J1.1 SHOULD BE LESS THAN .5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-1 (UUT J1-1)	TO W1 P1B-13D
FROM ID J1B-13D	TO ID A1J13.8
FROM ID A1P13.8	TO ID P12-68 (S701-4)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 177

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.5. SHORT J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J1.1 SHOULD BE GREATER THAN 13VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

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FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-1 (UUT J1-1)	TO W1 P1B-13D
FROM ID J1B-13D	TO ID A1J13.8
FROM ID A1P13.8	TO ID P12-68 (S701-4)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 178

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.5 AND J1.38. MOMENTARILY LIFT J1.16 FROM GROUND, CONNECT TO J1.10 AND RECONNECT TO GROUND. THE OUTPUT MEASURED AT PIN J1.7 SHOULD BE GREATER THAN 13VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-42 (UUT J1-42)	TO W1 P1A-9E
FROM ID J1A-9E	TO ID A1J14.25
FROM ID A1P14.25	TO ID P13-31 (S202-44)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31

FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-7 (UUT J1-7)	TO W1 P1A-8F
FROM ID J1A-8F	TO ID A1J14.24
FROM ID A1P14.24	TO ID P13-62 (S202-42)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-207 (S509-7)	TO ID A1P9.36
FROM ID A1J9.36	TO ID BUS 5
FROM ID BUS 5	TO ID A1J8.47
FROM ID A1P8.47	TO ID P10-73 (S301-48)
FROM ID P10-7 (S301-47)	TO ID A1P7.23
FROM ID A1J7.23	TO ID A1J4.15
FROM ID A1P4.15	TO ID R111.1
FROM ID R111.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-16 (UUT J1-16)	TO W1 P1A-12D
FROM ID J1A-12D	TO ID A1J11.9
FROM ID A1P11.9	TO ID P11-43 (S301-154)
FROM ID P11-233 (S301-153)	TO ID A1P11.11
FROM ID A1J11.11	TO ID A1J2.15 (DC2-HI J3-30)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

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FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 179

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.5, J1.38 AND J1.17. THE OUTPUT MEASURED AT PIN J1.7 SHOULD BE LESS THAN .5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-17 (UUT J1-17)	TO W1 P1A-2B
FROM ID J1A-2B	TO ID A1J14.4
FROM ID A1P14.4	TO ID P13-48 (S201-12)

FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P2-42 (UUT J1-42)	TO W1 P1A-9E
FROM ID J1A-9E	TO ID A1J14.25
FROM ID A1P14.25	TO ID P13-31 (S202-44)

FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2

FROM W1 P2-7 (UUT J1-7)	TO W1 P1A-8F
FROM ID J1A-8F	TO ID A1J14.24
FROM ID A1P14.24	TO ID P13-62 (S202-42)

FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-207 (S509-7)	TO ID A1P9.36
FROM ID A1J9.36	TO ID BUS 5

FROM ID BUS 5	TO ID A1J8.47
FROM ID A1P8.47	TO ID P10-73 (S301-48)
FROM ID P10-7 (S301-47)	TO ID A1P7.23
FROM ID A1J7.23	TO ID A1J4.15
FROM ID A1P4.15	TO ID R111.1
FROM ID R111.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V

FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

2.6 MODULE 2: DISCRETE OUTPUTS CONTINUED

Refer to [1.4 Reference Drawings](#) when diagnosing connection paths.

STEP 201

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.38. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"
SEE "APPLY IC"

FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)

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FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 202

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.38. SHORT J1.39 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)

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FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 203

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.38. SHORT J1.39 AND J3.22 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)

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FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 204

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.38. SHORT J1.39, J3.22 AND J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

Date: 04 March 2016

FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 205

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.38. SHORT J3.22 AND J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)

FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23

Date: 04 March 2016

FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 206

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.38. SHORT J1.39, J3.22 AND J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)

Date: 04 March 2016

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 207

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.38. SHORT J1.39 AND J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

Date: 04 March 2016

FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 208

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.38. SHORT J1.39 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

Date: 04 March 2016

FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 209

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.38. SHORT J1.39 AND J3.22 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8

Date: 04 March 2016

FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 210

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.38. SHORT J1.39, J3.22 AND J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13

Date: 04 March 2016

FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 211

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

Date: 04 March 2016

SEE "UUT POWER"
SEE "APPLY IC"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)

FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 212

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 AND J3.22 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)

Date: 04 March 2016

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 213

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39, J3.22 AND J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8

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FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 214

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J3.22 AND J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24

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FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 215

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39, J3.22 AND J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE GREATER THAN 13.0VDC

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)

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FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 216

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 AND J3.22 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

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FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 217

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36

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FROM ID A1J7.36

TO GROUND

STEP 218

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 AND J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)
FROM ID J1A-1F
FROM ID A1P14.11

TO W1 P1A-1F
TO ID A1J14.11
TO ID P13-51 (S201-28)

FROM W1 P2-39 (UUT J1-39)
FROM ID J1A-1E
FROM ID A1P14.9

TO W1 P1A-1E
TO ID A1J14.9
TO ID P13-17 (S201-26)

FROM ID P12-80 (S201-2)
FROM ID A1J12.40
FROM ID A1P10.8
FROM ID P11-205 (S508-10)
FROM ID A1J9.2

TO ID A1P12.40
TO ID A1J10.8
TO ID P11-139 (S508-2)
TO ID A1P9.2
TO ID BUS 8

FROM W1 P3-19 (UUT J3-19)
FROM ID J1B-9F
FROM ID A1P13.24

TO W1 P1B-9F
TO ID A1J13.24
TO ID P12-43 (S701-44)

FROM ID P12-44 (S701-2)
FROM ID A1J12.48
FROM ID A1P10.1
FROM ID P11-164 (S506-3)
FROM ID A1J9.23

TO ID A1P12.48
TO ID A1J10.1
TO ID P11-162 (S506-2)
TO ID A1P9.23
TO ID BUS 1

FROM ID P20-2 (DMM-HI)
FROM ID A1J15.49
FROM ID A1P8.28
FROM ID P10-77 (S503-3)
FROM ID A1J6.13

TO ID A1P15.49
TO ID A1J8.28
TO ID P10-203 (S503-1)
TO ID A1P6.13
TO ID BUS 1

FROM ID P20-3 (DMM-LO)
FROM ID A1J15.50
FROM ID A1P7.38
FROM ID P10-229 (S301-24)
FROM ID A1J7.36

TO ID A1P15.50
TO ID A1J7.38
TO ID P10-130 (S301-23)
TO ID A1P7.36
TO GROUND

Date: 04 March 2016

STEP 219

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39, J1.40 AND J3.22 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 220

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)

FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 221

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67, J3.6 AND J3.16 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P3-16 (UUT J3-16)	TO W1 P1B-6C
FROM ID J1B-6C	TO ID A1J12.12
FROM ID A1P12.12	TO ID P12-53 (S201-39)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

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FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 222

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-16 (UUT J3-16)	TO W1 P1B-6C
FROM ID J1B-6C	TO ID A1J12.12
FROM ID A1P12.12	TO ID P12-53 (S201-39)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

Date: 04 March 2016

FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 223

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39 AND J1.40 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-16 (UUT J3-16)	TO W1 P1B-6C
FROM ID J1B-6C	TO ID A1J12.12
FROM ID A1P12.12	TO ID P12-53 (S201-39)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2

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FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 224

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.39, J1.40 AND J3.22 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2

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FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-16 (UUT J3-16)	TO W1 P1B-6C
FROM ID J1B-6C	TO ID A1J12.12
FROM ID A1P12.12	TO ID P12-53 (S201-39)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 225

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J1.40 AND J3.22 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE GREATER THAN 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)

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FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-16 (UUT J3-16)	TO W1 P1B-6C
FROM ID J1B-6C	TO ID A1J12.12
FROM ID A1P12.12	TO ID P12-53 (S201-39)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 226

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J3.22 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)

Date: 04 March 2016

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-16 (UUT J3-16)	TO W1 P1B-6C
FROM ID J1B-6C	TO ID A1J12.12
FROM ID A1P12.12	TO ID P12-53 (S201-39)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 227

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J3.22 AND J1.39 TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)

Date: 04 March 2016

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-16 (UUT J3-16)	TO W1 P1B-6C
FROM ID J1B-6C	TO ID A1J12.12
FROM ID A1P12.12	TO ID P12-53 (S201-39)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 228

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. SHORT J3.22 AND J1.39 TO GROUND. DISCONNECT J3.16 FROM 28.0VDC. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

2.7 MODULE 3: OUTPUT SIGNALS

Refer to [1.4 Reference Drawings](#) when diagnosing connection paths.

Date: 04 March 2016

STEP 301

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J1.32 SHOULD BE GREATER THAN 17.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-32 (UUT J1-32)	TO W1 P1B-6B
FROM ID J1B-6B	TO ID A1J12.11
FROM ID A1P12.11	TO ID P12-22 (S201-37)

FROM ID P12-16 (S201-1)	TO ID A1P12.42
FROM ID A1J12.42	TO ID A1J10.6
FROM ID A1P10.6	TO ID P11-203 (S508-1)
FROM ID P11-77 (S508-3)	TO ID A1P9.15
FROM ID A1J9.15	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 302

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J3.4 SHOULD BE GREATER THAN 17.6VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P3-4 (UUT J3-4)	TO W1 P1A-3B
FROM ID J1A-3B	TO ID A1J15.2
FROM ID A1P15.2	TO ID P13-6 (S701-8)

FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23

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FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 303

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J3.4 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P3-4 (UUT J3-4)	TO W1 P1A-3B
FROM ID J1A-3B	TO ID A1J15.2
FROM ID A1P15.2	TO ID P13-6 (S701-8)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 304

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.34. THE OUTPUT MEASURED AT PIN J3.4 SHOULD BE GREATER THAN 17.6VDC WITH RESPECT TO GROUND.

Date: 04 March 2016

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P3-4 (UUT J3-4)	TO W1 P1A-3B
FROM ID J1A-3B	TO ID A1J15.2
FROM ID A1P15.2	TO ID P13-6 (S701-8)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 305

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.29. THE OUTPUT MEASURED AT J1.43 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"
SEE "APPLY IC"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-43 (UUT J1-43)	TO W1 P1B-9D
FROM ID J1B-9D	TO ID A1J13.17

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FROM ID A1P13.17	TO ID P12-41 (S701-38)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 306

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.47 AND J1.29. THE OUTPUT MEASURES AT PIN J1.43 SHOULD BE BETWEEN 16.5 AND 17.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-43 (UUT J1-43)	TO W1 P1B-9D
FROM ID J1B-9D	TO ID A1J13.17
FROM ID A1P13.17	TO ID P12-41 (S701-38)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23

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FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 307

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.29. THE OUTPUT MEASURES AT PIN J1.43 SHOULD BE BETWEEN 16.5 AND 17.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-43 (UUT J1-43)	TO W1 P1B-9D
FROM ID J1B-9D	TO ID A1J13.17
FROM ID A1P13.17	TO ID P12-41 (S701-38)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

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FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 308

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.29. J1.56 IS SHORTED TO GROUND. INITIATE COUNTER/TIMER TO MEASURE THE TIME INTERVAL BETWEEN PINS J1.56 AND J1.43. WHEN J1.56 DECREASES PAST 5 VOLTS, THE OUTPUT AT PIN J1.43 SHOULD DROP TO LESS THAN 0.5VDC RELATIVE TO GROUND IN BETWEEN 190 AND 725 MSECS.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-115 (S510-7)	TO ID A1P9.38
FROM ID A1J9.38	TO ID BUS 5
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-161 (S501-7)	TO ID A1P7.45
FROM ID A1J7.45	TO ID BUS 5
FROM W1 P2-43 (UUT J1-43)	TO W1 P1B-9D
FROM ID J1B-9D	TO ID A1J13.17
FROM ID A1P13.17	TO ID P12-41 (S701-38)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-193 (S506-6)	TO ID A1P9.50
FROM ID A1J9.50	TO ID BUS 4
FROM ID P19-19 (CT-IN2)	TO ID A1P22.1
FROM ID A1J22.1	TO ID A1J6.10
FROM ID A1P6.10	TO ID P10-71 (S502-2)
FROM ID P10-38 (S502-6)	TO ID A1P7.43
FROM ID A1J7.43	TO ID BUS 4
FROM W1 P2-30 (UUT J1-30)	TO W1 P1B-4B
FROM ID J1B-4B	TO ID A1J12.17
FROM ID A1P12.17	TO ID P12-96 (S202-47)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)

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FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-43 (UUT J1-43)	TO W1 P1B-9D
FROM ID J1B-9D	TO ID A1J13.17
FROM ID A1P13.17	TO ID P12-41 (S701-38)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 309

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.29 AND J1.34. THE OUTPUT MEASURED AT J1.43 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)

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FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-43 (UUT J1-43)	TO W1 P1B-9D
FROM ID J1B-9D	TO ID A1J13.17
FROM ID A1P13.17	TO ID P12-41 (S701-38)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 310

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.29, J1.13, AND J1.34. THE OUTPUT MEASURES AT PIN J1.43 SHOULD BE BETWEEN 16.5 AND 17.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-13 (UUT J1-13)	TO W1 P1B-14B
FROM ID J1B-14B	TO ID A1J13.3
FROM ID A1P13.3	TO ID P12-46 (S201-7)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

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FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-43 (UUT J1-43)	TO W1 P1B-9D
FROM ID J1B-9D	TO ID A1J13.17
FROM ID A1P13.17	TO ID P12-41 (S701-38)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 311

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.29 AND J1.34. THE OUTPUT MEASURES AT PIN J1.43 SHOULD BE GREATER THAN 27.0VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-43 (UUT J1-43)	TO W1 P1B-9D
FROM ID J1B-9D	TO ID A1J13.17
FROM ID A1P13.17	TO ID P12-41 (S701-38)

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FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 312

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6, J1.29 AND J1.34. PIN J1.56 IS SHORTED TO GROUND. INITIATE COUNTER/TIMER TO MEASURE THE TIME INTERVAL BETWEEN PINS J1.56 AND J1.43. WHEN J1.56 DECREASES PAST 5 VOLTS, THE OUTPUT AT PIN J1.43 SHOULD DROP TO LESS THAN 0.5VDC RELATIVE TO GROUND IN BETWEEN 190 AND 725 MSECS.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-115 (S510-7)	TO ID A1P9.38
FROM ID A1J9.38	TO ID BUS 5
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-161 (S501-7)	TO ID A1P7.45
FROM ID A1J7.45	TO ID BUS 5
FROM W1 P2-43 (UUT J1-43)	TO W1 P1B-9D
FROM ID J1B-9D	TO ID A1J13.17
FROM ID A1P13.17	TO ID P12-41 (S701-38)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-193 (S506-6)	TO ID A1P9.50
FROM ID A1J9.50	TO ID BUS 4

FROM ID P19-19 (CT-IN2)	TO ID A1P22.1
FROM ID A1J22.1	TO ID A1J6.10
FROM ID A1P6.10	TO ID P10-71 (S502-2)
FROM ID P10-38 (S502-6)	TO ID A1P7.43
FROM ID A1J7.43	TO ID BUS 4
FROM W1 P2-30 (UUT J1-30)	TO W1 P1B-4B
FROM ID J1B-4B	TO ID A1J12.17
FROM ID A1P12.17	TO ID P12-96 (S202-47)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-43 (UUT J1-43)	TO W1 P1B-9D
FROM ID J1B-9D	TO ID A1J13.17
FROM ID A1P13.17	TO ID P12-41 (S701-38)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 313

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6, J1.29, J1.47, J1.34 AND J1.22. THE OUTPUT MEASURED AT J1.59 SHOULD BE GREATER THAN 27.0VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P2-59 (UUT J1-59)	TO W1 P1A-4C
FROM ID J1A-4C	TO ID A1J15.6
FROM ID A1P15.6	TO ID P13-44 (S701-42)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36

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FROM ID A1J7.36

TO GROUND

STEP 314

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6, J1.29, J1.34 AND J1.22. THE OUTPUT MEASURED AT J1.59 SHOULD BE GREATER THAN 27.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-29 (UUT J1-29)
FROM ID J1A-1C
FROM ID A1P14.5

TO W1 P1A-1C
TO ID A1J14.5
TO ID P13-49 (S201-17)

FROM ID P12-20 (S201-3)
FROM ID A1J12.46
FROM ID A1P10.2
FROM ID P11-72 (S507-4)
FROM ID A1J9.27

TO ID A1P12.46
TO ID A1J10.2
TO ID P11-39 (S507-1)
TO ID A1P9.27
TO ID BUS 2

FROM W1 P2-34 (UUT J1-34)
FROM ID J1A-5F
FROM ID A1P14.18

TO W1 P1A-5F
TO ID A1J14.18
TO ID P13-89 (S202-18)

FROM W1 P2-22 (UUT J1-22)
FROM ID J1A-1B
FROM ID A1P14.3

TO W1 P1A-1B
TO ID A1J14.3
TO ID P13-80 (S201-11)

FROM W1 P2-59 (UUT J1-59)
FROM ID J1A-4C
FROM ID A1P15.6

TO W1 P1A-4C
TO ID A1J15.6
TO ID P13-44 (S701-42)

FROM ID P12-44 (S701-2)
FROM ID A1J12.48
FROM ID A1P10.1
FROM ID P11-164 (S506-3)
FROM ID A1J9.23

TO ID A1P12.48
TO ID A1J10.1
TO ID P11-162 (S506-2)
TO ID A1P9.23
TO ID BUS 1

FROM ID P20-2 (DMM-HI)
FROM ID A1J15.49
FROM ID A1P8.28
FROM ID P10-77 (S503-3)
FROM ID A1J6.13

TO ID A1P15.49
TO ID A1J8.28
TO ID P10-203 (S503-1)
TO ID A1P6.13
TO ID BUS 1

FROM ID P20-3 (DMM-LO)
FROM ID A1J15.50
FROM ID A1P7.38
FROM ID P10-229 (S301-24)
FROM ID A1J7.36

TO ID A1P15.50
TO ID A1J7.38
TO ID P10-130 (S301-23)
TO ID A1P7.36
TO GROUND

Date: 04 March 2016

STEP 315

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6, J1.29, J1.34 AND J1.22. PIN J1.56 IS GROUNDED. INITIATE COUNTER/TIMER TO MEASURE THE TIME INTERVAL BETWEEN PINS J1.56 AND J1.59. WHEN J1.56 DECREASES PAST 5 VOLTS, THE OUTPUT AT PIN J1.59 SHOULD DROP TO LESS THAN 0.5VDC RELATIVE TO GROUND IN BETWEEN 190 AND 725 MSEC.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-115 (S510-7)	TO ID A1P9.38
FROM ID A1J9.38	TO ID BUS 5
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-161 (S501-7)	TO ID A1P7.45
FROM ID A1J7.45	TO ID BUS 5
FROM W1 P2-59 (UUT J1-59)	TO W1 P1A-4C
FROM ID J1A-4C	TO ID A1J15.6
FROM ID A1P15.6	TO ID P13-44 (S701-42)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-193 (S506-6)	TO ID A1P9.50
FROM ID A1J9.50	TO ID BUS 4
FROM ID P19-19 (CT-IN2)	TO ID A1P22.1
FROM ID A1J22.1	TO ID A1J6.10
FROM ID A1P6.10	TO ID P10-71 (S502-2)
FROM ID P10-38 (S502-6)	TO ID A1P7.43
FROM ID A1J7.43	TO ID BUS 4
FROM W1 P2-30 (UUT J1-30)	TO W1 P1B-4B
FROM ID J1B-4B	TO ID A1J12.17
FROM ID A1P12.17	TO ID P12-96 (S202-47)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5

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FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)

STEP 316

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.29 AND J1.13. THE OUTPUT MEASURED AT J1.59 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-13 (UUT J1-13)	TO W1 P1B-14B
FROM ID J1B-14B	TO ID A1J13.3
FROM ID A1P13.3	TO ID P12-46 (S201-7)
FROM W1 P2-59 (UUT J1-59)	TO W1 P1A-4C
FROM ID J1A-4C	TO ID A1J15.6
FROM ID A1P15.6	TO ID P13-44 (S701-42)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49

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FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 317

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.29, J1.13 AND J2.22. THE OUTPUT MEASURED AT J1.59 SHOULD BE GREATER THAN 17.6VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-13 (UUT J1-13)	TO W1 P1B-14B
FROM ID J1B-14B	TO ID A1J13.3
FROM ID A1P13.3	TO ID P12-46 (S201-7)
FROM W1 P2-59 (UUT J1-59)	TO W1 P1A-4C
FROM ID J1A-4C	TO ID A1J15.6
FROM ID A1P15.6	TO ID P13-44 (S701-42)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)

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FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 318

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT J1.20 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-20 (UUT J1-20)	TO W1 P1B-4A
FROM ID J1B-4A	TO ID A1J12.16
FROM ID A1P12.16	TO ID P12-63 (S202-46)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-144 (S509-8)	TO ID A1P9.26
FROM ID A1J9.26	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)

Date: 04 March 2016

FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 319

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.21. THE OUTPUT MEASURED AT J1.20 SHOULD BE GREATER THAN 27.0VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-21 (UUT J1-21)	TO W1 P1B-5C
FROM ID J1B-5C	TO ID A1J12.15
FROM ID A1P12.15	TO ID P12-31 (S202-45)

FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2

FROM W1 P2-20 (UUT J1-20)	TO W1 P1B-4A
FROM ID J1B-4A	TO ID A1J12.16
FROM ID A1P12.16	TO ID P12-63 (S202-46)

FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-144 (S509-8)	TO ID A1P9.26
FROM ID A1J9.26	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND

FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

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FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 320

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT J1.54 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"
SEE "APPLY IC"

FROM W1 P2-54 (UUT J1-54)	TO W1 P1B-4C
FROM ID J1B-4C	TO ID A1J12.18
FROM ID A1P12.18	TO ID P12-32 (S202-48)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-144 (S509-8)	TO ID A1P9.26
FROM ID A1J9.26	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 321

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.55. THE OUTPUT MEASURED AT J1.54 SHOULD BE GREATER THAN 27.0VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-55 (UUT J1-55)	TO W1 P1A-2E
FROM ID J1A-2E	TO ID A1J14.10
FROM ID A1P14.10	TO ID P13-83 (S201-27)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-54 (UUT J1-54)	TO W1 P1B-4C
FROM ID J1B-4C	TO ID A1J12.18
FROM ID A1P12.18	TO ID P12-32 (S202-48)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-144 (S509-8)	TO ID A1P9.26
FROM ID A1J9.26	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

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FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 322

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT J3.1 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P3-1 (UUT J3-1)	TO W1 P1B-11E
FROM ID J1B-11E	TO ID A1J13.22
FROM ID A1P13.22	TO ID P12-75 (S701-43)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 323

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.30. THE OUTPUT MEASURED AT J3.1 SHOULD BE BETWEEN 27.0VDC AND 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-30 (UUT J1-30)	TO W1 P1B-4B
FROM ID J1B-4B	TO ID A1J12.17
FROM ID A1P12.17	TO ID P12-96 (S202-47)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P3-1 (UUT J3-1)	TO W1 P1B-11E
FROM ID J1B-11E	TO ID A1J13.22
FROM ID A1P13.22	TO ID P12-75 (S701-43)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

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FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 324

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT J1.24 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-24 (UUT J1-24)	TO W1 P1B-11F
FROM ID J1B-11F	TO ID A1J13.18
FROM ID A1P13.18	TO ID P12-40 (S701-29)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 325

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.30. THE OUTPUT MEASURED AT J1.24 SHOULD BE BETWEEN 27.0VDC AND 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-30 (UUT J1-30)	TO W1 P1B-4B
FROM ID J1B-4B	TO ID A1J12.17
FROM ID A1P12.17	TO ID P12-96 (S202-47)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-24 (UUT J1-24)	TO W1 P1B-11F
FROM ID J1B-11F	TO ID A1J13.18
FROM ID A1P13.18	TO ID P12-40 (S701-29)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

Date: 04 March 2016

FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 326

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT J1.63 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-63 (UUT J1-63)	TO W1 P1A-6B
FROM ID J1A-6B	TO ID A1J15.11
FROM ID A1P15.11	TO ID P13-10 (S701-33)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 327

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.30. THE OUTPUT MEASURED AT J1.63 SHOULD BE BETWEEN 27.0VDC AND 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-30 (UUT J1-30)	TO W1 P1B-4B
FROM ID J1B-4B	TO ID A1J12.17
FROM ID A1P12.17	TO ID P12-96 (S202-47)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-63 (UUT J1-63)	TO W1 P1A-6B
FROM ID J1A-6B	TO ID A1J15.11
FROM ID A1P15.11	TO ID P13-10 (S701-33)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

Date: 04 March 2016

FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 328

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT J1.58 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-58 (UUT J1-58)	TO W1 P1B-10D
FROM ID J1B-10D	TO ID A1J13.19
FROM ID A1P13.19	TO ID P12-42 (S701-35)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 329

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.30. THE OUTPUT MEASURED AT J1.58 SHOULD BE BETWEEN 27.0VDC AND 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-30 (UUT J1-30)	TO W1 P1B-4B
FROM ID J1B-4B	TO ID A1J12.17
FROM ID A1P12.17	TO ID P12-96 (S202-47)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-58 (UUT J1-58)	TO W1 P1B-10D
FROM ID J1B-10D	TO ID A1J13.19
FROM ID A1P13.19	TO ID P12-42 (S701-35)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

Date: 04 March 2016

FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 330

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT J1.25 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-25 (UUT J1-25)	TO W1 P1A-5C
FROM ID J1A-5C	TO ID A1J15.9
FROM ID A1P15.9	TO ID P13-75 (S701-31)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 331

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.30. THE OUTPUT MEASURED AT J1.25 SHOULD BE BETWEEN 27.0VDC AND 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-30 (UUT J1-30)	TO W1 P1B-4B
FROM ID J1B-4B	TO ID A1J12.17
FROM ID A1P12.17	TO ID P12-96 (S202-47)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-25 (UUT J1-25)	TO W1 P1A-5C
FROM ID J1A-5C	TO ID A1J15.9
FROM ID A1P15.9	TO ID P13-75 (S701-31)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

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FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 332

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT J1.65 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-65 (UUT J1-65)	TO W1 P1A-7A
FROM ID J1A-7A	TO ID A1J15.13
FROM ID A1P15.13	TO ID P13-45 (S701-39)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 333

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.30. THE OUTPUT MEASURED AT J1.65 SHOULD BE BETWEEN 27.0VDC AND 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-30 (UUT J1-30)	TO W1 P1B-4B
FROM ID J1B-4B	TO ID A1J12.17
FROM ID A1P12.17	TO ID P12-96 (S202-47)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-65 (UUT J1-65)	TO W1 P1A-7A
FROM ID J1A-7A	TO ID A1J15.13
FROM ID A1P15.13	TO ID P13-45 (S701-39)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

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FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 334

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT J1.66 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-66 (UUT J1-66)	TO W1 P1A-7C
FROM ID J1A-7C	TO ID A1J15.15
FROM ID A1P15.15	TO ID P13-76 (S701-41)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 335

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.30. THE OUTPUT MEASURED AT J1.66 SHOULD BE BETWEEN 27.0VDC AND 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-30 (UUT J1-30)	TO W1 P1B-4B
FROM ID J1B-4B	TO ID A1J12.17
FROM ID A1P12.17	TO ID P12-96 (S202-47)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-66 (UUT J1-66)	TO W1 P1A-7C
FROM ID J1A-7C	TO ID A1J15.15
FROM ID A1P15.15	TO ID P13-76 (S701-41)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

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FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 336

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT J1.66 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-66 (UUT J1-66)	TO W1 P1A-7C
FROM ID J1A-7C	TO ID A1J15.15
FROM ID A1P15.15	TO ID P13-76 (S701-41)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 337

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.34. THE OUTPUT MEASURED AT J1.66 SHOULD BE BETWEEN 27.0VDC AND 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-66 (UUT J1-66)	TO W1 P1A-7C
FROM ID J1A-7C	TO ID A1J15.15
FROM ID A1P15.15	TO ID P13-76 (S701-41)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 338

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT J1.25 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-25 (UUT J1-25)	TO W1 P1A-5C
FROM ID J1A-5C	TO ID A1J15.9
FROM ID A1P15.9	TO ID P13-75 (S701-31)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 339

DESCRIPTION:

Date: 04 March 2016

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.35. THE OUTPUT MEASURED AT J1.25 SHOULD BE BETWEEN 27.0VDC AND 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-35 (UUT J1-35)	TO W1 P1B-9C
FROM ID J1B-9C	TO ID A1J12.3
FROM ID A1P12.3	TO ID P12-51 (S201-21)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-25 (UUT J1-25)	TO W1 P1A-5C
FROM ID J1A-5C	TO ID A1J15.9
FROM ID A1P15.9	TO ID P13-75 (S701-31)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

2.8 MODULE 4: OUTPUT SIGNALS CONTINUED

Refer to [1.4 Reference Drawings](#) when diagnosing connection paths.

STEP 401**DESCRIPTION:**

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J1.32 SHOULD BE GREATER THAN 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-32 (UUT J1-32)	TO W1 P1B-6B
FROM ID J1B-6B	TO ID A1J12.11
FROM ID A1P12.11	TO ID P12-22 (S201-37)

FROM ID P12-16 (S201-1)	TO ID A1P12.42
FROM ID A1J12.42	TO ID A1J10.6
FROM ID A1P10.6	TO ID P11-203 (S508-1)
FROM ID P11-77 (S508-3)	TO ID A1P9.15
FROM ID A1J9.15	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 402**DESCRIPTION:**

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J1.32 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-32 (UUT J1-32)	TO W1 P1B-6B
FROM ID J1B-6B	TO ID A1J12.11
FROM ID A1P12.11	TO ID P12-22 (S201-37)

FROM ID P12-16 (S201-1)	TO ID A1P12.42
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FROM ID A1J12.42	TO ID A1J10.6
FROM ID A1P10.6	TO ID P11-203 (S508-1)
FROM ID P11-77 (S508-3)	TO ID A1P9.15
FROM ID A1J9.15	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 403

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J3.11 SHOULD BE BETWEEN 27 AND 28VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P3-11 (UUT J3-11)	TO W1 P1A-7E
FROM ID J1A-7E	TO ID A1J14.21
FROM ID A1P14.21	TO ID P13-90 (S202-26)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 404

DESCRIPTION:

Date: 04 March 2016

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J3.11 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P3-11 (UUT J3-11)	TO W1 P1A-7E
FROM ID J1A-7E	TO ID A1J14.21
FROM ID A1P14.21	TO ID P13-90 (S202-26)

FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 405

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.68 WITH RESPECT TO J1.11. THE OUTPUT MEASURED AT PIN J3.11 SHOULD BE BETWEEN 27.0 AND 28.0VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-68 (UUT J1-68)	TO W1 P1B-5A
FROM ID J1B-5A	TO ID A1J12.13
FROM ID A1P12.13	TO ID P12-92 (S202-22)

FROM W1 P3-11 (UUT J3-11)	TO W1 P1A-7E
FROM ID J1A-7E	TO ID A1J14.21
FROM ID A1P14.21	TO ID P13-90 (S202-26)

FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1

Date: 04 March 2016

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 406

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. THE VOLTAGE MEASURED AT PIN J3.7 WITH RESPECT TO GROUND SHOULD BE BETWEEN 27.0 AND 28.0VDC RELATIVE TO GROUND

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P3-7 (UUT J3-7)	TO W1 P1A-9F
FROM ID J1A-9F	TO ID A1J14.26
FROM ID A1P14.26	TO ID P13-64 (S202-50)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 407

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. THE VOLTAGE MEASURED AT PIN J3.7 WITH RESPECT TO GROUND SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

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CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P3-7 (UUT J3-7)	TO W1 P1A-9F
FROM ID J1A-9F	TO ID A1J14.26
FROM ID A1P14.26	TO ID P13-64 (S202-50)

FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 408

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO J1.34. THE VOLTAGE MEASURED AT PIN J3.7 WITH RESPECT TO GROUND SHOULD BE BETWEEN 27.0 AND 28.0VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)

FROM W1 P3-7 (UUT J3-7)	TO W1 P1A-9F
FROM ID J1A-9F	TO ID A1J14.26
FROM ID A1P14.26	TO ID P13-64 (S202-50)

FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13

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FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 409

DESCRIPTION:

15.0VDC TO PIN J3.30, AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J3.10. 15.0VDC IS APPLIED TO J1.10 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. THE VOLTAGE MEASURED AT PIN J3.7 WITH RESPECT TO GROUND SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P3-7 (UUT J3-7)	TO W1 P1A-9F
FROM ID J1A-9F	TO ID A1J14.26
FROM ID A1P14.26	TO ID P13-64 (S202-50)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 410

DESCRIPTION:

15.0VDC TO PIN J3.30, AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J3.10. 15.0VDC IS APPLIED TO J1.10 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J3.8 WITH RESPECT TO J1.11. THE VOLTAGE MEASURED AT PIN J3.7 WITH RESPECT TO GROUND SHOULD BE BETWEEN 27.0 AND 28.0VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

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FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P3-7 (UUT J3-7)	TO W1 P1A-9F
FROM ID J1A-9F	TO ID A1J14.26
FROM ID A1P14.26	TO ID P13-64 (S202-50)

FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 411

DESCRIPTION:

15.0VDC TO PIN J3.30, AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J3.10. 15.0VDC IS APPLIED TO J1.10 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. THE VOLTAGE MEASURED AT PIN J3.7 WITH RESPECT TO GROUND SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P3-7 (UUT J3-7)	TO W1 P1A-9F
FROM ID J1A-9F	TO ID A1J14.26
FROM ID A1P14.26	TO ID P13-64 (S202-50)

FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

2.9 MODULE 5: RATE COMMAND AND LAMP ILLUMINATION

Refer to [1.4 Reference Drawings](#) when diagnosing connection paths.

STEP 501

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. -1.2 +/-0.02VDC IS APPLIED TO J3.24. PINS J1.39 AND J1.40 ARE SHORTED TO GROUND. MEASURE THE VOLTAGE AT J3.24 AND ADJUST IF NEEDED.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E
FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8

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FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 502

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. -1.2 +/-0.02VDC IS APPLIED TO J3.24. PINS J1.39 AND J1.40 ARE SHORTED TO GROUND. THE VOLTAGE MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E
FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1

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FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 503

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. -1.2 +/-0.02VDC IS APPLIED TO J3.24. PINS J1.39 AND J1.40 ARE SHORTED TO GROUND. DECREASE THE VOLTAGE AT J3.24 UNTIL VOLTAGE AT J3.19 CHANGES TO A MINIMUM OF 13.0VDC.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E
FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16

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FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 504

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. -1.2 +/-0.02VDC IS APPLIED TO J3.24. PINS J1.39 AND J1.40 ARE SHORTED TO GROUND. DECREASE THE VOLTAGE AT J3.24 UNTIL VOLTAGE AT J3.19 CHANGES TO A MINIMUM OF 13.0VDC. THE VOLTAGE MEASURED AT J3.24 SHALL BE BETWEEN -1.685VDC AND -1.485VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E
FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1

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FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 505

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. -1.2 +/-0.02VDC IS APPLIED TO J3.24. PINS J1.39 AND J1.40 ARE SHORTED TO GROUND. INCREASE THE VOLTAGE AT J3.24 UNTIL VOLTAGE AT J3.19 CHANGES TO A MINIMUM OF 13.0VDC.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E
FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND

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FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 506

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. -1.2 +/-0.02VDC IS APPLIED TO J3.24. PINS J1.39 AND J1.40 ARE SHORTED TO GROUND. INCREASE THE VOLTAGE AT J3.24 UNTIL VOLTAGE AT J3.19 CHANGES TO A MINIMUM OF 13.0VDC. THE VOLTAGE MEASURED AT J3.24 SHALL BE BETWEEN -1.562VDC AND -1.362VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11

FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E
FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13

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FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 507

DESCRIPTION:

THE DIFFERENCE BETWEEN THE VOLTAGE MEASURED IN STEP 506 (J3_24_TWO) AND STEP 504 (J3_24_ONE) SHALL BE BETWEEN 0.083VDC AND 0.163VDC.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)

STEP 508

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. 0.0VDC IS APPLIED TO J3.24. PINS J1.39, J3.22 AND J3.25 ARE SHORTED TO GROUND. THE OUTPUT MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

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FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E
FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

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FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 509

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. +1.2 +/-0.02VDC IS APPLIED TO J3.24. PINS J1.39, J3.22 AND J3.25 ARE SHORTED TO GROUND. MEASURE THE VOLTAGE AT J3.24 AND ADJUST IF NEEDED.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E
FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22

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FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 510

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. +1.2 +/-0.02VDC IS APPLIED TO J3.24. PINS J1.39, J3.22 AND J3.25 ARE SHORTED TO GROUND. THE VOLTAGE MEASURED AT PIN J3.19 SHOULD BE LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E

FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 511

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. +1.2 +/-0.02VDC IS APPLIED TO J3.24. PINS J1.39, J3.22 AND J3.25 ARE SHORTED TO GROUND. INCREASE THE VOLTAGE AT J3.24 UNTIL VOLTAGE AT J3.19 CHANGES TO A MINIMUM OF 13.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E
FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36

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FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 512

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. +1.2 +/-0.02VDC IS APPLIED TO J3.24. PINS J1.39 J1.40 AND J3.25 ARE SHORTED TO GROUND. INCREASE THE VOLTAGE AT J3.24 UNTIL VOLTAGE AT J3.19 CHANGES TO A MINIMUM OF 13.0VDC. THE VOLTAGE MEASURED AT J3.24 SHALL BE BETWEEN 1.485VDC AND 1.685VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)

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FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E
FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 513

DESCRIPTION:

Date: 04 March 2016

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67 AND J3.6 WITH RESPECT TO J1.11. DECREASE THE VOLTAGE AT J3.24 UNTIL VOLTAGE AT J3.19 CHANGES TO LESS THAN 0.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E
FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7

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FROM W1 P3-19 (UUT J3-19)	TO W1 P1B-9F
FROM ID J1B-9F	TO ID A1J13.24
FROM ID A1P13.24	TO ID P12-43 (S701-44)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 514

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.67, AND J3.6 WITH RESPECT TO J1.11. DECREASE THE VOLTAGE AT J3.24 UNTIL VOLTAGE AT J3.19 CHANGES TO A MINIMUM OF 13.0VDC. THE VOLTAGE MEASURED AT J3.24 SHALL BE BETWEEN 1.362VDC AND 1.562VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E
FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38

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FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 515

DESCRIPTION:

THE DIFFERENCE BETWEEN THE VOLTAGE MEASURED IN STEP 514(J3_24_TWO) AND STEP 512 (J3_24_ONE) SHALL BE BETWEEN 0.083VDC AND 0.163VDC.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P3-25 (UUT J3-25)	TO W1 P1A-8E
FROM ID J1A-8E	TO ID A1J14.23
FROM ID A1P14.23	TO ID P13-91 (S202-35)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM W1 P3-24 (UUT J3-24)	TO W1 P1A-7F
FROM ID J1A-7F	TO ID A1J14.22
FROM ID A1P14.22	TO ID P13-92 (S202-34)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28

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FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 516

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34 AND J3.16. THE FREQUENCY AT PIN J3.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P3-16 (UUT J3-16)	TO W1 P1B-6C
FROM ID J1B-6C	TO ID A1J12.12
FROM ID A1P12.12	TO ID P12-53 (S201-39)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V

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FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 517

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.34. SHORT J2.39 TO GROUND. THE FREQUENCY AT PIN J3.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V

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FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 518

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.34. SHORT J2.39 AND J3.22 TO GROUND. THE VOLTAGE MEASURED AT PIN J3.3 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)

FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)

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FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 519

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.34. SHORT J1.39, J3.22, AND J1.40 TO GROUND. THE VOLTAGE MEASURED AT PIN J3.3 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8

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FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 520

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J1.34. SHORT J1.39 TO GROUND. THE FREQUENCY AT PIN J3.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)

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FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 521

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34 AND J1.5. SHORT J1.39 TO GROUND. THE FREQUENCY AT PIN J3.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1

FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

Date: 04 March 2016

STEP 522

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.5 AND J1.38. SHORT J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J3.3 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16

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FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 523

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND - 15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34 AND J1.38. SHORT J3.22 AND J1.40 TO GROUND. THE FREQUENCY AT PIN J3.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18

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FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 524

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34 AND J1.38. SHORT J3.22, J1.40, AND J1.46 TO GROUND. THE FREQUENCY AT PIN J3.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)

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FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8

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FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 525

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34 AND J1.38. SHORT J3.22, J1.40, AND J1.46 TO GROUND. THE FREQUENCY AT PIN J1.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)

FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P2-3 (UUT J1-3)	TO W1 P1B-14E
FROM ID J1B-14E	TO ID A1J13.9
FROM ID A1P13.9	TO ID P12-36 (S701-5)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3

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FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 526

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34 AND J1. 38. SHORT J3.22, J1.40 AND J1.46 TO GROUND. MOMENTARILY LIFT J1.16 FROM GROUND, CONNECT TO 28.0VDC AND RECONNECT TO GROUND. THE FREQUENCY AT PIN J3.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-16 (UUT J1-16)	TO W1 P1A-12D
FROM ID J1A-12D	TO ID A1J11.9
FROM ID A1P11.9	TO ID P11-43 (S301-154)
FROM ID P11-233 (S301-153)	TO ID A1P11.11
FROM ID A1J11.11	TO ID A1J2.15 (DC2-HI J3-30)
FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)

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FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 527

DESCRIPTION:

Date: 04 March 2016

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34 AND J1.38. SHORT J3.22, J1.40, AND J1.46 TO GROUND. MOMENTARILY LIFT J1.16 FROM GROUND, CONNECT TO 28.0VDC AND RECONNECT TO GROUND. THE VOLTAGE MEASURED AT PIN J1.3 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)

FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P2-3 (UUT J1-3)	TO W1 P1B-14E
FROM ID J1B-14E	TO ID A1J13.9
FROM ID A1P13.9	TO ID P12-36 (S701-5)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.48
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FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 528

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38 AND J1.9 (FOR 10 MSEC). SHORT J3.22, J1.40, AND J1.12 TO GROUND. THE FREQUENCY AT PIN J3.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

Date: 04 March 2016

STEP 529

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38 AND J1.9 (FOR 10 MSEC). SHORT J3.22, J1.40, AND J1.12 TO GROUND. THE FREQUENCY AT PIN J1.14 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-14 (UUT J1-14)	TO W1 P1A-3A
FROM ID J1A-3A	TO ID A1J15.1
FROM ID A1P15.1	TO ID P13-39 (S701-7)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

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FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 530

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34 AND J1.38. SHORT J3.22, J1.40, AND J1.12 TO GROUND. MOMENTARILY LIFT J1.16 FROM GROUND, CONNECT TO 28.0VDC AND RECONNECT TO GROUND. THE FREQUENCY AT PIN J3.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-16 (UUT J1-16)	TO W1 P1A-12D
FROM ID J1A-12D	TO ID A1J11.9
FROM ID A1P11.9	TO ID P11-43 (S301-154)
FROM ID P11-233 (S301-153)	TO ID A1P11.11
FROM ID A1J11.11	TO ID A1J2.15 (DC2-HI J3-30)
FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)

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FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 531

DESCRIPTION:

Date: 04 March 2016

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34 AND J1.38. SHORT J3.22, J1.40, AND J1.12 TO GROUND. MOMENTARILY LIFT J1.16 FROM GROUND, CONNECT TO 28.0VDC AND RECONNECT TO GROUND. THE VOLTAGE MEASURED AT PIN J1.14 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-14 (UUT J1-14)	TO W1 P1A-3A
FROM ID J1A-3A	TO ID A1J15.1
FROM ID A1P15.1	TO ID P13-39 (S701-7)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48

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FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 532

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38 AND J1.8 (FOR 10 MSEC). SHORT J3.22, J1.40, AND J1.12 TO GROUND. THE FREQUENCY AT PIN J3.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-50 (UUT J1-50)	TO W1 P1B-6A
FROM ID J1B-6A	TO ID A1J12.10
FROM ID A1P12.10	TO ID P12-83 (S201-32)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

Date: 04 March 2016

STEP 533

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38 AND J1.8 (FOR 10 MSEC). SHORT J3.22, J1.40, AND J1.12 TO GROUND. THE FREQUENCY AT PIN J1.49 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-50 (UUT J1-50)	TO W1 P1B-6A
FROM ID J1B-6A	TO ID A1J12.10
FROM ID A1P12.10	TO ID P12-83 (S201-32)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-49 (UUT J1-49)	TO W1 P1B-4B
FROM ID J1A-4B	TO ID A1J15.5
FROM ID A1P15.5	TO ID P13-72 (S701-15)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

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FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 534

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34 AND J1.38. SHORT J3.22, J1.40, AND J1.50 TO GROUND. MOMENTARILY LIFT J1.16 FROM GROUND, CONNECT TO 28.0VDC AND RECONNECT TO GROUND. THE FREQUENCY AT PIN J3.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-16 (UUT J1-16)	TO W1 P1A-12D
FROM ID J1A-12D	TO ID A1J11.9
FROM ID A1P11.9	TO ID P11-43 (S301-154)
FROM ID P11-233 (S301-153)	TO ID A1P11.11
FROM ID A1J11.11	TO ID A1J2.15 (DC2-HI J3-30)
FROM W1 P2-50 (UUT J1-50)	TO W1 P1B-6A
FROM ID J1B-6A	TO ID A1J12.10
FROM ID A1P12.10	TO ID P12-83 (S201-32)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)

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FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 535

DESCRIPTION:

Date: 04 March 2016

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34 AND J1.38. SHORT J3.22, J1.40, AND J1.50 TO GROUND. MOMENTARILY LIFT J1.16 FROM GROUND, CONNECT TO 28.0VDC AND RECONNECT TO GROUND. THE VOLTAGE MEASURED AT PIN J1.49 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-50 (UUT J1-50)	TO W1 P1B-6A
FROM ID J1B-6A	TO ID A1J12.10
FROM ID A1P12.10	TO ID P12-83 (S201-32)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-49 (UUT J1-49)	TO W1 P1B-4B
FROM ID J1A-4B	TO ID A1J15.5
FROM ID A1P15.5	TO ID P13-72 (S701-15)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48

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FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 536

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38 J2.9 (FOR 10 MSEC), AND J2.8. SHORT J3.22, J1.40, AND J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J3.3 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17

FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V

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FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 537

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38 J2.9 (FOR 10 MSEC), AND J2.8. SHORT J3.22, J1.40, AND J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.37 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B

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FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-37 (UUT J1-37)	TO W1 P1B-14F
FROM ID J1B-14F	TO ID A1J13.11
FROM ID A1P13.11	TO ID P12-38 (S701-11)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 538

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38 AND J2.9. SHORT J3.22, J1.40, AND J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J3.3 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

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FROM W1 P2-37 (UUT J1-37)	TO W1 P1B-14F
FROM ID J1B-14F	TO ID A1J13.11
FROM ID A1P13.11	TO ID P12-38 (S701-11)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 539

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38 AND J2.9. SHORT J3.22, J1.40, AND J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.2 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2

FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-2 (UUT J1-2)	TO W1 P1B-14D
FROM ID J1B-14D	TO ID A1J13.7
FROM ID A1P13.7	TO ID P12-4 (S701-3)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V

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FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 540

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38, J1.9 AND J1.8. SHORT J3.22, J1.40, J1.39, J1.46, J1.12, AND J1.50 TO GROUND. THE VOLTAGE MEASURED AT PIN J3.3 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)
FROM W1 P2-50 (UUT J1-50)	TO W1 P1B-6A
FROM ID J1B-6A	TO ID A1J12.10
FROM ID A1P12.10	TO ID P12-83 (S201-32)
FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)
FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A

FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24

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FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 541

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38, J1.9 AND J1.8. SHORT J3.22, J1.40, J1.39, J1.46, J1.12, AND J1.50 TO GROUND. THE FREQUENCY AT PIN J1.14 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)
FROM W1 P2-50 (UUT J1-50)	TO W1 P1B-6A
FROM ID J1B-6A	TO ID A1J12.10
FROM ID A1P12.10	TO ID P12-83 (S201-32)
FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A

FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)
FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-14 (UUT J1-14)	TO W1 P1A-3A
FROM ID J1A-3A	TO ID A1J15.1
FROM ID A1P15.1	TO ID P13-39 (S701-7)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

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FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 542

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38, J1.9 AND J1.8. SHORT J3.22, J1.40, J1.39, J1.46, J1.12, AND J1.50 TO GROUND. THE FREQUENCY AT PIN J1.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)
FROM W1 P2-50 (UUT J1-50)	TO W1 P1B-6A
FROM ID J1B-6A	TO ID A1J12.10
FROM ID A1P12.10	TO ID P12-83 (S201-32)
FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)

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FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-3 (UUT J1-3)	TO W1 P1B-14E
FROM ID J1B-14E	TO ID A1J13.9
FROM ID A1P13.9	TO ID P12-36 (S701-5)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)

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FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 543

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38, J1.9 AND J1.8. SHORT J3.22, J1.40, J1.39, J1.46, J1.12, AND J1.50 TO GROUND. THE FREQUENCY AT PIN J1.49 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)
FROM W1 P2-50 (UUT J1-50)	TO W1 P1B-6A
FROM ID J1B-6A	TO ID A1J12.10
FROM ID A1P12.10	TO ID P12-83 (S201-32)
FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)
FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1

FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-49 (UUT J1-49)	TO W1 P1B-4B
FROM ID J1A-4B	TO ID A1J15.5
FROM ID A1P15.5	TO ID P13-72 (S701-15)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16

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FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 544

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38, J1.9 AND J1.8. SHORT J3.22, J1.40, J1.39, J1.46, J1.12 AND J1.50 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.37 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)
FROM W1 P2-50 (UUT J1-50)	TO W1 P1B-6A
FROM ID J1B-6A	TO ID A1J12.10
FROM ID A1P12.10	TO ID P12-83 (S201-32)
FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)
FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-37 (UUT J1-37)	TO W1 P1B-14F
FROM ID J1B-14F	TO ID A1J13.11
FROM ID A1P13.11	TO ID P12-38 (S701-11)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9

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FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 545

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34, J1.38, J1.9 AND J1.8. SHORT J3.22, J1.40, J1.39, J1.46, J1.12 AND J1.50 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.2 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-8 (UUT J1-8)	TO W1 P1A-5E
FROM ID J1A-5E	TO ID A1J14.17
FROM ID A1P14.17	TO ID P13-87 (S202-10)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM W1 P2-46 (UUT J1-46)	TO W1 P1B-7B
FROM ID J1B-7B	TO ID A1J12.8
FROM ID A1P12.8	TO ID P12-50 (S201-30)
FROM W1 P2-50 (UUT J1-50)	TO W1 P1B-6A
FROM ID J1B-6A	TO ID A1J12.10
FROM ID A1P12.10	TO ID P12-83 (S201-32)
FROM W1 P2-12 (UUT J1-12)	TO W1 P1B-8A
FROM ID J1B-8A	TO ID A1J12.4
FROM ID A1P12.4	TO ID P12-18 (S201-22)

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FROM W1 P2-9 (UUT J1-9)	TO W1 P1A-1A
FROM ID J1A-1A	TO ID A1J14.1
FROM ID A1P14.1	TO ID P13-47 (S201-9)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-2 (UUT J1-2)	TO W1 P1B-14D
FROM ID J1B-14D	TO ID A1J13.7
FROM ID A1P13.7	TO ID P12-4 (S701-3)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)

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FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 546

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.31, J3.2 AND J3.8. THE VOLTAGE MEASURED AT PIN J1.65 SHOULD BE BETWEEN 27.0 AND 27.6VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-31 (UUT J1-31)	TO W1 P1A-2C
FROM ID J1A-2C	TO ID A1J14.6
FROM ID A1P14.6	TO ID P13-15 (S201-19)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)

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FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-65 (UUT J1-65)	TO W1 P1A-7A
FROM ID J1A-7A	TO ID A1J15.13
FROM ID A1P15.13	TO ID P13-45 (S701-39)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 547

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.31, J3.2 AND J3.8. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD BE BETWEEN 27.0 AND 27.6VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20

FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-31 (UUT J1-31)	TO W1 P1A-2C
FROM ID J1A-2C	TO ID A1J14.6
FROM ID A1P14.6	TO ID P13-15 (S201-19)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-25 (UUT J1-25)	TO W1 P1A-5C
FROM ID J1A-5C	TO ID A1J15.9
FROM ID A1P15.9	TO ID P13-75 (S701-31)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36

Date: 04 March 2016

FROM ID A1J7.36

TO GROUND

STEP 548

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2 AND J3.8. THE VOLTAGE MEASURED AT PIN J1.65 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)

FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)

FROM W1 P2-65 (UUT J1-65)	TO W1 P1A-7A
FROM ID J1A-7A	TO ID A1J15.13
FROM ID A1P15.13	TO ID P13-45 (S701-39)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23

Date: 04 March 2016

FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 549

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2 AND J3.8. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD BE LESS THAN .05VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-25 (UUT J1-25)	TO W1 P1A-5C
FROM ID J1A-5C	TO ID A1J15.9
FROM ID A1P15.9	TO ID P13-75 (S701-31)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

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FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 550

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29, J1.22 AND J1.47. THE VOLTAGE MEASURED AT PIN J1.65 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)

FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-65 (UUT J1-65)	TO W1 P1A-7A
FROM ID J1A-7A	TO ID A1J15.13
FROM ID A1P15.13	TO ID P13-45 (S701-39)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 551

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29, J1.22 AND J1.47. THE VOLTAGE MEASURED AT PIN J3.1 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P3-1 (UUT J3-1)	TO W1 P1B-11E
FROM ID J1B-11E	TO ID A1J13.22
FROM ID A1P13.22	TO ID P12-75 (S701-43)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30

Date: 04 March 2016

FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 552

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29, J1.22 AND J1.47. THE VOLTAGE MEASURED AT PIN J1.63 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14

FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-63 (UUT J1-63)	TO W1 P1A-6B
FROM ID J1A-6B	TO ID A1J15.11
FROM ID A1P15.11	TO ID P13-10 (S701-33)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36

Date: 04 March 2016

FROM ID A1J7.36

TO GROUND

STEP 553

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29 AND J1.22. THE VOLTAGE MEASURED AT PIN J1.65 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-65 (UUT J1-65)	TO W1 P1A-7A
FROM ID J1A-7A	TO ID A1J15.13
FROM ID A1P15.13	TO ID P13-45 (S701-39)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

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FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 554

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29 AND J1.22. THE VOLTAGE MEASURED AT PIN J3.1 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)

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FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P3-1 (UUT J3-1)	TO W1 P1B-11E
FROM ID J1B-11E	TO ID A1J13.22
FROM ID A1P13.22	TO ID P12-75 (S701-43)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 555

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29 AND J1.22. THE VOLTAGE MEASURED AT PIN J1.63 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-63 (UUT J1-63)	TO W1 P1A-6B
FROM ID J1A-6B	TO ID A1J15.11
FROM ID A1P15.11	TO ID P13-10 (S701-33)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)

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FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 556

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29 AND J1.22. SHORT J1.56 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.65 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8

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FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-65 (UUT J1-65)	TO W1 P1A-7A
FROM ID J1A-7A	TO ID A1J15.13
FROM ID A1P15.13	TO ID P13-45 (S701-39)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 557

DESCRIPTION:

Date: 04 March 2016

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29 AND J1.22. SHORT J1.56 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.63 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-63 (UUT J1-63)	TO W1 P1A-6B
FROM ID J1A-6B	TO ID A1J15.11
FROM ID A1P15.11	TO ID P13-10 (S701-33)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1

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FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 558

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29 AND J1.22. SHORT J1.56 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.64 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46

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FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-64 (UUT J1-64)	TO W1 P1B-10F
FROM ID J1B-10F	TO ID A1J13.21
FROM ID A1P13.21	TO ID P12-73 (S701-37)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 559

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29, J1.22 AND J1.62. J1.13 IS MOMENTARILY CONNECTED TO 28.0VDC. THE VOLTAGE MEASURED AT PIN J1.65 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

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CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-13 (UUT J1-13)	TO W1 P1B-14B
FROM ID J1B-14B	TO ID A1J13.3
FROM ID A1P13.3	TO ID P12-46 (S201-7)
FROM W1 P2-62 (UUT J1-62)	TO W1 P1B-7C
FROM ID J1B-7C	TO ID A1J12.9
FROM ID A1P12.9	TO ID P12-19 (S201-31)
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-65 (UUT J1-65)	TO W1 P1A-7A
FROM ID J1A-7A	TO ID A1J15.13
FROM ID A1P15.13	TO ID P13-45 (S701-39)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26

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FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 560

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29, J1.22 AND J1.62. J1.13 IS MOMENTARILY CONNECTED TO 28.0VDC. THE VOLTAGE MEASURED AT PIN J3.1 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-62 (UUT J1-62)	TO W1 P1B-7C
FROM ID J1B-7C	TO ID A1J12.9
FROM ID A1P12.9	TO ID P12-19 (S201-31)
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12

FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P3-1 (UUT J3-1)	TO W1 P1B-11E
FROM ID J1B-11E	TO ID A1J13.22
FROM ID A1P13.22	TO ID P12-75 (S701-43)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 561

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29, J1.22 AND J1.62. J1.13 IS MOMENTARILY CONNECTED TO 28.0VDC. THE VOLTAGE MEASURED AT PIN J1.24 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-62 (UUT J1-62)	TO W1 P1B-7C
FROM ID J1B-7C	TO ID A1J12.9
FROM ID A1P12.9	TO ID P12-19 (S201-31)
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-24 (UUT J1-24)	TO W1 P1B-11F
FROM ID J1B-11F	TO ID A1J13.18
FROM ID A1P13.18	TO ID P12-40 (S701-29)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)

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FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 562

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29, J1.22 AND J1.28. SHORT J1.56 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.65 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)

FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-65 (UUT J1-65)	TO W1 P1A-7A
FROM ID J1A-7A	TO ID A1J15.13
FROM ID A1P15.13	TO ID P13-45 (S701-39)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

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FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 563

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29, J1.22 AND J1.28. SHORT J1.56 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.24 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-24 (UUT J1-24)	TO W1 P1B-11F
FROM ID J1B-11F	TO ID A1J13.18
FROM ID A1P13.18	TO ID P12-40 (S701-29)

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FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 564

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J3.8, J1.29, J1.22 AND J1.28. SHORT J1.56 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.64 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C

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FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-8 (UUT J3-8)	TO W1 P1A-2D
FROM ID J1A-2D	TO ID A1J14.8
FROM ID A1P14.9	TO ID P13-50 (S201-25)
FROM W1 P2-64 (UUT J1-64)	TO W1 P1B-10F
FROM ID J1B-10F	TO ID A1J13.21
FROM ID A1P13.21	TO ID P12-73 (S701-37)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

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FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 565

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J1.29, J1.22, J1.28, J1.27 AND J1.34. SHORT J1.56, J1.39, J1.40 AND J3.22 TO GROUND. THE VOLTAGE MEASURED AT PIN J3.3 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C

FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 566

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J1.29, J1.22, J1.28, J1.27 AND J1.34. SHORT J1.56, J1.39, J1.40 AND J3.22 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.58 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)

FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)

FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)

FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)

FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)

FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)

FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)

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FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-58 (UUT J1-58)	TO W1 P1B-10D
FROM ID J1B-10D	TO ID A1J13.19
FROM ID A1P13.19	TO ID P12-42 (S701-35)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 567

DESCRIPTION:

Date: 04 March 2016

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J1.29, J1.22, J1.28, J1.27 AND J1.34. SHORT J1.56, J1.39, J1.40 AND J3.22 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.64 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM W1 P3-22 (UUT J3-22)	TO W1 P1A-3E
FROM ID J1A-3E	TO ID A1J14.13
FROM ID A1P14.13	TO ID P13-19 (S201-34)
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12

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FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-64 (UUT J1-64)	TO W1 P1B-10F
FROM ID J1B-10F	TO ID A1J13.21
FROM ID A1P13.21	TO ID P12-73 (S701-37)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 568

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J1.29, J1.22, J1.28, J1.27, J1.34, J1.38 AND J1.51. SHORT J1.56 AND J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J3.3 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-51 (UUT J1-51)	TO W1 P1B-9B
FROM ID J1B-9B	TO ID A1J12.2
FROM ID A1P12.2	TO ID P12-15 (S201-16)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 569

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J1.29, J1.22, J1.28, J1.27, J1.34, J1.38 AND J1.51. SHORT J1.56 AND J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.58 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

Date: 04 March 2016

FROM W1 P2-51 (UUT J1-51)	TO W1 P1B-9B
FROM ID J1B-9B	TO ID A1J12.2
FROM ID A1P12.2	TO ID P12-15 (S201-16)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2

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FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-58 (UUT J1-58)	TO W1 P1B-10D
FROM ID J1B-10D	TO ID A1J13.19
FROM ID A1P13.19	TO ID P12-42 (S701-35)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 570

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J1.29, J1.22, J1.28, J1.27, J1.34, J1.38 AND J1.51. SHORT J1.56 AND J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.64 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-51 (UUT J1-51)	TO W1 P1B-9B
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FROM ID J1B-9B	TO ID A1J12.2
FROM ID A1P12.2	TO ID P12-15 (S201-16)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)

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FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-64 (UUT J1-64)	TO W1 P1B-10F
FROM ID J1B-10F	TO ID A1J13.21
FROM ID A1P13.21	TO ID P12-73 (S701-37)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 571

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J1.29, J1.22, J1.28, J1.27, J1.34, J1.38 AND J1.51. SHORT J1.56 AND J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.48 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-51 (UUT J1-51)	TO W1 P1B-9B
FROM ID J1B-9B	TO ID A1J12.2

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FROM ID A1P12.2	TO ID P12-15 (S201-16)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-29 (UUT J1-29)	TO W1 P1A-1C
FROM ID J1A-1C	TO ID A1J14.5
FROM ID A1P14.5	TO ID P13-49 (S201-17)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27

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FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-48 (UUT J1-48)	TO W1 P1B-12D
FROM ID J1B-12D	TO ID A1J13.13
FROM ID A1P13.13	TO ID P12-69 (S701-13)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 572

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J1.22, J1.28, J1.27, J1.34, J1.38 AND J1.17. SHORT J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J3.3 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-17 (UUT J1-17)	TO W1 P1A-2B
FROM ID J1A-2B	TO ID A1J14.4
FROM ID A1P14.4	TO ID P13-48 (S201-12)

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FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30

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FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 573

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J1.22, J1.28, J1.27, J1.34, J1.38 AND J1.17. SHORT J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.48 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-17 (UUT J1-17)	TO W1 P1A-2B
FROM ID J1A-2B	TO ID A1J14.4
FROM ID A1P14.4	TO ID P13-48 (S201-12)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)

FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-48 (UUT J1-48)	TO W1 P1B-12D
FROM ID J1B-12D	TO ID A1J13.13
FROM ID A1P13.13	TO ID P12-69 (S701-13)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V

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FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 574

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J1.22, J1.28, J1.27, J1.34, J1.38, J1.17 AND J1.47. SHORT J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J3.3 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM W1 P2-17 (UUT J1-17)	TO W1 P1A-2B
FROM ID J1A-2B	TO ID A1J14.4
FROM ID A1P14.4	TO ID P13-48 (S201-12)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E

FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

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FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 575

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J1.22, J1.28, J1.27, J1.34, J1.38, J1.17 AND J1.47. SHORT J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.48 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM W1 P2-17 (UUT J1-17)	TO W1 P1A-2B
FROM ID J1A-2B	TO ID A1J14.4
FROM ID A1P14.4	TO ID P13-48 (S201-12)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)

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FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-48 (UUT J1-48)	TO W1 P1B-12D
FROM ID J1B-12D	TO ID A1J13.13
FROM ID A1P13.13	TO ID P12-69 (S701-13)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

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FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 576

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.2, J1.22, J1.28, J1.27, J1.34, J1.38, J1.17 AND J1.47. SHORT J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J3.1 SHOULD BE GREATER THAN 27.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM W1 P2-17 (UUT J1-17)	TO W1 P1A-2B
FROM ID J1A-2B	TO ID A1J14.4
FROM ID A1P14.4	TO ID P13-48 (S201-12)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)

FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A
FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM W1 P2-22 (UUT J1-22)	TO W1 P1A-1B
FROM ID J1A-1B	TO ID A1J14.3
FROM ID A1P14.3	TO ID P13-80 (S201-11)
FROM W1 P3-2 (UUT J3-2)	TO W1 P1A-2F
FROM ID J1A-2F	TO ID A1J14.12
FROM ID A1P14.12	TO ID P13-52 (S201-33)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-1 (UUT J3-1)	TO W1 P1B-11E
FROM ID J1B-11E	TO ID A1J13.22
FROM ID A1P13.22	TO ID P12-75 (S701-43)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

Date: 04 March 2016

STEP 577

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J3.6 AND J1.67 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.28, J1.27, J1.34, J1.38, J1.17 AND J1.47. SHORT J1.39 TO GROUND. THE VOLTAGE MEASURED AT PIN J3.3 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-67 (UUT J1-67)	TO W1 P1A-6F
FROM ID J1A-6F	TO ID A1J14.20
FROM ID A1P14.20	TO ID P13-24 (S202-20)
FROM W1 P2-47 (UUT J1-47)	TO W1 P1A-3F
FROM ID J1A-3F	TO ID A1J14.14
FROM ID A1P14.14	TO ID P13-18 (S201-35)
FROM W1 P2-17 (UUT J1-17)	TO W1 P1A-2B
FROM ID J1A-2B	TO ID A1J14.4
FROM ID A1P14.4	TO ID P13-48 (S201-12)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-38 (UUT J1-38)	TO W1 P1B-8B
FROM ID J1B-8B	TO ID A1J12.5
FROM ID A1P12.5	TO ID P12-17 (S201-23)
FROM W1 P2-27 (UUT J1-27)	TO W1 P1B-14C
FROM ID J1B-14C	TO ID A1J13.5
FROM ID A1P13.5	TO ID P12-14 (S201-13)
FROM W1 P2-39 (UUT J1-39)	TO W1 P1A-1E
FROM ID J1A-1E	TO ID A1J14.9
FROM ID A1P14.9	TO ID P13-17 (S201-26)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-28 (UUT J1-28)	TO W1 P1B-9A

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FROM ID J1B-9A	TO ID A1J12.1
FROM ID A1P12.1	TO ID P12-48 (S201-15)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 578

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.28, J1.27, J1.34, J1.38, J1.17 AND J1.47. SHORT J1.39 AND J1.15 TO GROUND. THE VOLTAGE MEASURED AT PIN J3.3 SHOULD BE LESS THAN 0.9VDC WITH RESPECT TO GROUND.

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CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)

FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P2-15 (UUT J1-15)	TO W1 P1B-8C
FROM ID J1B-8C	TO ID A1J12.6
FROM ID A1P12.6	TO ID P12-81 (S201-24)

FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23

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FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 579

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.28, J1.27, J1.34, J1.38, J1.17 AND J1.47. SHORT J1.39 AND J1.15 TO GROUND. THE FREQUENCY AT PIN J1.52 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-5 (UUT J1-5)	TO W1 P1B-14A
FROM ID J1B-14A	TO ID A1J13.1
FROM ID A1P13.1	TO ID P12-79 (S201-5)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-40 (UUT J1-40)	TO W1 P1A-1F
FROM ID J1A-1F	TO ID A1J14.11
FROM ID A1P14.11	TO ID P13-51 (S201-28)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-15 (UUT J1-15)	TO W1 P1B-8C
FROM ID J1B-8C	TO ID A1J12.6
FROM ID A1P12.6	TO ID P12-81 (S201-24)

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FROM W1 P2-52 (UUT J1-52)	TO W1 P1A-5A
FROM ID J1A-5A	TO ID A1J15.7
FROM ID A1P15.7	TO ID P13-7 (S701-17)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 580

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.28, J1.27, J1.34, J1.38, J1.17, J1.47 AND J1.19. SHORT J1.15, J1.40 AND J3.22 TO GROUND. THE FREQUENCY AT PIN J3.3 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-19 (UUT J1-19)	TO W1 P1B-13C
FROM ID J1B-13C	TO ID A1J13.6
FROM ID A1P13.6	TO ID P12-78 (S201-14)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2

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FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-15 (UUT J1-15)	TO W1 P1B-8C
FROM ID J1B-8C	TO ID A1J12.6
FROM ID A1P12.6	TO ID P12-81 (S201-24)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P3-3 (UUT J3-3)	TO W1 P1A-5B
FROM ID J1A-5B	TO ID A1J15.8
FROM ID A1P15.8	TO ID P13-42 (S701-23)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 581

DESCRIPTION:

Date: 04 March 2016

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.28, J1.27, J1.34, J1.38, J1.17, J1.47 AND J1.19. SHORT J1.15, J1.40, AND J3.22 TO GROUND. THE FREQUENCY AT PIN J1.52 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-19 (UUT J1-19)	TO W1 P1B-13C
FROM ID J1B-13C	TO ID A1J13.6
FROM ID A1P13.6	TO ID P12-78 (S201-14)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-15 (UUT J1-15)	TO W1 P1B-8C
FROM ID J1B-8C	TO ID A1J12.6
FROM ID A1P12.6	TO ID P12-81 (S201-24)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-52 (UUT J1-52)	TO W1 P1A-5A
FROM ID J1A-5A	TO ID A1J15.7
FROM ID A1P15.7	TO ID P13-7 (S701-17)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3

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FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 582

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.28, J1.27, J1.34, J1.38, J1.17, J1.47 AND J1.19. SHORT J1.15, J1.40 AND J3.22 TO GROUND. THE FREQUENCY AT PIN J1.53 SHOULD BE BETWEEN .5 AND 13 HZ.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W1 P2-19 (UUT J1-19)	TO W1 P1B-13C
FROM ID J1B-13C	TO ID A1J13.6
FROM ID A1P13.6	TO ID P12-78 (S201-14)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-15 (UUT J1-15)	TO W1 P1B-8C
FROM ID J1B-8C	TO ID A1J12.6
FROM ID A1P12.6	TO ID P12-81 (S201-24)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-53 (UUT J1-53)	TO W1 P1B-12F
FROM ID J1B-12F	TO ID A1J13.15
FROM ID A1P13.15	TO ID P12-71 (S701-19)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)

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FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
FROM ID A1J21.1	TO ID A1J6.8
FROM ID A1P6.8	TO ID P10-162 (S501-2)
FROM ID P10-164 (S501-3)	TO ID A1P7.29
FROM ID A1J7.29	TO ID BUS 1

STEP 583

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34 AND J1.19. SHORT J1.15 TO GROUND. THE VOLTAGE MEASURED AT PIN J1.18 SHOULD BE GREATER THAN 27.0VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-19 (UUT J1-19)	TO W1 P1B-13C
FROM ID J1B-13C	TO ID A1J13.6
FROM ID A1P13.6	TO ID P12-78 (S201-14)
FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2
FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)
FROM W1 P2-15 (UUT J1-15)	TO W1 P1B-8C
FROM ID J1B-8C	TO ID A1J12.6
FROM ID A1P12.6	TO ID P12-81 (S201-24)
FROM ID P12-80 (S201-2)	TO ID A1P12.40

FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8
FROM W1 P2-18 (UUT J1-18)	TO W1 P1A-3C
FROM ID J1A-3C	TO ID A1J15.3
FROM ID A1P15.3	TO ID P13-70 (S701-9)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 584

DESCRIPTION:

THIS STEP APPLIES 15.0VDC TO PINS J3.30 AND J1.10 AND -15.0VDC TO PIN J3.29 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PIN J3.6 WITH RESPECT TO J1.11. 28.0VDC IS APPLIED TO PINS J1.34 AND J1.19. SHORT J1.15 TO GROUND.

28.0VDC IS APPLIED TO PIN J1.34.
 26.0VDC IS APPLIED TO PIN J1.34.
 24.0VDC IS APPLIED TO PIN J1.34.
 22.0VDC IS APPLIED TO PIN J1.34.
 20.0VDC IS APPLIED TO PIN J1.34.
 19.0VDC IS APPLIED TO PIN J1.34.

Date: 04 March 2016

THE FREQUENCY AT PIN J1.18 SHOULD BE BETWEEN 0.5 AND 4.5 HZ.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W1 P2-19 (UUT J1-19)	TO W1 P1B-13C
FROM ID J1B-13C	TO ID A1J13.6
FROM ID A1P13.6	TO ID P12-78 (S201-14)

FROM ID P12-20 (S201-3)	TO ID A1P12.46
FROM ID A1J12.46	TO ID A1J10.2
FROM ID A1P10.2	TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)	TO ID A1P9.27
FROM ID A1J9.27	TO ID BUS 2

FROM W1 P2-34 (UUT J1-34)	TO W1 P1A-5F
FROM ID J1A-5F	TO ID A1J14.18
FROM ID A1P14.18	TO ID P13-89 (S202-18)

FROM W1 P2-15 (UUT J1-15)	TO W1 P1B-8C
FROM ID J1B-8C	TO ID A1J12.6
FROM ID A1P12.6	TO ID P12-81 (S201-24)

FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-205 (S508-10)	TO ID A1P9.2
FROM ID A1J9.2	TO ID BUS 8

FROM W1 P2-18 (UUT J1-18)	TO W1 P1A-3C
FROM ID J1A-3C	TO ID A1J15.3
FROM ID A1P15.3	TO ID P13-70 (S701-9)

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6

FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V

FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1

FROM ID P19-18 (CT-IN1)	TO ID A1P21.1
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FROM ID A1J21.1
FROM ID A1P6.8
FROM ID P10-164 (S501-3)
FROM ID A1J7.29

TO ID A1J6.8
TO ID P10-162 (S501-2)
TO ID A1P7.29
TO ID BUS 1

3.0 Functional Flow Chart (FFC)



