

English Language Test Description

MIPR # M9545012MP24797

CDRL F001

for

Unit Under Test

UUT Nomenclature: Stabilization System Processor

UUT Part Number: P/N 16102218-061

from

Light Armored Vehicle - 25A2

(LAV-25A2)

ATE SYSTEM

AN/USM-657B – Third Echelon Test System (TETS-B)

AN/USM-717 – Virtual Instrument Portable Equipment Repair/Test (VIPER/T)

Developed by

U.S. Army RDECOM

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ELTD REVISION SUMMARY

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1.0 Reference Documents

1.1 Virtual Instrument Portable Equipment Repair/Test (VIPER/T)

IEEE Std 716-1989	IEEE Standard Common Abbreviated Test Language for All Systems
TM TBD-CD	VIPER/T IETM (Interactive Electronic Technical Manual)
System Design Document Doc # 7992008	VIPER/T AN/USM-717
VIPER/T P/N 7992021	VIPER/T CPM (Computer Programming Manual)

1.2 Third Echelon Test System (TETS-B)

TM 10530A-CD	TETS IETM (Interactive Electronic Technical Manual)
System Design Description Doc # 93006A0018	Third Echelon Test System (TETS) AN/USM-657
TETS P/N 93006A0026	TETS CPM (Computer Programming Manual)

1.3 Unit Under Test

UUT P/N: 16102218-061
 UUT Nomenclature: Stabilization System Processor (SSP)
 UUT Type: Shop Replaceable Unit (SRU)

<u>DESCRIPTION</u>	<u>NUMBER</u>	<u>REVISION</u>	<u>DATE</u>
Parts List	16102218-061	W	19 July 2005
LRU QA Spec	ES13457	None	8-Aug-2006
Circuit Card Assy, Stab System Processor	16102218-061	W	19 July 2005
Schematic Diagram, Stab System Processor	16103832-001	D	19-Nov-2004

1.4 Reference Drawings

Refer to the following schematics when diagnosing connection paths.

ID Schematic



13020A0001
(SYSTEM INTERCON)

W7 Schematic



13020A7701 (CABLE,
W7, SCHEMATIC).pd

2.0 English Language Test Description Steps

2.1 Common Procedures

The following connections are common throughout the entire test:

2.1.1 UUT Power

Description:

28V Power is applied to J1-A1 (HI) and J1-A2 (LO) using DC4. 15V Power is applied to J2-10 (HI) and J2-11 (LO) using DC5.

Connection Path is as follows:

From W7 P2-A1 (UUT J1-A1)	to W7 P1B-1D
From ID J1B-1D	to ID A1J2.31
From ID A1P2.31	to ID P10-87 (S101-6)
From ID P10-23 (S101-5)	to ID A1P2.4
From ID A1J2.4	to ID A1J1.3
From ID A1P1.3	to ID P1-10 (DC4-HI)
From W7 P2-A2 (UUT J1-A2)	to W7 P1B-1F
From ID J1B-1F	to ID A1J1.11
From ID A1P1.11	to ID P1-11 (DC4-LO)
From W7 P3-10 (UUT J2-10)	to W7 P1A-14F
From ID J1A-14F	to ID A1J2.14
From ID A1P2.14	to ID P10-88 (S101-10)
From ID P10-24 (S101-9)	to ID A1P2.2
From ID A1J2.2	to ID A1J1.14
From ID A1P1.14	to ID P1-13 (DC5-HI)
From W7 P3-11 (UUT J2-11)	to W7 P1A-12F
From ID J1A-12F	to ID A1J1.7
From ID A1P1.7	to ID P1-14 (DC5-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C

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From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

2.1.2 Serial Comm

Description:

The SSP communicates with the Test Station through RS-422 communication.

J2-44 to RS-422 RXD+
J2-40 to RS-422 RXD-
J2-59 to RS-422 TXD+
J2-32 to RS-422 TXD-

Connection Path is as follows:

From W7 P3-44 (UUT J2-44)	to W7 P1B-11E
From ID J1B-11E	to ID A1J3.9
From ID A1P3.9	to ID J4-3
From W8 P2-3	to W8 P1-3 (ICJ6.3)
From W7 P3-40 (UUT J2-40)	to W7 P1B-9F
From ID J1B-9F	to ID A1J3.10
From ID A1P3.10	to ID J4-4
From W8 P2-4	to W8 P1-4 (ICJ6.4)
From W7 P3-59 (UUT J2-59)	to W7 P1B-10B
From ID J1B-10B	to ID A1J3.18
From ID A1P3.18	to ID J4-2
From W8 P2-2	to W8 P1-2 (ICJ6.2)
From W7 P3-32 (UUT J2-32)	to W7 P1B-10A
From ID J1B-10A	to ID A1J3.19
From ID A1P3.19	to ID J4-1
From W8 P2-1	to W8 P1-1 (ICJ6.1)

2.1.3 Boot Up

Description:

The SSP must boot up in Debug Mode, in order for it to respond to serial commands from the Test Station. J2-17 is held low during power on to enable Debug Mode.

Connection Path is as follows:

From W7 P3-17 (UUT J2-17)	to W7 P1A-8C
From ID J1A-8C	to ID A1J15.18
From ID A1P15.18	to ID P13-78 (S701-47)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3

From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From ID BUS 8	to ID A1J6.12
From ID A1P6.12	to ID P10-70 (S301-28)
From ID P10-133 (S301-27)	to ID A1P7.18
From ID A1J7.18	to ID A1J1.5
From ID A1P1.5	to ID P1-26 (DC9-LO)
From ID P1-26 (DC9-LO)	to ID A1P1.5
From ID A1J1.5	to ID A1J7.16
From ID A1P7.16	to ID P10-163 (S301-12)
From ID P10-98 (S301-11)	to ID A1P7.32
From ID A1J7.32	to GROUND

2.2 Interface ID

Refer to [Reference Drawings](#) when diagnosing connection paths.

Step 1

Description:

Connect R111 (324 ohms) to Bus 5. Connect R109 (698 ohms) to Bus 6. R109 and R111 are now in series between Bus 5 and 6. Connect DMM HI to Bus 5. Connect DMM LO to Bus 6. Expected Resistance: 1022 ohms \pm 5%

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID BUS 5	to ID A1J8.47
From ID A1P8.47	to ID P10-73 (S301-48)
From ID P10-7 (S301-47)	to ID A1P7.23
From ID A1J7.23	to ID A1J4.15
From ID A1P4.15	to ID R111.1
From ID R111.2	to ID A1P4.9
From ID A1J4.9	to +28V
From ID BUS 6	to ID A1J8.48
From ID A1P8.48	to ID P10-171 (S301-50)
From ID P10-42 (S301-49)	to ID A1P7.24
From ID A1J7.24	to ID A1J4.16

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From ID A1P4.16	to ID R109.1
From ID R109.2	to ID A1P4.9
From ID A1J4.9	to +28V

2.3 UUT ID

Step 2

Description:

This step verifies continuity connection between J2-77 and J1-B2. The DMM is used to measure the resistance using limits of LT 5.0 Ohms.

Connection Path is as follows:

From W7 P3-77 (UUT J2-77)	to W7 P1A-4E
From ID A1P14.15	to ID P13-21 (S201-41)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 3

Description:

This step verifies continuity between J2-24 and J1-C2. The DMM is used to measure the resistance using limits of LT 5.0 Ohms.

Connection Path is as follows:

From W7 P3-24 (UUT J2-24)	to W7 P1B-6C
From ID J1B-6C	to ID A1J12.12
From ID A1P12.12	to ID P12-53 (S201-39)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-C2 (UUT J1-C2)	to W7 P1B-3A
From ID J1B-3A	to ID A1J12.19
From ID A1P12.19	to ID P12-54 (S201-38)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

2.4 Safe To Turn On

Step 4

Description:

This step verifies resistance between J1-A38, J2-33, J2-57, J1-C36 & J1-C37, J2-34, J2-31, J1-B36. These pins are tied in Cable W7. The DMM is used to measure the resistance using limits of UL 45.3 and LL 35.3 Ohms.

Connection Path is as follows:

From W7 P2-A38 (UUT J1-A38)	to W7 P1B-4C
From ID J1B-4C	to ID A1J12.18
From ID A1P12.18	to ID P12-32 (S202-48)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1

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From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-C37 (UUT J1-C37)	to W7 P1B-4B
From ID J1B-4B	to ID A1J12.17
From ID A1P12.17	to ID P12-96 (S202-47)
From ID P13-93 (S202-3)	to ID A1P14.49
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 5

Description:

This step verifies resistance from J1-A37 to J1-B37. The DMM is used to measure the resistance using limits of UL 127 and LL 115 Ohms.

Connection Path is as follows:

From W7 P2-A37 (UUT J1-A37)	to W7 P1B-5C
From ID J1B-5C	to ID A1J12.15
From ID A1P12.15	to ID P12-31 (S202-45)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B37 (UUT J1-B37)	to W7 P1B-4A
From ID J1B-4A	to ID A1J12.16
From ID A1P12.16	to ID P12-63 (S202-46)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29

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From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 6

Description:

This step verifies resistance from J1-C38 to J1-B38. The DMM is used to measure the resistance using limits of UL 127 and LL 115 Ohms.

Connection Path is as follows:

From W7 P2-C38 (UUT J1-C38)	to W7 P1A-7F
From ID J1A-8F	to ID A1J14.24
From ID A1P14.24	to ID P13-62 (S202-42)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B38 (UUT J1-B38)	to W7 P1A-12E
From ID J1A-12E	to ID A1J14.31
From ID A1P14.31	to ID P13-30 (S202-41)
From ID P13-93 (S202-3)	to ID A1P14.49
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 7

Description:

This step verifies resistance from J1-A6 to J1-B2. The DMM is used to measure the resistance using limits of UL 1010 and LL 910 Ohms.

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Connection Path is as follows:

From W7 P2-A6 (UUT J1-A6)	to W7 P1B-14D
From ID J1B-14D	to ID A1J13.7
From ID A1P13.7	to ID P12-4 (S701-3)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 8

Description:

This step verifies resistance from J1-A7 to J1-B2. The DMM is used to measure the resistance using limits of UL 1010 and LL 910 Ohms.

Connection Path is as follows:

From W7 P2-A7 (UUT J1-A7)	to W7 P1B-13D
From ID J1B-13D	to ID A1J13.8
From ID A1P13.8	to ID P12-68 (S701-4)
From ID P12-44 (S701-2)	to ID A1P12.48

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From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 9

Description:

This step verifies resistance from J1-C13 to J1-A14. The DMM is used to measure the resistance using limits of UL 10200 and LL 9800 Ohms.

Connection Path is as follows:

From W7 P2-C13 (UUT J1-C13)	to W7 P1B-8A
From ID J1B-8A	to ID A1J12.4
From ID A1P12.4	to ID P12-18 (S201-22)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)

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From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 10

Description:

This step verifies resistance from J1-A15 to J1-B15. The DMM is used to measure the resistance using limits of UL 10200 and 9800 ohms.

Connection Path is as follows:

From W7 P2-A15 (UUT J1-A15)	to W7 P1B-9C
From ID J1B-9C	to ID A1J12.3
From ID A1P12.3	to ID P12-51 (S201-21)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B15 (UUT J1-B15)	to W7 P1B-8B
From ID J1B-8B	to ID A1J12.5
From ID A1P12.5	to ID P12-17 (S201-23)

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From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 11

Description:

This step verifies resistance from J1-A4 to J1-B4. The DMM is used to measure the resistance using limits of UL 14000 and LL 12000 Ohms.

Connection Path is as follows:

From W7 P2-A4 (UUT J1-A4)	to W7 P1B-14E
From ID J1B-14E	to ID A1J13.9
From ID A1P13.9	to ID P12-36 (S701-5)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B4 (UUT J1-B4)	to W7 P1B-13E
From ID J1B-13E	to ID A1J13.10
From ID A1P13.10	to ID P12-3 (S701-6)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2

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From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 12

Description:

This step verifies resistance from J1-C4 to J1-A5. The DMM is used to measure the resistance using limits of UL 14000 and LL 12000 Ohms.

Connection Path is as follows:

From W7 P2-C4 (UUT J1-C4)	to W7 P1A-3C
From ID J1A-3C	to ID A1J15.3
From ID A1P15.3	to ID P13-70 (S701-9)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-A5 (UUT J1-A5)	to W7 P1A-4A
From ID J1A-4A	to ID A1J15.4
From ID A1P15.4	to ID P13-38 (S701-10)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

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From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 13

Description:

This step verifies resistance from J1-B3 to J1-C3. The DMM is used to measure the resistance using limits of UL 14000 and LL 12000 Ohms.

Connection Path is as follows:

From W7 P2-B3 (UUT J1-B3)	to W7 P1A-3A
From ID J1A-3A	to ID A1J15.1
From ID A1P15.1	to ID P13-39 (S701-7)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-C3 (UUT J1-C3)	to W7 P1A-3B
From ID J1A-3B	to ID A1J15.2
From ID A1P15.2	to ID P13-6 (S701-8)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

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Step 14

Description:

This step verifies resistance from J1-A20 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

Connection Path is as follows:

From W7 P2-A20 (UUT J1-A20)	to W7 P1A-5B
From ID J1A-5B	to ID A1J15.8
From ID A1P15.8	to ID P13-42 (S701-23)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 15

Description:

This step verifies resistance from J1-A24 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

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Connection Path is as follows:

From W7 P2-A24 (UUT J1-A24)	to W7 P1A-12B
From ID J1A-12B	to ID A1J15.26
From ID A1P15.26	to ID P13-73 (S701-25)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 16

Description:

This step verifies resistance from J1-A26 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

Connection Path is as follows:

From W7 P2-A26 (UUT J1-A26)	to W7 P1B-7D
From ID J1B-7D	to ID A1J13.29
From ID A1P13.29	to ID P12-8 (S701-27)

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From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 17

Description:

This step verifies resistance from J1-B20 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

Connection Path is as follows:

From W7 P2-B20 (UUT J1-B20)	to W7 P1B-7E
From ID J1B-7E	to ID A1J13.31
From ID A1P13.31	to ID P12-7 (S701-30)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

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From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 18

Description:

This step verifies resistance from J1-A28 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

Connection Path is as follows:

From W7 P2-A28 (UUT J1-A28)	to W7 P1B-11F
From ID J1B-11F	to ID A1J13.18
From ID A1P13.18	to ID P12-40 (S701-29)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

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From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 19

Description:

This step verifies resistance from J1-B27 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

Connection Path is as follows:

From W7 P2-B27 (UUT J1-B27)	to W7 P1A-6C
From ID J1A-6C	to ID A1J15.12
From ID A1P15.12	to ID P13-74 (S701-34)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2

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From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 20

Description:

This step verifies resistance from J1-C20 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

Connection Path is as follows:

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)

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From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 21

Description:

This step verifies resistance from J1-C24 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

Connection Path is as follows:

From W7 P2-C24 (UUT J1-C24)	to W7 P1A-7A
From ID J1A-7A	to ID A1J15.13
From ID A1P15.13	to ID P13-45 (S701-39)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6

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From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 22

Description:

This step verifies resistance from J1-C25 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

Connection Path is as follows:

From W7 P2-C25 (UUT J1-C25)	to W7 P1A-7B
From ID J1A-7B	to ID A1J15.14
From ID A1P15.14	to ID P13-12 (S701-40)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)

From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2

From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

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Step 23

Description:

This step verifies resistance from J2-8 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

Connection Path is as follows:

From W7 P3-8 (UUT J2-8)	to W7 P1A-7C
From ID J1A-7C	to ID A1J15.15
From ID A1P15.15	to ID P13-76 (S701-41)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 24

Description:

This step verifies resistance from J2-37 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

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Connection Path is as follows:

From W7 P3-37 (UUT J2-37)	to W7 P1A-9B
From ID J1A-9B	to ID A1J15.20
From ID A1P15.20	to ID P13-46 (S701-48)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 25

Description:

This step verifies resistance from J2-12 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

Connection Path is as follows:

From W7 P3-12 (UUT J2-12)	to W7 P1A-4C
From ID J1A-4C	to ID A1J15.6
From ID A1P15.6	to ID P13-44 (S701-42)

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From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 26

Description:

This step verifies resistance from J2-13 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

Connection Path is as follows:

From W7 P3-13 (UUT J2-13)	to W7 P1A-8B
From ID J1A-8B	to ID A1J15.17
From ID A1P15.17	to ID P13-13 (S701-49)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

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From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 27

Description:

This step verifies isolation from J2-10 to J2-11. The DMM is used to measure the resistance using limits of GT 500000 Ohms.

Connection Path is as follows:

From W7 P3-10 (UUT J2-10)	to W7 P1A-14F
From ID J1A-14F	to ID A1J7.1
From ID A1P7.1	to ID P10-1 (S301-7)
From ID P10-35 (S301-8)	to ID A1P7.46
From ID A1J7.46	to ID A1J15.49
From ID A1P15.49	to ID P20.2 (DMM-HI)
From W7 P3-11 (UUT J2-11)	to W7 P1A-12F
From ID J1A-12F	to ID A1J7.12
From ID A1P7.12	to ID P10-66 (S301-6)
From ID P10-33 (S301-5)	to ID A1P7.40
From ID A1J7.40	to ID A1J15.50
From ID A1P15.50	to ID P20.3 (DMM-LO)

Step 28

Description:

This step verifies isolation from J1-A1 to J1-A2. The DMM is used to measure the resistance using limits of GT 19000 Ohms.

Connection Path is as follows:

From W7 P2-A1 (UUT J1-A1)	to W7 P1B-1D
From ID J1B-1D	to ID A1J7.13
From ID A1P7.13	to ID P10-226 (S301-3)
From ID P10-99 (S301-4)	to ID A1P7.44
From ID A1J7.44	to ID A1J15.49
From ID A1P15.49	to ID P20.2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 29

Description:

This step verifies isolation from J1-B1 to J1-B2. The DMM is used to measure the resistance using limits of GT 11000 Ohms.

Connection Path is as follows:

From W7 P2-B1 (UUT J1-B1)	to W7 P1B-9A
From ID J1B-9A	to ID A1J12.1
From ID A1P12.1	to ID P12-48 (S201-15)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

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From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 30

Description:

This step verifies isolation from J1-C1 to J1-B2. The DMM is used to measure the resistance using limits of GT 11000 Ohms.

Connection Path is as follows:

From W7 P2-C1 (UUT J1-C1)	to W7 P1B-9B
From ID J1B-9B	to ID A1J12.2
From ID A1P12.2	to ID P12-15 (S201-16)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50

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From ID A1P15.50

to ID P20-3 (DMM-LO)

Step 31

Description:

This step verifies isolation from J1-C40 to J1-B2. The DMM is used to measure the resistance using limits of GT 10050 Ohms.

Connection Path is as follows:

From W7 P2-C40 (UUT J1-C40)	to W7 P1A-2C
From ID J1A-2C	to ID A1J14.6
From ID A1P14.6	to ID P13-15 (S201-19)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 32

Description:

This step verifies isolation from J1-A3 to J1-B2. The DMM is used to measure the resistance using limits of GT 11000 Ohms.

Connection Path is as follows:

From W7 P2-A3 (UUT J1-A3)	to W7 P1A-1D
From ID J1A-1D	to ID A1J14.7
From ID A1P14.7	to ID P13-79 (S201-20)

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From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 33

Description:

This step verifies resistance from J1-B26 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

Connection Path is as follows:

From W7 P2-B26 (UUT J1-B26)	to W7 P1A-6B
From ID J1A-6B	to ID A1J15.11
From ID A1P15.11	to ID P13-10 (S701-33)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B

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From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 34

Description:

This step verifies resistance from J2-9 to J1-B2. The DMM is used to measure the resistance using limits of UL 21000 and LL 19000 Ohms.

Connection Path is as follows:

From W7 P3-9 (UUT J2-9)	to W7 P1B-13C
From ID J1B-13C	to ID A1J13.6
From ID A1P13.6	to ID P12-78 (S201-14)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27

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From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 35

Description:

This step verifies isolation from J1-C21 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

From W7 P2-C21 (UUT J1-C21)	to W7 P1B-10F
From ID J1B-10F	to ID A1J13.21
From ID A1P13.21	to ID P12-73 (S701-37)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

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From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 36

Description:

This step verifies isolation from J1-C22 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

From W7 P2-C22 (UUT J1-C22)	to W7 P1B-11E
From ID J1B-11E	to ID A1J13.17
From ID A1P13.17	to ID P12-41 (S701-38)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)

From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2

From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

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Step 37

Description:

This step verifies isolation from J1-A23 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

From W7 P2-A23 (UUT J1-A23)	to W7 P1A-11B
From ID J1A-11B	to ID A1J15.24
From ID A1P15.24	to ID P13-9 (S701-24)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 38

Description:

This step verifies isolation from J1-B24 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

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Connection Path is as follows:

From W7 P2-B24 (UUT J1-B24)	to W7 P1A-5C
From ID J1A-5C	to ID A1J15.9
From ID A1P15.9	to ID P13-75 (S701-31)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 39

Description:

This step verifies isolation from J1-A25 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

From W7 P2-A25 (UUT J1-A25)	to W7 P1A-13B
From ID J1A-13B	to ID A1J15.28
From ID A1P15.28	to ID P13-41 (S701-26)
From ID P12-44 (S701-2)	to ID A1P12.48

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From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 40

Description:

This step verifies isolation from J1-B25 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

From W7 P2-B25 (UUT J1-B25)	to W7 P1A-6A
From ID J1A-6A	to ID A1J15.10
From ID A1P15.10	to ID P13-43 (S701-32)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)

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From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 41

Description:

This step verifies isolation from J1-A27 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

From W7 P2-A27 (UUT J1-A27)	to W7 P1B-9E
From ID J1B-9E	to ID A1J13.23
From ID A1P13.23	to ID P12-72 (S701-28)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)

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From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 42

Description:

This step verifies isolation from J1-A32 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

From W7 P2-A32 (UUT J1-A32)	to W7 P1A-9F
From ID J1A-9F	to ID A1J14.26
From ID A1P14.26	to ID P13-64 (S202-50)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23

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From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 43

Description:

This step verifies isolation from J1-B32 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

From W7 P2-B32 (UUT J1-B32)	to W7 P1B-10D
From ID J1B-10D	to ID A1J13.19
From ID A1P13.19	to ID P12-42 (S701-35)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6

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From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 44

Description:

This step verifies isolation from J2-15 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

From W7 P3-15 (UUT J2-15)	to W7 P1B-7F
From ID J1B-7F	to ID A1J13.33
From ID A1P13.33	to ID P12-10 (S701-45)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)

From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2

From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

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Step 45

Description:

This step verifies isolation from J2-14 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

From W7 P3-14 (UUT J2-14)	to W7 P1A-9A
From ID J1A-9A	to ID A1J15.19
From ID A1P15.19	to ID P13-77 (S701-50)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 46

Description:

This step verifies isolation from J2-16 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

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From W7 P3-16 (UUT J2-16)	to W7 P1B-8E
From ID J1B-8E	to ID A1J13.26
From ID A1P13.26	to ID P12-74 (S701-46)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 47

Description:

This step verifies isolation from J2-17 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

From W7 P3-17 (UUT J2-17)	to W7 P1A-8C
From ID J1A-8C	to ID A1J15.18
From ID A1P15.18	to ID P13-78 (S701-47)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23

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From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 48

Description:

This step verifies isolation from J2-25 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

From W7 P3-25 (UUT J2-25)	to W7 P1A-4F
From ID J1A-4F	to ID A1J14.16
From ID A1P14.16	to ID P13-86 (S201-43)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

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From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 49

Description:

This step verifies isolation from J2-52 to J1-B2. The DMM is used to measure the resistance using limits of GT 20000 Ohms.

Connection Path is as follows:

From W7 P3-52 (UUT J2-52)	to W7 P1A-5E
From ID J1A-5E	to ID A1J14.17
From ID A1P14.17	to ID P13-87 (S202-10)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)

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From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 50

Description:

This step verifies resistance from J1-A8 to J1-B8. The DMM is used to measure the resistance using limits of UL 14000 and LL 12000 Ohms.

Connection Path is as follows:

From W7 P2-A8 (UUT J1-A8)	to W7 P1B-14F
From ID J1B-14F	to ID A1J13.11
From ID A1P13.11	to ID P12-38 (S701-11)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B8 (UUT J1-B8)	to W7 P1B-13F
From ID J1B-13F	to ID A1J13.12
From ID A1P13.12	to ID P12-5 (S701-12)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26

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From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 51

Description:

This step verifies resistance from J1-C8 to J1-A9. The DMM is used to measure the resistance using limits of UL 14000 and LL 12000 Ohms.

Connection Path is as follows:

From W7 P2-C8 (UUT J1-C8)	to W7 P1B-12D
From ID J1B-12D	to ID A1J13.13
From ID A1P13.13	to ID P12-69 (S701-13)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-A9 (UUT J1-A9)	to W7 P1B-12E
From ID J1B-12E	to ID A1J13.14
From ID A1P13.14	to ID P12-37 (S701-14)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)

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From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 52

Description:

This step verifies resistance from J1-B6 to J1-C6. The DMM is used to measure the resistance using limits of UL 31000 and LL 29000 Ohms.

Connection Path is as follows:

From W7 P2-B6 (UUT J1-B6)	to W7 P1A-5A
From ID J1A-5A	to ID A1J15.7
From ID A1P15.7	to ID P13-7 (S701-17)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-C6 (UUT J1-C6)	to W7 P1A-10B
From ID J1A-10B	to ID A1J15.22
From ID A1P15.22	to ID P13-71 (S701-18)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 53

Description:

Date: 04 March 2016

This step verifies resistance from J1-B7 to J1-C7. The DMM is used to measure the resistance using limits of UL 31000 and LL 29000 Ohms.

Connection Path is as follows:

From W7 P2-B7 (UUT J1-B7)	to W7 P1B-12F
From ID J1B-12F	to ID A1J13.15
From ID A1P13.15	to ID P12-71 (S701-19)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-C7 (UUT J1-C7)	to W7 P1B-8D
From ID J1B-8D	to ID A1J13.25
From ID A1P13.25	to ID P12-39 (S701-20)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 54

Description:

This step verifies resistance from J1-C10 to J1-A11. The DMM is used to measure the resistance using limits of UL 38000 and LL 34000 Ohms.

Connection Path is as follows:

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From W7 P2-C10 (UUT J1-C10)	to W7 P1A-4B
From ID J1A-4B	to ID A1J15.5
From ID A1P15.5	to ID P13-72 (S701-15)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-A11 (UUT J1-A11)	to W7 P1A-8A
From ID J1A-8A	to ID A1J15.16
From ID A1P15.16	to ID P13-40 (S701-16)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 55

Description:

This step verifies resistance from J1-B11 to J1-C11. The DMM is used to measure the resistance using limits of UL 38000 and LL 34000 Ohms.

Connection Path is as follows:

From W7 P2-B11 (UUT J1-B11)	to W7 P1B-11D
From ID J1B-11D	to ID A1J13.16
From ID A1P13.16	to ID P12-6 (S701-21)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3

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From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-C11 (UUT J1-C11)	to W7 P1B-8F
From ID J1B-8F	to ID A1J13.27
From ID A1P13.27	to ID P12-70 (S701-22)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-76 (UUT J2-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)

Step 56

Description:

This step verifies resistance from J2-44 to J2-40. The DMM is used to measure the resistance using limits of UL 465 and LL 420 Ohms.

Connection Path is as follows:

From W7 P3-44 (UUT J2-44)	to W7 P1B-9D
From ID J1B-9D	to ID A1J13.22
From ID A1P13.22	to ID P12-75 (S701-43)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28

From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P3-40 (UUT J2-40)	to W7 P1B-9F
From ID J1B-9F	to ID A1J13.24
From ID A1P13.24	to ID P12-43 (S701-44)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

2.5 MODULE 1 – PSU AND BASIC CPU TESTS

Description:

Power will be applied to the UUT and a series of measurements will be made of input and output voltage test points. Once all voltages are verified to be functional, the UUT is reset so the turn on behavior can be observed. A series of RS-422 messages will be sent between the UUT and the tester to ensure that proper bi-directional communication works as expected, as the majority of TPS steps rely on serial commands being executed by the UUT.

Refer to [Reference Drawings](#) when diagnosing connection paths

Step 101

Description:

This step measures voltage output on J1-C34 Primary PWR TP. The DMM is used to measure the voltage using limits of UL 28 V and LL 26 V.

Connection Path is as follows:

See "UUT Power"

From W7 P2-C34 (UUT J1-C34)	to W7 P1B-7A
From ID J1B-7A	to ID A1J12.7
From ID A1P12.7	to ID P12-82 (S201-29)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28

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From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 102

Description:

This step measures voltage output on J1-B1 to verify the +15 Volt Rail. The DMM is used to measure the voltage using limits of UL 15.75 V and LL 14.25 V.

Connection Path is as follows:

See "UUT Power"

From W7 P2-B1 (UUT J1-B1)	to W7 P1B-9A
From ID J1B-9A	to ID A1J12.1
From ID A1P12.1	to ID P12-48 (S201-15)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2

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From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 103

Description:

This step measures voltage output on J1-C1 to verify the -15 Volt Rail. The DMM is used to measure the voltage using limits of UL - 14.25 V and LL -15.75 V.

Connection Path is as follows:
See "UUT Power"

From W7 P2-C1 (UUT J1-C1)	to W7 P1B-9B
From ID J1B-9B	to ID A1J12.2
From ID A1P12.2	to ID P12-15 (S201-16)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)

From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2

From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 104

Description:

This step measures voltage output on J1-C40 to verify the +5 Volt Rail. The DMM is used to measure the voltage using limits of UL 5.25 V and LL 4.75 V.

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Connection Path is as follows:
See "UUT Power"

From W7 P2-C40 (UUT J1-C40)	to W7 P1A-2C
From ID J1A-2C	to ID A1J14.6
From ID A1P14.6	to ID P13-15 (S201-19)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 105

Description:

This step measures voltage output on J1-A3 to verify the -5 Volt Rail. The DMM is used to measure the voltage using limits of UL - 4.75 V and LL -5.25 V.

Connection Path is as follows:
See "UUT Power"

From W7 P2-A3 (UUT J1-A3)	to W7 P1A-1D
From ID J1A-1D	to ID A1J14.7
From ID A1P14.7	to ID P13-79 (S201-20)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

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From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 106

Description:

This step measures voltage output on J1-B12 to verify the +10 Volt Rail. The DMM is used to measure the voltage using limits of UL 10.25 V and LL 9.75 V.

Connection Path is as follows:
See "UUT Power"

From W7 P2-B12 (UUT J1-B12)	to W7 P1A-1C
From ID J1A-1C	to ID A1J14.5
From ID A1P14.5	to ID P13-49 (S201-17)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2

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From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 107

Description:

This step measures voltage output on J1-A12 to verify the -10 Volt Rail. The DMM is used to measure the voltage using limits of UL - 9.75 V and LL -10.25 V.

Connection Path is as follows:
See "UUT Power"

From W7 P2-A12 (UUT J1-A12)	to W7 P1A-10F
From ID J1A-10F	to ID A1J14.28
From ID A1P14.28	to ID P13-16 (S201-18)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

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Step 108

Description:

This step measures voltage output on J2-69 to verify the +5 Volt ISO Rail. The DMM is used to measure the voltage using limits of UL 5.25 V and LL 4.75 V.

Connection Path is as follows:
See "UUT Power"

From W7 P3-69 (UUT J2-69)	to W7 P1B-7B
From ID J1B-7B	to ID A1J12.8
From ID A1P12.8	to ID P12-50 (S201-30)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P3-11 (UUT J2-11)	to W7 P1A-12F
From ID J1A-12F	to ID A1J7.12
From ID A1P7.12	to ID P10-66 (S301-6)
From ID P10-33 (S301-5)	to ID A1P7.40
From ID A1J7.40	to ID A1J15.50
From ID A1P15.50	to ID P20.3 (DMM-LO)

Step 109

Description:

This step fully tests the turn-on behavior of the SSP, as well as bi-directional serial communication to ensure the SSP is ready for functional testing. Failure to meet the test parameters of this step likely means the UUT should be returned to the manufacturer for further repair.

Connection Path is as follows:

See "UUT Power"
See "Boot Up"
See "Serial Comm"

From W7 P2-A19 (UUT J1-A19)	to W7 P1A-1A
From ID J1A-1A	to ID A1J14.1
From ID A1P14.1	to ID P13-47 (S201-9)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6

From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8

2.6 MODULE 2 – SERIAL CLOCK & MEMORY TESTS

Description:

A series of commands will be sent to the UUT to validate the on board RS485 and RS422 communications interfaces. Measurements will be performed while ports are placed in a transmit mode to verify that proper signal levels are present. Controller Area Network (CAN) Bus measurements will be taken including termination resistor tests and CAN bus transceiver voltage tests to verify proper functional behavior. This module will only validate proper signal levels. Module 10 will check for valid CAN message content when tested on the VIPER/T. External MOOG Clock output will be verified, and memory tests on Data, Program and Circular Buffer RAM will be performed.

Refer to [Reference Drawings](#) when diagnosing connection paths.

Step 201

Description:

This step sends the "LOOPBACK 0 485" command to the SSP, and verifies the return message reads "UPASSED"

Connection Path is as follows:

See "UUT Power"
See "Boot Up"
See "Serial Comm"

Step 202

Description:

This step connects a 121 ohm resistor between UUT pins J2-44 and J2-40 and sends the "LOOPBACK 0 485" command. The DSO is used to measure the voltage on J2-44 using limits of UL 5.0 Vdc and LL 2.0 Vdc.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From W7 P3-44 (UUT J2-44)	to W7 P1B-9D
From ID J1B-9D	to ID A1J13.22
From ID A1P13.22	to ID P12-75 (S701-43)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-161 (S506-7)	to ID A1P9.40
From ID A1J9.40	to ID BUS 5

From ID BUS 5	to ID A1J8.45
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From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-40 (UUT J2-40)	to W7 P1B-9F
From ID J1B-9F	to ID A1J13.24
From ID A1P13.24	to ID P12-43 (S701-44)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-129 (S506-8)	to ID A1P9.30
From ID A1J9.30	to ID BUS 6
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From DSO-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From ID P19-1 (DSO-IN1)	to ID A1P17.1
From ID A1J17.1	to ID A1J6.1
From ID A1P6.1	to ID P10-177 (S504-1)
From ID P10-207 (S504-7)	to ID A1P6.49
From ID A1J6.49	to ID BUS 5
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8
From ID BUS 8	to ID A1J6.22
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to DSO-RTN

Step 203

Description:

This step connects a 121 ohm resistor between UUT pins J2-44 and J2-40 and sends the "LOOPBACK 0 485" command. The DSO is used to measure the voltage on J2-40 using limits of UL 2.5 Vdc and LL 0.5 Vdc.

Connection Path is as follows:

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See "UUT Power"
See "Serial Comm"

From W7 P3-44 (UUT J2-44)
From ID J1B-9D
From ID A1P13.22

to W7 P1B-9D
to ID A1J13.22
to ID P12-75 (S701-43)

From ID P12-76 (S701-1)
From ID A1J12.50
From ID A1P10.3
From ID P11-161 (S506-7)
From ID A1J9.40

to ID A1P12.50
to ID A1J10.3
to ID P11-194 (S506-1)
to ID A1P9.40
to ID BUS 5

From ID BUS 5
From ID A1P8.45
From ID P10-50 (S301-95)
From ID A1J8.25

to ID A1J8.45
to ID P10-148 (S301-96)
to ID A1P8.25
to ID R11.1

From W7 P3-40 (UUT J2-40)
From ID J1B-9F
From ID A1P13.24

to W7 P1B-9F
to ID A1J13.24
to ID P12-43 (S701-44)

From ID P12-44 (S701-2)
From ID A1J12.48
From ID A1P10.1
From ID P11-129 (S506-8)
From ID A1J9.30

to ID A1P12.48
to ID A1J10.1
to ID P11-162 (S506-2)
to ID A1P9.30
to ID BUS 6

From ID BUS 6
From ID A1P8.46
From ID P10-179 (S301-94)
From ID A1J8.23

to ID A1J8.46
to ID P10-145 (S301-93)
to ID A1P8.23
to ID R11.2

From DSO-RTN
From ID A1P6.11
From ID P10-102 (S301-25)
From ID A1J7.34

to ID A1J6.11
to ID P10-166 (S301-26)
to ID A1P7.34
to GROUND

From ID P19-1 (DSO-IN1)
From ID A1J17.1
From ID A1P6.1
From ID P10-144 (S504-8)
From ID A1J6.40

to ID A1P17.1
to ID A1J6.1
to ID P10-177 (S504-1)
to ID A1P6.40
to ID BUS 6

From W7 P2-B2 (UUT J1-B2)
From ID J1B-6B
From ID A1P12.11

to W7 P1B-6B
to ID A1J12.11
to ID P12-22 (S201-37)

From ID P12-16 (S201-1)
From ID A1J12.42
From ID A1P10.6
From ID P11-205 (S508-10)
From ID A1J9.2

to ID A1P12.42
to ID A1J10.6
to ID P11-203 (S508-1)
to ID A1P9.2
to ID BUS 8

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From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to DSO-RTN

Step 204

Description:

Compare the Serial Hi and Lo outputs and verify that the difference between Active Hi and Active Lo is equal to 2.25 ± 1.00 V. Hi is stored in TEMP1 and Lo in TEMP2 from previous tests (202 and 203).

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 205

Description:

This step sends the "LOOPBACK 1 422" command to the SSP, and verifies the return message reads "PASSED"

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 206

Description:

This step sends the "LOOPBACK 1 422" command to the SSP. The DSO is used to measure the voltage on J2-61 using limits of 3.50 ± 1.50 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-61 (UUT J2-61)	to W7 P1A-11F
From ID J1A-11F	to ID A1J14.30
From ID A1P14.30	to ID P13-59 (S202-25)

From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5

From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)

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From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8
From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to DSO-RTN
From DSO-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From ID P19-1 (DSO-IN1)	to ID A1P17.1
From ID A1J17.1	to ID A1J6.1
From ID A1P6.1	to ID P10-177 (S504-1)
From ID P10-207 (S504-7)	to ID A1P6.49
From ID A1J6.49	to ID BUS 5

Step 207

Description:

This step sends the "LOOPBACK 1 422" command to the SSP. The DSO is used to measure the voltage on J2-60 using limits of 1.50 ± 1.00 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-60 (UUT J2-60)	to W7 P1A-7E
From ID J1A-7E	to ID A1J14.21
From ID A1P14.21	to ID P13-90 (S202-26)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8
From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to DSO-RTN

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From DSO-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From ID P19-1 (DSO-IN1)	to ID A1P17.1
From ID A1J17.1	to ID A1J6.1
From ID A1P6.1	to ID P10-177 (S504-1)
From ID P10-144 (S504-8)	to ID A1P6.40
From ID A1J6.40	to ID BUS 6

Step 208

Description:

Compare the Serial Hi and Lo outputs and verify that the difference between Active Hi and Active Lo is equal to 2.25 ± 1.25 V. Hi is stored in TEMP1 and Lo in TEMP2 from previous tests (206 and 207).

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Step 209

Description:

This step sends the "LOOPBACK 2 422" command to the SSP, and verifies the return message reads "PASSED"

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Step 210

Description:

This step sends the "LOOPBACK 2 422" command to the SSP. The DSO is used to measure the voltage on J2-64 using limits of 3.50 ± 1.50 Vdc.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From W7 P3-64 (UUT J2-64)	to W7 P1A-7F
From ID J1A-7F	to ID A1J14.22
From ID A1P14.22	to ID P13-92 (S202-34)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J6.49

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From ID A1P6.49	to ID P10-207 (S504-7)
From ID P10-177 (S504-1)	to ID A1P6.1
From ID A1J6.1	to ID A1J17.1
From ID A1P17.1	to ID P19-1 (DSO-IN1)
From DSO-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8
From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to DSO-RTN

Step 211

Description:

This step sends the "LOOPBACK 2 422" command to the SSP. The DSO is used to measure the voltage on J2-43 using limits of 1.50 ± 1.00 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-43 (UUT J2-43)	to W7 P1A-8E
From ID J1A-8E	to ID A1J14.23
From ID A1P14.23	to ID P13-91 (S202-35)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J6.40
From ID A1P6.40	to ID P10-144 (S504-8)
From ID P10-177 (S504-1)	to ID A1P6.1
From ID A1J6.1	to ID A1J17.1
From ID A1P17.1	to ID P19-1 (DSO-IN1)
From DSO-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)

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From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8
From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to DSO-RTN

Step 212

Description:

Compare the Serial Hi and Lo outputs and verify that the difference between Active Hi and Active Lo is equal to 2.25 ± 1.25 V. Hi is stored in TEMP1 and Lo in TEMP2 from previous tests (210 and 211).

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 213

Description:

This step sends the "LOOPBACK 1 485" command to the SSP, and verifies the return message reads "PASSED"

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 214

Description:

This step sends the "LOOPBACK 1 485" command to the SSP. The DSO is used to measure the voltage on J2-72 using limits of 3.50 ± 1.50 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-72 (UUT J2-72)	to W7 P1A-11F
From ID J1A-11F	to ID A1J14.30
From ID A1P14.30	to ID P13-59 (S202-25)

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From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J6.49
From ID A1P6.49	to ID P10-207 (S504-7)
From ID P10-177 (S504-1)	to ID A1P6.1
From ID A1J6.1	to ID A1J17.1
From ID A1P17.1	to ID P19-1 (DSO-IN1)
From DSO-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8
From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to DSO-RTN

Step 215

Description:

This step sends the "LOOPBACK 1 485" command to the SSP. The DSO is used to measure the voltage on J2-68 using limits of 1.50 ± 1.00 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-68 (UUT J2-68)	to W7 P1A-7E
From ID J1A-7E	to ID A1J14.21
From ID A1P14.21	to ID P13-90 (S202-26)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J6.40

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From ID A1P6.40	to ID P10-144 (S504-8)
From ID P10-177 (S504-1)	to ID A1P6.1
From ID A1J6.1	to ID A1J17.1
From ID A1P17.1	to ID P19-1 (DSO-IN1)
From DSO-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8
From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to DSO-RTN

Step 216

Description:

Compare the Serial Hi and Lo outputs and verify that the difference between Active Hi and Active Lo is equal to 2.25 ± 1.25 V. Hi is stored in TEMP1 and Lo in TEMP2 from previous tests (214 and 215).

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 217

Description:

This step sends the "LOOPBACK 2 485" command to the SSP, and verifies the return message reads "PASSED"

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 218

Description:

This step sends the "LOOPBACK 2 485" command to the SSP. The DSO is used to measure the voltage on J2-45 using limits of 3.50 ± 1.50 Vdc.

Connection Path is as follows:

Date: 04 March 2016

See "UUT Power"
See "Serial Comm"

From W7 P3-45 (UUT J2-45)	to W7 P1A-7F
From ID J1A-7F	to ID A1J14.22
From ID A1P14.22	to ID P13-92 (S202-34)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J6.49
From ID A1P6.49	to ID P10-207 (S504-7)
From ID P10-177 (S504-1)	to ID A1P6.1
From ID A1J6.1	to ID A1J17.1
From ID A1P17.1	to ID P19-1 (DSO-IN1)
From DSO-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8
From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to DSO-RTN

Step 219

Description:

This step sends the "LOOPBACK 2 485" command to the SSP. The DSO is used to measure the voltage on J2-41 using limits of 1.50 ± 1.00 Vdc.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From W7 P3-41 (UUT J2-41)	to W7 P1A-8E
From ID J1A-8E	to ID A1J14.23
From ID A1P14.23	to ID P13-91 (S202-35)

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From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J6.40
From ID A1P6.40	to ID P10-144 (S504-8)
From ID P10-177 (S504-1)	to ID A1P6.1
From ID A1J6.1	to ID A1J17.1
From ID A1P17.1	to ID P19-1 (DSO-IN1)
From DSO-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B2 (UUT J1-B2)	to W7 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8
From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to DSO-RTN

Step 220

Description:

Compare the Serial Hi and Lo outputs and verify that the difference between Active Hi and Active Lo is equal to 2.25 ± 1.25 V. Hi is stored in TEMP1 and Lo in TEMP2 from previous tests (218 and 219).

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 221

Description:

Connect UUT pins J2-72 to J2-61 & J2-68 to J2-60. The DMM is used to measure the differential voltage between UUT pins J2-72 (HI) and J2-68 (LO). The measured voltage should equal 2.76 ± 0.14 V

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

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From W7 P3-72 (UUT J2-72)	to W7 P1A-11F
From ID J1A-11F	to ID A1J14.30
From ID A1P14.30	to ID P13-59 (S202-25)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P3-68 (UUT J2-68)	to W7 P1A-7E
From ID J1A-7E	to ID A1J14.21
From ID A1P14.21	to ID P13-90 (S202-26)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 222

Description:

Connect UUT pins J2-45 to J2-64 & J2-41 to J2-43. The DMM is used to measure the differential voltage between UUT pins J2-45 (HI) and J2-41 (LO). The measured voltage should equal 2.76 ± 0.14 V

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-45 (UUT J2-45)	to W7 P1A-7F
From ID J1A-7F	to ID A1J14.22
From ID A1P14.22	to ID P13-92 (S202-34)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1
From ID BUS 1	to ID A1J6.13

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From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P3-41 (UUT J2-41)	to W7 P1A-8E
From ID J1A-8E	to ID A1J14.23
From ID A1P14.23	to ID P13-91 (S202-35)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 223

Description:

CAN 0 BUS Test - Connect a 120 ± 15 ohm resistor between the UUT pins J2-67 and J2-35. The DMM is used to measure the UUT pins J2-67 (HI) to J2-35 (LO), and the measured resistance should be equal to 60 ± 10 ohms.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-67 (UUT J2-67)	to W7 P1A-6E
From ID J1A-6E	to ID A1J14.19
From ID A1P14.19	to ID P13-88 (S202-19)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-35 (UUT J2-35)	to W7 P1A-6F
From ID J1A-6F	to ID A1J14.20
From ID A1P14.20	to ID P13-24 (S202-20)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12

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From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From ID BUS 5	to ID A1J6.47
From ID A1P6.47	to ID P10-137 (S503-7)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID BUS 6	to ID A1J6.38
From ID A1P6.38	to ID P10-170 (S503-8)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 224

Description:

CAN 1 BUS Test - Connect a 120 ± 15 ohm resistor between the UUT pins J2-62 and J2-38. The DMM is used to measure the UUT pins J2-62 (HI) to J2-38 (LO), and the measured resistance should be equal to 60 ± 10 ohms.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-62 (UUT J2-62)	to W7 P1B-1A
From ID J1B-1A	to ID A1J12.21
From ID A1P12.21	to ID P12-61 (S202-21)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-38 (UUT J2-38)	to W7 P1B-5A
From ID J1B-5A	to ID A1J12.13
From ID A1P12.13	to ID P12-92 (S202-22)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12

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From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From ID BUS 5	to ID A1J6.47
From ID A1P6.47	to ID P10-137 (S503-7)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID BUS 6	to ID A1J6.38
From ID A1P6.38	to ID P10-170 (S503-8)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 225

Description:

Ground UUT pin J2-65 to remove the bus terminations. CAN 0 BUS Test - Connect a 120 ± 15 ohm resistor between the UUT pins J2-67 and J2-35. The DMM is used to measure the UUT pins J2-67 (HI) to J2-35 (LO), and the measured resistance should be equal to 120 ± 15 ohms.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-67 (UUT J2-67)	to W7 P1A-6E
From ID J1A-6E	to ID A1J14.19
From ID A1P14.19	to ID P13-88 (S202-19)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-35 (UUT J2-35)	to W7 P1A-6F
From ID J1A-6F	to ID A1J14.20
From ID A1P14.20	to ID P13-24 (S202-20)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12

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From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From ID BUS 5	to ID A1J6.47
From ID A1P6.47	to ID P10-137 (S503-7)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID BUS 6	to ID A1J6.38
From ID A1P6.38	to ID P10-170 (S503-8)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-65 (UUT J2-65)	to W7 P1B-14C
From ID J1B-14C	to ID A1J13.5
From ID A1P13.5	to ID P12-14 (S201-13)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8
From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to INSTR-RTN
From INSTR-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND

Step 226

Description:

Ground UUT pin J2-65 to remove the bus terminations. CAN 1 BUS Test - Connect a 120 ± 15 ohm resistor between the UUT pins J2-62 and J2-38. The DMM is used to measure the UUT pins J2-62 (HI) to J2-38 (LO), and the measured resistance should be equal to 120 ± 15 ohms.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-62 (UUT J2-62)	to W7 P1B-1A
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From ID J1B-1A	to ID A1J12.21
From ID A1P12.21	to ID P12-61 (S202-21)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-38 (UUT J2-38)	to W7 P1B-5A
From ID J1B-5A	to ID A1J12.13
From ID A1P12.13	to ID P12-92 (S202-22)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From ID BUS 5	to ID A1J6.47
From ID A1P6.47	to ID P10-137 (S503-7)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID BUS 6	to ID A1J6.38
From ID A1P6.38	to ID P10-170 (S503-8)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
From W7 P3-65 (UUT J2-65)	to W7 P1B-14C
From ID J1B-14C	to ID A1J13.5
From ID A1P13.5	to ID P12-14 (S201-13)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8
From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9

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From ID A1J6.9	to INSTR-RTN
From INSTR-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND

Step 227

Description:

Send the "CAN INIT" command to initialize both CAN ports, and verify the return message reads "PASSED".

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 228

Description:

Send the "CAN XMIT 0" command. The DSO is used to measure the UUT pins J2-67 (HI) to J1-B2 (LO), and the measured voltage should be equal to 3.50 ± 1.50 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-67 (UUT J2-67)	to W7 P1A-6E
From ID J1A-6E	to ID A1J14.19
From ID A1P14.19	to ID P13-88 (S202-19)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-35 (UUT J2-35)	to W7 P1A-6F
From ID J1A-6F	to ID A1J14.20
From ID A1P14.20	to ID P13-24 (S202-20)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J8.46

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From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1
From ID BUS 1	to ID A1J6.15
From ID A1P6.15	to ID P10-18 (S504-3)
From ID P10-177 (S504-1)	to ID A1P6.1
From ID A1J6.1	to ID A1J17.1
From ID A1P17.1	to ID P19-1 (DSO-IN1)
From W7 P3-11 (UUT J2-11)	to W7 P1A-12F
From ID J1A-12F	to ID A1J7.12
From ID A1P7.12	to ID P10-66 (S301-6)
From ID P10-33 (S301-5)	to ID A1P7.40
From ID A1J7.40	to ID A1J15.50
From ID A1P15.50	to ID P20.3 (DMM-LO)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From DSO-RTN	to ID A1J6.7
From ID A1P6.7	to ID P10-93 (S401-1)
From ID P10-254 (S401-3)	to ID A1P6.29
From ID A1J6.29	to ID BUS 2

Step 229

Description:

The DSO is used to measure the UUT pins J2-35 (HI) to J1-B2 (LO), and the measured voltage should be equal to 1.50 ± 1.00 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-67 (UUT J2-67)	to W7 P1A-6E
From ID J1A-6E	to ID A1J14.19
From ID A1P14.19	to ID P13-88 (S202-19)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5

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From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-35 (UUT J2-35)	to W7 P1A-6F
From ID J1A-6F	to ID A1J14.20
From ID A1P14.20	to ID P13-24 (S202-20)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1
From ID BUS 1	to ID A1J6.15
From ID A1P6.15	to ID P10-18 (S504-3)
From ID P10-177 (S504-1)	to ID A1P6.1
From ID A1J6.1	to ID A1J17.1
From ID A1P17.1	to ID P19-1 (DSO-IN1)
From W7 P3-11 (UUT J2-11)	to W7 P1A-12F
From ID J1A-12F	to ID A1J7.12
From ID A1P7.12	to ID P10-66 (S301-6)
From ID P10-33 (S301-5)	to ID A1P7.40
From ID A1J7.40	to ID A1J15.50
From ID A1P15.50	to ID P20.3 (DMM-LO)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From DSO-RTN	to ID A1J6.7
From ID A1P6.7	to ID P10-93 (S401-1)
From ID P10-254 (S401-3)	to ID A1P6.29
From ID A1J6.29	to ID BUS 2

Step 230

Description:

Verify the difference between the active high state on J2-67 and the active low state on J2-35 is equal to 2.25 ± 1.00 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

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Step 231

Description:

Send the "CAN INIT" command to initialize both CAN ports, and verify the return message reads "PASSED".

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 232

Description:

Send the "CAN XMIT 1" command. The DSO is used to measure the UUT pins J2-62 (HI) to J1-B2 (LO), and the measured voltage should be equal to 3.50 ± 1.50 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-62 (UUT J2-62)	to W7 P1B-1A
From ID J1B-1A	to ID A1J12.21
From ID A1P12.21	to ID P12-61 (S202-21)

From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1

From ID BUS 1	to ID A1J6.15
From ID A1P6.15	to ID P10-18 (S504-3)
From ID P10-177 (S504-1)	to ID A1P6.1
From ID A1J6.1	to ID A1J17.1
From ID A1P17.1	to ID P19-1 (DSO-IN1)

From DSO-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND

Step 233

Description:

The DSO is used to measure the UUT pins J2-38 (HI) to J1-B2 (LO), and the measured voltage should be equal to 1.50 ± 1.00 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-38 (UUT J2-38)	to W7 P1B-5A
From ID J1B-5A	to ID A1J12.13

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From ID A1P12.13	to ID P12-92 (S202-22)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1
From ID BUS 1	to ID A1J6.15
From ID A1P6.15	to ID P10-18 (S504-3)
From ID P10-177 (S504-1)	to ID A1P6.1
From ID A1J6.1	to ID A1J17.1
From ID A1P17.1	to ID P19-1 (DSO-IN1)
From DSO-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND

Step 234

Description:

Verify the difference between the active high state on J2-62 and the active low state on J2-38 is equal to 2.25 ± 1.00 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 235

Description:

Send the CMC ON Command to verify the performance of the SSP clock crystal. Use the CT to measure the frequency output from UUT pins J2-58 and J2-39. The measured frequency should be $100.174 \pm .02$ kHz.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-58 (UUT J2-58)	to W7 P1A-12E
From ID J1A-12E	to ID A1J14.31
From ID A1P14.31	to ID P13-30 (S202-41)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49

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From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P3-39 (UUT J2-39)	to W7 P1A-7F
From ID J1A-8F	to ID A1J14.24
From ID A1P14.24	to ID P13-62 (S202-42)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 236

Description:

Send the "DMRT" command to test the data memory RAM internal to the processor, and verify the respond message from the SSP is "PASSED".

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 237

Description:

Send the "PMRT" command to test the external RAM designated as program memory, and verify the respond message from the SSP is "PASSED".

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 238

Description:

Send the "CBRT" command to test the external RAM designated as circular buffer, and verify the respond message from the SSP is "PASSED".

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

2.7 MODULE 3 – ANALOG OUTPUT TESTS

Description:

A series of tests will be performed to verify the correct input offset voltage, the maximum positive and negative voltage, and the performance of the Digital to Analog (D/A) converter and associated amplifiers, buffers and interface interconnections. Tests for accuracy sending multiple voltages to each of the analog outputs will be performed. In addition, slew rate measurements for each of the 4 outputs will be performed. Finally, a test of the analog summing amplifier will be performed to verify proper built in test functionality.

Refer to [Reference Drawings](#) when diagnosing connection paths.

Step 301

Description:

Send the "ANAOUT" command to output 6.665 Vdc from the D/A converter to the SSP pins J1-A14/J1-B14. The DMM will measure the differential voltage. The measured voltage should be $6.666 \pm .148$ Vdc.

Connection Path is as follows:

See "UUT Power"

See "Boot Up"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID BUS 2	to ID A1J6.23
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From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 302

Description:

Send the "ANAOUT" command to output -6.665 Vdc from the D/A converter to the SSP pins J1-A14/J1-B14. The DMM will measure the differential voltage. The measured voltage should be $-6.666 \pm .148$ Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 303

Description:

Send the "ANAOUT" command to output 9.995 Vdc from the D/A converter to the SSP pins J1-A14/J1-B14. The DMM will measure the differential voltage. The measured voltage should be $9.995 \pm .148$ Vdc.

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Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 304

Description:

Send the "ANAOUT" command to output -10.000 Vdc from the D/A converter to the SSP pins J1-A14/J1-B14. The DMM will measure the differential voltage. The measured voltage should be -10.000 ± .215 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6

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From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 305

Description:

Send the "ANAOUT" command to output 0.000 Vdc from the D/A converter to the SSP pins J1-A14/J1-B14. The DMM will measure the differential voltage. The measured voltage should be 0.000 ± .080 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A

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From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 306

Description:

Send the "ANAOUT" command to output 6.665 Vdc from the D/A converter to the SSP pins J1-B15/J1-C15. The DMM will measure the differential voltage. The measured voltage should be 6.665 ± .148 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B15 (UUT J1-B15)	to W7 P1B-8B
From ID J1B-8B	to ID A1J12.5
From ID A1P12.5	to ID P12-17 (S201-23)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-C15 (UUT J1-C15)	to W7 P1B-8C
From ID J1B-8C	to ID A1J12.6
From ID A1P12.6	to ID P12-81 (S201-24)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)

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From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 307

Description:

Send the "ANAOUT" command to output -6.665 Vdc from the D/A converter to the SSP pins J1-B15/J1-C15. The DMM will measure the differential voltage. The measured voltage should be $-6.665 \pm .148$ Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B15 (UUT J1-B15)	to W7 P1B-8B
From ID J1B-8B	to ID A1J12.5
From ID A1P12.5	to ID P12-17 (S201-23)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

From W7 P2-C15 (UUT J1-C15)	to W7 P1B-8C
From ID J1B-8C	to ID A1J12.6
From ID A1P12.6	to ID P12-81 (S201-24)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 308

Description:

Send the "ANAOUT" command to output 9.995 Vdc from the D/A converter to the SSP pins J1-B15/J1-C15. The DMM will measure the differential voltage. The measured voltage should be $9.995 \pm .215$ Vdc.

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Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B15 (UUT J1-B15)	to W7 P1B-8B
From ID J1B-8B	to ID A1J12.5
From ID A1P12.5	to ID P12-17 (S201-23)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

From W7 P2-C15 (UUT J1-C15)	to W7 P1B-8C
From ID J1B-8C	to ID A1J12.6
From ID A1P12.6	to ID P12-81 (S201-24)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 309

Description:

Send the "ANAOUT" command to output -10.000 Vdc from the D/A converter to the SSP pins J1-B15/J1-C15. The DMM will measure the differential voltage. The measured voltage should be -10.000 ± .215 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B15 (UUT J1-B15)	to W7 P1B-8B
From ID J1B-8B	to ID A1J12.5
From ID A1P12.5	to ID P12-17 (S201-23)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6

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From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-C15 (UUT J1-C15)	to W7 P1B-8C
From ID J1B-8C	to ID A1J12.6
From ID A1P12.6	to ID P12-81 (S201-24)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 310

Description:

Send the "ANAOUT" command to output 0.000 Vdc from the D/A converter to the SSP pins J1-B15/J1-C15. The DMM will measure the differential voltage. The measured voltage should be 0.000 ± .080 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B15 (UUT J1-B15)	to W7 P1B-8B
From ID J1B-8B	to ID A1J12.5
From ID A1P12.5	to ID P12-17 (S201-23)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-C15 (UUT J1-C15)	to W7 P1B-8C

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From ID J1B-8C	to ID A1J12.6
From ID A1P12.6	to ID P12-81 (S201-24)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 311

Description:

Send the "ANAOUT" command to output 6.665 Vdc from the D/A converter to the SSP pins J1-A16/J1-B16. The DMM will measure the differential voltage. The measured voltage should be $6.666 \pm .148$ Vdc.

Connection Path is as follows:

See "UT Power"

See "Serial Comm"

From W7 P2-A16 (UUT J1-A16)	to W7 P1A-2D
From ID J1A-2D	to ID A1J14.8
From ID A1P14.9	to ID P13-50 (S201-25)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B16 (UUT J1-A16)	to W7 P1A-1E
From ID J1A-1E	to ID A1J14.9
From ID A1P14.9	to ID P13-17 (S201-26)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)

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From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 312

Description:

Send the "ANAOUT" command to output -6.665 Vdc from the D/A converter to the SSP pins J1-A16/J1-B16. The DMM will measure the differential voltage. The measured voltage should be $-6.666 \pm .148$ Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A16 (UUT J1-A16)	to W7 P1A-2D
From ID J1A-2D	to ID A1J14.8
From ID A1P14.9	to ID P13-50 (S201-25)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

From W7 P2-B16 (UUT J1-A16)	to W7 P1A-1E
From ID J1A-1E	to ID A1J14.9
From ID A1P14.9	to ID P13-17 (S201-26)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 313

Description:

Send the "ANAOUT" command to output 9.995 Vdc from the D/A converter to the SSP pins J1-A16/J1-B16. The DMM will measure the differential voltage. The measured voltage should be $9.995 \pm .215$ Vdc.

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Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A16 (UUT J1-A16)	to W7 P1A-2D
From ID J1A-2D	to ID A1J14.8
From ID A1P14.9	to ID P13-50 (S201-25)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

From W7 P2-B16 (UUT J1-A16)	to W7 P1A-1E
From ID J1A-1E	to ID A1J14.9
From ID A1P14.9	to ID P13-17 (S201-26)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 314

Description:

Send the "ANAOUT" command to output -10.000 Vdc from the D/A converter to the SSP pins J1-A16/J1-B16. The DMM will measure the differential voltage. The measured voltage should be -10.000 ± .215 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A16 (UUT J1-A16)	to W7 P1A-2D
From ID J1A-2D	to ID A1J14.8
From ID A1P14.9	to ID P13-50 (S201-25)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6

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From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B16 (UUT J1-A16)	to W7 P1A-1E
From ID J1A-1E	to ID A1J14.9
From ID A1P14.9	to ID P13-17 (S201-26)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 315

Description:

Send the "ANAOUT" command to output 0.000 Vdc from the D/A converter to the SSP pins J1-A16/J1-B16. The DMM will measure the differential voltage. The measured voltage should be 0.000 ± .080 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A16 (UUT J1-A16)	to W7 P1A-2D
From ID J1A-2D	to ID A1J14.8
From ID A1P14.9	to ID P13-50 (S201-25)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-B16 (UUT J1-A16)	to W7 P1A-1E

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From ID J1A-1E	to ID A1J14.9
From ID A1P14.9	to ID P13-17 (S201-26)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 316

Description:

Send the "ANAOUT" command to output 6.665 Vdc from the D/A converter to the SSP pins J1-C16/J1-A17. The DMM will measure the differential voltage. The measured voltage should be $6.666 \pm .148$ Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C16 (UUT J1-C16)	to W7 P1A-2E
From ID J1A-2E	to ID A1J14.10
From ID A1P14.10	to ID P13-83 (S201-27)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-A17 (UUT J1-A17)	to W7 P1A-1F
From ID J1A-1F	to ID A1J14.11
From ID A1P14.11	to ID P13-51 (S201-28)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)

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From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 317

Description:

Send the "ANAOUT" command to output -6.665 Vdc from the D/A converter to the SSP pins J1-C16/J1-A17. The DMM will measure the differential voltage. The measured voltage should be $-6.666 \pm .148$ Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C16 (UUT J1-C16)	to W7 P1A-2E
From ID J1A-2E	to ID A1J14.10
From ID A1P14.10	to ID P13-83 (S201-27)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

From W7 P2-A17 (UUT J1-A17)	to W7 P1A-1F
From ID J1A-1F	to ID A1J14.11
From ID A1P14.11	to ID P13-51 (S201-28)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 318

Description:

Send the "ANAOUT" command to output 9.995 Vdc from the D/A converter to the SSP pins J1-C16/J1-A17. The DMM will measure the differential voltage. The measured voltage should be $9.995 \pm .215$ Vdc.

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Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C16 (UUT J1-C16)	to W7 P1A-2E
From ID J1A-2E	to ID A1J14.10
From ID A1P14.10	to ID P13-83 (S201-27)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

From W7 P2-A17 (UUT J1-A17)	to W7 P1A-1F
From ID J1A-1F	to ID A1J14.11
From ID A1P14.11	to ID P13-51 (S201-28)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 319

Description:

Send the "ANAOUT" command to output -10.000 Vdc from the D/A converter to the SSP pins J1-C16/J1-A17. The DMM will measure the differential voltage. The measured voltage should be -10.000 ± .215 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C16 (UUT J1-C16)	to W7 P1A-2E
From ID J1A-2E	to ID A1J14.10
From ID A1P14.10	to ID P13-83 (S201-27)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6

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From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-A17 (UUT J1-A17)	to W7 P1A-1F
From ID J1A-1F	to ID A1J14.11
From ID A1P14.11	to ID P13-51 (S201-28)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 320

Description:

Send the "ANAOUT" command to output 0.000 Vdc from the D/A converter to the SSP pins J1-C16/J1-A17. The DMM will measure the differential voltage. The measured voltage should be 0.000 ± .080 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C16 (UUT J1-C16)	to W7 P1A-2E
From ID J1A-2E	to ID A1J14.10
From ID A1P14.10	to ID P13-83 (S201-27)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From W7 P2-A17 (UUT J1-A17)	to W7 P1A-1F

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From ID J1A-1F	to ID A1J14.11
From ID A1P14.11	to ID P13-51 (S201-28)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 321

Description:

Send the "ANAOUT" command to set the SSP output J1-A14/J1-B14 to -10 Vdc. Set the CT Triggers to start at -8V and stop at +8V. Send the "ANAOUT" command to set the SSP output J1-A14/J1-B14 to +10 Vdc. The Counter Timer will measure the rise time from 8 to +8 volts. The rise time should be 220 ± 40 us.

Connection Path is as follows:

See "UT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-173 (S508-9)	to ID A1P9.12
From ID A1J9.12	to ID BUS 7
From ID BUS 7	to ID A1J7.49
From ID A1P7.49	to ID P10-68 (S501-9)
From ID P10-162 (S501-2)	to ID A1P6.8
From ID A1J6.8	to ID A1J21.1
From ID A1P21.1	to ID P19-18 (CT-IN1)
From ID BUS 7	to ID A1J6.26
From ID A1P6.26	to ID P10-232 (S502-9)
From ID P10-71 (S502-2)	to ID A1P6.10
From ID A1J6.10	to ID A1J22.1
From ID A1P22.1	to ID P19-19 (CT-IN2)
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

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From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8
From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to INSTR-RTN

Step 322

Description:

Send the "ANAOUT" command to set the SSP output J1-A14/J1-B14 to +10 Vdc. Set the CT Triggers to start at +8V and stop at -8V. Send the "ANAOUT" command to set the SSP output J1-A14/J1-B14 to -10 Vdc. The Counter Timer will measure the fall time from +8 to -8 volts. The fall time should be 220 ± 40 us.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-173 (S508-9)	to ID A1P9.12
From ID A1J9.12	to ID BUS 7
From ID BUS 7	to ID A1J7.49
From ID A1P7.49	to ID P10-68 (S501-9)
From ID P10-162 (S501-2)	to ID A1P6.8
From ID A1J6.8	to ID A1J21.1
From ID A1P21.1	to ID P19-18 (CT-IN1)
From ID BUS 7	to ID A1J6.26
From ID A1P6.26	to ID P10-232 (S502-9)
From ID P10-71 (S502-2)	to ID A1P6.10
From ID A1J6.10	to ID A1J22.1
From ID A1P22.1	to ID P19-19 (CT-IN2)
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-205 (S508-10)	to ID A1P9.2

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From ID A1J9.2	to ID BUS 8
From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to INSTR-RTN

Step 323

Description:

Send the "ANAOUT" command to set the SSP output J1-B15/J1-C15 to -10 Vdc. Set the CT Triggers to start at -8V and stop at +8V. Send the "ANAOUT" command to set the SSP output J1-B15/J1-C15 to +10 Vdc. The Counter Timer will measure the rise time from -8 to +8 volts. The rise time should be 220 ± 40 us.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B15 (UUT J1-B15)	to W7 P1B-8B
From ID J1B-8B	to ID A1J12.5
From ID A1P12.5	to ID P12-17 (S201-23)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-173 (S508-9)	to ID A1P9.12
From ID A1J9.12	to ID BUS 7

From ID BUS 7	to ID A1J7.49
From ID A1P7.49	to ID P10-68 (S501-9)
From ID P10-162 (S501-2)	to ID A1P6.8
From ID A1J6.8	to ID A1J21.1
From ID A1P21.1	to ID P19-18 (CT-IN1)

From ID BUS 7	to ID A1J6.26
From ID A1P6.26	to ID P10-232 (S502-9)
From ID P10-71 (S502-2)	to ID A1P6.10
From ID A1J6.10	to ID A1J22.1
From ID A1P22.1	to ID P19-19 (CT-IN2)

From W7 P2-C15 (UUT J1-C15)	to W7 P1B-8C
From ID J1B-8C	to ID A1J12.6
From ID A1P12.6	to ID P12-81 (S201-24)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8

From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)

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From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to INSTR-RTN

Step 324

Description:

Send the "ANAOUT" command to set the SSP output J1-B15/J1-C15 to +10 Vdc. Set the CT Triggers to start at +8V and stop at -8V. Send the "ANAOUT" command to set the SSP output J1-B15/J1-C15 to -10 Vdc. The Counter Timer will measure the fall time from +8 to -8 volts. The fall time should be 220 ± 40 us.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B15 (UUT J1-B15)	to W7 P1B-8B
From ID J1B-8B	to ID A1J12.5
From ID A1P12.5	to ID P12-17 (S201-23)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-173 (S508-9)	to ID A1P9.12
From ID A1J9.12	to ID BUS 7

From ID BUS 7	to ID A1J7.49
From ID A1P7.49	to ID P10-68 (S501-9)
From ID P10-162 (S501-2)	to ID A1P6.8
From ID A1J6.8	to ID A1J21.1
From ID A1P21.1	to ID P19-18 (CT-IN1)

From ID BUS 7	to ID A1J6.26
From ID A1P6.26	to ID P10-232 (S502-9)
From ID P10-71 (S502-2)	to ID A1P6.10
From ID A1J6.10	to ID A1J22.1
From ID A1P22.1	to ID P19-19 (CT-IN2)

From W7 P2-C15 (UUT J1-C15)	to W7 P1B-8C
From ID J1B-8C	to ID A1J12.6
From ID A1P12.6	to ID P12-81 (S201-24)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8

From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to INSTR-RTN

Date: 04 March 2016

Step 325

Description:

Send the "ANAOUT" command to set the SSP output J1-A16/J1-B16 to -10 Vdc. Set the CT Triggers to start at -8V and stop at +8V. Send the "ANAOUT" command to set the SSP output J1-A16/J1-B16 to +10 Vdc. The Counter Timer will measure the rise time from -8 to +8 volts. The rise time should be 220 ± 40 us.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A16 (UUT J1-A16)	to W7 P1A-2D
From ID J1A-2D	to ID A1J14.8
From ID A1P14.9	to ID P13-50 (S201-25)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-173 (S508-9)	to ID A1P9.12
From ID A1J9.12	to ID BUS 7

From ID BUS 7	to ID A1J7.49
From ID A1P7.49	to ID P10-68 (S501-9)
From ID P10-162 (S501-2)	to ID A1P6.8
From ID A1J6.8	to ID A1J21.1
From ID A1P21.1	to ID P19-18 (CT-IN1)

From ID BUS 7	to ID A1J6.26
From ID A1P6.26	to ID P10-232 (S502-9)
From ID P10-71 (S502-2)	to ID A1P6.10
From ID A1J6.10	to ID A1J22.1
From ID A1P22.1	to ID P19-19 (CT-IN2)

From W7 P2-B16 (UUT J1-A16)	to W7 P1A-1E
From ID J1A-1E	to ID A1J14.9
From ID A1P14.9	to ID P13-17 (S201-26)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8

From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to INSTR-RTN

Step 326

Description:

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Send the "ANAOUT" command to set the SSP output J1-A16/J1-B16 to +10 Vdc. Set the CT Triggers to start at +8V and stop at -8V. Send the "ANAOUT" command to set the SSP output J1-A16/J1-B16 to -10 Vdc. The Counter Timer will measure the fall time from +8 to -8 volts. The fall time should be 220 ± 40 us.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A16 (UUT J1-A16)	to W7 P1A-2D
From ID J1A-2D	to ID A1J14.8
From ID A1P14.9	to ID P13-50 (S201-25)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-173 (S508-9)	to ID A1P9.12
From ID A1J9.12	to ID BUS 7

From ID BUS 7	to ID A1J7.49
From ID A1P7.49	to ID P10-68 (S501-9)
From ID P10-162 (S501-2)	to ID A1P6.8
From ID A1J6.8	to ID A1J21.1
From ID A1P21.1	to ID P19-18 (CT-IN1)

From ID BUS 7	to ID A1J6.26
From ID A1P6.26	to ID P10-232 (S502-9)
From ID P10-71 (S502-2)	to ID A1P6.10
From ID A1J6.10	to ID A1J22.1
From ID A1P22.1	to ID P19-19 (CT-IN2)

From W7 P2-B16 (UUT J1-A16)	to W7 P1A-1E
From ID J1A-1E	to ID A1J14.9
From ID A1P14.9	to ID P13-17 (S201-26)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8

From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to INSTR-RTN

Step 327

Description:

Send the "ANAOUT" command to set the SSP output J1-C16/J1-A17 to -10 Vdc. Set the CT Triggers to start at -8V and stop at +8V. Send the "ANAOUT" command to set the SSP output J1-C16/J1-A17 to +10 Vdc. The

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Counter Timer will measure the rise time from -8 to +8 volts. The rise time should be 220 ± 40 us.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C16 (UUT J1-C16)	to W7 P1A-2E
From ID J1A-2E	to ID A1J14.10
From ID A1P14.10	to ID P13-83 (S201-27)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-173 (S508-9)	to ID A1P9.12
From ID A1J9.12	to ID BUS 7

From ID BUS 7	to ID A1J7.49
From ID A1P7.49	to ID P10-68 (S501-9)
From ID P10-162 (S501-2)	to ID A1P6.8
From ID A1J6.8	to ID A1J21.1
From ID A1P21.1	to ID P19-18 (CT-IN1)

From ID BUS 7	to ID A1J6.26
From ID A1P6.26	to ID P10-232 (S502-9)
From ID P10-71 (S502-2)	to ID A1P6.10
From ID A1J6.10	to ID A1J22.1
From ID A1P22.1	to ID P19-19 (CT-IN2)

From W7 P2-A17 (UUT J1-A17)	to W7 P1A-1F
From ID J1A-1F	to ID A1J14.11
From ID A1P14.11	to ID P13-51 (S201-28)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8

From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to INSTR-RTN

Step 328

Description:

Send the "ANAOUT" command to set the SSP output J1-C16/J1-A17 to +10 Vdc. Set the CT Triggers to start at +8V and stop at -8V. Send the "ANAOUT" command to set the SSP output J1-C16/J1-A17 to -10 Vdc. The Counter Timer will measure the fall time from +8 to -8 volts. The fall time should be 220 ± 40 us.

Connection Path is as follows:

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See "UUT Power"
See "Serial Comm"

From W7 P2-C16 (UUT J1-C16)	to W7 P1A-2E
From ID J1A-2E	to ID A1J14.10
From ID A1P14.10	to ID P13-83 (S201-27)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-173 (S508-9)	to ID A1P9.12
From ID A1J9.12	to ID BUS 7

From ID BUS 7	to ID A1J7.49
From ID A1P7.49	to ID P10-68 (S501-9)
From ID P10-162 (S501-2)	to ID A1P6.8
From ID A1J6.8	to ID A1J21.1
From ID A1P21.1	to ID P19-18 (CT-IN1)

From ID BUS 7	to ID A1J6.26
From ID A1P6.26	to ID P10-232 (S502-9)
From ID P10-71 (S502-2)	to ID A1P6.10
From ID A1J6.10	to ID A1J22.1
From ID A1P22.1	to ID P19-19 (CT-IN2)

From W7 P2-A17 (UUT J1-A17)	to W7 P1A-1F
From ID J1A-1F	to ID A1J14.11
From ID A1P14.11	to ID P13-51 (S201-28)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-205 (S508-10)	to ID A1P9.2
From ID A1J9.2	to ID BUS 8

From ID BUS 8	to ID A1J6.22
From ID A1P6.22	to ID P10-253 (S402-5)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to INSTR-RTN

Step 329

Description:

Send the "ANAOUT" command to set all analog outputs to zero, and send the "ANADATA 16" command to request for the SSP to read the analog output voltage. The output voltage should be 0.00 ± 0.05 Vdc.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Step 330

Description:

Send the "ANAOUT" command to set SSP output J1-A14/J1-B14 to 5Vdc with the other analog outputs at 0 Vdc, then send the "ANADATA 16" command to request for the SSP to read the analog output voltage. The output voltage should be -1.24 ± 0.05 V.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 331

Description:

Send the "ANAOUT" command to set SSP output J1-B15/J1-C15 to 5Vdc with the other analog outputs at 0 Vdc, then send the "ANADATA 16" command to request for the SSP to read the analog output voltage. The output voltage should be -1.24 ± 0.05 V.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 332

Description:

Send the "ANAOUT" command to set SSP output J1-A16/J1-B16 to 5Vdc with the other analog outputs at 0 Vdc, then send the "ANADATA 16" command to request for the SSP to read the analog output voltage. The output voltage should be -1.24 ± 0.05 V.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 333

Description:

Send the "ANAOUT" command to set SSP output J1-C16/J1-A17 to 5Vdc with the other analog outputs at 0 Vdc, then send the "ANADATA 16" command to request for the SSP to read the analog output voltage. The output voltage should be -1.24 ± 0.05 V.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 334

Description:

Send the "ANAOUT" command to set all analog outputs to 5Vdc, then send the "ANADATA 16" command to request for the SSP to read the analog output voltage. The output voltage should be -4.98 ± 0.20 V.

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Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 335

Description:

Send the "ANAOOUT" command to set all analog outputs to 0Vdc, then send the "ANABIT ON" command to turn on ANALOG_BIT. Send the "ANADATA 16" command to request for the SSP to read the analog output voltage. The output voltage should be -4.05 ± 0.20 V.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

2.8 MODULE 4 – DISCRETE OUTPUT TESTS

Description:

Tests within this group will verify the functionality of the discrete output interfaces. 2 types of outputs are provided in the UUT: 1) Source and Sink and 2) Sink only. There are 6 discrete outputs that are source/sink and 1 output which is sink only. During activation, output voltage will be verified in both the source and sink states to verify output voltage in range. In addition, the rise time for each output will be measured to verify the operation of the output R/C filters.

Refer to [Reference Drawings](#) when diagnosing connection paths.

Step 401

Description:

Send the "DISOUT H" command to set the SSP pin J1-A30 to a high state. The DMM is used to measure the voltage output from the UUT pins J1-A30 to J1-B2. The measured voltage should be 12.9 ± 0.2 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Boot Up"

See "Serial Comm"

From ID P20-3 (DMM-LO)

to ID A1P15.50

From ID A1J15.50

to ID A1J7.38

From ID A1P7.38

to ID P10-130 (S301-23)

From ID P10-229 (S301-24)

to ID A1P7.36

From ID A1J7.36

to GROUND

From ID BUS 6

to ID A1J8.40

From ID A1P8.40

to ID P10-175 (S301-81)

From ID P10-48 (S301-82)

to ID A1P8.17

From ID A1J8.17

to ID R6.1

From ID R6.2

to +15V

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From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 402

Description:

Send the "DISOUT L" command to set the SSP pin J1-A30 to a low state. The DMM is used to measure the voltage output from the UUT pins J1-A30 to J1-B2. The measured voltage should be 2.00 ± 0.2 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)

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From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 403

Description:

Send the "DISOUT H" command to set the SSP pin J1-B30 to a high state. The DMM is used to measure the voltage output from the UUT pins J1-B30 to J1-B2. The measured voltage should be 12.9 ± 0.2 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V

From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6

From W7 P2-B30 (UUT J1-A30)	to W7 P1B-13B
From ID J1B-13B	to ID A1J13.4
From ID A1P13.4	to ID P12-13 (S201-8)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8

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From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 404

Description:

Send the "DISOUT L" command to set the SSP pin J1-B30 to a low state. The DMM is used to measure the voltage output from the UUT pins J1-B30 to J1-B2. The measured voltage should be 2.00 ± 0.2 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V

From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6

From W7 P2-B30 (UUT J1-A30)	to W7 P1B-13B
From ID J1B-13B	to ID A1J13.4
From ID A1P13.4	to ID P12-13 (S201-8)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 405

Description:

Date: 04 March 2016

Send the "DISOUT H" command to set the SSP pin J1-B31 to a high state. The DMM is used to measure the voltage output from the UUT pins J1-B31 to J1-B2. The measured voltage should be 12.9 ± 0.2 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V

From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6

From W7 P2-B31 (UUT J1-B31)	to W7 P1A-3E
From ID J1A-3E	to ID A1J14.13
From ID A1P14.13	to ID P13-19 (S201-34)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 406

Description:

Send the "DISOUT L" command to set the SSP pin J1-B31 to a low state. The DMM is used to measure the voltage output from the UUT pins J1-B31 to J1-B2. The measured voltage should be 2.00 ± 0.2 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P20-3 (DMM-LO)	to ID A1P15.50
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From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From W7 P2-B31 (UUT J1-B31)	to W7 P1A-3E
From ID J1A-3E	to ID A1J14.13
From ID A1P14.13	to ID P13-19 (S201-34)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 407

Description:

Send the "DISOUT H" command to set the SSP pin J1-A35 to a high state. The DMM is used to measure the voltage output from the UUT pins J1-A35 to J1-B2. The measured voltage should be 12.9 ± 0.2 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

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From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From W7 P2-A35 (UUT J1-A35)	to W7 P1B-7C
From ID J1B-7C	to ID A1J12.9
From ID A1P12.9	to ID P12-19 (S201-31)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 408

Description:

Send the "DISOUT L" command to set the SSP pin J1-A35 to a low state. The DMM is used to measure the voltage output from the UUT pins J1-A35 to J1-B2. The measured voltage should be 2.00 ± 0.2 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)

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From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From W7 P2-A35 (UUT J1-A35)	to W7 P1B-7C
From ID J1B-7C	to ID A1J12.9
From ID A1P12.9	to ID P12-19 (S201-31)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 409

Description:

Send the "DISOUT H" command to set the SSP pin J1-B35 to a high state. The DMM is used to measure the voltage output from the UUT pins J1-B35 to J1-B2. The measured voltage should be 12.9 ± 0.2 Vdc.

Connection Path is as follows:

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See "UUT Power"
See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From W7 P2-B35 (UUT J1-B35)	to W7 P1B-6A
From ID J1B-6A	to ID A1J12.10
From ID A1P12.10	to ID P12-83 (S201-32)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Date: 04 March 2016

Step 410

Description:

Send the "DISOUT L" command to set the SSP pin J1-B35 to a low state. The DMM is used to measure the voltage output from the UUT pins J1-B35 to J1-B2. The measured voltage should be 2.00 ± 0.2 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From W7 P2-B35 (UUT J1-B35)	to W7 P1B-6A
From ID J1B-6A	to ID A1J12.10
From ID A1P12.10	to ID P12-83 (S201-32)

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From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 411

Description:

Send the "DISOUT H" command to set the SSP pin J2-20 to a high state. The DMM is used to measure the voltage output from the UUT pins J2-20 to J1-B2. The measured voltage should be 12.9 ± 0.2 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V

From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6

From W7 P3-20 (UUT J2-20)	to W7 P1A-11E
From ID J1A-11E	to ID A1J14.29
From ID A1P14.29	to ID P13-82 (S201-36)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 412

Description:

Date: 04 March 2016

Send the "DISOUT L" command to set the SSP pin J2-20 to a low state. The DMM is used to measure the voltage output from the UUT pins J2-20 to J1-B2. The measured voltage should be 2.00 ± 0.2 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V

From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6

From W7 P3-20 (UUT J2-20)	to W7 P1A-11E
From ID J1A-11E	to ID A1J14.29
From ID A1P14.29	to ID P13-82 (S201-36)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 413

Description:

Send the "DISOUT H" command to set the SSP pin J2-7 to a high state. The DMM is used to measure the voltage output from the UUT pins J2-7 to J1-B2. The measured voltage should be 7.5 ± 0.75 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P20-3 (DMM-LO)	to ID A1P15.50
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From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From W7 P3-7 (UUT J2-7)	to W7 P1A-3F
From ID J1A-3F	to ID A1J14.14
From ID A1P14.14	to ID P13-18 (S201-35)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 414

Description:

Send the "DISOUT L" command to set the SSP pin J2-7 to a low state. The DMM is used to measure the voltage output from the UUT pins J2-7 to J1-B2. The measured voltage should be LT 1.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1

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From ID R6.2	to +15V
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From W7 P3-7 (UUT J2-7)	to W7 P1A-3F
From ID J1A-3F	to ID A1J14.14
From ID A1P14.14	to ID P13-18 (S201-35)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 415

Description:

Send the "DISOUT L" command to set the SSP pin J1-A30 to a low state. Set the CT triggers to 3.1 and 11.9 volts. Send the "DISOUT H" command to set the SSP pin J1-A30 to a high state. The Counter Timer will measure the rise time from 3.1 to 11.9 V. The expected rise time is 12.7 ± 5.3 us.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND

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From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-129 (S501-8)	to ID A1P7.47
From ID A1J7.47	to ID BUS 6
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-5 (S502-8)	to ID A1P6.36
From ID A1J6.36	to ID BUS 6
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 416

Description:

Send the "DISOUT L" command to set the SSP pin J1-B30 to a low state. Set the CT triggers to 3.1 and 11.9 volts. Send the "DISOUT H" command to set the SSP pin J1-B30 to a high state. The Counter Timer will measure the rise time from 3.1 to 11.9 V. The expected rise time is 12.7 ± 5.3 us.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)

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From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-129 (S501-8)	to ID A1P7.47
From ID A1J7.47	to ID BUS 6
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-5 (S502-8)	to ID A1P6.36
From ID A1J6.36	to ID BUS 6
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B30 (UUT J1-A30)	to W7 P1B-13B
From ID J1B-13B	to ID A1J13.4
From ID A1P13.4	to ID P12-13 (S201-8)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 417

Description:

Send the "DISOUT L" command to set the SSP pin J1-B31 to a low state. Set the CT triggers to 3.1 and 11.9 volts. Send the "DISOUT H" command to set the SSP pin J1-B31 to a high state. The Counter Timer will measure the rise time from 3.1 to 11.9 V. The expected rise time is 12.7 ± 5.3 us.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1

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From ID R6.2	to +15V
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-129 (S501-8)	to ID A1P7.47
From ID A1J7.47	to ID BUS 6
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-5 (S502-8)	to ID A1P6.36
From ID A1J6.36	to ID BUS 6
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B31 (UUT J1-B31)	to W7 P1A-3E
From ID J1A-3E	to ID A1J14.13
From ID A1P14.13	to ID P13-19 (S201-34)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 418

Description:

Send the "DISOUT L" command to set the SSP pin J1-A35 to a low state. Set the CT triggers to 3.1 and 11.9 volts. Send the "DISOUT H" command to set the SSP pin J1-A35 to a high state. The Counter Timer will measure the rise time from 3.1 to 11.9 V. The expected rise time is 12.7 ± 5.3 us.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1

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From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-129 (S501-8)	to ID A1P7.47
From ID A1J7.47	to ID BUS 6
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-5 (S502-8)	to ID A1P6.36
From ID A1J6.36	to ID BUS 6
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-A35 (UUT J1-A35)	to W7 P1B-7C
From ID J1B-7C	to ID A1J12.9
From ID A1P12.9	to ID P12-19 (S201-31)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Date: 04 March 2016

Step 419

Description:

Send the "DISOUT L" command to set the SSP pin J1-B35 to a low state. Set the CT triggers to 3.1 and 11.9 volts. Send the "DISOUT H" command to set the SSP pin J1-B35 to a high state. The Counter Timer will measure the rise time from 3.1 to 11.9 V. The expected rise time is 12.7 ± 5.3 us.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-129 (S501-8)	to ID A1P7.47
From ID A1J7.47	to ID BUS 6
From ID P19-19 (CT-IN2)	to ID A1P22.1

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From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-5 (S502-8)	to ID A1P6.36
From ID A1J6.36	to ID BUS 6
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B35 (UUT J1-B35)	to W7 P1B-6A
From ID J1B-6A	to ID A1J12.10
From ID A1P12.10	to ID P12-83 (S201-32)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 420

Description:

Send the "DISOUT L" command to set the SSP pin J2-20 to a low state. Set the CT triggers to 3.1 and 11.9 volts. Send the "DISOUT H" command to set the SSP pin J2-20 to a high state. The Counter Timer will measure the rise time from 3.1 to 11.9 V. The expected rise time is 12.7 ± 5.3 us.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V

From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND

From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)

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From ID P10-129 (S501-8)	to ID A1P7.47
From ID A1J7.47	to ID BUS 6
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-5 (S502-8)	to ID A1P6.36
From ID A1J6.36	to ID BUS 6
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P3-20 (UUT J2-20)	to W7 P1A-11E
From ID J1A-11E	to ID A1J14.29
From ID A1P14.29	to ID P13-82 (S201-36)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 421

Description:

Send the "DISOUTA L" command to set the all discrete outputs to a low state. Send the "ANADATA 14" command to request the analog voltage from the SSP. The expected value is 8.25 ± 0.50 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 422

Description:

Send the "DISOUT H" command to set SSP pin J1-A30 outputs to a high state. Send the "ANADATA 14" command to request the analog voltage from the SSP. The expected value is the result of Step 421 minus 0.825 ± 0.100 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 423

Description:

Send the "DISOUT H" command to set SSP pin J1-B30 outputs to a high state. Send the "ANADATA 14" command to request the analog voltage from the SSP. The expected value is the result of Step 421 minus 0.825 ± 0.100 Vdc.

Date: 04 March 2016

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 424

Description:

Send the "DISOUT H" command to set SSP pin J1-B31 outputs to a high state. Send the "ANADATA 14" command to request the analog voltage from the SSP. The expected value is the result of Step 421 minus 0.825 ± 0.100 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 425

Description:

Send the "DISOUT H" command to set SSP pin J1-A35 outputs to a high state. Send the "ANADATA 14" command to request the analog voltage from the SSP. The expected value is the result of Step 421 minus 0.825 ± 0.100 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

Step 426

Description:

Send the "DISOUT H" command to set SSP pin J1-B35 outputs to a high state. Send the "ANADATA 14" command to request the analog voltage from the SSP. The expected value is the result of Step 421 minus 0.825 ± 0.100 Vdc.

Connection Path is as follows:

See "UUT Power"

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See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

Step 427

Description:

Send the "DISOUT H" command to set SSP pin J2-20 outputs to a high state. Send the "ANADATA 14" command to request the analog voltage from the SSP. The expected value is the result of Step 421 minus 0.825 ± 0.100 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 428

Description:

Send the "DISOUTA H" command to set all discrete outputs to a high state. Send the "ANADATA 14" command to request the analog voltage from the SSP. The expected value is the result of Step 421 minus 14.03 ± 0.54 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 429

Description:

Send the "DISOUTA L" command to set all discrete outputs to a low state. Send the "ANABIT ON" command to set ANALOG_BIT high. Send the "ANADATA 14" command to request the analog voltage from the SSP. The expected value is the result of Step 421 minus 4.45 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

2.9 MODULE 5 – DISCRETE INPUT TESTS PART 1

Description:

This set of tests will test the functionality of the 42 unique discrete input circuits. In addition, several override control signals will be verified as well. These override control signals (BITHIGH, BITLOW, and PULLA) are used for special purposes with the discrete input circuits to override input conditions as required. Tests of each input, and the override control effect on the input, will be executed.

Refer to [Reference Drawings](#) when diagnosing connection paths.

Step 501

Description:

Send the "PULLA H" command to assert it to a high state. Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-C21 to open, and send "DISIN" command to verify SSP pin J1-C21 is high.

Connection Path is as follows:

See "UUT Power"
See "Boot Up"
See "Serial Comm"

Step 502

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-C21 is low.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Step 503

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-C21 to low, and send the "DISIN" command to verify SSP pin J1-C21 is low.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

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From W7 P2-C21 (UUT J1-C21)	to W7 P1B-10F
From ID J1B-10F	to ID A1J13.21
From ID A1P13.21	to ID P12-73 (S701-37)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 504

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-C21 is HIGH.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From W7 P2-C21 (UUT J1-C21)	to W7 P1B-10F
From ID J1B-10F	to ID A1J13.21
From ID A1P13.21	to ID P12-73 (S701-37)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 505

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-C22 to open, and send the "DISIN" command to verify SSP pin J1-C22 is high.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Step 506

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-C22 is low.

Date: 04 March 2016

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 507

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-C22 to low, and send the "DISIN" command to verify SSP pin J1-C22 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From W7 P2-C22 (UUT J1-C22)	to W7 P1B-11E
From ID J1B-11E	to ID A1J13.17
From ID A1P13.17	to ID P12-41 (S701-38)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 508

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-C22 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From W7 P2-C22 (UUT J1-C22)	to W7 P1B-11E
From ID J1B-11E	to ID A1J13.17
From ID A1P13.17	to ID P12-41 (S701-38)

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From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 509

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A23 to open, and send the "DISIN" command to verify SSP pin J1-A23 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 510

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-A23 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 511

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A23 to low, and send the "DISIN" command to verify SSP pin J1-A23 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

From W7 P2-A23 (UUT J1-A23)	to W7 P1A-11B
From ID J1A-11B	to ID A1J15.24
From ID A1P15.24	to ID P13-9 (S701-24)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10

Date: 04 March 2016

From ID A1J9.10

to ID BUS 8

Step 512

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-A23 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND

to ID A1J7.32

From ID A1P7.32

to ID P10-98 (S301-11)

From ID P10-163 (S301-12)

to ID A1P7.16

From ID A1J7.16

to ID A1J7.18

From ID A1P7.18

to ID P10-133 (S301-27)

From ID P10-70 (S301-28)

to ID A1P6.12

From ID A1J6.12

to ID BUS 8

From W7 P2-A23 (UUT J1-A23)

to W7 P1A-11B

From ID J1A-11B

to ID A1J15.24

From ID A1P15.24

to ID P13-9 (S701-24)

From ID P12-44 (S701-2)

to ID A1P12.48

From ID A1J12.48

to ID A1J10.1

From ID A1P10.1

to ID P11-162 (S506-2)

From ID P11-36 (S506-10)

to ID A1P9.10

From ID A1J9.10

to ID BUS 8

Step 513

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-B24 to open, and send the "DISIN" command to verify SSP pin J1-B24 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 514

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-B24 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 515

Description:

Date: 04 March 2016

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-B24 to low, and send the "DISIN" command to verify SSP pin J1-B24 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

From W7 P2-B24 (UUT J1-B24)	to W7 P1A-5C
From ID J1A-5C	to ID A1J15.9
From ID A1P15.9	to ID P13-75 (S701-31)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 516

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-B24 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

From W7 P2-B24 (UUT J1-B24)	to W7 P1A-5C
From ID J1A-5C	to ID A1J15.9
From ID A1P15.9	to ID P13-75 (S701-31)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Date: 04 March 2016

Step 517

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A25 to open, and send the "DISIN" command to verify SSP pin J1-A25 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 518

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-A25 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 519

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A25 to low, and send the "DISIN" command to verify SSP pin J1-A25 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From W7 P2-A25 (UUT J1-A25)	to W7 P1A-13B
From ID J1A-13B	to ID A1J15.28
From ID A1P15.28	to ID P13-41 (S701-26)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 520

Description:

Date: 04 March 2016

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-A25 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

From W7 P2-A25 (UUT J1-A25)	to W7 P1A-13B
From ID J1A-13B	to ID A1J15.28
From ID A1P15.28	to ID P13-41 (S701-26)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 521

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-B25 to open, and send the "DISIN" command to verify SSP pin J1-B25 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 522

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-B25 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 523

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-B25 to low, and send the "DISIN" command to verify SSP pin J1-B25 is low.

Connection Path is as follows:

Date: 04 March 2016

See "UUT Power"
See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From W7 P2-B25 (UUT J1-B25)	to W7 P1A-6A
From ID J1A-6A	to ID A1J15.10
From ID A1P15.10	to ID P13-43 (S701-32)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 524

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-B25 is HIGH.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From W7 P2-B25 (UUT J1-B25)	to W7 P1A-6A
From ID J1A-6A	to ID A1J15.10
From ID A1P15.10	to ID P13-43 (S701-32)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 525

Description:

Date: 04 March 2016

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A27 to open, and send the "DISIN" command to verify SSP pin J1-A27 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 526

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-A27 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 527

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A27 to low, and send the "DISIN" command to verify SSP pin J1-A27 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From W7 P2-A27 (UUT J1-A27)	to W7 P1B-9E
From ID J1B-9E	to ID A1J13.23
From ID A1P13.23	to ID P12-72 (S701-28)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 528

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-A27 is HIGH.

Connection Path is as follows:

Date: 04 March 2016

See "UUT Power"
See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

From W7 P2-A27 (UUT J1-A27)	to W7 P1B-9E
From ID J1B-9E	to ID A1J13.23
From ID A1P13.23	to ID P12-72 (S701-28)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 529

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-15 to open, and send the "DISIN" command to verify SSP pin J2-15 is high.

Connection Path is as follows:
See "UUT Power"
See "Serial Comm"

Step 530

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J2-15 is low.

Connection Path is as follows:
See "UUT Power"
See "Serial Comm"

Step 531

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-15 to low, and send the "DISIN" command to verify SSP pin J2-15 is low.

Connection Path is as follows:
See "UUT Power"
See "Serial Comm"

From GROUND	to ID A1J7.32
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From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From W7 P3-15 (UUT J2-15)	to W7 P1B-7F
From ID J1B-7F	to ID A1J13.33
From ID A1P13.33	to ID P12-10 (S701-45)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 532

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J2-15 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From W7 P3-15 (UUT J2-15)	to W7 P1B-7F
From ID J1B-7F	to ID A1J13.33
From ID A1P13.33	to ID P12-10 (S701-45)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 533

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-17 to open, and send the "DISIN" command to verify SSP pin J2-17 is high.

Connection Path is as follows:

See "UUT Power"

Date: 04 March 2016

See "Serial Comm"

Step 534

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J2-17 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 535

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-17 to low, and send the "DISIN" command to verify SSP pin J2-17 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From W7 P3-17 (UUT J2-17)	to W7 P1A-8C
From ID J1A-8C	to ID A1J15.18
From ID A1P15.18	to ID P13-78 (S701-47)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 536

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J2-17 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16

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From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
 From W7 P3-17 (UUT J2-17)	 to W7 P1A-8C
From ID J1A-8C	to ID A1J15.18
From ID A1P15.18	to ID P13-78 (S701-47)
 From ID P12-76 (S701-1)	 to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 537

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-14 to open, and send the "DISIN" command to verify SSP pin J2-14 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 538

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J2-14 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 539

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-14 to low, and send the "DISIN" command to verify SSP pin J2-14 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Date: 04 March 2016

From W7 P3-14 (UUT J2-14)	to W7 P1A-9A
From ID J1A-9A	to ID A1J15.19
From ID A1P15.19	to ID P13-77 (S701-50)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 540

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J2-14 is HIGH.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From W7 P3-14 (UUT J2-14)	to W7 P1A-9A
From ID J1A-9A	to ID A1J15.19
From ID A1P15.19	to ID P13-77 (S701-50)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 541

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-16 to open, and send the "DISIN" command to verify SSP pin J2-16 is high.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Step 542

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J2-16 is low.

Date: 04 March 2016

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 543

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-16 to low, and send the "DISIN" command to verify SSP pin J2-16 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From W7 P3-16 (UUT J2-16)	to W7 P1B-8E
From ID J1B-8E	to ID A1J13.26
From ID A1P13.26	to ID P12-74 (S701-46)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 544

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J2-16 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From W7 P3-16 (UUT J2-16)	to W7 P1B-8E
From ID J1B-8E	to ID A1J13.26
From ID A1P13.26	to ID P12-74 (S701-46)

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From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

Step 545

Description:

Send the "PULLA L" command to assert it to a low state. Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A20 to open, and send the "DISIN" command to verify SSP pin J1-A20 is low.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Step 546

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-A20 is HIGH.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Step 547

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A20 to high, and send the "DISIN" command to verify SSP pin J1-A20 is high.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P2-A20 (UUT J1-A20)	to W7 P1A-5B
From ID J1A-5B	to ID A1J15.8
From ID A1P15.8	to ID P13-42 (S701-23)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Date: 04 March 2016

Step 548

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-A20 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P2-A20 (UUT J1-A20)	to W7 P1A-5B
From ID J1A-5B	to ID A1J15.8
From ID A1P15.8	to ID P13-42 (S701-23)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 549

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-B20 to open, and send the "DISIN" command to verify SSP pin J1-B20 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 550

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-B20 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 551

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-B20 to high, and send the "DISIN" command to verify SSP pin J1-B20 is high.

Date: 04 March 2016

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-B20 (UUT J1-B20)	to W7 P1B-7E
From ID J1B-7E	to ID A1J13.31
From ID A1P13.31	to ID P12-7 (S701-30)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 552

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-B20 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-B20 (UUT J1-B20)	to W7 P1B-7E
From ID J1B-7E	to ID A1J13.31
From ID A1P13.31	to ID P12-7 (S701-30)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 553

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-C20 to open, and send the "DISIN" command to verify SSP pin J1-C20 is low.

Connection Path is as follows:

Date: 04 March 2016

See "UUT Power"
See "Serial Comm"

Step 554

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-C20 is high.

Connection Path is as follows:
See "UUT Power"
See "Serial Comm"

Step 555

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-C20 to high, and send the "DISIN" command to verify SSP pin J1-C20 is high.

Connection Path is as follows:
See "UUT Power"
See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 556

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-C20 is low.

Connection Path is as follows:
See "UUT Power"
See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29

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From ID A1J8.29	to ID BUS 1
From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 557

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A24 to open, and send the "DISIN" command to verify SSP pin J1-A24 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 558

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-A24 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 559

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A24 to high, and send the "DISIN" command to verify SSP pin J1-A24 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-A24 (UUT J1-A24)	to W7 P1A-12B
From ID J1A-12B	to ID A1J15.26
From ID A1P15.26	to ID P13-73 (S701-25)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 560

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-A24 is low.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P2-A24 (UUT J1-A24)	to W7 P1A-12B
From ID J1A-12B	to ID A1J15.26
From ID A1P15.26	to ID P13-73 (S701-25)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 561

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-B32 to open, and send the "DISIN" command to verify SSP pin J1-A34 is low.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Step 562

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-A34 is high.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Date: 04 March 2016

Step 563

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-B32 to high, and send the "DISIN" command to verify SSP pin J1-A34 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P2-B32 (UUT J1-B32)	to W7 P1B-10D
From ID J1B-10D	to ID A1J13.19
From ID A1P13.19	to ID P12-42 (S701-35)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 564

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-A34 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P2-B32 (UUT J1-B32)	to W7 P1B-10D
From ID J1B-10D	to ID A1J13.19
From ID A1P13.19	to ID P12-42 (S701-35)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Date: 04 March 2016

Step 565

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-C24 to open, and send the "DISIN" command to verify SSP pin J1-C24 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 566

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-C24 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 567

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-C24 to high, and send the "DISIN" command to verify SSP pin J1-C24 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-C24 (UUT J1-C24)	to W7 P1A-7A
From ID J1A-7A	to ID A1J15.13
From ID A1P15.13	to ID P13-45 (S701-39)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 568

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-C24 is low.

Date: 04 March 2016

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-C24 (UUT J1-C24)	to W7 P1A-7A
From ID J1A-7A	to ID A1J15.13
From ID A1P15.13	to ID P13-45 (S701-39)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 569

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-C25 to open, and send the "DISIN" command to verify SSP pin J1-C25 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 570

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-C25 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 571

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-C25 to high, and send the "DISIN" command to verify SSP pin J1-C25 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4

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From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-C25 (UUT J1-C25)	to W7 P1A-7B
From ID J1A-7B	to ID A1J15.14
From ID A1P15.14	to ID P13-12 (S701-40)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 572

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-C25 is low.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-C25 (UUT J1-C25)	to W7 P1A-7B
From ID J1A-7B	to ID A1J15.14
From ID A1P15.14	to ID P13-12 (S701-40)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 573

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A26 to open, and send the "DISIN" command to verify SSP pin J1-A26 is low.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Step 574

Description:

Date: 04 March 2016

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-A26 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 575

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A26 to high, and send the "DISIN" command to verify SSP pin J1-A26 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-A26 (UUT J1-A26)	to W7 P1B-7D
From ID J1B-7D	to ID A1J13.29
From ID A1P13.29	to ID P12-8 (S701-27)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 576

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-A26 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-A26 (UUT J1-A26)	to W7 P1B-7D
From ID J1B-7D	to ID A1J13.29
From ID A1P13.29	to ID P12-8 (S701-27)

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From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 577

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-B26 to open, and send the "DISIN" command to verify SSP pin J1-B26 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 578

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-B26 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 579

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-B26 to high, and send the "DISIN" command to verify SSP pin J1-B26 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P2-B26 (UUT J1-B26)	to W7 P1A-6B
From ID J1A-6B	to ID A1J15.11
From ID A1P15.11	to ID P13-10 (S701-33)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 580

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-B26 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P2-B26 (UUT J1-B26)	to W7 P1A-6B
From ID J1A-6B	to ID A1J15.11
From ID A1P15.11	to ID P13-10 (S701-33)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

2.10 MODULE 6 – DISCRETE INPUT TESTS PART 2

Description:

This set of tests will test the functionality of the 42 unique discrete input circuits. In addition, several override control signals will be verified as well. These override control signals (BITHIGH, BITLOW, and PULLA) are used for special purposes with the discrete input circuits to override input conditions as required. Tests of each input, and the override control effect on the input, will be executed.

Refer to [Reference Drawings](#) when diagnosing connection paths.

Step 601

Description:

Send the "PULLA L" command to assert it to a low state. Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-8 to open, and send the "DISIN" command to verify SSP pin J2-8 is low.

Connection Path is as follows:

See "UUT Power"

See "Boot Up"

See "Serial Comm"

Step 602

Description:

Date: 04 March 2016

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J2-8 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 603

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-8 to high, and send the "DISIN" command to verify SSP pin J2-8 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P3-8 (UUT J2-8)	to W7 P1A-7C
From ID J1A-7C	to ID A1J15.15
From ID A1P15.15	to ID P13-76 (S701-41)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 604

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J2-8 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P3-8 (UUT J2-8)	to W7 P1A-7C
From ID J1A-7C	to ID A1J15.15
From ID A1P15.15	to ID P13-76 (S701-41)

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From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 605

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-37 to open, and send the "DISIN" command to verify SSP pin J2-37 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 606

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J2-37 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 607

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-37 to high, and send the "DISIN" command to verify SSP pin J2-37 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P3-37 (UUT J2-37)	to W7 P1A-9B
From ID J1A-9B	to ID A1J15.20
From ID A1P15.20	to ID P13-46 (S701-48)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Date: 04 March 2016

Step 608

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J2-37 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P3-37 (UUT J2-37)	to W7 P1A-9B
From ID J1A-9B	to ID A1J15.20
From ID A1P15.20	to ID P13-46 (S701-48)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 609

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-12 to open, and send the "DISIN" command to verify SSP pin J2-12 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 610

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J2-12 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 611

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-12 to high, and send the "DISIN" command to verify SSP pin J2-12 is high.

Date: 04 March 2016

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P3-12 (UUT J2-12)	to W7 P1A-4C
From ID J1A-4C	to ID A1J15.6
From ID A1P15.6	to ID P13-44 (S701-42)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 612

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J2-12 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P3-12 (UUT J2-12)	to W7 P1A-4C
From ID J1A-4C	to ID A1J15.6
From ID A1P15.6	to ID P13-44 (S701-42)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 613

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-13 to open, and send the "DISIN" command to verify SSP pin J2-13 is low.

Connection Path is as follows:

Date: 04 March 2016

See "UUT Power"
See "Serial Comm"

Step 614

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J2-13 is HIGH.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Step 615

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-13 to high, and send the "DISIN" command to verify SSP pin J2-13 is high.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P3-13 (UUT J2-13)	to W7 P1A-8B
From ID J1A-8B	to ID A1J15.17
From ID A1P15.17	to ID P13-13 (S701-49)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 616

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J2-13 is low.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29

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From ID A1J8.29	to ID BUS 1
From W7 P3-13 (UUT J2-13)	to W7 P1A-8B
From ID J1A-8B	to ID A1J15.17
From ID A1P15.17	to ID P13-13 (S701-49)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 617

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-B27 to open, and send the "DISIN" command to verify SSP pin J1-B27 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 618

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-B27 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 619

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-B27 to high, and send the "DISIN" command to verify SSP pin J1-B27 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-B27 (UUT J1-B27)	to W7 P1A-6C
From ID J1A-6C	to ID A1J15.12
From ID A1P15.12	to ID P13-74 (S701-34)

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From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 620

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-B27 is low.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P2-B27 (UUT J1-B27)	to W7 P1A-6C
From ID J1A-6C	to ID A1J15.12
From ID A1P15.12	to ID P13-74 (S701-34)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 621

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A28 to open, and send the "DISIN" command to verify SSP pin J1-A28 is low.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Step 622

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J1-A28 is HIGH.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

Date: 04 March 2016

Step 623

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J1-A28 to high, and send the "DISIN" command to verify SSP pin J1-A28 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P2-A28 (UUT J1-A28)	to W7 P1B-11F
From ID J1B-11F	to ID A1J13.18
From ID A1P13.18	to ID P12-40 (S701-29)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 624

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J1-A28 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From W7 P2-A28 (UUT J1-A28)	to W7 P1B-11F
From ID J1B-11F	to ID A1J13.18
From ID A1P13.18	to ID P12-40 (S701-29)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Date: 04 March 2016

Step 625

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW, and send the "DISIN" command to verify SSP pin J1-A22 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 626

Description:

Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J1-A22 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 627

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J1-A22 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 628

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW, and send the "DISIN" command to verify SSP pin J1-B21 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 629

Description:

Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J1-B21 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 630

Description:

Date: 04 March 2016

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J1-B21 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 631

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW, and send the "DISIN" command to verify SSP pin J1-B22 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 632

Description:

Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J1-B22 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 633

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J1-B22 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 634

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW, and send the "DISIN" command to verify SSP pin J1-B23 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 635

Description:

Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J1-B23 is low.

Date: 04 March 2016

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 636

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J1-B23 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 637

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW, and send the "DISIN" command to verify SSP pin J1-C23 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 638

Description:

Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J1-C23 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 639

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J1-C23 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 640

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW, and send the "DISIN" command to verify SSP pin J1-A21 is low.

Connection Path is as follows:

See "UUT Power"

Date: 04 March 2016

See "Serial Comm"

Step 641

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J1-A21 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 642

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J1-A21 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 643

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW, and send the "DISIN" command to verify SSP pin J1-C33 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 644

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J1-C33 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 645

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J1-C33 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 646

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW, and send the "DISIN" command to verify SSP pin J2-36 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 647

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J2-36 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 648

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J2-36 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 649

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "PULLA L" command to assert low state and pull down the input. Send the "DISIN" command to verify that the SSP pin J1-A29 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 650

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J1-A29 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 651

Date: 04 March 2016

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "PULLA H" command to assert high state and pull up the input. Send the "DISIN" command to verify that the SSP pin J1-A29 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 652

Description:

Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J1-A29 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 653

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "PULLA L" command to assert low state and pull down the input. Send the "DISIN" command to verify that the SSP pin J1-B28 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 654

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J1-B28 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 655

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "PULLA H" command to assert high state and pull up the input. Send the "DISIN" command to verify that the SSP pin J1-B28 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 656

Description:

Date: 04 March 2016

Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J1-B28 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 657

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "PULLA L" command to assert low state and pull down the input. Send the "DISIN" command to verify that the SSP pin J1-B29 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 658

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J1-B29 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 659

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "PULLA H" command to assert high state and pull up the input. Send the "DISIN" command to verify that the SSP pin J1-B29 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 660

Description:

Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J1-B29 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 661

Description:

Date: 04 March 2016

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "PULLA L" command to assert low state and pull down the input. Send the "DISIN" command to verify that the SSP pin J1-C26 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 662

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J1-C26 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 663

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "PULLA H" command to assert high state and pull up the input. Send the "DISIN" command to verify that the SSP pin J1-C26 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 664

Description:

Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J1-C26 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 665

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "PULLA L" command to assert low state and pull down the input. Send the "DISIN" command to verify that the SSP pin J1-C28 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 666

Description:

Date: 04 March 2016

Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J1-C28 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 667

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "PULLA H" command to assert high state and pull up the input. Send the "DISIN" command to verify that the SSP pin J1-C28 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 668

Description:

Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J1-C28 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 669

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "PULLA L" command to assert low state and pull down the input. Send the "DISIN" command to verify that the SSP pin J1-C29 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 670

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify SSP pin J1-C29 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 671

Description:

Date: 04 March 2016

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "PULLA H" command to assert high state and pull up the input. Send the "DISIN" command to verify that the SSP pin J1-C29 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 672

Description:

Send the "BITLOW" command and send the "DISIN" command to verify SSP pin J1-C29 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 673

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-9 to open, and send the "DISIN" command to verify SSP pin J2-9 is low.

Connection Path is as follows:

See "UUT Power"

See "Boot Up"

See "Serial Comm"

Step 674

Description:

Send the "BITHIGH" command and send the "DISIN" command to verify that the SSP pin J2-9 is HIGH.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 675

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Set UUT pin J2-9 to high, and send the "DISIN" command to verify SSP pin J2-9 is high.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)

From ID A1J1.2

From ID A1P8.4

to ID A1P1.2

to ID A1J8.4

to ID P10-174 (S301-68)

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From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P3-9 (UUT J2-9)	to W7 P1B-13C
From ID J1B-13C	to ID A1J13.6
From ID A1P13.6	to ID P12-78 (S201-14)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

Step 676

Description:

Send the "BITLOW" command and send the "DISIN" command to verify that the SSP pin J2-9 is low.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P3-9 (UUT J2-9)	to W7 P1B-13C
From ID J1B-13C	to ID A1J13.6
From ID A1P13.6	to ID P12-78 (S201-14)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

2.11 MODULE 7 – DISCRETE INPUT TESTS PART 3

Description:

This series of tests will verify that the Schmitt trigger ICs on each input switch within their expected tolerances. The analog output J1-A14 will be looped back through the ID to each discrete input, one at a time. The output will then ramp up or down, stopping when the SSP detects that the discrete input has changed state. The DMM is then used to measure the ending voltage.

The second set of tests within this module will verify that the RC time constant of the input filter of each discrete inputs is in place and correct. This is done by measuring the time that it takes for the output of the Schmitt trigger inputs to change states as J1-A14 ramps up or down.

Refer to [Reference Drawings](#) when diagnosing connection paths.

Step 701

Description:

Send the "BITOFF" command to reset BITHIGH and BITLOW. Send the "PULLA H" command to assert high state and pull up the input. Send the "ANAOOUT 0" command to set the UUT pin J1-A14 to 0 Vdc. Connect UUT pin J1-A20 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-A20, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A20. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Boot Up"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-A20 (UUT J1-A20)	to W7 P1A-5B
From ID J1A-5B	to ID A1J15.8
From ID A1P15.8	to ID P13-42 (S701-23)

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From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 702

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-A20, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A20. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-A20 (UUT J1-A20)	to W7 P1A-5B
From ID J1A-5B	to ID A1J15.8
From ID A1P15.8	to ID P13-42 (S701-23)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 703

Description:

Connect UUT pin J1-B20 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-B20, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-B20. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-B20 (UUT J1-B20)	to W7 P1B-7E
From ID J1B-7E	to ID A1J13.31
From ID A1P13.31	to ID P12-7 (S701-30)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 704

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-B20, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-B20. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-B20 (UUT J1-B20)	to W7 P1B-7E
From ID J1B-7E	to ID A1J13.31
From ID A1P13.31	to ID P12-7 (S701-30)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 705

Description:

Connect UUT pin J1-C20 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-C20, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-C20. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 706

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-C20, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-C20. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 707

Description:

Connect UUT pin J1-A24 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-A24, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A24. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-A24 (UUT J1-A24)	to W7 P1A-12B
From ID J1A-12B	to ID A1J15.26
From ID A1P15.26	to ID P13-73 (S701-25)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 708

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-A24, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A24. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-A24 (UUT J1-A24)	to W7 P1A-12B
From ID J1A-12B	to ID A1J15.26
From ID A1P15.26	to ID P13-73 (S701-25)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 709

Description:

Connect UUT pin J1-B32 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-A34, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-B32. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-B32 (UUT J1-B32)	to W7 P1B-10D
From ID J1B-10D	to ID A1J13.19
From ID A1P13.19	to ID P12-42 (S701-35)

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From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 710

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-A34, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-B32. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-B32 (UUT J1-B32)	to W7 P1B-10D
From ID J1B-10D	to ID A1J13.19
From ID A1P13.19	to ID P12-42 (S701-35)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 711

Description:

Connect UUT pin J1-C24 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-C24, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-C24. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-C24 (UUT J1-C24)	to W7 P1A-7A
From ID J1A-7A	to ID A1J15.13
From ID A1P15.13	to ID P13-45 (S701-39)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 712

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-C24, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-C24. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-C24 (UUT J1-C24)	to W7 P1A-7A
From ID J1A-7A	to ID A1J15.13
From ID A1P15.13	to ID P13-45 (S701-39)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 713

Description:

Connect UUT pin J1-C25 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-C25, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-C25. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-C25 (UUT J1-C25)	to W7 P1A-7B
From ID J1A-7B	to ID A1J15.14
From ID A1P15.14	to ID P13-12 (S701-40)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 714

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-C25, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-C25. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-C25 (UUT J1-C25)	to W7 P1A-7B
From ID J1A-7B	to ID A1J15.14
From ID A1P15.14	to ID P13-12 (S701-40)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 715

Description:

Connect UUT pin J1-A26 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-A26, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A26. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-A26 (UUT J1-A26)	to W7 P1B-7D
From ID J1B-7D	to ID A1J13.29
From ID A1P13.29	to ID P12-8 (S701-27)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 716

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-A26, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A26. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-A26 (UUT J1-A26)	to W7 P1B-7D
From ID J1B-7D	to ID A1J13.29
From ID A1P13.29	to ID P12-8 (S701-27)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 717

Description:

Connect UUT pin J1-B26 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-B26, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-B26. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-B26 (UUT J1-B26)	to W7 P1A-6B
From ID J1A-6B	to ID A1J15.11
From ID A1P15.11	to ID P13-10 (S701-33)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 718

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-B26, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-B26. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-B26 (UUT J1-B26)	to W7 P1A-6B
From ID J1A-6B	to ID A1J15.11
From ID A1P15.11	to ID P13-10 (S701-33)

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From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 719

Description:

Connect UUT pin J2-8 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J2-8, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-8. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-8 (UUT J2-8)	to W7 P1A-7C
From ID J1A-7C	to ID A1J15.15
From ID A1P15.15	to ID P13-76 (S701-41)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 720

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J2-8, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-8. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-8 (UUT J2-8)	to W7 P1A-7C
From ID J1A-7C	to ID A1J15.15
From ID A1P15.15	to ID P13-76 (S701-41)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 721

Description:

Connect UUT pin J2-37 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J2-37, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-37. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-37 (UUT J2-37)	to W7 P1A-9B
From ID J1A-9B	to ID A1J15.20
From ID A1P15.20	to ID P13-46 (S701-48)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 722

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J2-37, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-37. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-37 (UUT J2-37)	to W7 P1A-9B
From ID J1A-9B	to ID A1J15.20
From ID A1P15.20	to ID P13-46 (S701-48)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 723

Description:

Connect UUT pin J2-12 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J2-12, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-12. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-12 (UUT J2-12)	to W7 P1A-4C
From ID J1A-4C	to ID A1J15.6
From ID A1P15.6	to ID P13-44 (S701-42)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 724

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J2-12, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-12. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P3-12 (UUT J2-12)	to W7 P1A-4C
From ID J1A-4C	to ID A1J15.6
From ID A1P15.6	to ID P13-44 (S701-42)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 725

Description:

Connect UUT pin J2-13 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J2-13, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-13. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-13 (UUT J2-13)	to W7 P1A-8B
From ID J1A-8B	to ID A1J15.17
From ID A1P15.17	to ID P13-13 (S701-49)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 726

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J2-13, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-13. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-13 (UUT J2-13)	to W7 P1A-8B
From ID J1A-8B	to ID A1J15.17
From ID A1P15.17	to ID P13-13 (S701-49)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 727

Description:

Connect UUT pin J1-B27 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-B27, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-B27. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-B27 (UUT J1-B27)	to W7 P1A-6C
From ID J1A-6C	to ID A1J15.12
From ID A1P15.12	to ID P13-74 (S701-34)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 728

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-B27, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-B27. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-B27 (UUT J1-B27)	to W7 P1A-6C
From ID J1A-6C	to ID A1J15.12
From ID A1P15.12	to ID P13-74 (S701-34)

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From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 729

Description:

Connect UUT pin J1-A28 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-A28, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A28. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-A28 (UUT J1-A28)	to W7 P1B-11F
From ID J1B-11F	to ID A1J13.18
From ID A1P13.18	to ID P12-40 (S701-29)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 730

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-A28, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A28. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-A28 (UUT J1-A28)	to W7 P1B-11F
From ID J1B-11F	to ID A1J13.18
From ID A1P13.18	to ID P12-40 (S701-29)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 731

Description:

Connect UUT pin J1-C21 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-C21, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-C21. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-C21 (UUT J1-C21)	to W7 P1B-10F
From ID J1B-10F	to ID A1J13.21
From ID A1P13.21	to ID P12-73 (S701-37)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 732

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-C21, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-C21. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-C21 (UUT J1-C21)	to W7 P1B-10F
From ID J1B-10F	to ID A1J13.21
From ID A1P13.21	to ID P12-73 (S701-37)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 733

Description:

Connect UUT pin J1-C22 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-C22, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-C22. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-C22 (UUT J1-C22)	to W7 P1B-11E
From ID J1B-11E	to ID A1J13.17
From ID A1P13.17	to ID P12-41 (S701-38)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 734

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-C22, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-C22. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-C22 (UUT J1-C22)	to W7 P1B-11E
From ID J1B-11E	to ID A1J13.17
From ID A1P13.17	to ID P12-41 (S701-38)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 735

Description:

Connect UUT pin J1-A23 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-A23, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A23. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-A23 (UUT J1-A23)	to W7 P1A-11B
From ID J1A-11B	to ID A1J15.24
From ID A1P15.24	to ID P13-9 (S701-24)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 736

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-A23, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A23. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-A23 (UUT J1-A23)	to W7 P1A-11B
From ID J1A-11B	to ID A1J15.24
From ID A1P15.24	to ID P13-9 (S701-24)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 737

Description:

Connect UUT pin J1-B24 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-B24, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-B24. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-B24 (UUT J1-B24)	to W7 P1A-5C
From ID J1A-5C	to ID A1J15.9
From ID A1P15.9	to ID P13-75 (S701-31)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 738

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-B24, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-B24. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-B24 (UUT J1-B24)	to W7 P1A-5C
From ID J1A-5C	to ID A1J15.9
From ID A1P15.9	to ID P13-75 (S701-31)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 739

Description:

Connect UUT pin J1-A25 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-A25, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A25. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-A25 (UUT J1-A25)	to W7 P1A-13B
From ID J1A-13B	to ID A1J15.28
From ID A1P15.28	to ID P13-41 (S701-26)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 740

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-A25, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A25. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-A25 (UUT J1-A25)	to W7 P1A-13B
From ID J1A-13B	to ID A1J15.28
From ID A1P15.28	to ID P13-41 (S701-26)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 741

Description:

Connect UUT pin J1-B25 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-B25, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-B25. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-B25 (UUT J1-B25)	to W7 P1A-6A
From ID J1A-6A	to ID A1J15.10
From ID A1P15.10	to ID P13-43 (S701-32)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 742

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-B25, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-B25. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-B25 (UUT J1-B25)	to W7 P1A-6A
From ID J1A-6A	to ID A1J15.10
From ID A1P15.10	to ID P13-43 (S701-32)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 743

Description:

Connect UUT pin J1-A27 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J1-A27, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A27. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P2-A27 (UUT J1-A27)	to W7 P1B-9E
From ID J1B-9E	to ID A1J13.23
From ID A1P13.23	to ID P12-72 (S701-28)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 744

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J1-A27, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J1-A27. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P2-A27 (UUT J1-A27)	to W7 P1B-9E
From ID J1B-9E	to ID A1J13.23
From ID A1P13.23	to ID P12-72 (S701-28)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 745

Description:

Connect UUT pin J2-15 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J2-15, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-15. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-15 (UUT J2-15)	to W7 P1B-7F
From ID J1B-7F	to ID A1J13.33
From ID A1P13.33	to ID P12-10 (S701-45)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 746

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J2-15, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-15. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-15 (UUT J2-15)	to W7 P1B-7F
From ID J1B-7F	to ID A1J13.33
From ID A1P13.33	to ID P12-10 (S701-45)

Date: 04 March 2016

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 747

Description:

Connect UUT pin J2-17 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J2-17, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-17. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-17 (UUT J2-17)	to W7 P1A-8C
From ID J1A-8C	to ID A1J15.18
From ID A1P15.18	to ID P13-78 (S701-47)

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From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 748

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J2-17, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-17. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-17 (UUT J2-17)	to W7 P1A-8C
From ID J1A-8C	to ID A1J15.18
From ID A1P15.18	to ID P13-78 (S701-47)

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From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 749

Description:

Connect UUT pin J2-14 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J2-14, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-14. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-14 (UUT J2-14)	to W7 P1A-9A
From ID J1A-9A	to ID A1J15.19
From ID A1P15.19	to ID P13-77 (S701-50)

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From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 750

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J2-14, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-14. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P3-14 (UUT J2-14)	to W7 P1A-9A
From ID J1A-9A	to ID A1J15.19
From ID A1P15.19	to ID P13-77 (S701-50)

Date: 04 March 2016

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 751

Description:

Connect UUT pin J2-16 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J2-16, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-16. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-16 (UUT J2-16)	to W7 P1B-8E
From ID J1B-8E	to ID A1J13.26
From ID A1P13.26	to ID P12-74 (S701-46)

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From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 752

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J2-16, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-16. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-16 (UUT J2-16)	to W7 P1B-8E
From ID J1B-8E	to ID A1J13.26
From ID A1P13.26	to ID P12-74 (S701-46)

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From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 753

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-A20. Send the "RC J1-A20" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-A20 (UUT J1-A20)	to W7 P1A-5B
From ID J1A-5B	to ID A1J15.8
From ID A1P15.8	to ID P13-42 (S701-23)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3

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From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 754

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-B20. Send the "RC J1-B20" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B20 (UUT J1-B20)	to W7 P1B-7E
From ID J1B-7E	to ID A1J13.31
From ID A1P13.31	to ID P12-7 (S701-30)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23

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From ID A1J9.23

to ID BUS 1

Step 755

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-C20. Send the "RC J1-C20" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)

to W7 P1B-14B

From ID J1B-14B

to ID A1J13.3

From ID A1P13.3

to ID P12-46 (S201-7)

From ID P12-16 (S201-1)

to ID A1P12.42

From ID A1J12.42

to ID A1J10.6

From ID A1P10.6

to ID P11-203 (S508-1)

From ID P11-77 (S508-3)

to ID A1P9.15

From ID A1J9.15

to ID BUS 1

From ID P19-18 (CT-IN1)

to ID A1P21.1

From ID A1J21.1

to ID A1J6.8

From ID A1P6.8

to ID P10-162 (S501-2)

From ID P10-164 (S501-3)

to ID A1P7.29

From ID A1J7.29

to ID BUS 1

From ID P19-19 (CT-IN2)

to ID A1P22.1

From ID A1J22.1

to ID A1J6.10

From ID A1P6.10

to ID P10-71 (S502-2)

From ID P10-168 (S502-3)

to ID A1P7.31

From ID A1J7.31

to ID BUS 1

From CT-RTN

to ID A1J6.11

From ID A1P6.11

to ID P10-166 (S301-26)

From ID P10-102 (S301-25)

to ID A1P7.34

From ID A1J7.34

to GROUND

From W7 P2-C20 (UUT J1-C20)

to W7 P1B-10E

From ID J1B-10E

to ID A1J13.20

From ID A1P13.20

to ID P12-9 (S701-36)

From ID P12-44 (S701-2)

to ID A1P12.48

From ID A1J12.48

to ID A1J10.1

From ID A1P10.1

to ID P11-162 (S506-2)

From ID P11-164 (S506-3)

to ID A1P9.23

From ID A1J9.23

to ID BUS 1

Date: 04 March 2016

Step 756

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-A24. Send the "RC J1-A24" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-A24 (UUT J1-A24)	to W7 P1A-12B
From ID J1A-12B	to ID A1J15.26
From ID A1P15.26	to ID P13-73 (S701-25)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 757

Date: 04 March 2016

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-B32. Send the "RC J1-A34" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B32 (UUT J1-B32)	to W7 P1B-10D
From ID J1B-10D	to ID A1J13.19
From ID A1P13.19	to ID P12-42 (S701-35)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 758

Description:

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Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-C24. Send the "RC J1-C24" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-C24 (UUT J1-C24)	to W7 P1A-7A
From ID J1A-7A	to ID A1J15.13
From ID A1P15.13	to ID P13-45 (S701-39)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 759

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal

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operation. Connect the UUT pin J1-A30 to the UUT pin J1-C25. Send the "RC J1-C25" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-C25 (UUT J1-C25)	to W7 P1A-7B
From ID J1A-7B	to ID A1J15.14
From ID A1P15.14	to ID P13-12 (S701-40)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 760

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-A26. Send the "RC J1-A26" command as a start command to create the pulse on EL

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STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-A26 (UUT J1-A26)	to W7 P1B-7D
From ID J1B-7D	to ID A1J13.29
From ID A1P13.29	to ID P12-8 (S701-27)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 761

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-B26. Send the "RC J1-B26" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the

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digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B26 (UUT J1-B26)	to W7 P1A-6B
From ID J1A-6B	to ID A1J15.11
From ID A1P15.11	to ID P13-10 (S701-33)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 762

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J2-8. Send the "RC J2-8" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

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Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P3-8 (UUT J2-8)	to W7 P1A-7C
From ID J1A-7C	to ID A1J15.15
From ID A1P15.15	to ID P13-76 (S701-41)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 763

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J2-37. Send the "RC J2-37" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

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See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P3-37 (UUT J2-37)	to W7 P1A-9B
From ID J1A-9B	to ID A1J15.20
From ID A1P15.20	to ID P13-46 (S701-48)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 764

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J2-12. Send the "RC J2-12" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

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From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P3-12 (UUT J2-12)	to W7 P1A-4C
From ID J1A-4C	to ID A1J15.6
From ID A1P15.6	to ID P13-44 (S701-42)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 765

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J2-13. Send the "RC J2-13" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3

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From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P3-13 (UUT J2-13)	to W7 P1A-8B
From ID J1A-8B	to ID A1J15.17
From ID A1P15.17	to ID P13-13 (S701-49)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 766

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-B27. Send the "RC J1-B27" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)

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From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B27 (UUT J1-B27)	to W7 P1A-6C
From ID J1A-6C	to ID A1J15.12
From ID A1P15.12	to ID P13-74 (S701-34)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 767

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-A28. Send the "RC J1-A28" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6

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From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-A28 (UUT J1-A28)	to W7 P1B-11F
From ID J1B-11F	to ID A1J13.18
From ID A1P13.18	to ID P12-40 (S701-29)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 768

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-C21. Send the "RC J1-C21" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15

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From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-C21 (UUT J1-C21)	to W7 P1B-10F
From ID J1B-10F	to ID A1J13.21
From ID A1P13.21	to ID P12-73 (S701-37)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 769

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-C22. Send the "RC J1-C22" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

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From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-C22 (UUT J1-C22)	to W7 P1B-11E
From ID J1B-11E	to ID A1J13.17
From ID A1P13.17	to ID P12-41 (S701-38)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 770

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-A23. Send the "RC J1-A23" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8

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From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-A23 (UUT J1-A23)	to W7 P1A-11B
From ID J1A-11B	to ID A1J15.24
From ID A1P15.24	to ID P13-9 (S701-24)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 771

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-B24. Send the "RC J1-B24" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29

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From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B24 (UUT J1-B24)	to W7 P1A-5C
From ID J1A-5C	to ID A1J15.9
From ID A1P15.9	to ID P13-75 (S701-31)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 772

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-A25. Send the "RC J1-A25" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1

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From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-A25 (UUT J1-A25)	to W7 P1A-13B
From ID J1A-13B	to ID A1J15.28
From ID A1P15.28	to ID P13-41 (S701-26)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 773

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-B25. Send the "RC J1-B25" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10

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From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-B25 (UUT J1-B25)	to W7 P1A-6A
From ID J1A-6A	to ID A1J15.10
From ID A1P15.10	to ID P13-43 (S701-32)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 774

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J1-A27. Send the "RC J1-A27" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31

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From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P2-A27 (UUT J1-A27)	to W7 P1B-9E
From ID J1B-9E	to ID A1J13.23
From ID A1P13.23	to ID P12-72 (S701-28)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 775

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J2-15. Send the "RC J2-15" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1

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From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P3-15 (UUT J2-15)	to W7 P1B-7F
From ID J1B-7F	to ID A1J13.33
From ID A1P13.33	to ID P12-10 (S701-45)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 776

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J2-17. Send the "RC J2-17" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)

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From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P3-17 (UUT J2-17)	to W7 P1A-8C
From ID J1A-8C	to ID A1J15.18
From ID A1P15.18	to ID P13-78 (S701-47)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 777

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J2-14. Send the "RC J2-14" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND

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From W7 P3-14 (UUT J2-14)	to W7 P1A-9A
From ID J1A-9A	to ID A1J15.19
From ID A1P15.19	to ID P13-77 (S701-50)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 778

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J2-16. Send the "RC J2-16" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P3-16 (UUT J2-16)	to W7 P1B-8E
From ID J1B-8E	to ID A1J13.26

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From ID A1P13.26	to ID P12-74 (S701-46)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

Step 779

Description:

Send the "DISOUT L J1-A30" command to set the UUT pin J1-A30 to low. Send the "BITOFF" command to set the discrete inputs to normal operation. Connect the UUT pin J1-A30 to the UUT pin J2-9. Send the "RC J2-9" command as a start command to create the pulse on EL STAB NUL MON. After received the prompt from the SSP, use the digitizer to measure the UUT pin J1-A30, and verify the pulse width should be 5.5 ± 2.5 msec.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A30 (UUT J1-A30)	to W7 P1B-14B
From ID J1B-14B	to ID A1J13.3
From ID A1P13.3	to ID P12-46 (S201-7)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1
From CT-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From W7 P3-9 (UUT J2-9)	to W7 P1B-13C
From ID J1B-13C	to ID A1J13.6
From ID A1P13.6	to ID P12-78 (S201-14)

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From ID P12-52 (S201-4)	to ID A1P12.44
From ID A1J12.44	to ID A1J10.4
From ID A1P10.4	to ID P11-71 (S507-2)
From ID P11-168 (S507-3)	to ID A1P9.17
From ID A1J9.17	to ID BUS 1

Step 780

Description:

Connect UUT pin J2-9 to the discrete input. Send the "SWITCH H" command to make the positive test on SSP pin J2-9, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-9. The measured voltage should be 8.24 ± 2.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From W7 P3-9 (UUT J2-9)	to W7 P1B-13C
From ID J1B-13C	to ID A1J13.6
From ID A1P13.6	to ID P12-78 (S201-14)

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From ID P12-52 (S201-4)	to ID A1P12.44
From ID A1J12.44	to ID A1J10.4
From ID A1P10.4	to ID P11-71 (S507-2)
From ID P11-168 (S507-3)	to ID A1P9.17
From ID A1J9.17	to ID BUS 1

Step 781

Description:

Send the "ANAOUT 10 J1-A14" command to set the output to 10Vdc. Send the "SWITCH L" command to make the negative test on SSP pin J2-9, and verify that the prompt is received from the SSP. The DMM is used to measure the UUT pin J2-9. The measured voltage should be 5.88 ± 2.70 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A14 (UUT J1-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)

From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1

From W7 P2-B14 (UUT J1-B14)	to W7 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2

From W7 P3-9 (UUT J2-9)	to W7 P1B-13C
From ID J1B-13C	to ID A1J13.6
From ID A1P13.6	to ID P12-78 (S201-14)

From ID P12-52 (S201-4)	to ID A1P12.44
From ID A1J12.44	to ID A1J10.4
From ID A1P10.4	to ID P11-71 (S507-2)
From ID P11-168 (S507-3)	to ID A1P9.17
From ID A1J9.17	to ID BUS 1

2.12 MODULE 8 – MOOG & BRAKE TESTS

Description:

This series of tests will verify the correct operation of the MOOG control interface. Both built in tests and voltage measurements at the UUT interface will be used to validate correct operation of the control interface.

The MOOG Elevation and Azimuth Brake control Outputs will also be tested in the module. Static tests of this interface in all required states will be tested. External resistive loads will be required within the ID to properly terminate the EL and AZ Brake RL interfaces.

Refer to [Reference Drawings](#) when diagnosing connection paths.

Step 801

Description:

Connect UUT pins J2-66 and J2-42 to a 120 ± 15 ohm resistor. Send the "CMC ON" command to generate an approximately 100 kHz square wave on the UUT pins J2-58 (HI) and J2-39 (LO). Send the "TMWRAP" command, and verify return message should be "PASSED".

Connection Path is as follows:

See "UUT Power"
See "Boot Up"
See "Serial Comm"

From W7 P3-66 (UUT J2-66)	to W7 P1B-1B
From ID J1B-1B	to ID A1J12.22
From ID A1P12.22	to ID P12-91 (S202-23)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-42 (UUT J2-42)	to W7 P1B-5B
From ID J1B-5B	to ID A1J12.14
From ID A1P12.14	to ID P12-27 (S202-24)

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From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2

Step 802

Description:

Send the "CMC OFF" command to stop generate an approximately 100 kHz square wave on the UUT pins J2-58 (HI) and J2-39 (LO). The DMM is used to measure the UUT pin J2-66 (HI) and J2-42 (LO), and the measured voltage should be 1.37 ± 0.65 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-66 (UUT J2-66)	to W7 P1B-1B
From ID J1B-1B	to ID A1J12.22
From ID A1P12.22	to ID P12-91 (S202-23)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-42 (UUT J2-42)	to W7 P1B-5B
From ID J1B-5B	to ID A1J12.14
From ID A1P12.14	to ID P12-27 (S202-24)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From ID P20-2 (DMM-HI)	to ID A1P15.49

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From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5

Step 803

Description:

Send the "MOOGWRAP" command, and verify return message should be "PASSED".

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-66 (UUT J2-66)	to W7 P1B-1B
From ID J1B-1B	to ID A1J12.22
From ID A1P12.22	to ID P12-91 (S202-23)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-42 (UUT J2-42)	to W7 P1B-5B
From ID J1B-5B	to ID A1J12.14
From ID A1P12.14	to ID P12-27 (S202-24)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2

Step 804

Description:

Date: 04 March 2016

The DSO is used to measure the UUT pin J2-42 (HI) and J1-A2 (LO), and the measured voltage should be 2.41 ± 1.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-66 (UUT J2-66)	to W7 P1B-1B
From ID J1B-1B	to ID A1J12.22
From ID A1P12.22	to ID P12-91 (S202-23)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-42 (UUT J2-42)	to W7 P1B-5B
From ID J1B-5B	to ID A1J12.14
From ID A1P12.14	to ID P12-27 (S202-24)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From ID P19-1 (DSO-IN1)	to ID A1P17.1
From ID A1J17.1	to ID A1J6.1
From ID A1P6.1	to ID P10-177 (S504-1)
From ID P10-144 (S504-8)	to ID A1P6.40
From ID A1J6.40	to ID BUS 6
From DSO-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND

Step 805

Description:

The DSO is used to measure the UUT pin J2-66 (HI) and J1-A2 (LO), and the measured voltage should be 1.03 ± 0.75 Vdc.

Date: 04 March 2016

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-66 (UUT J2-66)	to W7 P1B-1B
From ID J1B-1B	to ID A1J12.22
From ID A1P12.22	to ID P12-91 (S202-23)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-42 (UUT J2-42)	to W7 P1B-5B
From ID J1B-5B	to ID A1J12.14
From ID A1P12.14	to ID P12-27 (S202-24)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From ID P19-1 (DSO-IN1)	to ID A1P17.1
From ID A1J17.1	to ID A1J6.1
From ID A1P6.1	to ID P10-177 (S504-1)
From ID P10-207 (S504-7)	to ID A1P6.49
From ID A1J6.49	to ID BUS 5
From DSO-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND

Step 806

Description:

Verify the voltage difference between J2-66 and J2-42 is 1.54 V +/- 0.75 V

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Date: 04 March 2016

Step 807

Description:

Send the "DISOUT H J1-A32" command to bring the EL BRAKE RL high. Connect J1-C33 to 28V. The DMM is used to measure the UUT pin J1-A32, and the measured voltage should be 28 +/- 1.5 V

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-C33 (UUT J1-C33)	to W7 P1A-2B
From ID J1A-2B	to ID A1J14.4
From ID A1P14.4	to ID P13-48 (S201-12)
From ID P12-52 (S201-4)	to ID A1P12.44
From ID A1J12.44	to ID A1J10.4
From ID A1P10.4	to ID P11-71 (S507-2)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

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From W7 P2-A32 (UUT J1-A32)	to W7 P1A-9F
From ID J1A-9F	to ID A1J14.26
From ID A1P14.26	to ID P13-64 (S202-50)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6

Step 808

Description:

The DMM is used to measure the UUT pin J1-C32, and the measured voltage should be 28 ± 1.5 Vdc

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-C33 (UUT J1-C33)	to W7 P1A-2B
From ID J1A-2B	to ID A1J14.4
From ID A1P14.4	to ID P13-48 (S201-12)
From ID P12-52 (S201-4)	to ID A1P12.44
From ID A1J12.44	to ID A1J10.4
From ID A1P10.4	to ID P11-71 (S507-2)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28

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From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From W7 P2-C32 (UUT J1-C32)	to W7 P1A-1B
From ID J1A-1B	to ID A1J14.3
From ID A1P14.3	to ID P13-80 (S201-11)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-5 (S507-8)	to ID A1P9.24
From ID A1J9.24	to ID BUS 6

Step 809

Description:

Connect 5.6k ohm load to J1-B34. The DMM is used to measure the UUT pin J1-B34. The measured voltage should be <0.4 Vdc

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-C33 (UUT J1-C33)	to W7 P1A-2B
From ID J1A-2B	to ID A1J14.4
From ID A1P14.4	to ID P13-48 (S201-12)
From ID P12-52 (S201-4)	to ID A1P12.44
From ID A1J12.44	to ID A1J10.4
From ID A1P10.4	to ID P11-71 (S507-2)
From ID P11-72 (S507-4)	to ID A1P9.27

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From ID A1J9.27	to ID BUS 2
From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From W7 P2-B34 (UUT J1-B34)	to W7 P1A-2F
From ID J1A-2F	to ID A1J14.12
From ID A1P14.12	to ID P13-52 (S201-33)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND

Step 810

Description:

Send the "DISOUT L J1-A32" command to bring the EL BRAKE RL to low. The DMM is used to measure the UUT pin J1-B34, and the measured voltage should be 12.7 ± 0.5 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)

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From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-C33 (UUT J1-C33)	to W7 P1A-2B
From ID J1A-2B	to ID A1J14.4
From ID A1P14.4	to ID P13-48 (S201-12)
From ID P12-52 (S201-4)	to ID A1P12.44
From ID A1J12.44	to ID A1J10.4
From ID A1P10.4	to ID P11-71 (S507-2)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From W7 P2-B34 (UUT J1-B34)	to W7 P1A-2F
From ID J1A-2F	to ID A1J14.12
From ID A1P14.12	to ID P13-52 (S201-33)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND

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Step 811

Description:

Send the "DISOUT H J1-A32" command to bring the EL BRAKE RL to high. The DMM is used to measure the UUT pin J1-A32, and the measured voltage should be less than or equal to 0.80 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From W7 P2-C33 (UUT J1-C33)	to W7 P1A-2B
From ID J1A-2B	to ID A1J14.4
From ID A1P14.4	to ID P13-48 (S201-12)
From ID P12-52 (S201-4)	to ID A1P12.44
From ID A1J12.44	to ID A1J10.4
From ID A1P10.4	to ID P11-71 (S507-2)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

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From W7 P2-A32 (UUT J1-A32)	to W7 P1A-9F
From ID J1A-9F	to ID A1J14.26
From ID A1P14.26	to ID P13-64 (S202-50)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6

Step 812

Description:

Connect J1-C32 to 28V through 698 ohm resistor. Connect J1-A32 to GND. The DMM is used to measure the UUT pin J1-C32 (HI) to J1-A2 (LO), and the measured voltage should be 28.0 ± 1.0 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

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From ID BUS 6	to ID A1J8.48
From ID A1P8.48	to ID P10-171 (S301-50)
From ID P10-42 (S301-49)	to ID A1P7.24
From ID A1J7.24	to ID A1J4.16
From ID A1P4.16	to ID R109.1
From ID R109.2	to ID A1P4.9
From ID A1J4.9	to +28V
From W7 P2-C32 (UUT J1-C32)	to W7 P1A-1B
From ID J1A-1B	to ID A1J14.3
From ID A1P14.3	to ID P13-80 (S201-11)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6
From W7 P2-A32 (UUT J1-A32)	to W7 P1A-9F
From ID J1A-9F	to ID A1J14.26
From ID A1P14.26	to ID P13-64 (S202-50)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-146 (S509-10)	to ID A1P9.6
From ID A1J9.6	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 813

Description:

Connect J1-B32 to 28V through 26.7 ohm pull up resistor. Send the "DISOUT H J1-B32" command to bring the AZ BRAKE RL to low. The DMM is used to measure the UUT pin J1-A33 (HI) to J1-A2 (LO), and the measured voltage should be 28.0 ± 1.0 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1

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From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From W7 J2-B32 (UUT J1-B32)	to W7 P1A-13F
From ID J1A-13F	to ID A1J4.13
From ID A1P4.13	to ID R103.1
From ID R103.2	to ID A1P4.1
From ID A1J4.1	to ID A1J2.11
From ID A1P2.11	to ID P10-26 (S101-17)
From ID P10-90 (S101-18)	to ID A1P2.1
From ID A1J2.1	to ID A1J1.6
From ID A1P1.6	to ID P1-28 (DC10-HI)
From W7 P2-A33 (UUT J1-A33)	to W7 P1A-2A
From ID J1A-2A	to ID A1J14.2
From ID A1P14.2	to ID P13-14 (S201-10)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 814

Description:

The DMM is used to measure the UUT pin J1-A34 (HI)to J1-A2 (LO), and the measured voltage should be 28.0 ± 0.1 Vdc.

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Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From W7 J2-B32 (UUT J1-B32)	to W7 P1A-13F
From ID J1A-13F	to ID A1J4.13
From ID A1P4.13	to ID R103.1
From ID R103.2	to ID A1P4.1
From ID A1J4.1	to ID A1J2.11
From ID A1P2.11	to ID P10-26 (S101-17)
From ID P10-90 (S101-18)	to ID A1P2.1
From ID A1J2.1	to ID A1J1.6
From ID A1P1.6	to ID P1-28 (DC10-HI)
From W7 P2-A34 (UUT J1-A34)	to W7 P1A-10E
From ID J1A-10E	to ID A1J14.27
From ID A1P14.27	to ID P13-94 (S202-52)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12

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From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6

Step 815

Description:

Connect J1-B33 through 5.6K pull down resistor to GND. The DMM is used to measure the UUT pin J1-B33 (HI)to J1-A2 (LO), and the measured voltage should be less than 0.4 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1

From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

From W7 J2-B32 (UUT J1-B32)	to W7 P1A-13F
From ID J1A-13F	to ID A1J4.13
From ID A1P4.13	to ID R103.1
From ID R103.2	to ID A1P4.1
From ID A1J4.1	to ID A1J2.11

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From ID A1P2.11	to ID P10-26 (S101-17)
From ID P10-90 (S101-18)	to ID A1P2.1
From ID A1J2.1	to ID A1J1.6
From ID A1P1.6	to ID P1-28 (DC10-HI)
From W7 P2-B33 (UUT J1-B33)	to W7 P1A-13D
From ID J1A-13D	to ID A1J14.48
From ID A1P14.48	to ID P13-63 (S202-51)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND

Step 816

Description:

Send the "DISOUT L J1-B32" command to bring the AZ BRAKE RL to low. The DMM is used to measure the UUT pin J1-A34 (HI) to J1-A2 (LO), and the measured voltage should be less than 0.8 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2

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From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From W7 J2-B32 (UUT J1-B32)	to W7 P1A-13F
From ID J1A-13F	to ID A1J4.13
From ID A1P4.13	to ID R103.1
From ID R103.2	to ID A1P4.1
From ID A1J4.1	to ID A1J2.11
From ID A1P2.11	to ID P10-26 (S101-17)
From ID P10-90 (S101-18)	to ID A1P2.1
From ID A1J2.1	to ID A1J1.6
From ID A1P1.6	to ID P1-28 (DC10-HI)
From W7 P2-A34 (UUT J1-A34)	to W7 P1A-10E
From ID J1A-10E	to ID A1J14.27
From ID A1P14.27	to ID P13-94 (S202-52)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6

Step 817

Description:

Connect J1-B33 through 5.6K pull down resistor to GND. The DMM is used to measure the UUT pin J1-B33 (HI) to J1-A2 (LO), and the measured voltage should be 12.7 ± 0.5 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C20 (UUT J1-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-164 (S506-3)	to ID A1P9.23
From ID A1J9.23	to ID BUS 1
From ID P1-4 (DC2-HI)	to ID A1P1.2

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From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
From W7 J2-B32 (UUT J1-B32)	to W7 P1A-13F
From ID J1A-13F	to ID A1J4.13
From ID A1P4.13	to ID R103.1
From ID R103.2	to ID A1P4.1
From ID A1J4.1	to ID A1J2.11
From ID A1P2.11	to ID P10-26 (S101-17)
From ID P10-90 (S101-18)	to ID A1P2.1
From ID A1J2.1	to ID A1J1.6
From ID A1P1.6	to ID P1-28 (DC10-HI)
From W7 P2-B33 (UUT J1-B33)	to W7 P1A-13D
From ID J1A-13D	to ID A1J14.48
From ID A1P14.48	to ID P13-63 (S202-51)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 6	to ID A1J8.42
From ID A1P8.42	to ID P10-78 (S301-83)
From ID P10-81 (S301-84)	to ID A1P8.5
From ID A1J8.5	to ID R7.1
From ID R7.2	to GROUND

Step 818

Description:

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Connect J1-A33 through 698 ohm pull up resistor to 28V. Connect J1-B32 to GND. The DMM is used to measure the UUT pin J1-A33 (HI) to J1-B32 (LO), and the measured voltage should be 28.0 ± 1.0 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A33 (UUT J1-A33)	to W7 P1A-2A
From ID J1A-2A	to ID A1J14.2
From ID A1P14.2	to ID P13-14 (S201-10)

From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

From ID BUS 6	to ID A1J8.48
From ID A1P8.48	to ID P10-171 (S301-50)
From ID P10-42 (S301-49)	to ID A1P7.24
From ID A1J7.24	to ID A1J4.16
From ID A1P4.16	to ID R109.1
From ID R109.2	to ID A1P4.9
From ID A1J4.9	to +28V

From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6

From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

From W7 P2-B32 (UUT J1-B32)	to W7 P1B-10D
From ID J1B-10D	to ID A1J13.19
From ID A1P13.19	to ID P12-42 (S701-35)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

2.13 MODULE 9 – ANALOG INPUT TESTS

Description:

The Analog Inputs, Gyro and Tachometer series of tests will activate each of the Analog inputs of the CCA. Input test signals will be inserted differentially into each analog input amplifier and measurements of expected output voltage level including results when applying 0 input and when applying equal inputs at non-zero levels to each input (differentially) to verify adequate Common Mode Rejection of the input amplifiers. Gain accuracy will be measured based upon providing a known signal level into the analog inputs. In addition tests of the interfaces that provide gain selection (4 inputs) will be tested in low gain and high gain mode via gain switching.

The special inputs test for El and Az Test Command Inputs will verify the proper gain and output voltage is seen from the elevation and azimuth input amplifiers when the special "Test Command" inputs are used to apply an input signal. In addition to the Elevation and Azimuth special test command inputs, the analog bit enable override controls gains test will also be executed to check all components associated with these inputs.

Refer to [Reference Drawings](#) when diagnosing connection paths.

Step 901

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-C4. Apply 0.0 ± 0.1 Vdc to UUT pin J1-A5. Send the "ANAIN J1-C4/J1-A5" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Boot Up"

See "Serial Comm"

From W7 P2-C4 (UUT J1-C4)	to W7 P1A-3C
From ID J1A-3C	to ID A1J15.3
From ID A1P15.3	to ID P13-70 (S701-9)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20

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From ID A1J9.20	to ID BUS 7
From W7 P2-A5 (UUT J1-A5)	to W7 P1A-4A
From ID J1A-4A	to ID A1J15.4
From ID A1P15.4	to ID P13-38 (S701-10)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 902

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-C4. Apply 5.0 ± 0.1 Vdc to UUT pin J1-A5. Send the "ANAIN J1-C4/J1-A5" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C4 (UUT J1-C4)	to W7 P1A-3C
From ID J1A-3C	to ID A1J15.3
From ID A1P15.3	to ID P13-70 (S701-9)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-A5 (UUT J1-A5)	to W7 P1A-4A
From ID J1A-4A	to ID A1J15.4
From ID A1P15.4	to ID P13-38 (S701-10)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 903

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-C4. Apply 0.0 ± 0.1 Vdc to UUT pin J1-A5. Send the "ANAIN J1-C4/J1-A5" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -7.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

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See "Serial Comm"

From W7 P2-C4 (UUT J1-C4)	to W7 P1A-3C
From ID J1A-3C	to ID A1J15.3
From ID A1P15.3	to ID P13-70 (S701-9)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-A5 (UUT J1-A5)	to W7 P1A-4A
From ID J1A-4A	to ID A1J15.4
From ID A1P15.4	to ID P13-38 (S701-10)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 904

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-C4. Apply 5.0 ± 0.1 Vdc to UUT pin J1-A5. Send the "ANAIN J1-C4/J1-A5" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 7.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C4 (UUT J1-C4)	to W7 P1A-3C
From ID J1A-3C	to ID A1J15.3
From ID A1P15.3	to ID P13-70 (S701-9)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-A5 (UUT J1-A5)	to W7 P1A-4A
From ID J1A-4A	to ID A1J15.4

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From ID A1P15.4	to ID P13-38 (S701-10)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 905

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-B6. Apply 0.0 ± 0.1 Vdc to UUT pin J1-C6. Send the "ANAIN J1-B6/J1-C6" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B6 (UUT J1-B6)	to W7 P1A-5A
From ID J1A-5A	to ID A1J15.7
From ID A1P15.7	to ID P13-7 (S701-17)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C6 (UUT J1-C6)	to W7 P1A-10B
From ID J1A-10B	to ID A1J15.22
From ID A1P15.22	to ID P13-71 (S701-18)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 906

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-B6. Apply 5.0 ± 0.1 Vdc to UUT pin J1-C6. Send the "ANAIN J1-B6/J1-C6" command to verify the selected

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amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B6 (UUT J1-B6)	to W7 P1A-5A
From ID J1A-5A	to ID A1J15.7
From ID A1P15.7	to ID P13-7 (S701-17)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C6 (UUT J1-C6)	to W7 P1A-10B
From ID J1A-10B	to ID A1J15.22
From ID A1P15.22	to ID P13-71 (S701-18)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 907

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-B6. Apply 0.0 ± 0.1 Vdc to UUT pin J1-C6. Send the "ANAIN J1-B6/J1-C6" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B6 (UUT J1-B6)	to W7 P1A-5A
From ID J1A-5A	to ID A1J15.7
From ID A1P15.7	to ID P13-7 (S701-17)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C6 (UUT J1-C6)	to W7 P1A-10B
From ID J1A-10B	to ID A1J15.22
From ID A1P15.22	to ID P13-71 (S701-18)
From ID P12-44 (S701-2)	to ID A1P12.48

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From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 908

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-B6. Apply 5.0 ± 0.1 Vdc to UUT pin J1-C6. Send the "ANAIN J1-B6/J1-C6" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B6 (UUT J1-B6)	to W7 P1A-5A
From ID J1A-5A	to ID A1J15.7
From ID A1P15.7	to ID P13-7 (S701-17)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-C6 (UUT J1-C6)	to W7 P1A-10B
From ID J1A-10B	to ID A1J15.22
From ID A1P15.22	to ID P13-71 (S701-18)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

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Step 909

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-B7. Apply 0.0 ± 0.1 Vdc to UUT pin J1-C7. Send the "ANAIN J1-B7/J1-C7" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B7 (UUT J1-B7)	to W7 P1B-12F
From ID J1B-12F	to ID A1J13.15
From ID A1P13.15	to ID P12-71 (S701-19)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-C7 (UUT J1-C7)	to W7 P1B-8D
From ID J1B-8D	to ID A1J13.25
From ID A1P13.25	to ID P12-39 (S701-20)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 910

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-B7. Apply 5.0 ± 0.1 Vdc to UUT pin J1-C7. Send the "ANAIN J1-B7/J1-C7" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B7 (UUT J1-B7)	to W7 P1B-12F
From ID J1B-12F	to ID A1J13.15
From ID A1P13.15	to ID P12-71 (S701-19)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

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From W7 P2-C7 (UUT J1-C7)	to W7 P1B-8D
From ID J1B-8D	to ID A1J13.25
From ID A1P13.25	to ID P12-39 (S701-20)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 911

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-B7. Apply 0.0 ± 0.1 Vdc to UUT pin J1-C7. Send the "ANAIN J1-B7/J1-C7" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B7 (UUT J1-B7)	to W7 P1B-12F
From ID J1B-12F	to ID A1J13.15
From ID A1P13.15	to ID P12-71 (S701-19)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C7 (UUT J1-C7)	to W7 P1B-8D
From ID J1B-8D	to ID A1J13.25
From ID A1P13.25	to ID P12-39 (S701-20)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 912

Description:

Date: 04 March 2016

Apply 0.0 ± 0.1 Vdc to UUT pin J1-B7. Apply 5.0 ± 0.1 Vdc to UUT pin J1-C7. Send the "ANAIN J1-B7/J1-C7" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B7 (UUT J1-B7)	to W7 P1B-12F
From ID J1B-12F	to ID A1J13.15
From ID A1P13.15	to ID P12-71 (S701-19)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

From W7 P2-C7 (UUT J1-C7)	to W7 P1B-8D
From ID J1B-8D	to ID A1J13.25
From ID A1P13.25	to ID P12-39 (S701-20)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 913

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-C10. Apply 0.0 ± 0.1 Vdc to UUT pin J1-A11. Send the "ANAIN J1-C10/J1-A11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C10 (UUT J1-C10)	to W7 P1A-4B
From ID J1A-4B	to ID A1J15.5
From ID A1P15.5	to ID P13-72 (S701-15)

From ID P12-76 (S701-1)	to ID A1P12.50
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From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-A11 (UUT J1-A11)	to W7 P1A-8A
From ID J1A-8A	to ID A1J15.16
From ID A1P15.16	to ID P13-40 (S701-16)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 914

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-C10. Apply 5.0 ± 0.1 Vdc to UUT pin J1-A11. Send the "ANAIN J1-C10/J1-A11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C10 (UUT J1-C10)	to W7 P1A-4B
From ID J1A-4B	to ID A1J15.5
From ID A1P15.5	to ID P13-72 (S701-15)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-A11 (UUT J1-A11)	to W7 P1A-8A
From ID J1A-8A	to ID A1J15.16
From ID A1P15.16	to ID P13-40 (S701-16)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 915

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-C10. Apply 0.0 ± 0.1 Vdc to UUT pin J1-A11. Send the "ANAIN J1-C10/J1-A11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -5.00 ± 0.20 Vdc.

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Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C10 (UUT J1-C10)	to W7 P1A-4B
From ID J1A-4B	to ID A1J15.5
From ID A1P15.5	to ID P13-72 (S701-15)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-A11 (UUT J1-A11)	to W7 P1A-8A
From ID J1A-8A	to ID A1J15.16
From ID A1P15.16	to ID P13-40 (S701-16)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 916

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-C10. Apply 5.0 ± 0.1 Vdc to UUT pin J1-A11. Send the "ANAIN J1-C10/J1-A11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C10 (UUT J1-C10)	to W7 P1A-4B
From ID J1A-4B	to ID A1J15.5
From ID A1P15.5	to ID P13-72 (S701-15)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

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From W7 P2-A11 (UUT J1-A11)	to W7 P1A-8A
From ID J1A-8A	to ID A1J15.16
From ID A1P15.16	to ID P13-40 (S701-16)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 917

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-B11. Apply 0.0 ± 0.1 Vdc to UUT pin J1-C11. Send the "ANAIN J1-B11/J1-C11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B11 (UUT J1-B11)	to W7 P1B-11D
From ID J1B-11D	to ID A1J13.16
From ID A1P13.16	to ID P12-6 (S701-21)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C11 (UUT J1-C11)	to W7 P1B-8F
From ID J1B-8F	to ID A1J13.27
From ID A1P13.27	to ID P12-70 (S701-22)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 918

Description:

Date: 04 March 2016

Apply 5.0 ± 0.1 Vdc to UUT pin J1-B11. Apply 5.0 ± 0.1 Vdc to UUT pin J1-C11. Send the "ANAIN J1-B11/J1-C11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B11 (UUT J1-B11)	to W7 P1B-11D
From ID J1B-11D	to ID A1J13.16
From ID A1P13.16	to ID P12-6 (S701-21)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C11 (UUT J1-C11)	to W7 P1B-8F
From ID J1B-8F	to ID A1J13.27
From ID A1P13.27	to ID P12-70 (S701-22)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 919

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-B11. Apply 0.0 ± 0.1 Vdc to UUT pin J1-C11. Send the "ANAIN J1-B11/J1-C11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B11 (UUT J1-B11)	to W7 P1B-11D
From ID J1B-11D	to ID A1J13.16
From ID A1P13.16	to ID P12-6 (S701-21)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C11 (UUT J1-C11)	to W7 P1B-8F
From ID J1B-8F	to ID A1J13.27
From ID A1P13.27	to ID P12-70 (S701-22)

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From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 920

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-B11. Apply 5.0 ± 0.1 Vdc to UUT pin J1-C11. Send the "ANAIN J1-B11/J1-C11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B11 (UUT J1-B11)	to W7 P1B-11D
From ID J1B-11D	to ID A1J13.16
From ID A1P13.16	to ID P12-6 (S701-21)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-C11 (UUT J1-C11)	to W7 P1B-8F
From ID J1B-8F	to ID A1J13.27
From ID A1P13.27	to ID P12-70 (S701-22)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Date: 04 March 2016

Step 921

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-B3. Apply 0.0 ± 0.1 Vdc to UUT pin J1-C3. Send the "ANAIN J1-B3/J1-C3" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B3 (UUT J1-B3)	to W7 P1A-3A
From ID J1A-3A	to ID A1J15.1
From ID A1P15.1	to ID P13-39 (S701-7)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C3 (UUT J1-C3)	to W7 P1A-3B
From ID J1A-3B	to ID A1J15.2
From ID A1P15.2	to ID P13-6 (S701-8)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 922

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-B3. Apply 5.0 ± 0.1 Vdc to UUT pin J1-C3. Send the "ANAIN J1-B3/J1-C3" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B3 (UUT J1-B3)	to W7 P1A-3A
From ID J1A-3A	to ID A1J15.1
From ID A1P15.1	to ID P13-39 (S701-7)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

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From W7 P2-C3 (UUT J1-C3)	to W7 P1A-3B
From ID J1A-3B	to ID A1J15.2
From ID A1P15.2	to ID P13-6 (S701-8)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 923

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-B3. Apply 0.0 ± 0.1 Vdc to UUT pin J1-C3. Send the "ANAIN J1-B3/J1-C3" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 7.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B3 (UUT J1-B3)	to W7 P1A-3A
From ID J1A-3A	to ID A1J15.1
From ID A1P15.1	to ID P13-39 (S701-7)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C3 (UUT J1-C3)	to W7 P1A-3B
From ID J1A-3B	to ID A1J15.2
From ID A1P15.2	to ID P13-6 (S701-8)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 924

Description:

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Apply 0.0 ± 0.1 Vdc to UUT pin J1-B3. Apply 5.0 ± 0.1 Vdc to UUT pin J1-C3. Send the "ANAIN J1-B3/J1-C3" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -7.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B3 (UUT J1-B3)	to W7 P1A-3A
From ID J1A-3A	to ID A1J15.1
From ID A1P15.1	to ID P13-39 (S701-7)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

From W7 P2-C3 (UUT J1-C3)	to W7 P1A-3B
From ID J1A-3B	to ID A1J15.2
From ID A1P15.2	to ID P13-6 (S701-8)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 925

Description:

Apply 0.8 ± 0.1 Vdc to UUT pin J1-B3. Apply 0.0 ± 0.1 Vdc to UUT pin J1-C3. Send the "GAIN H" command to set the switched gains to the high state. Send the "ANAIN J1-B3/J1-C3" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 8.952 ± 0.300 Vdc. Send the "GAIN L" command to set the switched gains to the low state

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B3 (UUT J1-B3)	to W7 P1A-3A
From ID J1A-3A	to ID A1J15.1
From ID A1P15.1	to ID P13-39 (S701-7)

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From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C3 (UUT J1-C3)	to W7 P1A-3B
From ID J1A-3B	to ID A1J15.2
From ID A1P15.2	to ID P13-6 (S701-8)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 926

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-A4. Apply 0.0 ± 0.1 Vdc to UUT pin J1-B4. Send the "ANAIN J1-A4/J1-B4" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A4 (UUT J1-A4)	to W7 P1B-14E
From ID J1B-14E	to ID A1J13.9
From ID A1P13.9	to ID P12-36 (S701-5)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-B4 (UUT J1-B4)	to W7 P1B-13E
From ID J1B-13E	to ID A1J13.10
From ID A1P13.10	to ID P12-3 (S701-6)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20

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From ID A1J9.20

to ID BUS 7

Step 927

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-A4. Apply 5.0 ± 0.1 Vdc to UUT pin J1-B4. Send the "ANAIN J1-A4/J1-B4" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A4 (UUT J1-A4)

to W7 P1B-14E

From ID J1B-14E

to ID A1J13.9

From ID A1P13.9

to ID P12-36 (S701-5)

From ID P12-76 (S701-1)

to ID A1P12.50

From ID A1J12.50

to ID A1J10.3

From ID A1P10.3

to ID P11-194 (S506-1)

From ID P11-68 (S506-9)

to ID A1P9.20

From ID A1J9.20

to ID BUS 7

From W7 P2-B4 (UUT J1-B4)

to W7 P1B-13E

From ID J1B-13E

to ID A1J13.10

From ID A1P13.10

to ID P12-3 (S701-6)

From ID P12-44 (S701-2)

to ID A1P12.48

From ID A1J12.48

to ID A1J10.1

From ID A1P10.1

to ID P11-162 (S506-2)

From ID P11-68 (S506-9)

to ID A1P9.20

From ID A1J9.20

to ID BUS 7

Step 928

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-A4. Apply 0.0 ± 0.1 Vdc to UUT pin J1-B4. Send the "ANAIN J1-A4/J1-B4" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 7.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A4 (UUT J1-A4)

to W7 P1B-14E

From ID J1B-14E

to ID A1J13.9

From ID A1P13.9

to ID P12-36 (S701-5)

From ID P12-76 (S701-1)

to ID A1P12.50

From ID A1J12.50

to ID A1J10.3

From ID A1P10.3

to ID P11-194 (S506-1)

From ID P11-68 (S506-9)

to ID A1P9.20

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From ID A1J9.20	to ID BUS 7
From W7 P2-B4 (UUT J1-B4)	to W7 P1B-13E
From ID J1B-13E	to ID A1J13.10
From ID A1P13.10	to ID P12-3 (S701-6)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 929

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-A4. Apply 5.0 ± 0.1 Vdc to UUT pin J1-B4. Send the "ANAIN J1-A4/J1-B4" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -7.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A4 (UUT J1-A4)	to W7 P1B-14E
From ID J1B-14E	to ID A1J13.9
From ID A1P13.9	to ID P12-36 (S701-5)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-B4 (UUT J1-B4)	to W7 P1B-13E
From ID J1B-13E	to ID A1J13.10
From ID A1P13.10	to ID P12-3 (S701-6)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)

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From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 930

Description:

Apply 0.8 ± 0.1 Vdc to UUT pin J1-A4. Apply 0.0 ± 0.1 Vdc to UUT pin J1-B4. Send the "GAIN H" command to set the switched gains to the high state. Send the "ANAIN J1-A4/J1-B4" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 8.952 ± 0.300 Vdc. Send the "GAIN L" command to set the switched gains to the low state

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A4 (UUT J1-A4)	to W7 P1B-14E
From ID J1B-14E	to ID A1J13.9
From ID A1P13.9	to ID P12-36 (S701-5)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-B4 (UUT J1-B4)	to W7 P1B-13E
From ID J1B-13E	to ID A1J13.10
From ID A1P13.10	to ID P12-3 (S701-6)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 931

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-A8. Apply 0.0 ± 0.1 Vdc to UUT pin J1-B8. Send the "ANAIN J1-A8/J1-B8" command to verify the selected

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amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 \pm 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A8 (UUT J1-A8)	to W7 P1B-14F
From ID J1B-14F	to ID A1J13.11
From ID A1P13.11	to ID P12-38 (S701-11)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-B8 (UUT J1-B8)	to W7 P1B-13F
From ID J1B-13F	to ID A1J13.12
From ID A1P13.12	to ID P12-5 (S701-12)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 932

Description:

Apply 5.0 \pm 0.1 Vdc to UUT pin J1-A8. Apply 5.0 \pm 0.1 Vdc to UUT pin J1-B8. Send the "ANAIN J1-A8/J1-B8" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 \pm 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A8 (UUT J1-A8)	to W7 P1B-14F
From ID J1B-14F	to ID A1J13.11
From ID A1P13.11	to ID P12-38 (S701-11)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-B8 (UUT J1-B8)	to W7 P1B-13F
From ID J1B-13F	to ID A1J13.12
From ID A1P13.12	to ID P12-5 (S701-12)

From ID P12-44 (S701-2)	to ID A1P12.48
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From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 933

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-A8. Apply 0.0 ± 0.1 Vdc to UUT pin J1-B8. Send the "ANAIN J1-A8/J1-B8" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 4.35 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From W7 P2-A8 (UUT J1-A8)	to W7 P1B-14F
From ID J1B-14F	to ID A1J13.11
From ID A1P13.11	to ID P12-38 (S701-11)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-B8 (UUT J1-B8)	to W7 P1B-13F
From ID J1B-13F	to ID A1J13.12
From ID A1P13.12	to ID P12-5 (S701-12)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 934

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-A8. Apply 5.0 ± 0.1 Vdc to UUT pin J1-B8. Send the "ANAIN J1-A8/J1-B8" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -4.35 ± 0.20 Vdc.

Connection Path is as follows:

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See "UUT Power"
See "Serial Comm"

From W7 P2-A8 (UUT J1-A8)	to W7 P1B-14F
From ID J1B-14F	to ID A1J13.11
From ID A1P13.11	to ID P12-38 (S701-11)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-B8 (UUT J1-B8)	to W7 P1B-13F
From ID J1B-13F	to ID A1J13.12
From ID A1P13.12	to ID P12-5 (S701-12)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 935

Description:

Apply 0.8 ± 0.1 Vdc to UUT pin J1-A8. Apply 0.0 ± 0.1 Vdc to UUT pin J1-B8. Send the "GAIN H" command to set the switched gains to the high state. Send the "ANAIN J1-A8/J1-B8" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 5.56 ± 0.20 Vdc. Send the "GAIN L" command to set the switched gains to the low state

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From W7 P2-A8 (UUT J1-A8)	to W7 P1B-14F
From ID J1B-14F	to ID A1J13.11
From ID A1P13.11	to ID P12-38 (S701-11)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

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From W7 P2-B8 (UUT J1-B8)	to W7 P1B-13F
From ID J1B-13F	to ID A1J13.12
From ID A1P13.12	to ID P12-5 (S701-12)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 936

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-C8. Apply 0.0 ± 0.1 Vdc to UUT pin J1-A9. Send the "ANAIN J1-C8/J1-A9" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C8 (UUT J1-C8)	to W7 P1B-12D
From ID J1B-12D	to ID A1J13.13
From ID A1P13.13	to ID P12-69 (S701-13)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-A9 (UUT J1-A9)	to W7 P1B-12E
From ID J1B-12E	to ID A1J13.14
From ID A1P13.14	to ID P12-37 (S701-14)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 937

Description:

Date: 04 March 2016

Apply 5.0 ± 0.1 Vdc to UUT pin J1-C8. Apply 5.0 ± 0.1 Vdc to UUT pin J1-A9. Send the "ANAIN J1-C8/J1-A9" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C8 (UUT J1-C8)	to W7 P1B-12D
From ID J1B-12D	to ID A1J13.13
From ID A1P13.13	to ID P12-69 (S701-13)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-A9 (UUT J1-A9)	to W7 P1B-12E
From ID J1B-12E	to ID A1J13.14
From ID A1P13.14	to ID P12-37 (S701-14)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 938

Description:

Apply 5.0 ± 0.1 Vdc to UUT pin J1-C8. Apply 0.0 ± 0.1 Vdc to UUT pin J1-A9. Send the "ANAIN J1-C8/J1-A9" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 4.35 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C8 (UUT J1-C8)	to W7 P1B-12D
From ID J1B-12D	to ID A1J13.13
From ID A1P13.13	to ID P12-69 (S701-13)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-A9 (UUT J1-A9)	to W7 P1B-12E
From ID J1B-12E	to ID A1J13.14
From ID A1P13.14	to ID P12-37 (S701-14)

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From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 939

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-C8. Apply 5.0 ± 0.1 Vdc to UUT pin J1-A9. Send the "ANAIN J1-C8/J1-A9" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -4.35 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C8 (UUT J1-C8)	to W7 P1B-12D
From ID J1B-12D	to ID A1J13.13
From ID A1P13.13	to ID P12-69 (S701-13)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

From W7 P2-A9 (UUT J1-A9)	to W7 P1B-12E
From ID J1B-12E	to ID A1J13.14
From ID A1P13.14	to ID P12-37 (S701-14)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Date: 04 March 2016

Step 940

Description:

Apply 0.8 ± 0.1 Vdc to UUT pin J1-C8. Apply 0.0 ± 0.1 Vdc to UUT pin J1-A9. Send the "GAIN H" command to set the switched gains to the high state. Send the "ANAIN J1-C8/J1-A9" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 5.56 ± 0.20 Vdc. Send the "GAIN L" command to set the switched gains to the low state

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C8 (UUT J1-C8)	to W7 P1B-12D
From ID J1B-12D	to ID A1J13.13
From ID A1P13.13	to ID P12-69 (S701-13)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-A9 (UUT J1-A9)	to W7 P1B-12E
From ID J1B-12E	to ID A1J13.14
From ID A1P13.14	to ID P12-37 (S701-14)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 941

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-C4. Apply -5.0 ± 0.1 Vdc to UUT pin J1-A5. Send the "ANAIN J1-C4/J1-A5" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

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From W7 P2-C4 (UUT J1-C4)	to W7 P1A-3C
From ID J1A-3C	to ID A1J15.3
From ID A1P15.3	to ID P13-70 (S701-9)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-A5 (UUT J1-A5)	to W7 P1A-4A
From ID J1A-4A	to ID A1J15.4
From ID A1P15.4	to ID P13-38 (S701-10)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 942

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-C4. Apply 0.0 ± 0.1 Vdc to UUT pin J1-A5. Send the "ANAIN J1-C4/J1-A5" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 7.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C4 (UUT J1-C4)	to W7 P1A-3C
From ID J1A-3C	to ID A1J15.3
From ID A1P15.3	to ID P13-70 (S701-9)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-A5 (UUT J1-A5)	to W7 P1A-4A
From ID J1A-4A	to ID A1J15.4
From ID A1P15.4	to ID P13-38 (S701-10)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)

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From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 943

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-C4. Apply -5.0 ± 0.1 Vdc to UUT pin J1-A5. Send the "ANAIN J1-C4/J1-A5" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -7.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C4 (UUT J1-C4)	to W7 P1A-3C
From ID J1A-3C	to ID A1J15.3
From ID A1P15.3	to ID P13-70 (S701-9)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-A5 (UUT J1-A5)	to W7 P1A-4A
From ID J1A-4A	to ID A1J15.4
From ID A1P15.4	to ID P13-38 (S701-10)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 944

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-B6. Apply -5.0 ± 0.1 Vdc to UUT pin J1-C6. Send the "ANAIN J1-B6/J1-C6" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Date: 04 March 2016

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B6 (UUT J1-B6)	to W7 P1A-5A
From ID J1A-5A	to ID A1J15.7
From ID A1P15.7	to ID P13-7 (S701-17)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C6 (UUT J1-C6)	to W7 P1A-10B
From ID J1A-10B	to ID A1J15.22
From ID A1P15.22	to ID P13-71 (S701-18)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 945

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-B6. Apply 0.0 ± 0.1 Vdc to UUT pin J1-C6. Send the "ANAIN J1-B6/J1-C6" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B6 (UUT J1-B6)	to W7 P1A-5A
From ID J1A-5A	to ID A1J15.7
From ID A1P15.7	to ID P13-7 (S701-17)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C6 (UUT J1-C6)	to W7 P1A-10B
From ID J1A-10B	to ID A1J15.22
From ID A1P15.22	to ID P13-71 (S701-18)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10

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From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 946

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-B6. Apply -5.0 ± 0.1 Vdc to UUT pin J1-C6. Send the "ANAIN J1-B6/J1-C6" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B6 (UUT J1-B6)	to W7 P1A-5A
From ID J1A-5A	to ID A1J15.7
From ID A1P15.7	to ID P13-7 (S701-17)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-C6 (UUT J1-C6)	to W7 P1A-10B
From ID J1A-10B	to ID A1J15.22
From ID A1P15.22	to ID P13-71 (S701-18)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 947

Description:

Date: 04 March 2016

Apply -5.0 ± 0.1 Vdc to UUT pin J1-B7. Apply -5.0 ± 0.1 Vdc to UUT pin J1-C7. Send the "ANAIN J1-B7/J1-C7" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B7 (UUT J1-B7)	to W7 P1B-12F
From ID J1B-12F	to ID A1J13.15
From ID A1P13.15	to ID P12-71 (S701-19)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-C7 (UUT J1-C7)	to W7 P1B-8D
From ID J1B-8D	to ID A1J13.25
From ID A1P13.25	to ID P12-39 (S701-20)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 948

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-B7. Apply 0.0 ± 0.1 Vdc to UUT pin J1-C7. Send the "ANAIN J1-B7/J1-C7" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B7 (UUT J1-B7)	to W7 P1B-12F
From ID J1B-12F	to ID A1J13.15
From ID A1P13.15	to ID P12-71 (S701-19)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-C7 (UUT J1-C7)	to W7 P1B-8D
From ID J1B-8D	to ID A1J13.25
From ID A1P13.25	to ID P12-39 (S701-20)

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From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 949

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-B7. Apply -5.0 ± 0.1 Vdc to UUT pin J1-C7. Send the "ANAIN J1-B7/J1-C7" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B7 (UUT J1-B7)	to W7 P1B-12F
From ID J1B-12F	to ID A1J13.15
From ID A1P13.15	to ID P12-71 (S701-19)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-C7 (UUT J1-C7)	to W7 P1B-8D
From ID J1B-8D	to ID A1J13.25
From ID A1P13.25	to ID P12-39 (S701-20)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Date: 04 March 2016

Step 950

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-C10. Apply -5.0 ± 0.1 Vdc to UUT pin J1-A11. Send the "ANAIN J1-C10/J1-A11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C10 (UUT J1-C10)	to W7 P1A-4B
From ID J1A-4B	to ID A1J15.5
From ID A1P15.5	to ID P13-72 (S701-15)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-A11 (UUT J1-A11)	to W7 P1A-8A
From ID J1A-8A	to ID A1J15.16
From ID A1P15.16	to ID P13-40 (S701-16)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 951

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-C10. Apply 0.0 ± 0.1 Vdc to UUT pin J1-A11. Send the "ANAIN J1-C10/J1-A11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C10 (UUT J1-C10)	to W7 P1A-4B
From ID J1A-4B	to ID A1J15.5
From ID A1P15.5	to ID P13-72 (S701-15)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

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From W7 P2-A11 (UUT J1-A11)	to W7 P1A-8A
From ID J1A-8A	to ID A1J15.16
From ID A1P15.16	to ID P13-40 (S701-16)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 952

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-C10. Apply -5.0 ± 0.1 Vdc to UUT pin J1-A11. Send the "ANAIN J1-C10/J1-A11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C10 (UUT J1-C10)	to W7 P1A-4B
From ID J1A-4B	to ID A1J15.5
From ID A1P15.5	to ID P13-72 (S701-15)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-A11 (UUT J1-A11)	to W7 P1A-8A
From ID J1A-8A	to ID A1J15.16
From ID A1P15.16	to ID P13-40 (S701-16)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18

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From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 953

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-B11. Apply -5.0 ± 0.1 Vdc to UUT pin J1-C11. Send the "ANAIN J1-B11/J1-C11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B11 (UUT J1-B11)	to W7 P1B-11D
From ID J1B-11D	to ID A1J13.16
From ID A1P13.16	to ID P12-6 (S701-21)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-C11 (UUT J1-C11)	to W7 P1B-8F
From ID J1B-8F	to ID A1J13.27
From ID A1P13.27	to ID P12-70 (S701-22)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 954

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-B11. Apply 0.0 ± 0.1 Vdc to UUT pin J1-C11. Send the "ANAIN J1-B11/J1-C11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B11 (UUT J1-B11)	to W7 P1B-11D
From ID J1B-11D	to ID A1J13.16
From ID A1P13.16	to ID P12-6 (S701-21)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3

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From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C11 (UUT J1-C11)	to W7 P1B-8F
From ID J1B-8F	to ID A1J13.27
From ID A1P13.27	to ID P12-70 (S701-22)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 955

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-B11. Apply -5.0 ± 0.1 Vdc to UUT pin J1-C11. Send the "ANAIN J1-B11/J1-C11" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -5.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B11 (UUT J1-B11)	to W7 P1B-11D
From ID J1B-11D	to ID A1J13.16
From ID A1P13.16	to ID P12-6 (S701-21)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-C11 (UUT J1-C11)	to W7 P1B-8F
From ID J1B-8F	to ID A1J13.27
From ID A1P13.27	to ID P12-70 (S701-22)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

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From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 956

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-B3. Apply -5.0 ± 0.1 Vdc to UUT pin J1-C3. Send the "ANAIN J1-B3/J1-C3" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From W7 P2-B3 (UUT J1-B3)	to W7 P1A-3A
From ID J1A-3A	to ID A1J15.1
From ID A1P15.1	to ID P13-39 (S701-7)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C3 (UUT J1-C3)	to W7 P1A-3B
From ID J1A-3B	to ID A1J15.2
From ID A1P15.2	to ID P13-6 (S701-8)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 957

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-B3. Apply 0.0 ± 0.1 Vdc to UUT pin J1-C3. Send the "ANAIN J1-B3/J1-C3" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -7.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"
See "Serial Comm"

From W7 P2-B3 (UUT J1-B3)	to W7 P1A-3A
From ID J1A-3A	to ID A1J15.1

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From ID A1P15.1	to ID P13-39 (S701-7)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-C3 (UUT J1-C3)	to W7 P1A-3B
From ID J1A-3B	to ID A1J15.2
From ID A1P15.2	to ID P13-6 (S701-8)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 958

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-B3. Apply -5.0 ± 0.1 Vdc to UUT pin J1-C3. Send the "ANAIN J1-B3/J1-C3" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 7.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B3 (UUT J1-B3)	to W7 P1A-3A
From ID J1A-3A	to ID A1J15.1
From ID A1P15.1	to ID P13-39 (S701-7)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-C3 (UUT J1-C3)	to W7 P1A-3B
From ID J1A-3B	to ID A1J15.2
From ID A1P15.2	to ID P13-6 (S701-8)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1

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From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 959

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-A4. Apply -5.0 ± 0.1 Vdc to UUT pin J1-B4. Send the "ANAIN J1-A4/J1-B4" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A4 (UUT J1-A4)	to W7 P1B-14E
From ID J1B-14E	to ID A1J13.9
From ID A1P13.9	to ID P12-36 (S701-5)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-B4 (UUT J1-B4)	to W7 P1B-13E
From ID J1B-13E	to ID A1J13.10
From ID A1P13.10	to ID P12-3 (S701-6)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 960

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-A4. Apply 0.0 ± 0.1 Vdc to UUT pin J1-B4. Send the "ANAIN J1-A4/J1-B4" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -7.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

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See "Serial Comm"

From W7 P2-A4 (UUT J1-A4)	to W7 P1B-14E
From ID J1B-14E	to ID A1J13.9
From ID A1P13.9	to ID P12-36 (S701-5)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-B4 (UUT J1-B4)	to W7 P1B-13E
From ID J1B-13E	to ID A1J13.10
From ID A1P13.10	to ID P12-3 (S701-6)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 961

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-A4. Apply -5.0 ± 0.1 Vdc to UUT pin J1-B4. Send the "ANAIN J1-A4/J1-B4" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 7.00 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A4 (UUT J1-A4)	to W7 P1B-14E
From ID J1B-14E	to ID A1J13.9
From ID A1P13.9	to ID P12-36 (S701-5)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-B4 (UUT J1-B4)	to W7 P1B-13E
From ID J1B-13E	to ID A1J13.10

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From ID A1P13.10	to ID P12-3 (S701-6)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 962

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-A8. Apply -5.0 ± 0.1 Vdc to UUT pin J1-B8. Send the "ANAIN J1-A8/J1-B8" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A8 (UUT J1-A8)	to W7 P1B-14F
From ID J1B-14F	to ID A1J13.11
From ID A1P13.11	to ID P12-38 (S701-11)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-B8 (UUT J1-B8)	to W7 P1B-13F
From ID J1B-13F	to ID A1J13.12
From ID A1P13.12	to ID P12-5 (S701-12)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 963

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-A8. Apply 0.0 ± 0.1 Vdc to UUT pin J1-B8. Send the "ANAIN J1-A8/J1-B8" command to verify the

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selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -4.35 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A8 (UUT J1-A8)	to W7 P1B-14F
From ID J1B-14F	to ID A1J13.11
From ID A1P13.11	to ID P12-38 (S701-11)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-B8 (UUT J1-B8)	to W7 P1B-13F
From ID J1B-13F	to ID A1J13.12
From ID A1P13.12	to ID P12-5 (S701-12)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 964

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-A8. Apply -5.0 ± 0.1 Vdc to UUT pin J1-B8. Send the "ANAIN J1-A8/J1-B8" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 4.35 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A8 (UUT J1-A8)	to W7 P1B-14F
From ID J1B-14F	to ID A1J13.11
From ID A1P13.11	to ID P12-38 (S701-11)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)

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From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-B8 (UUT J1-B8)	to W7 P1B-13F
From ID J1B-13F	to ID A1J13.12
From ID A1P13.12	to ID P12-5 (S701-12)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 965

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-C8. Apply -5.0 ± 0.1 Vdc to UUT pin J1-A9. Send the "ANAIN J1-C8/J1-A9" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 0.00 ± 0.10 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C8 (UUT J1-C8)	to W7 P1B-12D
From ID J1B-12D	to ID A1J13.13
From ID A1P13.13	to ID P12-69 (S701-13)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From W7 P2-A9 (UUT J1-A9)	to W7 P1B-12E
From ID J1B-12E	to ID A1J13.14
From ID A1P13.14	to ID P12-37 (S701-14)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

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Step 966

Description:

Apply -5.0 ± 0.1 Vdc to UUT pin J1-C8. Apply 0.0 ± 0.1 Vdc to UUT pin J1-A9. Send the "ANAIN J1-C8/J1-A9" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal -4.35 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C8 (UUT J1-C8)	to W7 P1B-12D
From ID J1B-12D	to ID A1J13.13
From ID A1P13.13	to ID P12-69 (S701-13)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

From W7 P2-A9 (UUT J1-A9)	to W7 P1B-12E
From ID J1B-12E	to ID A1J13.14
From ID A1P13.14	to ID P12-37 (S701-14)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 967

Description:

Apply 0.0 ± 0.1 Vdc to UUT pin J1-C8. Apply -5.0 ± 0.1 Vdc to UUT pin J1-A9. Send the "ANAIN J1-C8/J1-A9" command to verify the selected amplifier for the SSP. The SSP should respond by sending ASCII decimal 4.35 ± 0.20 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C8 (UUT J1-C8)	to W7 P1B-12D
From ID J1B-12D	to ID A1J13.13

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From ID A1P13.13	to ID P12-69 (S701-13)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-A9 (UUT J1-A9)	to W7 P1B-12E
From ID J1B-12E	to ID A1J13.14
From ID A1P13.14	to ID P12-37 (S701-14)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 968

Description:

Apply $+8.0 \pm 0.1$ Vdc to the UUT pins J1-A6 (HI) to J1-A2 (LO). Send the "ANAIN J1-A6" command to read the output voltage of the amplifier from the SSP. The SSP should respond by sending ASCII decimal 7.28 ± 0.40 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A6 (UUT J1-A6)	to W7 P1B-14D
From ID J1B-14D	to ID A1J13.7
From ID A1P13.7	to ID P12-4 (S701-3)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 969

Description:

Apply $+8.0 \pm 0.1$ Vdc to the UUT pins J1-A7 (HI) to J1-A2 (LO). Send the "ANAIN J1-A7" command to read the output voltage of the

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amplifier from the SSP. The SSP should respond by sending ASCII decimal 7.28 ± 0.40 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A7 (UUT J1-A7)	to W7 P1B-13D
From ID J1B-13D	to ID A1J13.8
From ID A1P13.8	to ID P12-68 (S701-4)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 970

Description:

Apply -8.0 ± 0.1 Vdc to the UUT pins J1-A6 (HI) to J1-A2 (LO). Send the "ANAIN J1-A6" command to read the output voltage of the amplifier from the SSP. The SSP should respond by sending ASCII decimal 7.28 ± 0.40 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A6 (UUT J1-A6)	to W7 P1B-14D
From ID J1B-14D	to ID A1J13.7
From ID A1P13.7	to ID P12-4 (S701-3)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 971

Description:

Apply -8.0 ± 0.1 Vdc to the UUT pins J1-A7 (HI) to J1-A2 (LO). Send the "ANAIN J1-A7" command to read the output voltage of the amplifier from the SSP. The SSP should respond by sending ASCII decimal 7.28 ± 0.40 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A7 (UUT J1-A7)	to W7 P1B-13D
From ID J1B-13D	to ID A1J13.8
From ID A1P13.8	to ID P12-68 (S701-4)

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From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-68 (S506-9)	to ID A1P9.20
From ID A1J9.20	to ID BUS 7

Step 972

Description:

Connect UUT pin J1-A5 to UUT pin J1-A2. Apply $+0.0 \pm 0.1$ Vdc to J1-C4. Send the "ANABIT ON" command to enable the ANALOG_BIT_EN function. Send the "ANAIN J1-C4/J1-A5" command to read the ASCII value from the SSP. The voltage value should read -5.684 ± 0.200 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C4 (UUT J1-C4)	to W7 P1A-3C
From ID J1A-3C	to ID A1J15.3
From ID A1P15.3	to ID P13-70 (S701-9)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-A5 (UUT J1-A5)	to W7 P1A-4A
From ID J1A-4A	to ID A1J15.4
From ID A1P15.4	to ID P13-38 (S701-10)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 973

Description:

Connect UUT pin J1-C6 to UUT pin J1-A2. Apply $+0.0 \pm 0.1$ Vdc to J1-B6. Send the "ANAIN J1-B6/J1-C6" command to read the ASCII value from the SSP. The voltage value should read -4.067 ± 0.200 Vdc.

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Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B6 (UUT J1-B6)	to W7 P1A-5A
From ID J1A-5A	to ID A1J15.7
From ID A1P15.7	to ID P13-7 (S701-17)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-C6 (UUT J1-C6)	to W7 P1A-10B
From ID J1A-10B	to ID A1J15.22
From ID A1P15.22	to ID P13-71 (S701-18)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 974

Description:

Connect UUT pin J1-C7 to UUT pin J1-A2. Apply $+0.0 \pm 0.1$ Vdc to J1-B7. Send the "ANAIN J1-B7/J1-C7" command to read the ASCII value from the SSP. The voltage value should read -4.067 ± 0.200 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B7 (UUT J1-B7)	to W7 P1B-12F
From ID J1B-12F	to ID A1J13.15
From ID A1P13.15	to ID P12-71 (S701-19)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-C7 (UUT J1-C7)	to W7 P1B-8D

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From ID J1B-8D	to ID A1J13.25
From ID A1P13.25	to ID P12-39 (S701-20)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 975

Description:

Connect UUT pin J1-A11 to UUT pin J1-A2. Apply $+0.0 \pm 0.1$ Vdc to J1-C10. Send the "ANAIN J1-C10/J1-A11" command to read the ASCII value from the SSP. The voltage value should read -4.067 ± 0.200 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C10 (UUT J1-C10)	to W7 P1A-4B
From ID J1A-4B	to ID A1J15.5
From ID A1P15.5	to ID P13-72 (S701-15)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-A11 (UUT J1-A11)	to W7 P1A-8A
From ID J1A-8A	to ID A1J15.16
From ID A1P15.16	to ID P13-40 (S701-16)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12

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From ID A1J6.12

to ID BUS 8

Step 976

Description:

Connect UUT pin J1-C11 to UUT pin J1-A2. Apply $+0.0 \pm 0.1$ Vdc to J1-B11. Send the "ANAIN J1-B11/J1-C11" command to read the ASCII value from the SSP. The voltage value should read -4.067 ± 0.200 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B11 (UUT J1-B11)

to W7 P1B-11D

From ID J1B-11D

to ID A1J13.16

From ID A1P13.16

to ID P12-6 (S701-21)

From ID P12-76 (S701-1)

to ID A1P12.50

From ID A1J12.50

to ID A1J10.3

From ID A1P10.3

to ID P11-194 (S506-1)

From ID P11-36 (S506-10)

to ID A1P9.10

From ID A1J9.10

to ID BUS 8

From W7 P2-C11 (UUT J1-C11)

to W7 P1B-8F

From ID J1B-8F

to ID A1J13.27

From ID A1P13.27

to ID P12-70 (S701-22)

From ID P12-44 (S701-2)

to ID A1P12.48

From ID A1J12.48

to ID A1J10.1

From ID A1P10.1

to ID P11-162 (S506-2)

From ID P11-36 (S506-10)

to ID A1P9.10

From ID A1J9.10

to ID BUS 8

From GROUND

to ID A1J7.32

From ID A1P7.32

to ID P10-98 (S301-11)

From ID P10-163 (S301-12)

to ID A1P7.16

From ID A1J7.16

to ID A1J7.18

From ID A1P7.18

to ID P10-133 (S301-27)

From ID P10-70 (S301-28)

to ID A1P6.12

From ID A1J6.12

to ID BUS 8

Step 977

Description:

Connect UUT pin J1-C3 to UUT pin J1-A2. Apply $+0.0 \pm 0.1$ Vdc to J1-B3. Send the "ANAIN J1-B3/J1-C3" command to read the ASCII value from the SSP. The voltage value should read 5.684 ± 0.200 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B3 (UUT J1-B3)

to W7 P1A-3A

From ID J1A-3A

to ID A1J15.1

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From ID A1P15.1	to ID P13-39 (S701-7)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-C3 (UUT J1-C3)	to W7 P1A-3B
From ID J1A-3B	to ID A1J15.2
From ID A1P15.2	to ID P13-6 (S701-8)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 978

Description:

Connect UUT pin J1-B4 to UUT pin J1-A2. Apply $+0.0 \pm 0.1$ Vdc to J1-A4. Send the "ANAIN J1-A4/J1-B4" command to read the ASCII value from the SSP. The voltage value should read 5.684 ± 0.200 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A4 (UUT J1-A4)	to W7 P1B-14E
From ID J1B-14E	to ID A1J13.9
From ID A1P13.9	to ID P12-36 (S701-5)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-B4 (UUT J1-B4)	to W7 P1B-13E
From ID J1B-13E	to ID A1J13.10
From ID A1P13.10	to ID P12-3 (S701-6)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)

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From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 979

Description:

Connect UUT pin J1-B8 to UUT pin J1-A2. Apply $+0.0 \pm 0.1$ Vdc to J1-A8. Send the "ANAIN J1-A8/J1-B8" command to read the ASCII value from the SSP. The voltage value should read 4.067 ± 0.200 Vdc.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-A8 (UUT J1-A8)	to W7 P1B-14F
From ID J1B-14F	to ID A1J13.11
From ID A1P13.11	to ID P12-38 (S701-11)

From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

From W7 P2-B8 (UUT J1-B8)	to W7 P1B-13F
From ID J1B-13F	to ID A1J13.12
From ID A1P13.12	to ID P12-5 (S701-12)

From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8

From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 980

Description:

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Connect UUT pin J1-A9 to UUT pin J1-A2. Apply $+0.0 \pm 0.1$ Vdc to J1-C8. Send the "ANAIN J1-C8/J1-A9" command to read the ASCII value from the SSP. The voltage value should read 4.067 ± 0.200 Vdc. Send the "ANABIT OFF" command to disable the ANALOG_BIT_EN function.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-C8 (UUT J1-C8)	to W7 P1B-12D
From ID J1B-12D	to ID A1J13.13
From ID A1P13.13	to ID P12-69 (S701-13)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From W7 P2-A9 (UUT J1-A9)	to W7 P1B-12E
From ID J1B-12E	to ID A1J13.14
From ID A1P13.14	to ID P12-37 (S701-14)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From GROUND	to ID A1J7.32
From ID A1P7.32	to ID P10-98 (S301-11)
From ID P10-163 (S301-12)	to ID A1P7.16
From ID A1J7.16	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8

Step 981

Description:

Send the "ANADATA 15" command to the SSP, and verify the return ASCII value should read 4.94 ± 0.35 Vdc. Send the "ANABIT ON" command to enable the ANALOG_BIT_EN function.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

Step 982

Description:

Send the "ANADATA 15" command to the SSP, and verify the return ASCII value should read 8.99 ± 0.42 Vdc. Send the "ANABIT OFF" command to disable the ANALOG_BIT_EN function.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

2.14 MODULE 10 – RESOLVER AND CAN TESTS

Description:

The Resolver to digital converter function will be functionally tested to assure all components are operating as expected and the correct angular position is displayed as a function of several different angular positions sent to the R to D converter. Simulated angular displacements from 0 to 315 degrees will be applied to the sine and cosine inputs of the resolver and in each case, an angular displacement will be read back from the SSP.

The CAN tests will verify proper bi-directional transmission of CAN messages from the SSP to the VIPER/T, on two separate channels.

Note that Steps 1017 - 1020 are only available when testing on the VIPER/T. The TETS-B does not contain CAN hardware required to perform these tests.

Refer to [Reference Drawings](#) when diagnosing connection paths.

Step 1001

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
Apply the 11.8 Vrms reference. The angle for the waveforms is set to 0.10. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 0.10 ± 0.2 DEG.

Connection Path is as follows:

See "UUT Power"

See "Boot Up"

See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26

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From W204 P2-26	to W204 P1-26 (SRS Ref Lo)
From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)
From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)
From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)
From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

Step 1002

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 15.0. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 15.0 ± 0.21 DEG.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)
From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)
From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)

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From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)
From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)
From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)
From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

Step 1003

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 30.0. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 30.0 ± 0.25 DEG.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)
From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)
From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)
From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12

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From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)
From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)
From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)
From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

Step 1004

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 45.0. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 45.0 \pm 0.3 DEG.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)
From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)
From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)
From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)

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From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)

From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)

From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

Step 1005

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 60.0. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 60.0 ± 0.36 DEG.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)

From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)

From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13

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From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)
From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)
From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

Step 1006

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 75.0. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 75.0 \pm 0.43 DEG.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)
From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)
From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)
From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)
From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)

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From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)

From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

Step 1007

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 90.1. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 90.1 ± 0.49 DEG.

Connection Path is as follows:

See "UUT Power"
 See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)

From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)

From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)

From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14

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From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)
From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

Step 1008

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 105.0. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 105.0 ± 0.56 DEG.

Connection Path is as follows:

See "UUT Power"
 See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)

From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)

From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)

From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)

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From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

Step 1009

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 120.0. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 120.0 \pm 0.63 DEG.

Connection Path is as follows:

See "UUT Power"
 See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)

From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)

From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)

From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)

From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
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From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

Step 1010

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 135.0. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 135.0 \pm 0.7 DEG.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)

From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)

From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)

From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)

From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16

From W204 P2-16

to W204 P1-16 (SRS Cos -)

Step 1011

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 150.0. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 150.0 \pm 0.78 DEG.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)

From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)

From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)

From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)

From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

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Step 1012

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 165.0. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 165.0 ± 0.85 DEG.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)

From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)

From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)

From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)

From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

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Step 1013

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 180.1. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 180.1 ± 0.92 DEG.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)

From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)

From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)

From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)

From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

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Step 1014

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 225.0. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 225.0 ± 1.14 DEG.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)

From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)

From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)

From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)

From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

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Step 1015

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 270.1. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 270.1 ± 1.37 DEG.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)

From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)

From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)

From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)

From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

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Step 1016

Description:

Connect the UUT pin J1-B40 to the SRS Reference HI Pin (S/R I/O 25).
 Connect the UUT pin J1-C18 to the SRS Reference LO Pin (S/R I/O 26).
 Connect the UUT pin J1-B17 to the SRS S3 Sin+ Pin (S/R I/O 15).
 Connect the UUT pin J1-C17 to the SRS S1 Sin- Pin (S/R I/O 13).
 Connect the UUT pin J1-B19 to the SRS S2 Cos+ Pin (S/R I/O 14).
 Connect the UUT pin J1-C19 to the SRS S4 Cos- Pin (S/R I/O 16).
 Apply the 11.8 Vrms reference. The angle for the waveforms is set to 315.0. Send the "ANGLE" command to the SSP, and verify the return ASCII value should read 315.0 ± 1.59 DEG.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P2-B18 (UUT J1-B18)	to W7 P1B-10C
From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-B40 (UUT J1-B40)	to W7 P1B-11C
From ID J1B-11C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)

From W7 P2-C18 (UUT J1-C18)	to W7 P1B-11A
From ID J1B-11A	to ID A1J3.16
From ID A1P3.16	to ID J3-26
From W204 P2-26	to W204 P1-26 (SRS Ref Lo)

From W7 P2-C17 (UUT J1-C17)	to W7 P1B-12C
From ID J1B-12C	to ID A1J3.12
From ID A1P3.12	to ID J3-13
From W204 P2-13	to W204 P1-13 (SRS Sin -)

From W7 P2-B19 (UUT J1-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)

From W7 P2-B17 (UUT J1-B17)	to W7 P1B-12A
From ID J1B-12A	to ID A1J3.14
From ID A1P3.14	to ID J3-15
From W204 P2-15	to W204 P1-15 (SRS Sin +)

From W7 P2-C19 (UUT J1-C19)	to W7 P1B-11B
From ID J1B-11B	to ID A1J3.15
From ID A1P3.15	to ID J3-16
From W204 P2-16	to W204 P1-16 (SRS Cos -)

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Note that Steps 1017 - 1020 are only available when testing on the VIPER/T. The TETS-B does not contain CAN hardware required to perform these tests.

Step 1017

Description:

Instruct the SSP to transmit a CAN message on CH 0. Verify the message is received by the VIPER on CH 1.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-67 (UUT J2-67)	to W7 P1A-6E
From ID J1A-6E	to ID A1J14.19
From ID A1P14.19	to ID P13-88 (S202-19)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-35 (UUT J2-35)	to W7 P1A-6F
From ID J1A-6F	to ID A1J14.20
From ID A1P14.20	to ID P13-24 (S202-20)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From W7 P4-16 (ATE CAN 1 HI)	to W7 P3-67 (UUT J2-67)
From W7 P4-17 (ATE CAN 1 LO)	to W7 P3-35 (UUT J2-35)
From W7 P4-18 (ATE CAN 1 SHIELD)	to W7 P3-52 (UUT J2-52)

Step 1018

Description:

Instruct the SSP to transmit a CAN message on CH 1. Verify the message is received by the VIPER on CH 2.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-62 (UUT J2-62)	to W7 P1B-1A
From ID J1B-1A	to ID A1J12.21

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From ID A1P12.21	to ID P12-61 (S202-21)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-38 (UUT J2-38)	to W7 P1B-5A
From ID J1B-5A	to ID A1J12.13
From ID A1P12.13	to ID P12-92 (S202-22)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From W7 P4-22 (ATE CAN 2 HI)	to W7 P3-62 (UUT J2-62)
From W7 P4-23 (ATE CAN 2 LO)	to W7 P3-38 (UUT J2-38)
From W7 P4-24 (ATE CAN 2 SHIELD)	to W7 P3-76 (UUT J2-76)

Step 1019

Description:

Send a CAN message from VIPER CAN 1 to SSP CAN 0. Verify the SSP received the expected message.

Connection Path is as follows:
See "UUT Power"
See "Serial Comm"

From W7 P3-67 (UUT J2-67)	to W7 P1A-6E
From ID J1A-6E	to ID A1J14.19
From ID A1P14.19	to ID P13-88 (S202-19)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-35 (UUT J2-35)	to W7 P1A-6F
From ID J1A-6F	to ID A1J14.20

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From ID A1P14.20	to ID P13-24 (S202-20)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From W7 P4-16 (ATE CAN 1 HI)	to W7 P3-67 (UUT J2-67)
From W7 P4-17 (ATE CAN 1 LO)	to W7 P3-35 (UUT J2-35)
From W7 P4-18 (ATE CAN 1 SHIELD)	to W7 P3-52 (UUT J2-52)

Step 1020

Description:

Send a CAN message from VIPER CAN 2 to SSP CAN 1. Verify the SSP received the expected message.

Connection Path is as follows:

See "UUT Power"

See "Serial Comm"

From W7 P3-62 (UUT J2-62)	to W7 P1B-1A
From ID J1B-1A	to ID A1J12.21
From ID A1P12.21	to ID P12-61 (S202-21)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From W7 P3-38 (UUT J2-38)	to W7 P1B-5A
From ID J1B-5A	to ID A1J12.13
From ID A1P12.13	to ID P12-92 (S202-22)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From W7 P4-22 (ATE CAN 2 HI)	to W7 P3-62 (UUT J2-62)
From W7 P4-23 (ATE CAN 2 LO)	to W7 P3-38 (UUT J2-38)
From W7 P4-24 (ATE CAN 2 SHIELD)	to W7 P3-76 (UUT J2-76)

3.0 Functional Flow Chart (FFC)





