ELTD-7566139-121 Rev - Date: 04 March 2016

English Language Test Description

MIPR # M9545012MP24797 CDRL F001 **for**

Unit Under Test

CDA Control Logic B CCA P/N 7566139-121 from

Light Armored Vehicle - 25A2 (LAV-25A2)

ATE SYSTEM

AN/USM-657B – Third Echelon Test System (TETS-B) AN/USM-717 – Virtual Instrument Portable Equipment Repair/Test (VIPER/T) **Developed by**

U.S. Army RDECOM
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ELTD REVISION SUMMARY

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1.0 Reference Documents

1.1 Virtual Instrument Portable Equipment Repair/Test (VIPER/T)

IEEE Std 716-1989 IEEE Standard Common

Abbreviated Test Language

for All Systems

TM TBD-CD VIPER/T IETM (Interactive

Electronic Technical Manual)

System Design Document Doc # 7992008 VIPER/T AN/USM-717

VIPER/T P/N 7992021 VIPER/T CPM (Computer

Programming Manual)

1.2 Third Echelon Test System (TETS-B)

TM 10530A-CD TETS IETM (Interactive

Electronic Technical Manual)

System Design Description Third Echelon Test System

(TETS)

Doc # 93006A0018 AN/USM-657

TETS P/N 93006A0026 TETS CPM (Computer

Programming Manual)

1.3 Unit Under Test

UUT P/N: 7566139-121

UUT Nomenclature: Control Logic B CCA UUT Type: Shop Replaceable Unit (SRU)

<u>DESCRIPTION</u>	<u>NUMBER</u>	<u>REVISION</u>	<u>DATE</u>
Parts List	7566139-121	AK	09 Sep 1998
LRU QA Specification	ES13456	None	None
Circuit Card Assy, Control Logic B Schematic Diagram, Control	7566139-121	AK	09 Sep 1998
Logic B, CCA	7566139-121	AK	09 Sep 1998

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1.4 Reference Drawings

Refer to the following schematics when diagnosing connection paths.

ID Schematic



W2 Schematic



2.0 English Language Test Description Steps

2.1 Common Procedures

The following connections are common throughout the entire test

2.1.1 UUT Power

Description:

28V Power is applied to J1-70 (HI) and J1-51 (LO) using DC4.

28V Power is applied to BUS7 (HI) and GND (LO) using DC9.

FROM ID P1-10 (DC4-HI)	TO ID A1P1.3
FROM ID A1J1.3	TO ID A1J2.4
FROM ID A1P2.4	TO ID P10-23 (S101-5)
FROM ID P10-87 (S101-6)	TO ID A1P2.31
FROM ID A1J2.31	TO ID J2B-14C
FROM W2 P1B-14C	TO W2 P2-70 (UUT J1-70)
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND

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2.1.2 APPLY IC

DESCRIPTION:

CONNECT J2-32 TO DC 4 HI CONNECT BUS8 TO INSTR-GND

CONNECTION PATH IS AS FOLLOWS:

FROM	W2	P2-69 (UUT J1-69)	TO	W2	P1A-4D
FROM	ID	J2A-4D	ТО	ID	A1J14.44
FROM	ID	A1P14.44	ТО	ID	P13-61 (S202-33)
FROM	ID	P12-59 (S202-1)	ТО	ID	A1P12.38
FROM	ID	A1J12.38	TO	ID	A1J10.10
FROM	ID	A1P10.10	TO	ID	P11-177 (S509-1)
FROM	ID	P11-211 (S509-9)	ТО	ID	A1P9.16
FROM	ID	A1J9.16	TO	ID	BUS 7

2.2 INTERFACE ID

STEP 1

DESCRIPTION:

This step verifies the correct ID is installed. The DMM is used to measure the resistance across ID resistors R111 (324 ohms) and R109 (698 ohms). The DMM should measure between 971 and 1073 Ohms.

FROM ID	P20-2 (DMM-HI)	TO	ID A	A1P15.49
FROM ID	A1J15.49	TO	ID A	A1J8.28
FROM ID	A1P8.28	TO	ID I	P10-203 (S503-1)
FROM ID	P10-137 (S503-7)	TO	ID A	A1P6.47
FROM ID	A1J6.47	TO	ID I	BUS 5
	P20-3 (DMM-LO)			
FROM ID	A1J15.50	TO	ID A	A1J8.26
FROM ID	A1P8.26	TO	ID I	P10-139 (S503-2)
FROM ID	P10-170 (S503-8)	TO	ID A	A1P6.38
FROM ID	A1J6.38	TO	ID I	BUS 6
FROM ID	BUS 5			A1J8.47
-	A1P8.47		ID I	P10-73 (S301-48)
FROM ID	P10-7 (S301-47)	TO	ID A	A1P7.23
FROM ID	A1J7.23	TO	ID A	A1J4.15
FROM ID	A1P4.15	TO	ID I	R111.1
FROM ID	R111.2	TO	ID A	A1P4.9
FROM ID	A1J4.9	TO	+287	V
FROM ID	BUS 6	TO	ID A	A1J8.48
FROM ID	A1P8.48	TO	ID I	P10-171 (S301-50)
FROM ID	P10-42 (S301-49)	TO	ID A	A1P7.24
FROM ID	A1J7.24	TO	ID A	A1J4.16

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FROM	ID	A1P4.16	TO	ID	R109.1
FROM	ID	R109.2	TO	ID	A1P4.9
FROM	ID	A1J4.9	TO	+28	3V

2.3 UUT ID

STEP 2

DESCRIPTION:

This step verifies the correct UUT is installed. The DMM is used to measure the continuity between UUT pins J1.15 and J1.48. The DMM should measure less than $10~\rm ohms$.

CONNECTION PATH IS AS FOLLOWS:

FROM	W2	P2-15 (UUT J1-15)	ТО	W2	P1B-7B
FROM	ID	J2B-7B	ТО	ID	A1J12.23
FROM	ID	J2B-7B A1P12.23	TO	ID	P12-85 (S201-45)
		P12-16 (S201-1)			
FROM	ID	A1J12.42	TO	ID	A1J10.6
H. K () IVI	11)		.1.()	11)	DII-/U3 (SSU8-I)
FROM	ID	P11-77 (S508-3)	TO	ID	A1P9.15
FROM	ID	A1J9.15	TO	ID	BUS 1
FROM	ID	BUS 1 A1P6.13	TO	ID	A1J6.13
FROM	ID	A1P6.13	TO	ID	P10-77 (S503-3)
FROM	ID	P10-203 (S503-1)	TO	ID	A1P8.28
FROM	ID	A1J8.28 A1P15.49	TO	ID	A1J15.49
FROM	ID	A1P15.49	TO	ID	P20-2 (DMM-HI)
	_				
		P2-48 (UUT J1-48)			
FROM	ID	J2A-1D A1P14.37	TO	ID	A1J14.37
FROM	ID	A1P14.37	TO	ID	P13-54 (S201-51)
		710 00 (7001 0)			-1-10 46
		P12-20 (S201-3)			
FROM	TD	A1J12.46	TO	TD	ALU10.2
		A1P10.2	TO	ID	P11-39 (S507-1)
		P11-72 (S507-4)			
F'ROM	TD	A1J9.27	TO	TD	BUS 2
ED OM	TD	DIIG 0	ШΟ	TD	7176 02
FROM	TD	BUS 2 A1P6.23	10	TD	ALU6.23
FROM	TD	A1P0.23	10	TD	P10-12 (S503-4)
		P10-139 (S503-2)	I.O	TD	A1715 50
		A1J8.26 A1P15.50	10	TD	ALUID.DU
FROM	ΤD	A1P15.50	TO	ΤD	P20-3 (DMM-LO)

2.4 SAFE TO TURN ON

Refer to

<u>1.4 Reference</u> Drawings when diagnosing connection paths.

STEP 3

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DESCRIPTION:

THIS TEST VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.69 TO UUT J1.45. THE DMM SHOULD MEASURE GREATER THAN 10K OHMS.

CONNECTION PATH IS AS FOLLOWS:

		P20-2 (DMM-HI)		ID	A1P15.49
FROM	ID	A1J15.49	TO	ID	A1J8.28
FROM	ID	A1P8.28	TO	ID	P10-203 (S503-1)
FROM	ID	P10-173 (S503-9)	TO	ID	A1P6.28
FROM	ID	A1J6.28	ТО	ID	BUS 7
FROM	W2	P2-69 (UUT J1-69)	TO	W2	P1A-4D
FROM	ID	J2A-4D	TO	ID	A1J14.44
FROM	ID	A1P14.44	TO	ID	P13-61 (S202-33)
FROM	ID	P12-59 (S202-1)	ТО	ID	A1P12.38
FROM	ID	A1J12.38	ТО	ID	A1J10.10
FROM	ID	A1P10.10	ТО	ID	P11-177 (S509-1)
FROM	ID	P11-211 (S509-9)	ТО	ID	A1P9.16
FROM	ID	A1J9.16	ТО	ID	BUS 7
FROM	ID	P20-3 (DMM-LO)	ТО	ID	A1P15.50
FROM	ID	A1J15.50	TO	ID	A1J7.38
FROM	ID	A1P7.38	ТО	ID	P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	ТО	ID	A1P7.36
FROM	ID	A1J7.36	ТО	GRO	DUND

STEP 4

DESCRIPTION:

THIS TEST VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.70 TO UUT J1.45. THE DMM SHOULD MEASURE GREATER THAN 5K OHMS.

FROM]	ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM]	ID A1J15.49	TO ID A1J7.44
FROM]	ID A1P7.44	TO ID P10-99 (S301-4)
FROM]	ID P10-226 (S301-3)	TO ID A1P7.13
FROM]	ID A1J7.13	O ID A1J2.31
FROM]	ID A1P2.31	TO ID P10-87 (S101-6)
FROM]	ID P10-23 (S101-5)	TO ID A1P2.4
FROM]	ID A1J2.4	TO ID A1J1.3
FROM]	ID A1P1.3	TO ID P1-10 (DC4-HI J1-70)
FROM]	ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM]	ID A1J15.50	TO ID A1J7.38
FROM]	ID A1P7.38	TO ID P10-130 (S301-23)
FROM]	ID P10-229 (S301-24)	TO ID A1P7.36
FROM]	ID A1J7.36	TO GROUND

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STEP 5

DESCRIPTION:

THIS TEST VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.10 TO UUT J1.45. THE DMM SHOULD MEASURE GREATER THAN 1K OHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM ID	P20-2 (DMM-HI)	TO	ID	A1P15.49
FROM ID	A1J15.49	TO	ID	A1J8.28
FROM ID	A1P8.28	TO	ID	P10-203 (S503-1)
FROM ID	P10-77 (S503-3)	TO	ID	A1P6.13
FROM ID	A1J6.13	TO	ID	BUS 1
FROM W2	P2-10 (UUT J1-10)	TO	W2	P1B-9A
FROM ID	J2B-9A	TO	ID	A1J13.27
FROM ID	A1P13.27	TO	ID	P12-70 (S701-22)
FROM ID	P12-44 (S701-2)	TO	ID	A1P12.48
FROM ID	A1J12.48	TO	ID	A1J10.1
FROM ID	A1P10.1	TO	ID	P11-162 (S506-2)
FROM ID	P11-164 (S506-3)	TO	ID	A1P9.23
FROM ID	A1J9.23	TO	ID	BUS 1
FROM ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM ID	A1J15.50	TO	ID	A1J7.38
FROM ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM ID	A1J7.36	TO	GRO	DUND

STEP 6

DESCRIPTION:

THIS TEST VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.18 TO UUT J1.61. THE DMM SHOULD MEASURE BETWEEN 225 AND 255 OHMS.

FROM	W2	P2-18 (UUT J1-18)	TO	W2	P1A-3C
FROM	ID	J2A-3C	TO	ID	A1J14.32
FROM	ID	A1P14.32	TO	ID	P13-53 (S201-42)
FROM	ID	P12-52 (S201-4)	TO	ID	A1P12.44
FROM	ID	A1J12.44	TO	ID	A1J10.4
FROM	ID	A1P10.4	TO	ID	P11-71 (S507-2)
FROM	ID	P11-168 (S507-3)	TO	ID	A1P9.17
FROM	ID	A1J9.17	TO	ID	BUS 1
FROM	ID	P20-2 (DMM-HI)	TO	ID	A1P15.49
FROM	ID	A1J15.49	TO	ID	A1J8.28
FROM	ID	A1P8.28	TO	ID	P10-203 (S503-1)

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FROM	ID	P10-77 (S503-3)	TO	ID	A1P6.13
FROM	ID	A1J6.13	TO	ID	BUS 1
FROM	W2	P2-61 (UUT J1-61)	ТО	W2	P1A-3F
FROM	ID	J2A-3F	TO	ID	A1J14.41
FROM	ID	A1P14.41	TO	ID	P13-57 (S202-12)
FROM	ID	P12-90 (S202-2)	ТО	ID	A1P12.36
FROM	ID	A1J12.36	TO	ID	A1J10.12
FROM	ID	A1P10.12	TO	ID	P11-242 (S509-2)
FROM	ID	P11-146 (S509-10)	TO	ID	A1P9.6
FROM	ID	A1J9.6	TO	ID	BUS 8
FROM	ID	P20-3 (DMM-LO)	ТО	ID	A1P15.50
FROM	ID	A1J15.50	TO	ID	A1J8.26
FROM	ID	A1P8.26	TO	ID	P10-139 (S503-2)
FROM	ID	P10-205 (S503-10)	TO	ID	A1P6.16
FROM	ID	A1J6.16	TO	ID	BUS 8

STEP 7

DESCRIPTION:

THIS TEST VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.17 TO UUT J1.62. THE DMM SHOULD MEASURE BETWEEN 225 AND 255 OHMS.

FROM W2 P2-17 (UUT J1-17)	
FROM ID J2A-8C	TO ID A1J15.18
FROM ID A1P15.18	TO ID A1J15.18 TO ID P13-78 (S701-47)
FROM ID P12-76 (S701-1)	
FROM ID A1J12.50	TO ID A1J10.3
	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
111011 12 11110.20	10 10 110 203 (8303 1)
FROM ID P10-77 (S503-3)	
FROM ID A1J6.13	TO ID BUS 1
FROM W2 P2-62 (UUT J1-62)	
FROM ID J2B-13F FROM ID A1P12.32	TO ID A1J12.32
FROM ID A1P12.32	TO ID P12-88 (S202-14)
TROW TR R10 00 (G000 0)	TO TO 31010 26
FROM ID P12-90 (S202-2)	TO ID AIPI2.36
FROM ID A1J12.36 FROM ID A1P10.12	TO ID AIJIU.12
FROM ID AIPIU.12	TO ID PII-242 (S509-2)
FROM ID P11-146 (S509-10)	
FROM ID A1J9.6	TO ID BUS 8

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FROM ID P20-3 (DMM-LO) TO ID A1P15.50
FROM ID A1J15.50 TO ID A1J8.26
FROM ID A1P8.26 TO ID P10-139 (S503-2)
FROM ID P10-205 (S503-10) TO ID A1P6.16
FROM ID A1J6.16 TO ID BUS 8

STEP 8

DESCRIPTION:

THIS TEST CHECKS RESISTORS R57, R42, AND RELAY K8'S B2 TO B3 CONTACTS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.65 TO UUT J1.62. THE DMM SHOULD MEASURE BETWEEN 20.15K AND 20.35K OHMS.

CONNECTION PATH IS AS FOLLOWS:

		P2-65 (UUT J1-65)			
FROM :	ID	J2B-9C A1P13.23	ТО	ID	A1J13.23
FROM I	ID	A1P13.23	ТО	ID	P12-72 (S701-28)
FROM I	ID	P12-44 (S701-2)	ТО	ID	A1P12.48
FROM I	ID	A1J12.48 A1P10.1	TO	ID	A1J10.1
FROM I	ID	A1P10.1	TO	ID	P11-162 (S506-2)
FROM I	ID	P11-164 (S506-3)	TO	ID	A1P9.23
		A1J9.23	TO	ID	BUS 1
FROM I	ID	P20-2 (DMM-HI)	ТО	ID	A1P15.49
FROM :	ID	A1J15.49	TO	ID	A1J8.28
FROM :	ID	A1J15.49 A1P8.28 P10-77 (S503-3)	TO	ID	P10-203 (S503-1)
FROM I	ID	P10-77 (S503-3)	TO	ID	A1P6.13
		A1J6.13	ТО	ID	BUS 1
FROM V	W2	P2-62 (UUT J1-62)	TO	W2	P1B-13F
FROM T	W2 ID	P2-62 (UUT J1-62) J2B-13F	TO TO	W2 ID	P1B-13F A1J12.32
FROM FROM I	W2 ID ID	P2-62 (UUT J1-62) J2B-13F A1P12.32	TO TO	W2 ID ID	P1B-13F A1J12.32 P12-88 (S202-14)
FROM I	ID ID	J2B-13F A1P12.32	TO TO	ID ID	A1J12.32 P12-88 (S202-14)
FROM I	ID ID	J2B-13F A1P12.32	TO TO	ID ID	A1J12.32 P12-88 (S202-14)
FROM I	ID ID ID ID	J2B-13F A1P12.32 P12-90 (S202-2) A1J12.36 A1P10.12	TO TO TO TO	ID ID ID ID	A1J12.32 P12-88 (S202-14) A1P12.36 A1J10.12 P11-242 (S509-2)
FROM I	ID ID ID ID	J2B-13F A1P12.32 P12-90 (S202-2) A1J12.36 A1P10.12	TO TO TO TO	ID ID ID ID	A1J12.32 P12-88 (S202-14) A1P12.36 A1J10.12 P11-242 (S509-2)
FROM : FROM : FROM : FROM : FROM :	ID ID ID ID ID	J2B-13F A1P12.32	TO TO TO TO TO	ID ID ID ID ID	A1J12.32 P12-88 (S202-14) A1P12.36 A1J10.12 P11-242 (S509-2) A1P9.6
FROM : FROM : FROM : FROM : FROM : FROM :	ID ID ID ID ID ID ID	J2B-13F A1P12.32 P12-90 (S202-2) A1J12.36 A1P10.12 P11-146 (S509-10) A1J9.6	TO TO TO TO TO TO	ID ID ID ID ID ID	A1J12.32 P12-88 (S202-14) A1P12.36 A1J10.12 P11-242 (S509-2) A1P9.6 BUS 8
FROM : FROM : FROM : FROM : FROM : FROM :	ID ID ID ID ID ID ID	J2B-13F A1P12.32 P12-90 (S202-2) A1J12.36 A1P10.12 P11-146 (S509-10) A1J9.6	TO TO TO TO TO TO	ID ID ID ID ID ID	A1J12.32 P12-88 (S202-14) A1P12.36 A1J10.12 P11-242 (S509-2) A1P9.6 BUS 8
FROM :	ID	J2B-13F A1P12.32 P12-90 (S202-2) A1J12.36 A1P10.12 P11-146 (S509-10) A1J9.6 P20-3 (DMM-LO) A1J15.50 A1P8.26	TO TO TO TO TO TO TO TO	ID ID ID ID ID ID ID ID ID	A1J12.32 P12-88 (S202-14) A1P12.36 A1J10.12 P11-242 (S509-2) A1P9.6 BUS 8 A1P15.50 A1J8.26 P10-139 (S503-2)
FROM :	ID	J2B-13F A1P12.32 P12-90 (S202-2) A1J12.36 A1P10.12 P11-146 (S509-10) A1J9.6 P20-3 (DMM-LO) A1J15.50 A1P8.26	TO TO TO TO TO TO TO TO	ID ID ID ID ID ID ID ID ID	A1J12.32 P12-88 (S202-14) A1P12.36 A1J10.12 P11-242 (S509-2) A1P9.6 BUS 8 A1P15.50 A1J8.26 P10-139 (S503-2)
FROM :	ID I	J2B-13F A1P12.32 P12-90 (S202-2) A1J12.36 A1P10.12 P11-146 (S509-10) A1J9.6	TO TO TO TO TO TO TO	ID	A1J12.32 P12-88 (S202-14) A1P12.36 A1J10.12 P11-242 (S509-2) A1P9.6 BUS 8 A1P15.50 A1J8.26 P10-139 (S503-2) A1P6.16

STEP 9

DESCRIPTION:

THIS TEST CHECKS THE RESISTANCE AT THE COIL OF K2 (K2/CR1). THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.8 TO UUT GROUND. THE DMM SHOULD MEASURE BETWEEN 500 AND 850 OHMS.

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1	CONNE	CTTON	DATH	TC AC	S FOLLOWS:

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM W2 P2-8 (UUT J1-8)	TO W2 P1A-2B
FROM ID J2A-2B	TO ID A1J14.31
FROM ID A1P14.31	TO ID P13-30 (S202-41)
FROM ID AIP14.31	10 1D P13-30 (3202-41)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
EDOM ID DOO 2 (DMM IO)	TO ID 31015 50
FROM ID P20-3 (DMM-LO)	
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 10

DESCRIPTION:

THIS TEST CHECKS THE RESISTANCE AT THE COIL OF K4 (K4/CR2). THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.22 TO UUT GROUND. THE DMM SHOULD MEASURE BETWEEN 500 AND 850 OHMS.

	P20-2 (DMM-HI) A1J15.49			A1P15.49 A1J8.28
_	A1P8.28	_		P10-203 (S503-1)
FROM ID	P10-77 (S503-3)	ТО	ID	A1P6.13
	A1J6.13		ID	BUS 1
FROM W2	P2-22 (UUT J1-22)	ТО	W2	P1A-4F
FROM ID	J2A-4F	TO	ID	A1J14.46
FROM ID	A1P14.46	TO	ID	P13-95 (S202-43)
FROM ID	P12-59 (S202-1)	TO	ID	A1P12.38
FROM ID	A1J12.38	TO	ID	A1J10.10
-	A1P10.10		ID	P11-177 (S509-1)
FROM ID	P11-18 (S509-3)	TO	ID	A1P9.19
FROM ID	A1J9.19	TO	ID	BUS 1
FROM ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM ID	A1J15.50	TO	ID	A1J7.38
FROM ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM ID	A1J7.36	TO	GRO	DUND

Date: 04 March 2016

STEP 11

DESCRIPTION:

THIS TEST CHECKS THE RESISTANCE AT THE COIL OF K7 (K7/CR3). THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.28 TO UUT GROUND. THE DMM SHOULD MEASURE BETWEEN 500 AND 850 OHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM	ID	P20-2 (DMM-HI)	TO	ID	A1P15.49
FROM	ID	A1J15.49	ТО	ID	A1J8.28
FROM	ID	A1P8.28	ТО	ID	P10-203 (S503-1)
FROM	ID	P10-77 (S503-3)	ТО	ID	A1P6.13
FROM	ID	A1J6.13	TO	ID	BUS 1
FROM	W2	P2-28 (UUT J1-28)	TO	W2	P1B-14F
FROM	ID	J2B-14F	TO	ID	A1J12.29
FROM	ID	A1P12.29	TO	ID	P12-24 (S202-7)
FROM	ID	P12-59 (S202-1)	TO	ID	A1P12.38
FROM	ID	A1J12.38	TO	ID	A1J10.10
FROM	ID	A1P10.10	TO	ID	P11-177 (S509-1)
FROM	ID	P11-18 (S509-3)	TO	ID	A1P9.19
FROM	ID	A1J9.19	TO	ID	BUS 1
FROM	ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM	ID	A1J15.50	TO	ID	A1J7.38
FROM	ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM	ID	A1J7.36	TO	GRO	DUND

STEP 12

DESCRIPTION:

THIS TEST CHECKS THE RESISTANCE AT THE INPUT OF U6. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT TP-11 TO UUT GROUND. THE DMM SHOULD MEASURE GREATER THAN 10 K OHMS.

FROM	W2	P3-B6	(UUT	J2-12))	ТО	W2	P1A-14C	
FROM	ID	J2A-14	l C			TO	ID	A1J10.19)
FROM	ID	A1P10.	.19			TO	ID	P11-253	(S405-5)
FROM	ID	P11-94	1 (S40)5-1)		TO	ID	A1P9.47	
FROM	ID	A1J9.4	17			TO	ID	BUS 3	
FROM	ID	P20-2	(DMM-	-HI)		TO	ID	A1P15.49)
FROM	ID	A1J15.	. 49			TO	ID	A1J8.28	
FROM	ID	A1P8.2	28			TO	ID	P10-203	(S503-1)
FROM	ID	P10-10	(S50)3-5)		TO	ID	A1P6.31	
FROM	ID	A1J6.3	31			TO	ID	BUS 3	
FROM	ID	P20-3	(DMM-	-LO)		TO	ID	A1P15.50)

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FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

2.5 MODULE 1: OUTPUT SIGNALS

Refer to

1.4 Reference Drawings when diagnosing connection paths.

STEP 101

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT PIN J2.12 SHOULD BE BETWEEN 27.0 AND 28.5 VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W2 P3-B6 (UUT J2-12)	TO W2 P1A-14C
FROM ID J2A-14C	TO ID A1J10.19
FROM ID A1P10.19	TO ID P11-253 (S405-5)
FROM ID P11-94 (S405-1)	TO ID A1P9.47
FROM ID A1J9.47	TO ID BUS 3
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-10 (S503-5)	TO ID A1P6.31
FROM ID A1J6.31	TO ID BUS 3
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 102

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT PIN J1.10 SHOULD BE BETWEEN 14.5 AND 15.5VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W2	P2-10 (UUT J1-10)	TO	W2	P1B-9A
FROM ID	J2B-9A	TO	ID	A1J13.27
FROM ID	A1P13.27	TO	ID	P12-70 (S701-22)
FROM ID	P12-44 (S701-2)	TO	ID	A1P12.48

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FROM ID A1J12.48 FROM ID A1P10.1 FROM ID P11-164 (S506-3) FROM ID A1J9.23	TO ID A1J10.1 TO ID P11-162 (S506-2) TO ID A1P9.23 TO ID BUS 1
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1P15.49 TO ID A1J8.28 TO ID P10-203 (S503-1) TO ID A1P6.13 TO ID BUS 1
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1P15.50 TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 103

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.28. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM	W2	P2-28 (UUT J1-28)	TO	W2	P1B-14F
FROM	ID	J2B-14F	TO	ID	A1J12.29
FROM	ID	A1P12.29	TO	ID	P12-24 (S202-7)
FROM	W2	P2-23 (UUT J1-23)	TO	W2	P1A-7B
FROM	ID	J2A-7B	TO	ID	A1J15.24
FROM	ID	A1P15.24	TO	ID	P13-9 (S701-24)
FROM	ID	P12-44 (S701-2)	TO	ID	A1P12.48
FROM	ID	A1J12.48	TO	ID	A1J10.1
FROM	ID	A1P10.1	TO	ID	P11-162 (S506-2)
FROM	ID	P11-164 (S506-3)	TO	ID	A1P9.23
FROM	ID	A1J9.23	TO	ID	BUS 1
FROM	ID	P20-2 (DMM-HI)	TO	ID	A1P15.49
FROM	ID				A1J8.28
				ID	P10-203 (S503-1)
FROM	ID	P10-77 (S503-3)	TO	ID	A1P6.13
FROM	ID	A1J6.13	TO	ID	BUS 1
FROM	ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM	ID	A1J15.50	TO		A1J7.38
FROM	ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM	ID	A1J7.36	TO	GRO	DUND

Date: 04 March 2016

STEP 104

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.28 AND J1.8. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE GREATER THAN 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM ID	P2-28 (UUT J1-28) J2B-14F A1P12.29	TO :	ID	P1B-14F A1J12.29 P12-24 (S202-7)
FROM ID	P2-8 (UUT J1-8) J2A-2B A1P14.31	TO :	ID	P1A-2B A1J14.31 P13-30 (S202-41)
FROM ID	P2-23 (UUT J1-23) J2A-7B A1P15.24	TO :	ID	P1A-7B A1J15.24 P13-9 (S701-24)
FROM ID FROM ID FROM ID	P12-44 (S701-2) A1J12.48 A1P10.1 P11-164 (S506-3) A1J9.23	TO :	ID ID ID	A1P12.48 A1J10.1 P11-162 (S506-2) A1P9.23 BUS 1
FROM ID FROM ID FROM ID	P20-2 (DMM-HI) A1J15.49 A1P8.28 P10-77 (S503-3) A1J6.13	TO :	ID ID ID	A1P15.49 A1J8.28 P10-203 (S503-1) A1P6.13 BUS 1
FROM ID FROM ID FROM ID	P20-3 (DMM-LO) A1J15.50 A1P7.38 P10-229 (S301-24) A1J7.36	TO :	ID ID ID	A1P15.50 A1J7.38 P10-130 (S301-23) A1P7.36

STEP 105

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.28. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

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CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

Date: 04 March 2016

FROM W2 P2-28 (UUT J1-28) FROM ID J2B-14F FROM ID A1P12.29	
FROM W2 P2-23 (UUT J1-23) FROM ID J2A-7B FROM ID A1P15.24	TO W2 P1A-7B TO ID A1J15.24 TO ID P13-9 (S701-24)
FROM ID P12-44 (S701-2) FROM ID A1J12.48 FROM ID A1P10.1 FROM ID P11-164 (S506-3) FROM ID A1J9.23	TO ID A1J10.1 TO ID P11-162 (S506-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 106

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.28 AND J1.22. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE GREATER THAN 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM W2 P2-28 (UUT J1-28)	TO W2 P1B-14F
FROM ID J2B-14F	TO ID A1J12.29
FROM ID A1P12.29	TO ID P12-24 (S202-7)
FROM W2 P2-22 (UUT J1-22) FROM ID J2A-4F FROM ID A1P14.46	TO W2 P1A-4F TO ID A1J14.46 TO ID P13-95 (S202-43)
FROM W2 P2-23 (UUT J1-23)	TO W2 P1A-7B
FROM ID J2A-7B	TO ID A1J15.24
FROM ID A1P15.24	TO ID P13-9 (S701-24)
FROM ID P12-44 (S701-2) FROM ID A1J12.48 FROM ID A1P10.1 FROM ID P11-164 (S506-3) FROM ID A1J9.23	TO ID A1P12.48 TO ID A1J10.1 TO ID P11-162 (S506-2) TO ID A1P9.23 TO ID BUS 1

Date: 04 March 2016

FROM	ID	P20-2 (DMM-HI)	TO	ID	A1P15.49
FROM	ID	A1J15.49	TO	ID	A1J8.28
FROM	ID	A1P8.28	TO	ID	P10-203 (S503-1)
FROM	ID	P10-77 (S503-3)	TO	ID	A1P6.13
FROM	ID	A1J6.13	TO	ID	BUS 1
FROM	ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM	ID	A1J15.50	TO	ID	A1J7.38
FROM	ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM	ID	A1J7.36	TO	GRO	DUND

STEP 107

DESCRIPTION:

THIS STEP APPLIES 28.0 VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0 VDC IS APPLIED TO PIN J1.28. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE LESS THAN 0.2 VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-28 (UUT J1-28) FROM ID J2B-14F FROM ID A1P12.29	TO W2 P1B-14F TO ID A1J12.29 TO ID P12-24 (S202-7)
FROM W2 P2-23 (UUT J1-23) FROM ID J2A-7B FROM ID A1P15.24	TO W2 P1A-7B TO ID A1J15.24 TO ID P13-9 (S701-24)
FROM ID P12-44 (S701-2) FROM ID A1J12.48 FROM ID A1P10.1 FROM ID P11-164 (S506-3) FROM ID A1J9.23	TO ID A1J10.1 TO ID P11-162 (S506-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

Date: 04 March 2016

STEP 108

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.28 AND J1.24. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE GREATER THAN 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM ID	P2-28 (UUT J1-28) J2B-14F A1P12.29	TO ID	P1B-14F A1J12.29 P12-24 (S202-7)
FROM ID	P2-24 (UUT J1-24) J2B-8C A1P12.25	TO ID	P1B-8C A1J12.25 P12-89 (S202-5)
FROM ID	P2-23 (UUT J1-23) J2A-7B A1P15.24	TO ID	P1A-7B A1J15.24 P13-9 (S701-24)
FROM ID FROM ID FROM ID	P12-44 (S701-2) A1J12.48 A1P10.1 P11-164 (S506-3) A1J9.23	TO ID TO ID TO ID	A1P12.48 A1J10.1 P11-162 (S506-2) A1P9.23 BUS 1
FROM ID FROM ID FROM ID	P20-2 (DMM-HI) A1J15.49 A1P8.28 P10-77 (S503-3) A1J6.13	TO ID TO ID TO ID	A1P15.49 A1J8.28 P10-203 (S503-1) A1P6.13 BUS 1
FROM ID FROM ID FROM ID	P20-3 (DMM-LO) A1J15.50 A1P7.38 P10-229 (S301-24) A1J7.36	TO ID	A1P15.50 A1J7.38 P10-130 (S301-23) A1P7.36 OUND

STEP 109

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.64. THE OUTPUT MEASURED AT PIN J1.32 SHOULD BE GREATER THAN 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-64 (UUT J1-64) TO W2 P1B-10F FROM ID J2B-10F TO ID A1J12.39

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FROM ID	A1P12.39	TO	ID	P12-93 (S202-31)
FROM W2	P2-32 (UUT J1-32)	ТО	W2	P1B-7A
FROM ID	J2B-7A	ТО	ID	A1J12.24
FROM ID	A1P12.24	ТО	ID	P12-21 (S201-46)
FROM ID	P12-80 (S201-2)	то	ID	A1P12.40
FROM ID	A1J12.40	ТО	ID	A1J10.8
FROM ID	A1P10.8	ТО	ID	P11-139 (S508-2)
FROM ID	P11-77 (S508-3)	ТО	ID	A1P9.15
FROM ID	A1J9.15	TO	ID	BUS 1
EDOM ID	P20-2 (DMM-HI)	т∩	TD	λ1D15 49
	A1J15.49			A1J8.28
FROM ID		_		P10-203 (S503-1)
_				A1P6.13
	P10-77 (S503-3)			
FROM ID	A1J6.13	1.0	TD	BUS 1
FROM ID	P20-3 (DMM-LO)	ТО	ID	A1P15.50
FROM ID	A1J15.50	TO	ID	A1J7.38
FROM ID	A1P7.38	ТО	ID	P10-130 (S301-23)
FROM ID	P10-229 (S301-24)	ТО	ID	A1P7.36
FROM ID	A1J7.36	TO	GRO	DUND

STEP 110

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.64 AND J1.28. THE OUTPUT MEASURED AT PIN J1.32 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM W2 P2-64 (UUT J1-64) FROM ID J2B-10F FROM ID A1P12.39	TO ID A1J12.39
FROM W2 P2-28 (UUT J1-28) FROM ID J2B-14F FROM ID A1P12.29	TO W2 P1B-14F TO ID A1J12.29 TO ID P12-24 (S202-7)
FROM W2 P2-32 (UUT J1-32) FROM ID J2B-7A FROM ID A1P12.24	TO W2 P1B-7A TO ID A1J12.24 TO ID P12-21 (S201-46)
FROM ID P12-80 (S201-2) FROM ID A1J12.40 FROM ID A1P10.8 FROM ID P11-77 (S508-3) FROM ID A1J9.15	TO ID A1J10.8 TO ID P11-139 (S508-2)
FROM ID P20-2 (DMM-HI) FROM ID AlJ15.49	TO ID A1P15.49 TO ID A1J8.28

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FROM ID A1P8.28
FROM ID P10-77 (S503-3)
FROM ID A1J6.13
FROM ID A1J6.13
TO ID A1P6.13
TO ID BUS 1

FROM ID P20-3 (DMM-LO)
FROM ID A1J15.50
FROM ID A1J15.50
FROM ID A1P7.38
FROM ID P10-229 (S301-24)
TO ID A1P7.36
TO ID P10-130 (S301-23)
TO ID A1F7.36
TO ID A1F7.36
TO GROUND

STEP 111

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT PIN J1.32 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-32 (UUT J1-32)
FROM ID J2B-7A
FROM ID J2B-7A
FROM ID AlP12.24
TO ID AlJ12.24
FROM ID P12-80 (S201-2)
FROM ID AlJ12.40
FROM ID AlJ12.40
FROM ID AlP10.8
FROM ID P11-77 (S508-3)
FROM ID AlJ9.15
TO ID AlP9.15
FROM ID AlJ15.49
FROM ID AlJ15.49
FROM ID AlP8.28
FROM ID AlP8.28
FROM ID P10-77 (S503-3)
FROM ID P10-77 (S503-3)
FROM ID P10-77 (S503-3)
FROM ID AlJ6.13
FROM ID P20-3 (DMM-LO)
FROM ID AlJ15.50
FROM ID AlJ7.38
FROM ID AlP7.38
FROM ID P10-229 (S301-24)
FROM ID AlP7.36
FROM ID ALJ7.36
FROM ID ALJ7.36
FROM ID ALJ7.36

STEP 112

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.67. THE OUTPUT MEASURED AT PIN J1.32 SHOULD BE GREATER THAN 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W2 P2-67 (UUT J1-67) TO W2 P1B-12E

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FROM	ID	J2B-12E	ТО	ID	A1J12.34
FROM	ID	J2B-12E A1P12.34	TO	ID	P12-58 (S202-16)
		P12-90 (S202-2)			
FROM	ID	A1J12.36	TO	ID	A1J10.12
FROM	ID	A1P10.12	TO	ID	P11-242 (S509-2)
${\tt FROM}$	ID	P11-211 (S509-9)	TO	ID	A1P9.16
FROM	ID	A1J9.16	TO	ID	BUS 7
FROM	W2	P2-32 (UUT J1-32)	TO	W2	P1B-7A
FROM	ID	J2B-7A	TO	ID	A1J12.24
FROM	ID	P2-32 (UUT J1-32) J2B-7A A1P12.24	TO	ID	P12-21 (S201-46)
		P12-80 (S201-2)	TO	TD	AIP12.40
		A1J12.40	TO	ID	AlJ10.8
					P11-139 (S508-2)
		P11-77 (S508-3)			
FROM	ID	A1J9.15	ТО	ID	BUS 1
EDOM	TD	P20-2 (DMM-HI)	ШΟ	TD	71D1E 40
FROM	TD	A1J15.49 A1P8.28	TO	TD	D10 202 (GE02 1)
		P10-77 (S503-3)			
		A1J6.13			BUS 1
1.1(014)	עד	A100.13	10	דט	B05 1
FROM	TD	P20-3 (DMM-LO)	ΤО	TD	A1P15.50
FROM	TD	A1,T15.50	ТО	TD	A1J7.38
FROM	ID	A1J15.50 A1P7.38	TO	ID	P10-130 (S301-23)
_		P10-229 (S301-24)			
		A1J7.36			DUND

STEP 113

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT J1.16 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM	W2	P2-16 (UUT J1-16)	ТО	W2	P1B-11F
FROM	ID	J2B-11F	ТО	ID	A1J12.37
FROM	ID	A1P12.37	ТО	ID	P12-60 (S202-30)
FROM	ID	P13-29 (S202-4)	ТО	ID	A1P14.50
FROM	ID	A1J14.50	TO	ID	A1J10.50
FROM	ID	A1P10.50	TO	ID	P11-244 (S510-2)
FROM	ID	P11-180 (S510-3)	ТО	ID	A1P9.21
FROM	ID	A1J9.21	ТО	ID	BUS 1
FROM	ID	P20-2 (DMM-HI)	TO	ID	A1P15.49
FROM	ID	A1J15.49	ТО	ID	A1J8.28
FROM	ID	A1P8.28	ТО	ID	P10-203 (S503-1)

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FROM ID P10-77 (S503-3) TO ID A1P6.13
FROM ID A1J6.13 TO ID BUS 1
FROM ID P20-3 (DMM-LO) TO ID A1P15.50
FROM ID A1J15.50 TO ID A1J7.38
FROM ID A1P7.38 TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24) TO ID A1P7.36
FROM TD A1J7.36 TO GROUND

STEP 114

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.42. THE OUTPUT MEASURED AT J1.16 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

	TO W2 P1A-1C TO ID A1J14.34 TO ID P13-58 (S202-17)
FROM W2 P2-16 (UUT J1-16) FROM ID J2B-11F FROM ID A1P12.37	TO W2 P1B-11F TO ID A1J12.37 TO ID P12-60 (S202-30)
FROM ID P13-29 (S202-4) FROM ID A1J14.50 FROM ID A1P10.50 FROM ID P11-180 (S510-3) FROM ID A1J9.21	TO ID A1J10.50 TO ID P11-244 (S510-2) TO ID A1P9.21
	TO ID A1J8.28 TO ID P10-203 (S503-1)
	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 115

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT J1.16 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

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FROM ID	P2-16 (UUT J1-16) J2B-11F A1P12.37	TO I	N2 P1B-11F ID A1J12.37 ID P12-60 (S202-30)
FROM ID FROM ID FROM ID	P13-29 (S202-4) A1J14.50 A1P10.50 P11-180 (S510-3) A1J9.21	TO I	ID A1P14.50 ID A1J10.50 ID P11-244 (S510-2) ID A1P9.21 ID BUS 1
FROM ID FROM ID FROM ID	P20-2 (DMM-HI) A1J15.49 A1P8.28 P10-77 (S503-3) A1J6.13	TO I	ID A1P15.49 ID A1J8.28 ID P10-203 (S503-1) ID A1P6.13 ID BUS 1
FROM ID FROM ID FROM ID	P20-3 (DMM-LO) A1J15.50 A1P7.38 P10-229 (S301-24) A1J7.36	TO I	ID A1P15.50 ID A1J7.38 ID P10-130 (S301-23) ID A1P7.36 GROUND

STEP 116

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.41. THE OUTPUT MEASURED AT J1.16 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM ID	P2-41 (UUT J1-41) J2B-12D A1P12.33	TO I	ID	P1B-12D A1J12.33 P12-26 (S202-15)
FROM ID	P2-16 (UUT J1-16) J2B-11F A1P12.37	TO I	ID	P1B-11F A1J12.37 P12-60 (S202-30)
FROM ID FROM ID FROM ID	P13-29 (S202-4) A1J14.50 A1P10.50 P11-180 (S510-3) A1J9.21	TO 1 TO 1	ID ID ID	A1P14.50 A1J10.50 P11-244 (S510-2) A1P9.21 BUS 1
FROM ID FROM ID FROM ID	P20-2 (DMM-HI) A1J15.49 A1P8.28 P10-77 (S503-3) A1J6.13	TO I	ID ID ID	A1P15.49 A1J8.28 P10-203 (S503-1) A1P6.13 BUS 1
FROM ID	P20-3 (DMM-LO)	TO I	ID	A1P15.50

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FROM ID A1J15.50 TO ID A1J7.38 FROM ID A1P7.38 TO ID P10-130 (S301-23) FROM ID P10-229 (S301-24) TO ID A1P7.36 FROM ID A1J7.36 TO GROUND

STEP 117

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT J1.16 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-16 (UUT J1-16) TO W2 P1B-11F
FROM ID J2B-11F TO ID A1J12.37
FROM ID A1P12.37 TO ID P12-60 (S202-30) FROM ID P13-29 (SZUZ 1,
FROM ID A1J14.50 TO ID A1J10.5U
FROM ID A1P10.50 TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3) TO ID A1P9.21
TO A1J19 21 TO ID BUS 1 FROM ID P13-29 (S202-4) TO ID A1P14.50 FROM ID P20-2 (DMM-HI) TO ID A1P15.49
FROM ID A1J15.49 TO ID A1J8.28
FROM ID A1P8.28 TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3) TO ID A1P6.13 FROM ID A1J6.13 TO ID BUS 1 FROM ID P20-3 (DMM-LO) TO ID A1P15.50
FROM ID A1J15.50 TO ID A1J7.38
FROM ID A1P7.38 TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24) TO ID A1P7.36 TO GROUND

STEP 118

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT J1.12 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

FROM ID A1J7.36

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-12 (UUT J1-12) TO W2 P1B-9F FROM ID J2B-9F TO ID A1J12.41 FROM ID A1P12.41 TO ID P12-29 (S TO ID P12-29 (S202-32)

FROM ID P13-29 (S202-4) TO ID A1P14.50

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FROM	ID	A1J14.50	TO	ID	A1J10.50	
FROM	ID	A1P10.50	TO	ID	P11-244	(S510-2)
FROM	ID	P11-180 (S510-3)	TO	ID	A1P9.21	
FROM	ID	A1J9.21	TO	ID	BUS 1	
FROM	ID	P20-2 (DMM-HI)	TO	ID	A1P15.49	
FROM	ID	A1J15.49	TO	ID	A1J8.28	
FROM	ID	A1P8.28	TO	ID	P10-203	(S503-1)
FROM	ID	P10-77 (S503-3)	ТО	ID	A1P6.13	
FROM	ID	A1J6.13	TO	ID	BUS 1	
FROM	ID	P20-3 (DMM-LO)	ТО	ID	A1P15.50	
FROM	ID	A1J15.50	TO	ID	A1J7.38	
FROM	ID	A1P7.38	ТО	ID	P10-130	(S301-23)
FROM	ID	P10-229 (S301-24)	ТО	ID	A1P7.36	
FROM	ID	A1J7.36	ТО	GRO	OUND	

STEP 119

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.55. THE OUTPUT MEASURED AT J1.12 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM	W2	P2-55 (UUT J1-55)	TO	W2	P1B-12F
FROM	ID	J2B-12F	TO	ID	A1J12.35
FROM	ID	A1P12.35	TO	ID	P12-28 (S202-29)
FROM	W2	P2-12 (UUT J1-12)	TO	W2	P1B-9F
FROM	ID	J2B-9F			A1J12.41
FROM	ID	A1P12.41	TO	ID	P12-29 (S202-32)
		P13-29 (S202-4)			
		A1J14.50			
					P11-244 (S510-2)
FROM	ID	P11-180 (S510-3)	TO	ID	A1P9.21
FROM	ID	A1J9.21	TO	ID	BUS 1
		P20-2 (DMM-HI)			
FROM	ID				A1J8.28
_		A1P8.28	TO	ID	P10-203 (S503-1)
FROM	ID	P10-77 (S503-3)	TO	ID	A1P6.13
FROM	ID	A1J6.13	TO	ID	BUS 1
FROM	ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM	ID	A1J15.50	TO	ID	A1J7.38
		A1P7.38			P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM	ID	A1J7.36	TO	GRO	DUND

Date: 04 March 2016

STEP 120

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT J1.12 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W2 P2-12 (UUT J1-12) FROM ID J2B-9F FROM ID A1P12.41	TO W2 P1B-9F TO ID A1J12.41 TO ID P12-29 (S202-32)
FROM ID P13-29 (S202-4) FROM ID A1J14.50 FROM ID A1P10.50 FROM ID P11-180 (S510-3)	TO ID A1J10.50 TO ID P11-244 (S510-2) TO ID A1P9.21
FROM ID A1J9.21 FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID BUS 1 TO ID A1P15.49 TO ID A1J8.28 TO ID P10-203 (S503-1) TO ID A1P6.13 TO ID BUS 1
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50	TO ID A1P15.50 TO ID A1J7.38

TO ID P10-130 (S301-23)

STEP 121

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.54. THE OUTPUT MEASURED AT J1.12 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

FROM ID P10-229 (S301-24) TO ID A1P7.36
FROM ID A1J7.36 TO GROUND

CONNECTION PATH IS AS FOLLOWS:

FROM ID A1P7.38

SEE "UUT POWER"

FROM W2 P2-54 (UUT J1-54) FROM ID J2A-2F FROM ID A1P14.42	TO W2 P1A-2F TO ID A1J14.42 TO ID P13-28 (S202-27)
FROM W2 P2-12 (UUT J1-12) FROM ID J2B-9F FROM ID A1P12.41	TO W2 P1B-9F TO ID A1J12.41 TO ID P12-29 (S202-32)
FROM ID P13-29 (S202-4)	TO ID A1P14.50

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FROM	ID	A1J14.50	TO	ID	A1J10.50	
FROM	ID	A1P10.50	ТО	ID	P11-244	(S510-2)
FROM	ID	P11-180 (S510-3)	ТО	ID	A1P9.21	
FROM	ID	A1J9.21	TO	ID	BUS 1	
FROM	ID	P20-2 (DMM-HI)	ТО	ID	A1P15.49	
FROM	ID	A1J15.49	TO	ID	A1J8.28	
FROM	ID	A1P8.28	ТО	ID	P10-203	(S503-1)
FROM	ID	P10-77 (S503-3)	TO	ID	A1P6.13	
FROM	ID	A1J6.13	ТО	ID	BUS 1	
FROM	ID	P20-3 (DMM-LO)	TO	ID	A1P15.50	
FROM	ID	A1J15.50	TO	ID	A1J7.38	
FROM	ID	A1P7.38	TO	ID	P10-130	(S301-23)
FROM	ID	P10-229 (S301-24)	ТО	ID	A1P7.36	
FROM	ID	A1J7.36	ТО	GRO	OUND	

STEP 122

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.67. THE OUTPUT MEASURED AT J1.33 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-67 (UUT J1-67) FROM ID J2B-12E FROM ID A1P12.34	
FROM ID P12-90 (S202-2) FROM ID A1J12.36 FROM ID A1P10.12 FROM ID P11-211 (S509-9) FROM ID A1J9.16	TO ID A1J10.12 TO ID P11-242 (S509-2)
FROM W2 P2-33 (UUT J1-33) FROM ID J2B-14E FROM ID A1P12.28	TO W2 P1B-14E TO ID A1J12.28 TO ID P12-86 (S201-48)
FROM ID P12-80 (S201-2) FROM ID A1J12.40 FROM ID A1P10.8 FROM ID P11-77 (S508-3) FROM ID A1J9.15	TO ID A1J10.8 TO ID P11-139 (S508-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)

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FROM ID P20-3 (DMM-LO) TO ID A1P15.50
FROM ID A1J15.50 TO ID A1J7.38
FROM ID A1P7.38 TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24) TO ID A1P7.36
FROM ID A1J7.36 TO GROUND

STEP 123

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.28 AND J1.67. THE OUTPUT MEASURED AT PIN J1.33 SHOULD BE GREATER THAN 27.6VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W2 P2-67 (UUT J1-67) FROM ID J2B-12E FROM ID A1P12.34	TO W2 P1B-12E TO ID A1J12.34 TO ID P12-58 (S202-16)
FROM ID P12-90 (S202-2) FROM ID A1J12.36 FROM ID A1P10.12 FROM ID P11-211 (S509-9) FROM ID A1J9.16	TO ID A1J10.12 TO ID P11-242 (S509-2)
FROM W2 P2-28 (UUT J1-28) FROM ID J2B-14F FROM ID A1P12.29	TO W2 P1B-14F TO ID A1J12.29 TO ID P12-24 (S202-7)
FROM W2 P2-33 (UUT J1-33) FROM ID J2B-14E FROM ID A1P12.28	TO W2 P1B-14E TO ID A1J12.28 TO ID P12-86 (S201-48)
FROM ID P12-80 (S201-2) FROM ID A1J12.40 FROM ID A1P10.8 FROM ID P11-77 (S508-3) FROM ID A1J9.15	TO ID A1J10.8 TO ID P11-139 (S508-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

Date: 04 March 2016

STEP 124

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT J1.34 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-34 (UUT J1-34) FROM ID J2A-4E FROM ID A1P14.45	TO W2 P1A-4E TO ID A1J14.45 TO ID P13-27 (S202-36)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 125

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO J1.28. THE OUTPUT MEASURED AT J1.34 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W2 P2-28 (UUT J1-28)	TO W2 P1B-14F
FROM ID J2B-14F	TO ID A1J12.29
FROM ID A1P12.29	TO ID P12-24 (S202-7)
FROM W2 P2-34 (UUT J1-34)	TO W2 P1A-4E
FROM ID J2A-4E	TO ID A1J14.45
FROM ID A1P14.45	TO ID P13-27 (S202-36)
FROM ID P13-29 (S202-4)	TO ID A1P14.50

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FROM ID A1J14.50 FROM ID A1P10.50 FROM ID P11-180 (S510-3) FROM ID A1J9.21	TO ID A1J10.50 TO ID P11-244 (S510-2) TO ID A1P9.21 TO ID BUS 1
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1P15.49 TO ID A1J8.28 TO ID P10-203 (S503-1) TO ID A1P6.13 TO ID BUS 1
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1P15.50 TO ID A1J7.38 TO ID P10-130 (S301-23) TO ID A1P7.36 TO GROUND

STEP 126

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT J1.47 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM	W2	P2-47 (UUT J1-47)	ТО	W2	P1B-7F
FROM	ID	J2B-7F	TO	ID	A1J12.45
FROM	ID	A1P12.45	TO	ID	P12-95 (S202-38)
FROM	ID	P13-29 (S202-4)	TO	ID	A1P14.50
FROM	ID	A1J14.50	TO	ID	A1J10.50
FROM	ID	A1P10.50	TO	ID	P11-244 (S510-2)
FROM	ID	P11-180 (S510-3)	TO	ID	A1P9.21
FROM	ID	A1J9.21	TO	ID	BUS 1
FROM	ID	P20-2 (DMM-HI)	TO	ID	A1P15.49
FROM	ID	A1J15.49	TO	ID	A1J8.28
_		A1P8.28		ID	P10-203 (S503-1)
FROM	ID	P10-77 (S503-3)	TO	ID	A1P6.13
FROM	ID	A1J6.13	TO	ID	BUS 1
FROM	ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM	ID	A1J15.50	TO	ID	A1J7.38
FROM	ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM	ID	A1J7.36	TO	GRO	OUND

STEP 127

DESCRIPTION:

Date: 04 March 2016

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.53. THE OUTPUT MEASURED AT J1.47 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM W2 P2-53 (UUT J1-53 FROM ID J2A-1B FROM ID A1P14.30	
FROM W2 P2-47 (UUT J1-47 FROM ID J2B-7F FROM ID A1P12.45	TO W2 P1B-7F TO ID A1J12.45 TO ID P12-95 (S202-38)
FROM ID P13-29 (S202-4) FROM ID A1J14.50 FROM ID A1P10.50 FROM ID P11-180 (S510-3) FROM ID A1J9.21	TO ID A1J10.50 TO ID P11-244 (S510-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24 FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 128

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT J1.47 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-47 (UUT J1-47)	TO W2 P1B-7F
FROM ID J2B-7F	TO ID A1J12.45
FROM ID A1P12.45	TO ID P12-95 (S202-38)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1

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FROM ID	P20-2 (DMM-HI)	TO	ID	A1P15.49
FROM ID	A1J15.49	TO	ID	A1J8.28
FROM ID	A1P8.28	TO	ID	P10-203 (S503-1)
FROM ID	P10-77 (S503-3)	TO	ID	A1P6.13
FROM ID	A1J6.13	TO	ID	BUS 1
FROM ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM ID	A1J15.50	TO	ID	A1J7.38
FROM ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM ID	A1J7.36	TO	GRO	DUND

STEP 129

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.52. THE OUTPUT MEASURED AT J1.47 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM W2	P2-52 (UUT J1-52)	TO W2	P1B-7C
FROM ID	J2B-7C	TO ID	A1J12.22
FROM ID	A1P12.22	TO ID	P12-91 (S202-23)
FROM W2	P2-47 (UUT J1-47)	TO W2	P1B-7F
FROM ID	J2B-7F	TO ID	A1J12.45
FROM ID	A1P12.45	TO ID	P12-95 (S202-38)
FROM ID	P13-29 (S202-4)	TO ID	A1P14.50
FROM ID	A1J14.50	TO ID	A1J10.50
FROM ID	A1P10.50	TO ID	P11-244 (S510-2)
FROM ID	P11-180 (S510-3)	TO ID	A1P9.21
FROM ID	A1J9.21	TO ID	BUS 1
FROM ID	P20-2 (DMM-HI)	TO ID	A1P15.49
FROM ID	A1J15.49	TO ID	A1J8.28
FROM ID	A1P8.28	TO ID	P10-203 (S503-1)
FROM ID	P10-77 (S503-3)	TO ID	A1P6.13
FROM ID	A1J6.13	TO ID	BUS 1
FROM ID	P20-3 (DMM-LO)	TO ID	A1P15.50
FROM ID	A1J15.50	TO ID	A1J7.38
FROM ID	A1P7.38	TO ID	P10-130 (S301-23)
FROM ID	P10-229 (S301-24)	TO ID	A1P7.36
FROM ID	A1J7.36	TO GR	OUND

STEP 130

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.47 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

Date: 04 March 2016

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-47 (UUT J1-47) TO W2 P1B-7F FROM ID J2B-7F TO ID A1J12.45 FROM ID A1P12.45 TO ID P12-95 (S202-38)

FROM ID P13-29 (S202-4) TO ID A1P14.50
FROM ID A1J14.50 TO ID A1J10.50
FROM ID A1P10.50 TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3) TO ID A1P9.21
FROM ID A1J9.21 TO ID BUS 1

FROM ID P20-2 (DMM-HI) TO ID A1P15.49
FROM ID A1J15.49 TO ID A1J8.28
FROM ID A1P8.28 TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3) TO ID A1P6.13
FROM ID A1J6.13 TO ID BUS 1

FROM ID P20-3 (DMM-LO) TO ID A1P15.50
FROM ID A1J15.50 TO ID A1J7.38
FROM ID A1P7.38 TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24) TO ID A1P7.36
FROM ID A1J7.36 TO GROUND

STEP 131

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.60 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-60 (UUT J1-60) TO W2 P1A-1F FROM ID J2A-1F TO ID A1J14.43 FROM ID A1P14.43 TO ID P13-60 (S202-28)

FROM ID P13-29 (S202-4) TO ID A1P14.50
FROM ID A1J14.50 TO ID A1J10.50
FROM ID A1P10.50 TO ID P11-244 (S510-2)
FROM ID P11-20 (S510-8) TO ID A1P9.28
FROM ID A1J9.28 TO ID BUS 6

FROM ID BUS 6

FROM ID A1P8.50

FROM ID P10-9 (S301-53)

FROM ID A1J7.26

FROM ID A1P4.18

FROM ID A1P4.18

FROM ID R108.2

TO ID A1J8.50

TO ID P10-138 (S301-54)

TO ID A1P7.26

TO ID A1J4.18

TO ID R108.1

FROM ID R108.2

TO ID A1P4.10 FROM ID A1J4.10 TO GROUND

Date: 04 March 2016

FROM ID P13-29 (S202-4) FROM ID A1J14.50 FROM ID A1P10.50 FROM ID P11-180 (S510-3) FROM ID A1J9.21	TO ID A1J10.50 TO ID P11-244 (S510-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 132

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.49. THE VOLTAGE MEASURED AT PIN J1.60 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

	2 P2-49 (UUT J1-49)		
FROM II) J2B-6A) A1P12.21	TO	ID A1J12.21
FROM II	A1P12.21	TO	ID P12-61 (S202-21)
	2 P2-60 (UUT J1-60)		
			ID A1J14.43
FROM II	A1P14.43	TO	ID P13-60 (S202-28)
FROM II	P13-29 (S202-4)	TO	ID A1P14.50
	A1J14.50	TO	ID A1J10.50
			ID P11-244 (S510-2)
FROM II	P11-20 (S510-8)	TO	ID A1P9.28
FROM II	A1J9.28	TO	ID BUS 6
FROM II	BUS 6	TO	ID A1J8.50
DDOM T	-1-0 -0	mo.	ID P10-138 (S301-54)
FROM II	A1P8.50	TO	TD LIO 130 (8301 34)
FROM II	P10-9 (S301-53)	TO	ID A1P7.26
FROM II	P10-9 (S301-53)	TO	ID A1P7.26
FROM II) P10-9 (S301-53)) A1J7.26) A1P4.18	TO TO	ID A1P7.26 ID A1J4.18 ID R108.1
FROM II FROM II FROM II) P10-9 (S301-53)) A1J7.26) A1P4.18	TO TO	ID A1P7.26 ID A1J4.18 ID R108.1
FROM II FROM II FROM II FROM II) P10-9 (S301-53)) A1J7.26) A1P4.18	TO TO TO	ID A1P7.26
FROM II FROM II FROM II FROM II) P10-9 (S301-53)) A1J7.26) A1P4.18) R108.2	TO TO TO	ID A1P7.26 ID A1J4.18 ID R108.1 ID A1P4.10
FROM II FROM II FROM II FROM II	P10-9 (S301-53) A1J7.26 A1P4.18 R108.2 A1J4.10 P13-29 (S202-4)	TO TO TO TO	ID A1P7.26 ID A1J4.18 ID R108.1 ID A1P4.10 GROUND ID A1P14.50
FROM II FROM II FROM II FROM II FROM II	P10-9 (S301-53) A1J7.26 A1P4.18 R108.2 A1J4.10 P13-29 (S202-4)	TO TO TO TO	ID A1P7.26 ID A1J4.18 ID R108.1 ID A1P4.10 GROUND ID A1P14.50
FROM II	P10-9 (S301-53) A1J7.26 A1P4.18 R108.2 A1J4.10 P13-29 (S202-4) A1J14.50	TO TO TO TO TO TO	ID A1P7.26 ID A1J4.18 ID R108.1 ID A1P4.10 GROUND ID A1P14.50 ID A1J10.50
FROM II	P10-9 (S301-53) A1J7.26 A1P4.18 R108.2 A1J4.10 P13-29 (S202-4) A1J14.50	TO TO TO TO TO TO	ID A1P7.26 ID A1J4.18 ID R108.1 ID A1P4.10 GROUND ID A1P14.50 ID A1J10.50 ID P11-244 (S510-2)

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FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1P15.49 TO ID A1J8.28 TO ID P10-203 (S503-1) TO ID A1P6.13 TO ID BUS 1
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1P15.50 TO ID A1J7.38 TO ID P10-130 (S301-23) TO ID A1P7.36 TO GROUND

STEP 133

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.49 AND J1.24. THE VOLTAGE MEASURED AT PIN J1.60 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

	TO W2 P1B-8C TO ID A1J12.25 TO ID P12-89 (S202-5)
FROM W2 P2-49 (UUT J1-49) FROM ID J2B-6A FROM ID A1P12.21	TO ID A1J12.21
FROM W2 P2-60 (UUT J1-60) FROM ID J2A-1F FROM ID A1P14.43	TO ID A1J14.43
	TO ID A1J10.50 TO ID P11-244 (S510-2)
FROM ID A1P8.50 FROM ID P10-9 (S301-53) FROM ID A1J7.26 FROM ID A1P4.18 FROM ID R108.2	TO ID A1J4.18 TO ID R108.1 TO ID A1P4.10
FROM ID A1J4.10 FROM ID P13-29 (S202-4) FROM ID A1J14.50 FROM ID A1P10.50	

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FROM ID	P11-180 (S510-3)	TO	ID	A1P9.21
FROM ID	A1J9.21	TO	ID	BUS 1
FROM ID	P20-2 (DMM-HI)	TO	ID	A1P15.49
FROM ID	A1J15.49	TO	ID	A1J8.28
FROM ID	A1P8.28	TO	ID	P10-203 (S503-1)
FROM ID	P10-77 (S503-3)	TO	ID	A1P6.13
FROM ID	A1J6.13	TO	ID	BUS 1
FROM ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM ID	A1J15.50	TO	ID	A1J7.38
FROM ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM ID	A1J7.36	TO	GRO	DUND

STEP 134

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.49 AND J1.24. THE 28.0VDC IS REMOVED FROM PIN J1.24. THE VOLTAGE MEASURED AT PIN J1.60 SHOULD DROP TO LESS THAN 0.2VDC RELATIVE TO GROUND 2 SECONDS AFTER DISCONNECTING J1.24 FROM 28.0VDC.

FROM W2 P2-49 (UUT J1-49) FROM ID J2B-6A FROM ID A1P12.21	TO W2 P1B-6A TO ID A1J12.21 TO ID P12-61 (S202-21)
FROM W2 P2-60 (UUT J1-60) FROM ID J2A-1F FROM ID A1P14.43	TO W2 P1A-1F TO ID A1J14.43 TO ID P13-60 (S202-28)
FROM ID P13-29 (S202-4) FROM ID A1J14.50 FROM ID A1P10.50 FROM ID P11-20 (S510-8) FROM ID A1J9.28	TO ID A1J10.50 TO ID P11-244 (S510-2)
FROM ID P10-9 (S301-53) FROM ID A1J7.26 FROM ID A1P4.18	TO ID A1J8.50 TO ID P10-138 (S301-54) TO ID A1P7.26 TO ID A1J4.18 TO ID R108.1 TO ID A1P4.10 TO GROUND
FROM ID P13-29 (S202-4) FROM ID A1J14.50 FROM ID A1P10.50 FROM ID P11-180 (S510-3) FROM ID A1J9.21	TO ID A1J10.50 TO ID P11-244 (S510-2)

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FROM ID P20-2	(DMM-HI)	TO I	ID .	A1P15.49	
FROM ID A1J15.	49	TO I	ID .	A1J8.28	
FROM ID A1P8.2	8	TO I	ID	P10-203	(S503-1)
FROM ID P10-77	(S503-3)	TO I	ID .	A1P6.13	
FROM ID A1J6.1	3	TO I	ID	BUS 1	
FROM ID P20-3	(DMM-LO)	TO I	ID .	A1P15.50	
FROM ID A1J15.	50	TO I	ID .	A1J7.38	
FROM ID A1P7.3	8	TO I	ID	P10-130	(S301-23)
FROM ID P10-22	9 (S301-24)	TO I	ID .	A1P7.36	
FROM ID A1J7.3	6	TO (GRO	UND	

STEP 135

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.49 AND J1.8. THE VOLTAGE MEASURED AT PIN J1.60 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE TO GROUND.

FROM W2 P2-49 (UUT J1-49) FROM ID J2B-6A FROM ID A1P12.21	
FROM W2 P2-8 (UUT J1-8)	TO W2 P1A-2B
FROM ID J2A-2B FROM ID A1P14.31	TO ID A1J14.31
FROM ID AlP14.31	TO ID P13-30 (S202-41)
FROM W2 P2-60 (UUT J1-60)	
FROM ID J2A-1F FROM ID A1P14.43	TO ID A1J14.43
FROM ID A1P14.43	TO ID P13-60 (S202-28)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1J14.50 FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-20 (S510-8)	TO ID A1P9.28
FROM ID P11-20 (S510-8) FROM ID A1J9.28	TO ID BUS 6
FROM ID BUS 6 FROM ID A1P8.50	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1J7.26 FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P13-29 (S202-4) FROM ID A1J14.50 FROM ID A1P10.50 FROM ID P11-180 (S510-3)	TO ID Alp9.21

FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49	TO ID A1P15.49 TO ID A1J8.28
FROM ID A1P8.28 FROM ID P10-77 (S503-3)	TO ID P10-203 (S503-1) TO ID A1P6.13
FROM ID AlJ6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 136

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.49 AND J1.8. THE 28.0VDC IS REMOVED FROM PIN J1.8. THE VOLTAGE MEASURED AT PIN J1.60 SHOULD DROP TO LESS THAN 0.2VDC RELATIVE TO GROUND 2 SECONDS AFTER DISCONNECTING J1.8 FROM 28.0VDC.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER" FROM W2 P2-8 (UUT J1-8) TO W2 P1A-2B FROM ID J2A-2B TO ID A1J14.31 FROM ID A1P14.31 TO ID P13-30 (S202-41) FROM W2 P2-60 (UUT J1-60) TO W2 P1A-1F FROM ID J2A-1F TO ID A1J14.43 FROM ID A1P14.43 TO ID P13-60 (S202-28) FROM ID A1J14.50 TO ID A1J10.50 FROM ID A1J14.50 TO ID A1J10.50 FROM ID A1P10.50 TO ID A1P9.28 FROM ID A1J9.28 TO ID BUS 6 FROM ID BUS 6 FROM ID BUS 6 FROM ID A1P8.50 TO ID A1J8.50 FROM ID A1P8.50 TO ID A1J8.50 FROM ID A1P8.50 TO ID A1J8.50 FROM ID A1P8.50 TO ID A1J4.18 FROM ID A1J7.26 TO ID A1J4.18 FROM ID A1J7.26 TO ID A1J4.18 FROM ID A1J4.18 TO ID R108.1 FROM ID A1J4.10 TO GROUND FROM ID P13-29 (S202-4) TO ID A1P14.50 FROM ID A1J4.50 TO ID A1J10.50 FROM ID A1J14.50 TO ID A1P9.21 FROM ID A1J9.21 TO ID BUS 1

FROM ID P20-2 (DMM-HI) TO ID A1P15.49

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FROM	ID	A1J15.49	TO	ID	A1J8.28
FROM	ID	A1P8.28	ТО	ID	P10-203 (S503-1)
FROM	ID	P10-77 (S503-3)	ТО	ID	A1P6.13
FROM	ID	A1J6.13	ТО	ID	BUS 1
FROM	ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM	ID	A1J15.50	TO	ID	A1J7.38
FROM	ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	ТО	ID	A1P7.36
FROM	ID	A1J7.36	TO	GRO	DUND

STEP 137

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.49 AND J1.22. THE VOLTAGE MEASURED AT PIN J1.60 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE TO GROUND.

FROM W2 P2-8 (UUT J1-8)	TO W2 P1A-2B
FROM ID J2A-2B	TO ID A1J14.31
FROM W2 P2-8 (001 J1-8) FROM ID J2A-2B FROM ID A1P14.31	TO ID P13-30 (S202-41)
FROM W2 P2-49 (UUT J1-49) TO W2 P1B-6A
FROM ID J2B-6A	TO ID A1J12.21
FROM ID J2B-6A FROM ID A1P12.21	TO ID P12-61 (S202-21)
FROM W2 P2-22 (UUT J1-22	
FROM ID J2A-4F	TO ID A1J14.46
FROM ID J2A-4F FROM ID A1P14.46	TO ID P13-95 (S202-43)
FROM W2 P2-60 (UUT J1-60	1) TO W2 P1A-1F
FROM ID J2A-1F FROM ID A1P14.43	TO ID P13-60 (S202-28)
FROM ID AIF14.45	10 1D F13-00 (5202-20)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID AIPIU.50	10 10 P11-244 (5510-2)
FROM ID P11-20 (S510-8)	TO ID A1P9.28
FROM ID A1J9.28	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50 TO ID P10-138 (S301-54)
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J7.26 FROM ID A1P4.18 FROM ID R108.2 FROM ID A1J4.10	TO GROUND
FROM ID P13-29 (S202-4)	TO ID A1P14 50
FROM ID A1J14.50 FROM ID A1P10.50	TO ID P11-244 (S510-2)
PROM ID AIPIU.50	10 1D P11-244 (5010-2)

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FROM	ID	P11-180 (S510-3)	ТО	ID	A1P9.21
FROM	ID	A1J9.21	ТО	ID	BUS 1
FROM	ID	P20-2 (DMM-HI)	TO	ID	A1P15.49
FROM	ID	A1J15.49	TO	ID	A1J8.28
FROM	ID	A1P8.28	TO	ID	P10-203 (S503-1)
FROM	ID	P10-77 (S503-3)	ТО	ID	A1P6.13
FROM	ID	A1J6.13	TO	ID	BUS 1
FROM	ID	P20-3 (DMM-LO)	ТО	ID	A1P15.50
FROM	ID	A1J15.50	TO	ID	A1J7.38
FROM	ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	ТО	ID	A1P7.36
FROM	ID	A1J7.36	ТО	GRO	DUND

STEP 138

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.49 AND J1.22. THE 28.0VDC IS REMOVED FROM PIN J1.22. THE VOLTAGE MEASURED AT PIN J1.60 SHOULD DROP TO LESS THAN 0.2VDC RELATIVE TO GROUND 2 SECONDS AFTER DISCONNECTING J1.22 FROM 28.0VDC.

FROM W2	P2-8 (UUT J1-8)	TO		P1A-2B
FROM ID	J2A-2B	TO	ID	A1J14.31
FROM ID	A1P14.31	TO	ID	P13-30 (S202-41)
FROM W2	P2-49 (UUT J1-49)	TO	W2	P1B-6A
FROM ID	J2B-6A	TO	ID	A1J12.21
FROM ID	A1P12.21	TO	ID	P12-61 (S202-21)
FROM W2	P2-60 (UUT J1-60)	TO	W2	P1A-1F
FROM ID	J2A-1F	TO	ID	A1J14.43
FROM ID	A1P14.43	TO	ID	P13-60 (S202-28)
FROM ID	P13-29 (S202-4)	TO	ID	A1P14.50
FROM ID	A1J14.50	TO	ID	A1J10.50
FROM ID	A1P10.50	TO	ID	P11-244 (S510-2)
FROM ID	P11-20 (S510-8)	TO	ID	A1P9.28
FROM ID	A1J9.28	TO	ID	BUS 6
FROM ID	BUS 6	_		A1J8.50
-	A1P8.50	TO	ID	P10-138 (S301-54)
	P10-9 (S301-53)			
FROM ID	A1J7.26 A1P4.18 R108.2	TO	ID	A1J4.18
FROM ID	A1P4.18	TO	ID	R108.1
FROM ID	R108.2	TO	ID	A1P4.10
FROM ID	A1J4.10	TO	GRO	DUND
FROM ID	P13-29 (S202-4)	TO	ID	A1P14.50

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FROM ID A1J14.50 FROM ID A1P10.50 FROM ID P11-180 (S510-3) FROM ID A1J9.21	TO ID A1J10.50 TO ID P11-244 (S510-2) TO ID A1P9.21 TO ID BUS 1
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1P15.49 TO ID A1J8.28 TO ID P10-203 (S503-1) TO ID A1P6.13 TO ID BUS 1
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1P15.50 TO ID A1J7.38 TO ID P10-130 (S301-23) TO ID A1P7.36 TO GROUND

STEP 139

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-25 (UUT J1-25) FROM ID J2A-5D FROM ID A1P14.47	TO W2 P1A-5D TO ID A1J14.47
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID AIP10.48	TO ID P11-52 (S510-1)
FROM ID P11-20 (S510-8)	TO ID A1P9.28
FROM ID A1J9.28	TO ID BUS 6
FROM ID BUS 6 FROM ID A1P8.50	TO ID A1J8.50
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
$\Box\Box\cap\cap M$ $\Box\Box$ Δ Δ Δ Δ Δ Δ Δ	m
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
	TO ID P11-52 (S510-1)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID Alj8.28

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FROM ID A1P8.28
FROM ID P10-77 (S503-3)
FROM ID A1J6.13
FROM ID A1J6.13
TO ID A1P6.13
TO ID BUS 1

FROM ID P20-3 (DMM-LO)
FROM ID A1J15.50
FROM ID A1P7.38
FROM ID A1P7.38
FROM ID P10-229 (S301-24)
FROM ID A1.77.36
TO GROUND

STEP 140

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.28. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM ID J2B-14F TO ID A FROM ID A1P12.29 TO ID P	
FROM ID A1P12.29 TO ID P	
FROM W2 P2-25 (UUT J1-25) TO W2 P	1A-5D
FROM ID J2A-5D TO ID A	.1J14.47
FROM W2 P2-25 (UUT J1-25) TO W2 P FROM ID J2A-5D TO ID A FROM ID A1P14.47 TO ID P	13-32 (S202-49)
FROM ID P13-93 (S202-3) TO ID A	
FROM ID A1J14.49 TO ID A FROM ID A1P10.48 TO ID P	11_52 (9510_1)
FROM ID P11-20 (S510-8) TO ID A	1D0 20
FROM ID Ally9.28 TO ID B	TIC 6
FROM ID BUS 6 TO ID A FROM ID A1P8.50 TO ID P	.1J8.50
FROM ID A1P8.50 TO ID P	10-138 (S301-54)
- FROM 10 PIOSO (33015331 - 10 10 A	1 P / . 7.0
FROM ID A1J7.26 TO ID A	.1J4.18
FROM ID A1P4.18 TO ID R	108.1
FROM ID R108.2 TO ID A	1P4.10
FROM ID A1J7.26 TO ID A FROM ID A1P4.18 TO ID R FROM ID R108.2 TO ID A FROM ID A1J4.10 TO GROU	ND
FROM ID P13-93 (S202-3) TO ID A	.1P14.49
FROM ID A1J14.49 TO ID A	
FROM ID A1P10.48 TO ID P	11-52 (S510-1)
FROM ID P11-180 (S510-3) TO ID A	1P9.21
FROM ID AlJ9.21 TO ID B	US 1
FROM ID P20-2 (DMM-HI) TO ID A	1P15.49
FROM ID A1J15.49 TO ID A	.1J8.28
FROM ID A1P8.28 TO ID P	10-203 (S503-1)
FROM ID P10-77 (S503-3) TO ID A	
FROM ID A1J6.13 TO ID B	US 1

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FROM ID P20-3 (DMM-LO) TO ID A1P15.50
FROM ID A1J15.50 TO ID A1J7.38
FROM ID A1P7.38 TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24) TO ID A1P7.36
FROM ID A1J7.36 TO GROUND

STEP 141

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.28 AND J1.24. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W2 P2-28 (UUT J1-28) FROM ID J2B-14F FROM ID A1P12.29	
FROM W2 P2-24 (UUT J1-24) FROM ID J2B-8C FROM ID A1P12.25	TO W2 P1B-8C TO ID A1J12.25 TO ID P12-89 (S202-5)
FROM W2 P2-25 (UUT J1-25) FROM ID J2A-5D FROM ID A1P14.47	
FROM ID P13-93 (S202-3) FROM ID A1J14.49 FROM ID A1P10.48 FROM ID P11-20 (S510-8) FROM ID A1J9.28	TO ID P11-52 (S510-1)
FROM ID BUS 6 FROM ID A1P8.50 FROM ID P10-9 (S301-53) FROM ID A1J7.26 FROM ID A1P4.18 FROM ID R108.2 FROM ID A1J4.10	TO ID A1J8.50 TO ID P10-138 (S301-54) TO ID A1P7.26 TO ID A1J4.18 TO ID R108.1 TO ID A1P4.10 TO GROUND
FROM ID P13-93 (S202-3) FROM ID A1J14.49	TO ID A1P14.49 TO ID A1J10.48 TO ID P11-52 (S510-1)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28

FROM ID P20-3 (DMM-LO) TO ID A1P15.50
FROM ID A1J15.50 TO ID A1J7.38
FROM ID A1P7.38 TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24) TO ID A1P7.36
FROM ID A1J7.36 TO GROUND

STEP 142

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.28 AND J1.24. THE 28.0VDC IS REMOVED FROM PIN J1.24. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD DROP TO LESS THAN 0.2VDC RELATIVE TO GROUND 2 SECONDS AFTER DISCONNECTING J1.24 FROM 28.0VDC.

FROM	W2	P2-28 (UUT J1-28)	ТО	W2	P1B-14F
		J2B-14F	ТО	ID	A1J12.29
FROM	ID	A1P12.29			P12-24 (S202-7)
					,
FROM	W2	P2-25 (UUT J1-25)	то	W2	P1A-5D
		J2A-5D			A1J14.47
FROM	ID	A1P14.47	ТО	ID	P13-32 (S202-49)
					, ,
FROM	ID	P13-93 (S202-3)	ТО	ID	A1P14.49
		A1J14.49			A1J10.48
		A1P10.48			P11-52 (S510-1)
FROM	ID	P11-20 (S510-8)			A1P9.28
		A1J9.28			BUS 6
FROM	ID	BUS 6	то	ID	A1J8.50
FROM	ID	A1P8.50	ТО	ID	P10-138 (S301-54)
		P10-9 (S301-53)			A1P7.26
		A1J7.26			A1J4.18
FROM	ID	A1P4.18	ТО	ID	R108.1
		R108.2			A1P4.10
		A1J4.10			DUND
FROM	ID	P13-93 (S202-3)	ТО	ID	A1P14.49
		A1J14.49			A1J10.48
FROM	ID	A1P10.48	ТО	ID	P11-52 (S510-1)
FROM	ID	P11-180 (S510-3)			A1P9.21
		A1J9.21	ТО	ID	BUS 1
FROM	ID	P20-2 (DMM-HI)	ТО	ID	A1P15.49
		A1J15.49	ТО	ID	A1J8.28
		A1P8.28			P10-203 (S503-1)
		P10-77 (S503-3)			A1P6.13
		A1J6.13	ТО	ID	BUS 1
FROM	ID	P20-3 (DMM-LO)	то	ID	A1P15.50
		(,			

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FROM ID A1J15.50 TO ID A1J7.38
FROM ID A1P7.38 TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24) TO ID A1P7.36
FROM ID A1J7.36 TO GROUND

STEP 143

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.28 AND J1.8. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE TO GROUND.

FROM W2 FROM ID	P2-28 (UUT J1-28) J2B-14F	TO TO		P1B-14F A1J12.29
FROM ID	J2B-14F A1P12.29	TO	ID	P12-24 (S202-7)
	P2-8 (UUT J1-8)			P1A-2B
FROM ID	J2A-2B	TO	ID	A1J14.31
FROM ID	A1P14.31	TO	ID	P13-30 (S202-41)
	P2-25 (UUT J1-25)			
_	J2A-5D	_		A1J14.47
FROM ID	A1P14.47	ТО	ID	P13-32 (S202-49)
	P13-93 (S202-3)			
_	A1J14.49	_		A1J10.48
	A1P10.48			P11-52 (S510-1)
	P11-20 (S510-8)			A1P9.28
FROM ID	A1J9.28	ТО	ID	BUS 6
FROM ID		_		A1J8.50
	A1P8.50			P10-138 (S301-54)
	P10-9 (S301-53)			A1P7.26
	A1J7.26			A1J4.18
	A1P4.18			R108.1
	R108.2			A1P4.10
FROM ID	A1J4.10	ТО	GRO	DUND
	P13-93 (S202-3)			A1P14.49
_	A1J14.49			A1J10.48
	A1P10.48			P11-52 (S510-1)
	P11-180 (S510-3)			A1P9.21
FROM ID	A1J9.21	ТО	ID	BUS 1
	P20-2 (DMM-HI)			A1P15.49
	A1J15.49	_		A1J8.28
	A1P8.28	TO	ID	P10-203 (S503-1)
	P10-77 (S503-3)	TO	ID	A1P6.13
FROM ID	A1J6.13	ТО	ID	BUS 1

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FROM ID P20-3 (DMM-LO) TO ID A1P15.50
FROM ID A1J15.50 TO ID A1J7.38
FROM ID A1P7.38 TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24) TO ID A1P7.36
FROM ID A1J7.36 TO GROUND

STEP 144

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.28 AND J1.8. THE 28.0VDC IS REMOVED FROM PIN J1.8. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD DROP TO LESS THAN 0.2VDC RELATIVE TO GROUND 2 SECONDS AFTER DISCONNECTING J1.8 FROM 28.0VDC.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

		TO II	P1B-14F A1J12.29 P12-24 (S202-7)
FROM ID J2. FROM ID A1. FROM ID A1. FROM ID A1. FROM ID A1.	P14.47 3-93 (S202-3) J14.49 P10.48 1-20 (S510-8)	TO II	D A1J14.47 D P13-32 (S202-49) D A1P14.49 D A1J10.48 D P11-52 (S510-1)
FROM ID P1 FROM ID A1 FROM ID A1 FROM ID R1 FROM ID A1	P4.18 08.2	TO II TO II TO II	
FROM ID A1: FROM ID A1: FROM ID P1 FROM ID A1:	P10.48 1-180 (S510-3) J9.21	TO II TO II TO II TO II	0 A1J10.48 0 P11-52 (S510-1) 0 A1P9.21 0 BUS 1
FROM ID A1	P8.28 0-77 (S503-3)	TO II TO II TO II	D A1J8.28 D P10-203 (S503-1)
FROM ID A1	P7.38 0-229 (S301-24)	TO II TO II TO II	A1J7.38 P10-130 (S301-23)

STEP 145

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.28 AND J1.22. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE TO GROUND.

FROM W2	P2-28 (UUT J1-28)	TO W2	P1B-14F
			A1J12.29
FROM ID) J2B-14F) A1P12.29		P12-24 (S202-7)
			,
FROM W2	P2-22 (UUT J1-22)	TO W2	P1A-4F
EDUM IL	1 .T2λ_4F	TO TE	A1J14.46
FROM ID	A1P14.46		P13-95 (S202-43)
TROM ID	, ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	10 11	, 113)3 (8202 13)
FROM W2	P2-25 (UUT J1-25)	TO W2	P1A-5D
EDOM TE	, TOX ED	TO TE	A1J14.47
FROM ID	0 02A-3D 0 A1P14.47	TO ID	P13-32 (S202-49)
TROM ID	, , , , , , , , , , , , , , , , , , , ,	10 11	, 113 32 (5202 1),
FROM ID) P13-93 (S202-3)) A1J14.49	TO II	A1P14.49
FROM ID	A1J14.49	TO II	A1J10.48
FROM ID	A1P10.48	TO II	P11-52 (S510-1)
	P11-20 (S510-8)		
FROM ID	A1J9.28	TO TE	BUS 6
FROM ID	BUS 6 A1P8.50 P10-9 (S301-53) A1J7.26	TO II	A1J8.50
FROM ID	A1P8.50	TO II	P10-138 (S301-54)
FROM ID	P10-9 (S301-53)	TO II	A1P7.26
FROM ID	A1J7.26	TO II	A1J4.18
FROM ID	A1P4.18	TO II	R108.1
	R108.2	TO II	A1P4.10
FROM ID	A1J7.26 A1P4.18 R108.2 A1J4.10	TO GR	OUND
FROM ID	P13-93 (S202-3)	TO II	A1P14.49
	A1J14.49		A1J10.48
FROM ID	A1P10.48	TO II	P11-52 (S510-1)
FROM ID	P11-180 (S510-3)	TO II	A1P9.21
FROM ID	A1J9.21	TO II	BUS 1
FROM ID	P20-2 (DMM-HI)	TO II	A1P15.49
FROM ID	A1J15.49	TO II	A1J8.28
FROM ID	A1P8.28	TO II	P10-203 (S503-1)
FROM ID	P10-77 (S503-3)		A1P6.13
FROM ID	A1J6.13		BUS 1
FROM ID) P20-3 (DMM-LO)	TO II	A1P15.50
FROM ID	A1J15.50	TO II	A1J7.38
FROM ID	A1015.50 A1P7.38	TO II	P10-130 (S301-23)
FROM ID	P10-229 (S301-24)		
	,		

FROM ID A1J7.36 TO GROUND

STEP 146

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.28 AND J1.22. THE 28.0VDC IS REMOVED FROM PIN J1.22. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD DROP TO LESS THAN 0.2VDC RELATIVE TO GROUND 2 SECONDS AFTER DISCONNECTING J1.22 FROM 28.0VDC.

FROM W2 P2-28	(UUT J1-28)				
FROM ID JZB-I4	± F.			A1J12.29	
FROM ID J2B-14 FROM ID A1P12.	. 29	TO	ID	P12-24 (S202-7)
	(UUT J1-25)	ТО	W2	P1A-5D	
				A1J14.47	,
FROM ID J2A-51 FROM ID A1P14.	47				S202-49)
TROP ID THE T	. 17	10		113 32 (5202 1),
FROM ID P13-93	3 (S202-3) .49 .48	ТО	ID	A1P14.49)
FROM ID A1J14.	. 49	TO	ID	A1J10.48	}
FROM ID A1P10.	. 48	TO	ID	P11-52 (S510-1)
FROM ID P11-20) (S510-8)	TO	ID	A1P9.28	
FROM ID A1J9.2	28	TO	ID	BUS 6	
FROM ID BUS 6		TO	ID	A1J8.50	
FROM ID BUS 6 FROM ID A1P8.5	50	TO	ID	P10-138	(S301-54)
FROM ID P10-9	(S301-53)	TO	ID	A1P7.26	
FROM ID A1J7.2	50 (S301-53) 26	ТО	ID	A1J4.18	
FROM ID A1P4.1	18	TO	ID	R108.1	
FROM ID R108.2				A1P4.10	
FROM ID A1J4.1				UND	
FROM ID P13-93	3 (S202-3) .49	TO	ID	A1P14.49)
FROM ID A1J14.	. 49	TO	ID	A1J10.48	}
FROM ID A1P10.	. 48	TO	ID	P11-52 (S510-1)
FROM ID P11-18	30 (S510-3)	TO	ID	A1P9.21	
FROM ID A1J9.2				BUS 1	
FROM ID P20-2	(DMM-HI) .49	TO	ID	A1P15.49)
FROM ID A1J15.	.49	TO	ID	A1J8.28	
FROM ID A1P8.2	28	TO	ID	P10-203	(S503-1)
FROM ID P10-77	7 (S503-3)	TO	ID	A1P6.13	
FROM ID A1J6.1	L3			BUS 1	
FROM ID P20-3	(DMM-LO)	TO	ID	A1P15.50)
FROM ID A1J15.	.50			A1J7.38	
FROM ID A1P7.3	38	ТО	ID	P10-130	(S301-23)
	29 (S301-24)	ТО	ID	A1P7.36	, ,
FROM ID A1J7.3				UND	
			5110		

Date: 04 March 2016

STEP 147

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.39 SHOULD BE GREATER THAN 27.5VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-39 (UUT J1-39) TO W2 P1A-5E FROM ID J2A-5E TO ID A1J14.48 FROM ID A1P14.48 TO ID P13-63 (S202-51) TO ID A1J14.48 FROM ID P13-93 (S202-3) TO ID A1P14.49 FROM ID P13-93 (S202-3)

FROM ID A1J14.49

FROM ID A1P10.48

FROM ID P11-115 (S510-7)

FROM ID A1J9.38

TO ID A1P9.38

TO ID BUS 5 FROM ID BUS 5

FROM ID A1P8.47

FROM ID A1P8.47

FROM ID P10-7 (S301-47)

FROM ID A1J7.23

FROM ID A1P4.15

FROM ID R111.2

FROM ID A1J4.9

FROM ID A1J4.9 FROM ID P13-93 (S202-3) TO ID A1P14.49
FROM ID A1J14.49 TO ID A1J10.48
FROM ID A1P10.48 TO ID P11-52 (S510-1)
FROM ID P11-180 (S510-3) TO ID A1P9.21
FROM ID A1J9.21 TO ID BUS 1 FROM ID P20-2 (DMM-HI) TO ID A1P15.49
FROM ID A1J15.49 TO ID A1J8.28
FROM ID A1P8.28 TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3) TO ID A1P6.13
FROM ID A1J6.13 TO ID BUS 1 FROM ID P20-3 (DMM-LO) TO ID A1P15.50
FROM ID A1J15.50 TO ID A1J7.38
FROM ID A1P7.38 TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24) TO ID A1P7.36
FROM ID A1J7.36 TO GROUND

STEP 148

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.23. THE VOLTAGE MEASURED AT PIN J1.39 SHOULD DROP TO LESS THAN 0.2VDC RELATIVE TO GROUND 100 MILLISECONDS AFTER CONNECTING J1.23 TO 28.0VDC.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM	W2	P2-23 (UUT J1-23)	ТО	W2	P1A-7B
		J2A-7B			A1J15.24
FROM	ID	A1P15.24	ТО	ID	P13-9 (S701-24)
FROM	ID	P12-44 (S701-2)	TO	ID	A1P12.48
FROM	ID	A1J12.48	TO	ID	A1J10.1
		A1P10.1			P11-162 (S506-2)
FROM	ID	P11-68 (S506-9)	TO	ID	A1P9.20
FROM	ID	A1J9.20	TO	ID	BUS 7
				_	_
		P2-39 (UUT J1-39)			
					A1J14.48
FROM	ID	A1P14.48	ТО	ID	P13-63 (S202-51)
FROM	TD	P13-93 (S202-3)	ТΟ	TD	1014 49
FROM	TD	A1J14.49 A1P10.48	TΩ	TD	D11_52 (Q510_1)
		P11-115 (S510-7)			
		A1J9.38			BUS 5
FROM	ΤD	A109.36	10	ID	B02 2
FROM	ID	BUS 5	ТО	ID	A1J8.47
FROM	ID	A1P8.47	TO	ID	P10-73 (S301-48)
		P10-7 (S301-47)			
					A1J4.15
					R111.1
					A1P4.9
		A1J4.9			BV
		P13-93 (S202-3)			
FROM	ID	A1J14.49	TO	ID	A1J10.48
FROM	ID	A1P10.48	TO	ID	P11-52 (S510-1)
FROM	ID	P11-180 (S510-3)	TO	ID	A1P9.21
FROM	ID	A1J9.21	TO	ID	BUS 1
ED OM	TD	D20 2 (DMM III)	ШΟ	TD	71D1E 40
		P20-2 (DMM-HI)			
		A1J15.49			A1J8.28
		A1P8.28			P10-203 (S503-1)
		P10-77 (S503-3)			A1P6.13
F'ROM	TD	A1J6.13	TO	TD	BUS 1
FROM	ID	P20-3 (DMM-LO)	то	ID	A1P15.50
		A1J15.50			A1J7.38
		A1P7.38			P10-130 (S301-23)
		P10-229 (S301-24)			A1P7.36
		A1J7.36			DUND
_ 1.011			- 0	C110	

STEP 149

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.37 SHOULD BE GREATER THAN 27.5VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

	P2-38 (UUT J1-38)	т О т	ωτ Ο	ה1 א 1 ה
FROM WZ	72-36 (UUI UI-36)	TO 1	W Z T D	71A-1E 7171///
FROM ID	71D14 40	TO -	TD TD	P13-25 (S202-11)
FROM ID	P2-38 (UUT J1-38) J2A-1E A1P14.40	10 .	ΙD	P13-25 (5202-11)
FROM ID	P13-93 (S202-3)	TO I	ID	A1P14.49
FROM ID	A1J14.49	TO I	ID	A1J10.48
FROM ID	A1P10.48	TO I	ID	P11-52 (S510-1)
FROM ID	P11-19 (S510-10)	TO I	ID	A1P9.8
FROM ID	A1J9.8	TO I	ID	BUS 8
FROM ID	P1-26 (DC9-LO)	то -	TD	A1P1.5
	A1J1.5			A1J7.18
	A1P7.18			P10-133 (S301-27)
	P10-70 (S301-28)			
FROM ID	J2A-2E	TO I	ID	A1J14.39
FROM ID	A1P14.39	TO I	ID	P13-56 (S202-9)
	712 02 (7000 2)			71714 40
	P13-93 (S202-3)			
	A1J14.49			A1J10.48
	A1P10.48			P11-52 (S510-1)
	P11-147 (S510-4)			
FROM ID	A1J9.31	10 .	TD	BUS 2
FROM ID	BUS 2	TO I	ID	A1J8.31
	A1P8.31			P10-44 (S301-69)
FROM ID	P10-141 (S301-70)	TO I	ID	A1P8.2
	A1J8.2			A1J1.3
FROM ID	A1P1.3	TO I	ID	P1-10 (DC4-HI)
FROM W2	P2-37 (UUT J1-37)	TO T	w2	P1A-8A
FROM ID				A1J15.22
	A1P15.22			P13-71 (S701-18)
	P12-44 (S701-2)			A1P12.48
	A1J12.48			A1J10.1
FROM ID	A1P10.1	TO I	ID	P11-162 (S506-2)
FROM ID	P11-161 (S506-7)	TO I	ID	A1P9.40
FROM ID	A1J9.40	TO I	ID	BUS 5
FROM ID	BUS 5	TO T	ID	A1J8.47
	A1P8.47			P10-73 (S301-48)
	P10-7 (S301-47)			A1P7.23
	A1J7.23			A1J4.15
	A1P4.15			R111.1
FROM ID				A1P4.9
	•			

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FROM	ID	A1J4.9	ТО	+28V
FROM	ID	P12-44 (S701-2)	то	ID A1P12.48
FROM	ID	A1J12.48	TO	ID A1J10.1
FROM	ID	A1P10.1	TO	ID P11-162 (S506-2)
FROM	ID	P11-164 (S506-3)	TO	ID A1P9.23
FROM	ID	A1J9.23	ТО	ID BUS 1
FROM	ID	P20-2 (DMM-HI)	ТО	ID A1P15.49
FROM	ID	A1J15.49	TO	ID A1J8.28
FROM	ID	A1P8.28	TO	ID P10-203 (S503-1)
FROM	ID	P10-77 (S503-3)	TO	ID A1P6.13
FROM	ID	A1J6.13	ТО	ID BUS 1
FROM	ID	P20-3 (DMM-LO)	TO	ID A1P15.50
FROM	ID	A1J15.50	TO	ID A1J7.38
FROM	ID	A1P7.38	ТО	ID P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	ТО	ID A1P7.36
FROM	ID	A1J7.36	ТО	GROUND

STEP 150

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.42. THE VOLTAGE MEASURED AT PIN J1.37 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

FROM W2	P2-42 (UUT J1-42)	TO I	W2	P1A-1C
FROM ID	J2A-1C	TO :	ID	A1J14.34
FROM ID	A1P14.34	TO :	ID	P13-58 (S202-17)
FROM W2	P2-38 (UUT J1-38)	TO I	W2	P1A-1E
FROM ID	J2A-1E	TO :	ID	A1J14.40
FROM ID	A1P14.40	TO	ID	P13-25 (S202-11)
FROM ID	P13-93 (S202-3)	TO :	ID	A1P14.49
FROM ID	A1J14.49	TO	ID	A1J10.48
FROM ID	A1P10.48	TO	ID	P11-52 (S510-1)
FROM ID	P11-19 (S510-10)	TO :	ID	A1P9.8
FROM ID	A1J9.8	TO :	ID	BUS 8
FROM ID	P1-26 (DC9-LO)	TO :	ID	A1P1.5
FROM ID	A1J1.5	TO :	ID	A1J7.18
FROM ID	A1P7.18	TO :	ID	P10-133 (S301-27)
FROM ID	P10-70 (S301-28)	TO :	ID	A1P6.12

FROM ID	A1J6.12	то	ID	BUS 8
FROM W2	P2-36 (UUT J1-36)	ТΟ	w2	P1A-2F
FROM ID	T2A-2E	ΤO		A1J14.39
FROM ID	J2A-2E A1P14.39	TO		P13-56 (S202-9)
111011 12	1111 111.37	10	10	113 30 (5202),
FROM ID	P13-93 (S202-3)	ТО	ID	A1P14.49
FROM ID	A1J14.49	TO	ID	A1J10.48
	A1P10.48		ID	P11-52 (S510-1)
	P11-147 (S510-4)			A1P9.31
FROM ID	A1J9.31	TO	ID	BUS 2
FROM ID	BIJS 2	ТΟ	TD	A1J8.31
	A1P8.31			P10-44 (S301-69)
	P10-141 (S301-70)			A1P8.2
	A1J8.2			A1J1.3
	A1P1.3			P1-10 (DC4-HI)
111011 12	1111 1 . 3	10		11 10 (201 111)
FROM W2	P2-37 (UUT J1-37)	TO	W2	P1A-8A
FROM ID	J2A-8A	TO	ID	A1J15.22
FROM ID	A1P15.22	TO	ID	P13-71 (S701-18)
FPOM ID	P12-44 (S701-2)	ТΟ	TD	л1D12 48
	A1J12.48			A1J10.1
	A1P10.1			P11-162 (S506-2)
	P11-161 (S506-7)			A1P9.40
	A1J9.40			BUS 5
FROM ID				A1J8.47
	A1P8.47			P10-73 (S301-48)
	P10-7 (S301-47)			A1P7.23
	A1J7.23			A1J4.15
	A1P4.15			R111.1
	R111.2			A1P4.9
F'ROM ID	A1J4.9	JO	+28	3 V
FROM ID	P12-44 (S701-2)	ТО	ID	A1P12.48
FROM ID	A1J12.48	TO	ID	A1J10.1
FROM ID	A1P10.1	TO	ID	P11-162 (S506-2)
FROM ID	P11-164 (S506-3)	ТО	ID	A1P9.23
FROM ID	A1J9.23	TO	ID	BUS 1
FROM ID	P20-2 (DMM-HI)	ТΟ	TD	A1P15.49
	A1J15.49			A1J8.28
	A1013.49 A1P8.28			P10-203 (S503-1)
	P10-77 (S503-3)			A1P6.13
	A1J6.13			BUS 1
	, <u></u>	_ •		-
	P20-3 (DMM-LO)			A1P15.50
FROM ID	A1J15.50			A1J7.38
FROM ID	A1P7.38	TO	ID	P10-130 (S301-23)
	P10-229 (S301-24)			A1P7.36
FROM ID	A1J7.36	ТО	GRO	DUND

STEP 151

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.42. THE 28.0VDC IS REMOVED FROM PIN J1.42. THE VOLTAGE AT PIN J1.37 SHOULD INCREASE TO 28.0VDC +/-0.5VDC 15 SECONDS AFTER REMOVING J1.42 FROM 28.0VDC.

F	ROM	W2	P2-38 (UUT J1-38)	ΤО	W2	P1A-1E
			J2A-1E			A1J14.40
			A1P14.40			P13-25 (S202-11)
_	11011					110 10 (2101 11,
Ε	ROM	ID	P13-93 (S202-3)	то	ID	A1P14.49
			A1J14.49			A1J10.48
			A1P10.48			P11-52 (S510-1)
			P11-19 (S510-10)			A1P9.8
			A1J9.8			BUS 8
_	11011					
Ε	ROM	ID	P1-26 (DC9-LO)	то	ID	A1P1.5
			A1J1.5			A1J7.18
			A1P7.18			P10-133 (S301-27)
			P10-70 (S301-28)			A1P6.12
			A1J6.12			BUS 8
			P2-36 (UUT J1-36)			P1A-2E
			J2A-2E			A1J14.39
			A1P14.39			P13-56 (S202-9)
_						
Ε	ROM	ID	P13-93 (S202-3)	то	ID	A1P14.49
			A1J14.49			A1J10.48
			A1P10.48			P11-52 (S510-1)
			P11-147 (S510-4)			A1P9.31
			A1J9.31			BUS 2
E	ROM	ID	BUS 2	ТО	ID	A1J8.31
E	ROM	ID	A1P8.31	ТО	ID	P10-44 (S301-69)
E	ROM	ID	P10-141 (S301-70)			A1P8.2
			A1J8.2	ТО	ID	A1J1.3
Ε	ROM	ID	A1P1.3	ТО	ID	P1-10 (DC4-HI)
E	ROM	W2	P2-37 (UUT J1-37)	TO	W2	P1A-8A
Ε	ROM	ID	J2A-8A	TO	ID	A1J15.22
Ε	ROM	ID	A1P15.22	TO	ID	P13-71 (S701-18)
			P12-44 (S701-2)	TO	ID	A1P12.48
E	ROM	ID	A1J12.48	TO	ID	A1J10.1
F	ROM	ID	A1P10.1	TO	ID	P11-162 (S506-2)
E	ROM	ID	P11-161 (S506-7)	TO	ID	A1P9.40
F	ROM	ID	A1J9.40	TO	ID	BUS 5
F	ROM	ID	BUS 5	TO	ID	A1J8.47
E	ROM	ID	A1P8.47	TO	ID	P10-73 (S301-48)

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FROM ID P10-7 (S301-47) FROM ID A1J7.23 FROM ID A1P4.15 FROM ID R111.2 FROM ID A1J4.9	TO ID A1P7.23 TO ID A1J4.15 TO ID R111.1 TO ID A1P4.9 TO +28V
FROM ID P12-44 (S701-2) FROM ID A1J12.48 FROM ID A1P10.1 FROM ID P11-164 (S506-3) FROM ID A1J9.23	TO ID A1J10.1 TO ID P11-162 (S506-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 152

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PINJ1.37 SHOULD BE GREATER THAN 27.5VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-36 (UUT J1-36) TO W2 P1A-2E FROM ID J2A-2E TO ID A1J14.39 FROM ID A1P14.39 TO ID P13-56 (9)

FROM ID A1P14.39 TO ID P13-56 (S202-9)

FROM ID P13-93 (S202-3) TO ID A1P14.49
FROM ID A1J14.49 TO ID A1J10.48
FROM ID A1P10.48 TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4) TO ID A1P9.31

FROM ID A1J9.31 TO ID BUS 2

FROM ID BUS 2 TO ID A1J8.31

FROM ID A1P8.31 TO ID P10-44 (S301-69)

FROM ID P10-141 (S301-70) TO ID A1P8.2 FROM ID A1J8.2 TO ID A1J1.3

FROM ID A1P1.3 TO ID P1-10 (DC4-HI)

FROM W2 P2-37 (UUT J1-37) TO W2 P1A-8A

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FROM	ID	J2A-8A A1P15.22	ТО		A1J15.22	
FROM	ID	A1P15.22	ТО	ID	P13-71 (S701-18)
FROM	ID	P12-44 (S701-2)	то	ID	A1P12.48	
		A1J12.48			A1J10.1	
FROM	ID	A1P10.1	ТО	ID	P11-162	(S506-2)
FROM	ID	P11-161 (S506-7)	ТО	ID	A1P9.40	
		A1J9.40			BUS 5	
FROM	TD	BUS 5	ТΟ	TD	A1J8.47	
_		A1P8.47			P10-73 (S301-48)
		P10-7 (S301-47)				0301 10)
		A1J7.23	TO	TD	A1.T4.15	
		A1J7.23 A1P4.15	TO	ID	R111.1	
					A1P4.9	
		A1J4.9		+28		
EDOM	TD	D10 44 (G701 0)	ш0	TD	31510 40	
		P12-44 (S701-2)				
		A1J12.48			A1J10.1	/ GE 0.6 . 0.)
_					P11-162	(8506-2)
		P11-164 (S506-3) A1J9.23			BUS 1	
FROM	ΤD	A109.23	10	TD	BOS I	
FROM	ID	P20-2 (DMM-HI)	ТО	ID	A1P15.49	
		A1J15.49	TO	ID	A1J8.28	
FROM	ID	A1P8.28	TO	ID	P10-203	(S503-1)
FROM	ID	P10-77 (S503-3)	TO	ID	A1P6.13	
FROM	ID	A1J6.13	TO	ID	BUS 1	
FROM	ID	P20-3 (DMM-LO)	то	ID	A1P15.50	
FROM	ID	A1J15.50			A1J7.38	
FROM	ID	A1P7.38				(S301-23)
FROM	ID	P10-229 (S301-24)				,
		A1J7.36			OUND	

STEP 153

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.38. THE VOLTAGE MEASURED AT PIN J1.37 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

FROM W2 P2-38 (UUT J1-38) FROM ID J2A-1E FROM ID A1P14.40	TO W2 P1A-1E TO ID A1J14.40 TO ID P13-25 (S202-11)
FROM W2 P2-36 (UUT J1-36) FROM ID J2A-2E FROM ID A1P14.39	TO W2 P1A-2E TO ID A1J14.39 TO ID P13-56 (S202-9)
FROM ID P13-93 (S202-3)	TO ID A1P14.49

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FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO TD A1P9 31
FROM ID A1J9.31	TO ID BUS 2
11011 12 1110 9:31	10 10 000 2
FROM ID BUS 2	TO ID A1J8.31
FROM ID A1P8.31	TO ID P10-44 (S301-69)
FROM ID P10-141 (S301-70)	
FROM ID Alj8.2	TO ID AlJ1.3
FROM ID A1P1.3	TO ID P1-10 (DC4-HI)
111011 12 1111 110	10 12 11 10 (201 111)
FROM W2 P2-37 (UUT J1-37)	TO W2 P1A-8A
FROM ID J2A-8A	TO ID A1J15.22
FROM ID A1P15.22	TO ID P13-71 (S701-18)
	,
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-161 (S506-7)	TO ID A1P9.40
FROM ID A1J9.40	TO ID BUS 5
FROM ID BUS 5	TO ID A1J8.47
FROM ID A1P8.47	TO ID P10-73 (S301-48)
FROM ID P10-7 (S301-47)	TO ID A1P7.23
FROM ID A1J7.23	TO ID A1J4.15
FROM ID A1P4.15	TO ID R111.1
FROM ID R111.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-44 (S701-2)	
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50	TO ID A1713.30
FROM ID A1015.50 FROM ID A107.38	TO ID P10-130 (S301-23)
FROM ID AIP7.38 FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID P10-229 (5301-24) FROM ID A1J7.36	TO GROUND
FROM ID AIU/.30	IO GROOND

STEP 154

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.7 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

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CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-7 (UUT J1-7) TO W2 P1B-8A FROM W2 P2-7 (CC2 FROM ID J2B-8A TO ID A1013.23 TO ID P12-39 (S701-20)

FROM ID P12-44 (S701-2) TO ID A1P12.48

FROM ID A1J12.48 TO ID A1J10.1

FROM ID A1P10.1 TO ID P11-162 (S506-2)

FROM ID P11-225 (S506-5) TO ID A1P9.45

FROM ID A1J9.45 TO ID BUS 3

FROM ID P20-2 (DMM-HI) TO ID A1P15.49
FROM ID A1J15.49 TO ID A1J8.28
FROM ID A1P8.28 TO ID P10-203 (S503-1)
FROM ID P10-10 (S503-5) TO ID A1P6.31
FROM ID A1J6.31 TO ID BUS 3

FROM ID P20-3 (DMM-LO) TO ID A1P15.50 FROM ID A1J15.50 TO ID A1J7.38 FROM ID A1P7.38 TO ID P10-130 (S301-23)

FROM ID A1P7.38

FROM ID P10-229 (S301-24)

TO ID A1P7.36

TO GROUND

STEP 155

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.42. THE VOLTAGE MEASURED AT PIN J1.7 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM W2 P2-6 (UUT J1-6) TO W2 P1A-1A FROM W2 P2-0 (001 5 FROM ID J2A-1A TO ID A1014.20 TO ID P13-16 (S201-18)

FROM ID P12-52 (S201-4)
FROM ID A1J12.44
FROM ID A1P10.4
FROM ID A1P10.4
FROM ID P11-5 (S507-8)
FROM ID A1J9.24
TO ID A1P9.24
FROM ID A1J9.24
TO ID BUS 6

FROM ID BUS 6 TO ID A1J8.42 FROM ID A1P8.42 TO ID P10-78 (S301-83) FROM ID P10-81 (S301-84) TO ID A1P8.5 FROM ID A1J8.5 TO ID R7.1 FROM ID R7.2 TO GROUND

FROM W2 P2-42 (UUT J1-42) TO W2 P1A-1C FROM ID J2A-1C TO ID A1J14.34

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FROM ID A1P14.34	TO ID P13-58 (S202-17)
FROM W2 P2-7 (UUT J1-7) FROM ID J2B-8A FROM ID A1P13.25	TO W2 P1B-8A TO ID A1J13.25 TO ID P12-39 (S701-20)
FROM ID P12-44 (S701-2) FROM ID A1J12.48 FROM ID A1P10.1 FROM ID P11-225 (S506-5) FROM ID A1J9.45	TO ID A1J10.1 TO ID P11-162 (S506-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-10 (S503-5) FROM ID A1J6.31	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 156

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.42 AND J1.6. THE VOLTAGE MEASURED AT PIN J1.7 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE TO GROUND.

FROM ID P1-4 (DC2-HI)	
FROM ID A1J1.2	TO ID A1J8.4
FROM ID A1P8.4	TO ID P10-174 (S301-68)
FROM ID P10-204 (S301-67)	TO ID A1P8.29
FROM ID A1J8.29	TO ID BUS 1
FROM ID P1-5 (DC2-LO)	TO ID A1P1.10
FROM ID A1J1.10	TO GROUND
FROM W2 P2-6 (UUT J1-6)	TO W2 P1A-1A
FROM ID J2A-1A	TO ID A1J14.28
FROM ID A1P14.28	TO ID P13-16 (S201-18)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-168 (S507-3)	TO ID A1P9.17

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FROM ID	A1J9.17	ТО	ID	BUS 1
	P2-42 (UUT J1-42)			
FROM ID	J2A-1C A1P14.34	TO	ID	A1J14.34
FROM ID	A1P14.34	ТО	ID	P13-58 (S202-17)
	P2-7 (UUT J1-7)			
FROM ID	J2B-8A	TO	ID	A1J13.25
FROM ID	A1P13.25	ТО	ID	P12-39 (S701-20)
FROM ID	P12-44 (S701-2)	ТО	ID	A1P12.48
FROM ID	A1J12.48	TO	ID	A1J10.1
FROM ID	A1P10.1	TO	ID	P11-162 (S506-2)
FROM ID	P11-225 (S506-5)	TO	ID	A1P9.45
FROM ID	A1J9.45	ТО	ID	BUS 3
FROM ID	P20-2 (DMM-HI)	то	ID	A1P15.49
FROM ID	A1J15.49			
				P10-203 (S503-1)
FROM ID	P10-10 (S503-5)			
FROM ID	A1J6.31	ТО	ID	BUS 3
FROM ID	P20-3 (DMM-LO)	то	ID	A1P15.50
FROM ID	A1J15.50			
_				P10-130 (S301-23)
FROM ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM ID	A1J7.36	TO	GRO	DUND

STEP 157

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.49 AND J1.24. THE VOLTAGE MEASURED AT PIN J1.58 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-49 (UUT J1-49)	TO W2 P1B-6A
FROM ID J2B-6A	TO ID A1J12.21
FROM ID A1P12.21	TO ID P12-61 (S202-21)
FROM W2 P2-24 (UUT J1-24) FROM ID J2B-8C FROM ID A1P12.25	TO W2 P1B-8C TO ID A1J12.25 TO ID P12-89 (S202-5)
FROM W2 P2-58 (UUT J1-58) FROM ID J1A-11E FROM ID A1P15.17	TO W2 P1A-11E TO ID A1J15.17 TO ID P13.13 (S701-49)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)

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	P11-225 (S506-5)			A1P9.45
FROM ID	AlJ9.45	ТО	ID	BUS 3
FROM ID	P20-2 (DMM-HI)	ТО	ID	A1P15.49
FROM ID	A1J15.49	ТО	ID	A1J8.28
FROM ID	A1P8.28	TO	ID	P10-203 (S503-1)
FROM ID	P10-10 (S503-5)	TO	ID	A1P6.31
FROM ID	A1J6.31	TO	ID	BUS 3
FROM ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM ID	A1J15.50	TO	ID	A1J7.38
FROM ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM ID	A1J7.36	TO	GRO	DUND

STEP 158

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.49, J1.24 AND J1.1. THE VOLTAGE MEASURED AT PIN J1.58 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC WITH RESPECT TO GROUND.

FROM ID	P1-4 (DC2-HI) A1J1.2 A1P8.4	TO	ID	A1P1.2
FROM ID	A1J1.2	TO	ID	A1J8.4
FROM ID	A1P8.4	TO	ID	P10-174 (S301-68)
FROM ID	P10-204 (S301-67)	TO	ID	A1P8.29
FROM ID	A1J8.29	TO	ID	BUS 1
FROM ID	P1-5 (DC2-LO)	то	ID	A1P1.10
FROM ID	A1J1.10	ТО	GRO	DUND
EDOM MO	D2 40 (IIIIII T1 40)	ШΟ	T-7 O	חות כז
	P2-49 (UUT J1-49)			
F'ROM ID	J2B-6A A1P12.21	TO	TD	ALUIZ.ZI
FROM ID	A1P12.21	ТО	ID	P12-61 (S202-21)
FROM W2	P2-24 (UUT J1-24)	ТО	W2	P1B-8C
FROM ID	J2B-8C A1P12.25	TO	ID	P12-89 (S202-5)
EDOM MO	P2-1 (UUT J1-1)	ΤО	TAT 2	D1 A _ 7C
FROM TD	T27 7C	TO	W Z	71 T1E 16
FROM ID	J2A-7C A1P15.16	TO	TD	D13_40 (C701_16)
I KOM ID	AIFIJ.IO	10	עב	P13-40 (5/01-10)
FROM ID	P12-44 (S701-2)	TO	ID	A1P12.48
FROM ID	A1J12.48 A1P10.1	TO	ID	A1J10.1
FROM ID	A1P10.1	TO	ID	P11-162 (S506-2)
FROM ID	P11-164 (S506-3)	TO	ID	A1P9.23
FROM ID	A1J9.23	TO	ID	BUS 1
FROM W2	P2-58 (UUT J1-58)	ΤО	W2	D1 A = 1 1 F.
1 1001.1 WZ	12 33 (001 01 30)	10	11 Z	

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FROM ID J1A-11E FROM ID A1P15.17	TO ID A1J15.17 TO ID P13.13 (S701-49)
FROM ID P12-76 (S701-1) FROM ID A1J12.50 FROM ID A1P10.3 FROM ID P11-225 (S506-5) FROM ID A1J9.45	TO ID A1J10.3 TO ID P11-194 (S506-1)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-10 (S503-5) FROM ID A1J6.31	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 159

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.49 AND J1.24. THE VOLTAGE MEASURED AT PIN J1.58 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM ID P10-204 (S301-67)	TO ID A1J8.4 TO ID P10-174 (S301-68)
FROM ID P1-5 (DC2-LO) FROM ID A1J1.10	TO ID A1P1.10 TO GROUND
FROM W2 P2-49 (UUT J1-49) FROM ID J2B-6A FROM ID A1P12.21	TO W2 P1B-6A TO ID A1J12.21 TO ID P12-61 (S202-21)
FROM W2 P2-24 (UUT J1-24)	TO W2 P1B-8C
FROM ID A1P12.25	TO ID P12-89 (S202-5)
FROM ID P12-44 (S701-2) FROM ID A1J12.48 FROM ID A1P10.1	TO ID A1J10.1
FROM ID P11-164 (S506-3)	· · · · · · · · · · · · · · · · · · ·

FROM ID A1J9.23 TO ID BUS 1

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FROM W2 P2-58 (UUT J1-58) FROM ID J1A-11E FROM ID A1P15.17	TO W2 P1A-11E TO ID A1J15.17 TO ID P13.13 (S701-49)
FROM ID P12-76 (S701-1) FROM ID A1J12.50 FROM ID A1P10.3 FROM ID P11-225 (S506-5) FROM ID A1J9.45	TO ID A1J10.3 TO ID P11-194 (S506-1) TO ID A1P9.45
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-10 (S503-5) FROM ID A1J6.31	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

2.6 MODULE 2: RATE COMMAND AND LAMP DRIVERS

Refer to

1.4 Reference Drawings when diagnosing connection paths.

STEP 201

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.20 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

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CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

SEE "APPLY IC"

FROM W2 P2-20 (UUT J1-20) TO W2 P1B-14D

FROM ID J2B-14D TO ID A1J12.27

FROM ID A1P12.27 TO ID P12-55 (S201-47)

FROM ID P12-16 (S201-1) TO ID A1P12.42

FROM ID A1J12.42 TO ID A1J10.6

FROM ID A1P10.6 TO ID P11-203 (S508-1)

FROM ID P11-77 (S508-3) TO ID A1P9.15

FROM ID A1J9.15 TO ID BUS 1

FROM ID P20-2 (DMM-HI) TO ID A1P15.49

FROM ID A1J15.49 TO ID A1J8.28

FROM ID A1P8.28 TO ID P10-203 (S503-1)

FROM ID P10-77 (S503-3) TO ID A1P6.13
```

FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 202

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.59. THE VOLTAGE MEASURED AT PIN J1.20 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM W2	P2-20 (UUT J1-20)	TO	W2	P1B-14D
FROM ID	J2B-14D	TO	ID	A1J12.27
FROM ID	J2B-14D A1P12.27	TO	ID	P12-55 (S201-47)
	P12-16 (S201-1)			
	A1J12.42			
		_		A1J10.6
_	A1P10.6			P11-203 (S508-1)
	P11-77 (S508-3)			
FROM ID	A1J9.15	TO	ID	BUS 1
FROM W2	P2-59 (UUT J1-59)	то	W2	P1B-13D
	J2B-13D			A1J12.30
FROM ID	A1P12.30	-		P12-56 (S202-8)
11(011 12	1111 12:30	10		111 30 (5101 0)
	P12-90 (S202-2)	TO	ID	A1P12.36
FROM ID	A1J12.36	TO	ID	A1J10.12
-	A1P10.12		ID	P11-242 (S509-2)
FROM ID	P11-211 (S509-9)	TO	ID	A1P9.16
FROM ID	A1J9.16	TO	ID	BUS 7
======================================	DOO 0 (DVD4 TIT)			31D15 40
	P20-2 (DMM-HI)			
				A1J8.28
_				P10-203 (S503-1)
	P10-77 (S503-3)	TO	ID	A1P6.13
FROM ID	A1J6.13	TO	ID	BUS 1
FDOM ID	P20-3 (DMM-LO)	ТΟ	TD	A1P15.50
	A1J15.50			A1J7.38
		_		
TROM ID				P10-130 (S301-23)
	P10-229 (S301-24)			
F'ROM ID	A1J7.36	.I.O	GRC	UND

STEP 203

DESCRIPTION:

Date: 04 March 2016

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.59. THE VOLTAGE MEASURED AT PIN J1.19 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM W2 P2-59 (UUT J1-59 FROM ID J2B-13D FROM ID A1P12.30	TO W2 P1B-13D TO ID A1J12.30 TO ID P12-56 (S202-8)
FROM ID P12-90 (S202-2) FROM ID A1J12.36 FROM ID A1P10.12 FROM ID P11-211 (S509-9) FROM ID A1J9.16	TO ID A1J10.12 TO ID P11-242 (S509-2)
FROM W2 P2-19 (UUT J1-19 FROM ID J2A-2C FROM ID A1P14.33	TO W2 P1A-2C TO ID A1J14.33 TO ID P13-22 (S201-44)
FROM ID P12-80 (S201-2) FROM ID A1J12.40 FROM ID A1P10.8 FROM ID P11-77 (S508-3) FROM ID A1J9.15	TO ID A1J10.8 TO ID P11-139 (S508-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24 FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 204

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.59. THE VOLTAGE MEASURED AT PIN J1.21 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

FROM	W2	P2-59 (UUT J1-59)	TO	W2	P1B-13D
FROM	ID	J2B-13D	TO	ID	A1J12.30
FROM	ID	A1P12.30	TO	ID	P12-56 (S202-8)
FROM	ID	P12-90 (S202-2)	TO	ID	A1P12.36

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FROM II	D A1J12.36 D A1P10.12 D P11-211 (S509-9) D A1J9.16	TO TO	ID ID	A1J10.12 P11-242 (S509-2) A1P9.16 BUS 7
	2 P2-21 (UUT J1-21)			P1A-3D
FROM II) J2A-3D	TO	ID	A1J14.35
FROM II	D A1P14.35	TO	ID	P13-23 (S201-49)
FROM II	P12-16 (S201-1)	TO	ID	A1P12.42
FROM II	A1J12.42	TO	ID	A1J10.6
FROM II	D A1P10.6	ТО	ID	P11-203 (S508-1)
FROM II	D P11-77 (S508-3)	TO	ID	A1P9.15
	A1J9.15		ID	BUS 1
FROM II	D P20-2 (DMM-HI)	TO	ID	A1P15.49
	A1J15.49			A1J8.28
FROM II	D A1P8.28	ТО	ID	P10-203 (S503-1)
FROM II	P10-77 (S503-3)	ТО	ID	A1P6.13
	A1J6.13		ID	BUS 1
FROM II	D P20-3 (DMM-LO)	TO	ID	A1P15.50
	A1J15.50		ID	A1J7.38
FROM II	D A1P7.38	ТО	ID	P10-130 (S301-23)
FROM II	P10-229 (S301-24)	TO	ID	A1P7.36
	D A1J7.36		GRO	DUND

STEP 205

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.59 AND J1.8. THE VOLTAGE MEASURED AT PIN J1.20 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

FROM	W2	P2-59 (UUT J1-59)	ТО	W2	P1B-13D
FROM	ID	J2B-13D	TO	ID	A1J12.30
FROM	ID	A1P12.30	TO	ID	P12-56 (S202-8)
FROM	W2	P2-8 (UUT J1-8)	TO	W2	P1A-2B
FROM	ID	J2A-2B	TO	ID	A1J14.31
FROM	ID	A1P14.31	TO	ID	P13-30 (S202-41)
FROM	ID	P12-90 (S202-2)	TO	ID	A1P12.36
FROM	ID	A1J12.36	TO	ID	A1J10.12
FROM	ID	A1P10.12	TO	ID	P11-242 (S509-2)
FROM	ID	P11-211 (S509-9)	TO	ID	A1P9.16
FROM	ID	A1J9.16	TO	ID	BUS 7
FROM	W2	P2-20 (UUT J1-20)	TO	W2	P1B-14D
FROM	ID	J2B-14D	TO	ID	A1J12.27
FROM	ID	A1P12.27	TO	ID	P12-55 (S201-47)

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FROM ID	P12-16 (S201-1)	TO	ID	A1P12.42
FROM ID	A1J12.42	TO	ID	A1J10.6
FROM ID	A1P10.6	TO	ID	P11-203 (S508-1)
FROM ID	P11-77 (S508-3)	TO	ID	A1P9.15
FROM ID	A1J9.15	TO	ID	BUS 1
FROM ID	P20-2 (DMM-HI)	TO	ID	A1P15.49
FROM ID	A1J15.49	TO	ID	A1J8.28
FROM ID	A1P8.28	TO	ID	P10-203 (S503-1)
FROM ID	P10-77 (S503-3)	TO	ID	A1P6.13
FROM ID	A1J6.13	TO	ID	BUS 1
FROM ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM ID	A1J15.50	TO	ID	A1J7.38
FROM ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM ID	A1J7.36	TO	GRO	OUND

STEP 206

DESCRIPTION:

THIS STEP APPLIES 28.0 VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0 VDC IS APPLIED TO PINS J1.57 AND J1.8. THE VOLTAGE MEASURED AT PIN J1.20 SHOULD BE BETWEEN 27.5 VDC AND 28.5 VDC WITH RESPECT TO GROUND.

FROM ID	P2-8 (UUT J1-8) J2A-2B A1P14.31	TO ID	P1A-2B A1J14.31 P13-30 (S202-41)
FROM ID	P2-57 (UUT J1-57) J2B-10A A1P13.31	TO ID	P1B-10A A1J13.31 P12-7 (S701-30)
FROM ID FROM ID FROM ID	P12-44 (S701-2) A1J12.48 A1P10.1 P11-68 (S506-9) A1J9.20	TO ID TO ID TO ID	A1P12.48 A1J10.1 P11-162 (S506-2) A1P9.20 BUS 7
FROM ID FROM ID FROM ID FROM ID FROM ID FROM ID	P2-20 (UUT J1-20) J2B-14D A1P12.27 P12-16 (S201-1) A1J12.42 A1P10.6 P11-77 (S508-3) A1J9.15	TO ID TO ID TO ID TO ID TO ID TO ID	P1B-14D A1J12.27 P12-55 (S201-47) A1P12.42 A1J10.6 P11-203 (S508-1) A1P9.15 BUS 1

Date: 04 March 2016

FROM	ID	P20-2 (DMM-HI)	ТО	ID	A1P15.49
FROM	ID	A1J15.49	ТО	ID	A1J8.28
FROM	ID	A1P8.28	TO	ID	P10-203 (S503-1)
FROM	ID	P10-77 (S503-3)	TO	ID	A1P6.13
FROM	ID	A1J6.13	TO	ID	BUS 1
FROM	ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM	ID	A1J15.50	TO	ID	A1J7.38
FROM	ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM	ID	A1J7.36	TO	GRO	DUND

STEP 207

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.57. THE VOLTAGE MEASURED AT PIN J1.20 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

FROM W2 P2-57 (UUT FROM ID J2B-10A FROM ID A1P13.31		W2 P1B-10A ID A1J13.3 ID P12-7 (1
FROM ID P12-44 (S70 FROM ID A1J12.48 FROM ID A1P10.1 FROM ID P11-68 (S50 FROM ID A1J9.20	TO TO 06-9) TO	ID A1P12.4 ID A1J10.1 ID P11-162 ID A1P9.20 ID BUS 7	(S506-2)
FROM W2 P2-20 (UUT FROM ID J2B-14D FROM ID A1P12.27	TO	W2 P1B-14D ID A1J12.2 ID P12-55	7
FROM ID P12-16 (S20 FROM ID A1J12.42 FROM ID A1P10.6 FROM ID P11-77 (S50 FROM ID A1J9.15	TO TO 08-3) TO	ID A1J10.6 ID P11-203	(S508-1)
FROM ID P20-2 (DMM-FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S50 FROM ID A1J6.13	TO TO 03-3) TO	ID A1J8.28 ID P10-203	(S503-1)
FROM ID P20-3 (DMM-FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S:FROM ID A1J7.36	TO TO 301-24) TO	ID A1P15.5 ID A1J7.38 ID P10-130 ID A1P7.36 GROUND	(S301-23)

Date: 04 March 2016

STEP 208

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.50 WITH RESPECT TO GROUND SHOULD BE LESS THAN 0.2VDC.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER" SEE "APPLY IC"

FROM W2 P2-50 (UUT J1-50) FROM ID J2B-8B FROM ID A1P12.26	TO W2 P1B-8B TO ID A1J12.26 TO ID P12-25 (S202-6)
FROM ID P13-29 (S202-4) FROM ID A1J14.50 FROM ID A1P10.50 FROM ID P11-180 (S510-3) FROM ID A1J9.21	TO ID A1J10.50 TO ID P11-244 (S510-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 209

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.14. THE VOLTAGE MEASURED AT PIN J1.50 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

FROM W2 P2-14 (FROM ID J2B-6C FROM ID A1P12.1	TO	O ID	P1B-6C A1J12.19 P12-54 (S201-38)
FROM ID P12-52 FROM ID A1J12.4 FROM ID A1P10.4 FROM ID P11-232 FROM ID A1J9.14	T(T((S507-9)	O ID O ID O ID	A1P12.44 A1J10.4 P11-71 (S507-2) A1P9.14 BUS 7
FROM W2 P2-50 (1	UUT J1-50) TO	O W2	P1B-8B

Date: 04 March 2016

FROM	ID	J2B-8B	TO	ID	A1J12.26
FROM	ID	A1P12.26	ТО	ID	P12-25 (S202-6)
FROM	ID	P13-29 (S202-4)	ТО	ID	A1P14.50
FROM	ID	A1J14.50	ТО	ID	A1J10.50
FROM	ID	A1P10.50	ТО	ID	P11-244 (S510-2)
FROM	ID	P11-180 (S510-3)	ТО	ID	A1P9.21
FROM	ID	A1J9.21	ТО	ID	BUS 1
FROM	ID	P20-2 (DMM-HI)	ТО	ID	A1P15.49
FROM	ID	A1J15.49	ТО	ID	A1J8.28
FROM	ID	A1P8.28	ТО	ID	P10-203 (S503-1)
FROM	ID	P10-77 (S503-3)	ТО	ID	A1P6.13
FROM	ID	A1J6.13	ТО	ID	BUS 1
FROM	ID	P20-3 (DMM-LO)	ТО	ID	A1P15.50
FROM	ID	A1J15.50	ТО	ID	A1J7.38
FROM	ID	A1P7.38	ТО	ID	P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	ТО	ID	A1P7.36
FROM	ID	A1J7.36	ТО	GRO	DUND
	_		_		

STEP 210

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.14. THE VOLTAGE MEASURED AT PIN J1.15 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

FROM W2 P2-14 (UUT J1-14) FROM ID J2B-6C FROM ID A1P12.19	TO ID A1J12.19
FROM ID P12-52 (S201-4) FROM ID A1J12.44 FROM ID A1P10.4 FROM ID P11-232 (S507-9) FROM ID A1J9.14	TO ID A1J10.4 TO ID P11-71 (S507-2)
FROM W2 P2-15 (UUT J1-15) FROM ID J2B-7B FROM ID A1P12.23	TO W2 P1B-7B TO ID A1J12.23 TO ID P12-85 (S201-45)
FROM ID P12-16 (S201-1) FROM ID A1J12.42 FROM ID A1P10.6 FROM ID P11-77 (S508-3) FROM ID A1J9.15	TO ID A1J10.6 TO ID P11-203 (S508-1)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49	TO ID A1P15.49 TO ID A1J8.28

Date: 04 March 2016

FROM	ID	A1P8.28	TO	ID	P10-203 (S503-1)	
FROM	ID	P10-77 (S503-3)	TO	ID	A1P6.13	
FROM	ID	A1J6.13	TO	ID	BUS 1	
FROM	ID	P20-3 (DMM-LO)	TO	ID	A1P15.50	
FROM	ID	A1J15.50	TO	ID	A1J7.38	
FROM	ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	TO	ID	A1P7.36	
FROM	ID	A1J7.36	TO	GRO	DUND	

STEP 211

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.14. THE VOLTAGE MEASURED AT PIN J1.48 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

	P2-14 (UUT J1-14) J2B-6C A1P12.19		
FROM ID .	A1P10.4 P11-232 (S507-9)	TO ID TO ID TO ID	A1J10.4 P11-71 (S507-2)
	P2-48 (UUT J1-48) J2A-1D A1P14.37		
FROM ID .	P11-77 (S508-3)	TO ID	P11-203 (S508-1) A1P9.15
FROM ID .	P10-77 (S503-3)	TO ID TO ID	A1J8.28 P10-203 (S503-1) A1P6.13
FROM ID .	P10-229 (S301-24)	TO ID	A1J7.38 P10-130 (S301-23) A1P7.36

STEP 212

Date: 04 March 2016

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.14 AND J1.8. THE VOLTAGE MEASURED AT PIN J1.50 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM	ID	P2-14 (UUT J1-14) J2B-6C			P1B-6C A1J12.19
FROM	ID	A1P12.19	TO	ID	P12-54 (S201-38)
		P12-52 (S201-4)			
			_		A1J10.4
		A1P10.4 P11-232 (S507-9)			P11-71 (S507-2)
		A1J9.14			BUS 7
FROM	ΤD	A109.14	10	ΤD	DUD /
		P2-8 (UUT J1-8)			
					A1J14.31
FROM	ID	A1P14.31	TO	ID	P13-30 (S202-41)
FROM	W2	P2-50 (UUT J1-50)	ΤО	W2	P1B-8B
		J2B-8B			A1J12.26
_		A1P12.26	_		P12-25 (S202-6)
FROM	ID	P13-29 (S202-4)	ТО	ID	A1P14.50
		A1J14.50	TO	ID	A1J10.50
					P11-244 (S510-2)
FROM	ID	P11-180 (S510-3)	TO	ID	A1P9.21
FROM	ID	A1J9.21	TO	ID	BUS 1
		P20-2 (DMM-HI)			
		A1J15.49			A1J8.28
		A1P8.28			P10-203 (S503-1)
		P10-77 (S503-3)			A1P6.13
FROM	ΤD	A1J6.13	10	TD	BUS 1
FROM	ID	P20-3 (DMM-LO)	то	ID	A1P15.50
FROM	ID	A1J15.50			A1J7.38
FROM	ID	A1P7.38	ТО	ID	P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	ТО	ID	A1P7.36
FROM	ID	A1J7.36	TO	GRO	DUND

STEP 213

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.43 AND J1.8. THE VOLTAGE MEASURED AT PIN J1.50 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

Date: 04 March 2016

FROM W2 P2-43 (UUT J1-43) FROM ID J2A-2D FROM ID A1P14.36	TO W2 P1A-2D TO ID A1J14.36 TO ID P13-55 (S201-50)
FROM ID P12-52 (S201-4) FROM ID A1J12.44 FROM ID A1P10.4 FROM ID P11-232 (S507-9) FROM ID A1J9.14	TO ID A1J10.4 TO ID P11-71 (S507-2)
	TO W2 P1A-2B TO ID A1J14.31 TO ID P13-30 (S202-41)
FROM W2 P2-50 (UUT J1-50) FROM ID J2B-8B FROM ID A1P12.26	
	TO ID A1J10.50 TO ID P11-244 (S510-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 214

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.43. THE VOLTAGE MEASURED AT PIN J1.50 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

FROM	W2	P2-43 (UUT J1-43)	TO	W2	P1A-2D
FROM	ID	J2A-2D	TO	ID	A1J14.36
FROM	ID	A1P14.36	ТО	ID	P13-55 (S201-50)
FROM	ID	P12-52 (S201-4)	TO	ID	A1P12.44
FROM	ID	A1J12.44	ТО	ID	A1J10.4
FROM	ID	A1P10.4	TO	ID	P11-71 (S507-2)

Date: 04 March 2016

FROM ID P11-	232 (S507-9)	TO I	ID A1P9.	14
FROM ID A1J9	.14	TO I	ID BUS 7	
FROM W2 P2-5	0 (UUT J1-50)	TO W	√2 P1B-8	В
FROM ID J2B-	8B	TO I	ID A1J12	.26
FROM ID A1P1	2.26	TO I	ID P12-2	5 (S202-6)
FROM ID P13-	29 (S202-4)	TO I	D A1P14	.50
FROM ID A1J1	4.50	TO I	ID A1J10	.50
FROM ID A1P1	0.50	TO I	ID P11-2	44 (S510-2)
FROM ID P11-	180 (S510-3)	TO I	ID A1P9.	21
FROM ID A1J9	.21	TO I	ID BUS 1	
FROM ID P20-	2 (DMM-HI)	TO I	ID A1P15	.49
FROM ID A1J1	5.49		ID A1J8.	28
FROM ID A1P8				03 (S503-1)
FROM ID P10-	77 (S503-3)	TO I	ID A1P6.	13
FROM ID A1J6	.13	TO I	ID BUS 1	
FROM ID P20-	3 (DMM-LO)	TO I	ID A1P15	.50
FROM ID A1J1	5.50	TO I	ID A1J7.	38
FROM ID A1P7	.38	TO I	ID P10-1	30 (S301-23)
FROM ID P10-	229 (S301-24)	TO I	ID A1P7.	36
FROM ID A1J7	.36	TO G	GROUND	

STEP 215

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.20 WITH RESPECT TO GROUND SHOULD BE LESS THAN 0.2 VDC.

CONNECTION PATH IS AS FOLLOWS:

FROM W2 P2-20 (UUT J1-20)	TO W2 P1B-14D
FROM ID J2B-14D	TO ID A1J12.27
FROM ID A1P12.27	TO ID P12-55 (S201-47)
FROM ID P12-16 (S201-1) FROM ID A1J12.42 FROM ID A1P10.6 FROM ID P11-77 (S508-3) FROM ID A1J9.15	TO ID A1J10.6 TO ID P11-203 (S508-1)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1P15.49 TO ID A1J8.28 TO ID P10-203 (S503-1) TO ID A1P6.13 TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38

Date: 04 March 2016

FROM ID A1P7.38 TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24) TO ID A1P7.36
FROM ID A1J7.36 TO GROUND

STEP 216

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.56. THE VOLTAGE MEASURED AT PIN J1.20 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM ID J		ТО	ID	P1A-3E A1J14.38 P13-85 (S201-52)
FROM ID A	1J12.44 1P10.4 11-232 (S507-9)	TO TO TO	ID ID ID	A1P12.44 A1J10.4 P11-71 (S507-2) A1P9.14 BUS 7
FROM ID J	2B-14D	ТО	ID	P1B-14D A1J12.27 P12-55 (S201-47)
FROM ID A	1P10.6 11-77 (S508-3)	TO TO TO	ID ID ID	A1J10.6 P11-203 (S508-1)
FROM ID A	1P8.28 10-77 (S503-3)	TO TO TO	ID ID ID	A1J8.28 P10-203 (S503-1)
FROM ID A	1J15.50 1P7.38 10-229 (S301-24)	TO TO TO	ID ID ID	A1P15.50 A1J7.38 P10-130 (S301-23) A1P7.36

STEP 217

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.56 AND J1.22. THE VOLTAGE MEASURED AT PIN J1.20 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

Date: 04 March 2016

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

DHH	001	LIOWER			
		P2-56 (UUT J1-56) J2A-3E A1P14.38	TO	ID	P1A-3E A1J14.38 P13-85 (S201-52)
		P12-52 (S201-4)			
FROM	ID	A1J12.44	TO	ID	A1J10.4
		A1P10.4			P11-71 (S507-2)
FROM	ID	P11-232 (S507-9)	TO	ID	A1P9.14
		A1J9.14			BUS 7
		P2-22 (UUT J1-22)	ТО	W2	P1A-4F
		J2A-4F	TO	TD	ALJ14.46
FROM	ID	A1P14.46	ТО	ID	P13-95 (S202-43)
FROM	W2	P2-20 (UUT J1-20)	ТО	W2	P1B-14D
FROM	ID	J2B-14D	ТО	ID	A1J12.27
FROM	ID	A1P12.27	ТО	ID	P12-55 (S201-47)
		P12-16 (S201-1)	то	ID	A1P12.42
		A1J12.42	TO	ID	A1J10.6
					P11-203 (S508-1)
		P11-77 (S508-3)			
FROM	ID	A1J9.15	ТО	ID	BUS 1
		P20-2 (DMM-HI)			
		A1J15.49			A1J8.28
		A1P8.28	TO		P10-203 (S503-1)
FROM	ID	P10-77 (S503-3)			A1P6.13
FROM	ID	A1J6.13	ТО	ID	BUS 1
FROM	ID	P20-3 (DMM-LO)	ТО	ID	A1P15.50
FROM	ID	A1J15.50	TO	ID	A1J7.38
FROM	ID	A1P7.38	TO	ID	P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	TO	ID	A1P7.36
		A1J7.36			DUND

STEP 218

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.56. THE VOLTAGE MEASURED AT PIN J1.20 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

FROM W2 P2-56 (UUT J1-56)	TO W2 P1A-3E
FROM ID J2A-3E	TO ID A1J14.38
FROM ID A1P14.38	TO ID P13-85 (S201-52)
FROM ID P12-52 (S201-4)	TO ID A1P12.44

Date: 04 March 2016

FROM ID	A1J12.44 A1P10.4 P11-232 (S507-9) A1J9.14	TO TO	ID ID	A1J10.4 P11-71 (S507-2) A1P9.14 BUS 7
	P2-20 (UUT J1-20)			P1B-14D
	J2B-14D	_		A1J12.27
FROM ID	A1P12.27	J.O	TD	P12-55 (S201-47)
EBUM ID	P12-16 (S201-1)	т∩	TD	A1P12.42
	A1J12.42			A1J10.6
_	A1P10.6	_		P11-203 (S508-1)
_	P11-77 (S508-3)			
	A1J9.15			BUS 1
TROM ID	11100.10	10	10	202 1
FROM ID	P20-2 (DMM-HI)	ТО	ID	A1P15.49
	A1J15.49			A1J8.28
FROM ID	A1P8.28	TO	ID	P10-203 (S503-1)
FROM ID	P10-77 (S503-3)	TO	ID	A1P6.13
FROM ID	A1J6.13	TO	ID	BUS 1
FROM ID	P20-3 (DMM-LO)	TO	ID	A1P15.50
FROM ID	A1J15.50	TO	ID	A1J7.38
_	A1P7.38		ID	P10-130 (S301-23)
FROM ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM ID	A1J7.36	TO	GRO	DUND

STEP 219

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.50 WITH RESPECT TO GROUND SHOULD BE LESS THAN 0.2 VDC.

CONNECTION PATH IS AS FOLLOWS:

FROM W2 P2-50 (UUT J1-50) FROM ID J2B-8B FROM ID A1P12.26	TO W2 P1B-8B TO ID A1J12.26 TO ID P12-25 (S202-6)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1

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FROM ID P20-3 (DMM-LO) TO ID A1P15.50
FROM ID A1J15.50 TO ID A1J7.38
FROM ID A1P7.38 TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24) TO ID A1P7.36
FROM ID A1J7.36 TO GROUND

STEP 220

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.13. THE VOLTAGE MEASURED AT PIN J1.50 WITH RESPECT TO GROUND SHOULD BE LESS THAN 0.2VDC.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM W2 P2-13 (UUT J1-13) FROM ID J2A-2A FROM ID A1P14.29	TO W2 P1A-2A TO ID A1J14.29 TO ID P13-82 (S201-36)
FROM ID P12-52 (S201-4) FROM ID A1J12.44 FROM ID A1P10.4 FROM ID P11-232 (S507-9) FROM ID A1J9.14	TO ID A1J10.4 TO ID P11-71 (S507-2)
FROM W2 P2-50 (UUT J1-50) FROM ID J2B-8B FROM ID A1P12.26	TO W2 P1B-8B TO ID A1J12.26 TO ID P12-25 (S202-6)
FROM ID P13-29 (S202-4) FROM ID A1J14.50 FROM ID A1P10.50 FROM ID P11-180 (S510-3) FROM ID A1J9.21	TO ID A1J10.50 TO ID P11-244 (S510-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 221

DESCRIPTION:

Date: 04 March 2016

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.13 AND J1.22. THE VOLTAGE MEASURED AT PIN J1.50 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS: SEE "UUT POWER"

FROM ID	P2-13 (UUT J1-13) J2A-2A A1P14.29	TO ID	P1A-2A A1J14.29 P13-82 (S201-36)
FROM ID FROM ID FROM ID	P12-52 (S201-4) A1J12.44 A1P10.4 P11-232 (S507-9) A1J9.14	TO ID TO ID TO ID	A1P12.44 A1J10.4 P11-71 (S507-2) A1P9.14 BUS 7
FROM ID	P2-22 (UUT J1-22) J2A-4F A1P14.46	TO ID	P1A-4F A1J14.46 P13-95 (S202-43)
FROM ID	P2-50 (UUT J1-50) J2B-8B A1P12.26	TO ID	P1B-8B A1J12.26 P12-25 (S202-6)
FROM ID FROM ID FROM ID	P13-29 (S202-4) A1J14.50 A1P10.50 P11-180 (S510-3) A1J9.21	TO ID TO ID TO ID	A1P14.50 A1J10.50 P11-244 (S510-2) A1P9.21 BUS 1
FROM ID FROM ID FROM ID	P20-2 (DMM-HI) A1J15.49 A1P8.28 P10-77 (S503-3) A1J6.13	TO ID TO ID TO ID	A1P15.49 A1J8.28 P10-203 (S503-1) A1P6.13 BUS 1
FROM ID FROM ID FROM ID	P20-3 (DMM-LO) A1J15.50 A1P7.38 P10-229 (S301-24) A1J7.36	TO ID	A1P15.50 A1J7.38 P10-130 (S301-23) A1P7.36 OUND

STEP 222

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.13. THE VOLTAGE MEASURED AT PIN J1.50 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

Date: 04 March 2016

FROM W2 P2-13 (UUT J1-13) FROM ID J2A-2A FROM ID A1P14.29	TO W2 P1A-2A TO ID A1J14.29 TO ID P13-82 (S201-36)
FROM ID P12-52 (S201-4) FROM ID A1J12.44 FROM ID A1P10.4 FROM ID P11-232 (S507-9) FROM ID A1J9.14	TO ID A1J10.4 TO ID P11-71 (S507-2)
FROM W2 P2-50 (UUT J1-50) FROM ID J2B-8B FROM ID A1P12.26	TO W2 P1B-8B TO ID A1J12.26 TO ID P12-25 (S202-6)
FROM ID P13-29 (S202-4) FROM ID A1J14.50 FROM ID A1P10.50 FROM ID P11-180 (S510-3) FROM ID A1J9.21	TO ID A1J10.50 TO ID P11-244 (S510-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 223

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PINS J1.2 SHOULD BE GREATER THAN 27.0VDC.

CONNECTION PATH IS AS FOLLOWS:

FROM ID	P2-2 (UUT J1-2) J2A-6B A1P15.26	TO	ID	P1A-6B A1J15.26 P13-73 (S701-25)
FROM ID FROM ID	P11-129 (S506-8)	TO TO TO	ID ID ID	A1P12.50 A1J10.3 P11-194 (S506-1) A1P9.30 BUS 6
FROM ID	BUS 6	то	ID	A1J8.48

Date: 04 March 2016

FROM ID A1P8.48 FROM ID P10-42 (S301-49) FROM ID A1J7.24 FROM ID A1P4.16 FROM ID R109.2 FROM ID A1J4.9	
FROM ID P12-76 (S701-1) FROM ID A1J12.50 FROM ID A1P10.3 FROM ID P11-164 (S506-3) FROM ID A1J9.23	TO ID A1J10.3 TO ID P11-194 (S506-1)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 224

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PINS J1.4 SHOULD BE GREATER THAN 27.0 VDC.

CONNECTION PATH IS AS FOLLOWS:

FROM W2 P2-4 (UUT J1-4) FROM ID J2B-9B FROM ID A1P13.29	TO W2 P1B-9B TO ID A1J13.29 TO ID P12-8 (S701-27)
FROM ID P12-76 (S701-1) FROM ID A1J12.50 FROM ID A1P10.3 FROM ID P11-129 (S506-8) FROM ID A1J9.30	TO ID A1J10.3 TO ID P11-194 (S506-1)
FROM ID BUS 6 FROM ID A1P8.48 FROM ID P10-42 (S301-49) FROM ID A1J7.24 FROM ID A1P4.16 FROM ID R109.2 FROM ID A1J4.9	TO ID A1J8.48 TO ID P10-171 (S301-50) TO ID A1P7.24 TO ID A1J4.16 TO ID R109.1 TO ID A1P4.9 TO +28V

Date: 04 March 2016

FROM	ID	P12-76 (S701-1)	ТО	ID	A1P12.50	
FROM	ID	A1J12.50	ТО	ID	A1J10.3	
FROM	ID	A1P10.3	TO	ID	P11-194	(S506-1)
FROM	ID	P11-164 (S506-3)	TO	ID	A1P9.23	
FROM	ID	A1J9.23	TO	ID	BUS 1	
FROM	ID	P20-2 (DMM-HI)	TO	ID	A1P15.49	
FROM	ID	A1J15.49	TO	ID	A1J8.28	
FROM	ID	A1P8.28	TO	ID	P10-203	(S503-1)
FROM	ID	P10-77 (S503-3)	TO	ID	A1P6.13	
FROM	ID	A1J6.13	TO	ID	BUS 1	
FROM	ID	P20-3 (DMM-LO)	TO	ID	A1P15.50	
FROM	ID	A1J15.50	TO	ID	A1J7.38	
FROM	ID	A1P7.38	TO	ID	P10-130	(S301-23)
FROM	ID	P10-229 (S301-24)	TO	ID	A1P7.36	
FROM	ID	A1J7.36	TO	GRO	DUND	

STEP 225

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PINS J1.63 SHOULD BE GREATER THAN 27.0VDC.

•	J1-63) TO W2 P1B-5F TO ID A1J12.49 TO ID P12-30 (S202-40)
FROM ID A1J14.50 FROM ID A1P10.50	2-4) TO ID A1P14.50 TO ID A1J10.50 TO ID P11-244 (S510-2) 0-8) TO ID A1P9.28 TO ID BUS 6
FROM ID BUS 6 FROM ID A1P8.50 FROM ID P10-9 (S301 FROM ID A1J7.26 FROM ID A1P4.18 FROM ID R108.2 FROM ID A1J4.10	TO ID A1J8.50 TO ID P10-138 (S301-54) TO ID A1P7.26 TO ID A1J4.18 TO ID R108.1 TO ID A1P4.10 TO GROUND
FROM ID A1J14.50 FROM ID A1P10.50 FROM ID P11-180 (S5 FROM ID A1J9.21	2-4) TO ID A1P14.50 TO ID A1J10.50 TO ID P11-244 (S510-2) 10-3) TO ID A1P9.21 TO ID BUS 1
FROM ID A1J15.49	HI) TO ID A1P15.49 TO ID A1J8.28

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FROM ID A1P8.28
FROM ID P10-77 (S503-3)
FROM ID A1J6.13
TO ID A1P6.13
TO ID BUS 1

FROM ID P20-3 (DMM-LO)
FROM ID A1J15.50
FROM ID A1J15.50
FROM ID A1P7.38
FROM ID P10-229 (S301-24)
FROM ID A1J7.36
FROM ID A1J7.36
TO GROUND

STEP 226

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT J1.46 SHOULD BE LESS THAN 0.9 VDC.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM W2 P2-46 (UUT J1-46) FROM ID J2A-8B FROM ID A1P15.20	TO W2 P1A-8B TO ID A1J15.20 TO ID P13-46 (S701-48)
FROM ID P12-44 (S701-2)	
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-129 (S506-8)	
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6 FROM ID A1P8.50 FROM ID P10-9 (S301-53)	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J7.26 FROM ID A1P4.18 FROM ID R108.2 FROM ID A1J4.10	TO GROUND
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID AlJ12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	
FROM ID A1J15.49 FROM ID A1P8.28	TO ID A1J8.28
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	
FROM ID A1J7.36	TO ID A1P7.36

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STEP 227

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT J1.9 SHOULD BE LESS THAN 0.9 VDC.

CONNECTION PATH IS AS FOLLOWS:

SEE "UUT POWER"

FROM	W2	P2-9 (UUT J1-9)	ТО	W2	P1B-10B
FROM	ID	J2B-10B	ТО	ID	A1J13.33
FROM	ID	J2B-10B A1P13.33	ТО	ID	P12-10 (S701-45)
FROM	ID	P12-76 (S701-1)			A1P12.50
FROM	ID	A1J12.50			A1J10.3
FROM	ID	A1P10.3	ТО	ID	P11-194 (S506-1)
		P11-129 (S506-8)			
		A1J9.30	TO	ID	BUS 6
FROM	ID	BUS 6	ТО	ID	A1J8.50
FROM	ID	A1P8.50	ТО	ID	P10-138 (S301-54)
FROM	ID	P10-9 (S301-53)	ТО	ID	A1P7.26
FROM	ID	A1J7.26	ТО	ID	A1J4.18
FROM	ID	A1P4.18	ТО	ID	R108.1
FROM	ID	R108.2	ТО	ID	A1P4.10
FROM	ID	A1J7.26 A1P4.18 R108.2 A1J4.10	ТО	GRO	OUND
FROM	ID	P12-76 (S701-1)	ТО	ID	A1P12.50
		A1J12.50			A1J10.3
FROM	ID	A1P10.3	ТО	ID	P11-194 (S506-1)
FROM	ID	P11-164 (S506-3)	ТО	ID	A1P9.23
		A1J9.23		ID	BUS 1
FROM	ID	P20-2 (DMM-HI)	ТО	ID	A1P15.49
FROM	ID	A1J15.49	TO	ID	A1J8.28
FROM	ID	A1P8.28	TO	ID	P10-203 (S503-1)
FROM	ID	P10-77 (S503-3)	ТО	ID	A1P6.13
FROM	ID	A1J6.13	ТО	ID	BUS 1
${\tt FROM}$	ID	P20-3 (DMM-LO) A1J15.50 A1P7.38	ТО	ID	A1P15.50
${\tt FROM}$	ID	A1J15.50	TO	ID	A1J7.38
${\tt FROM}$	ID	A1P7.38	TO	ID	P10-130 (S301-23)
${\tt FROM}$	ID	P10-229 (S301-24)	TO	ID	A1P7.36
FROM	ID	A1J7.36	TO	GRO	DUND

STEP 228

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.3. THE VOLTAGE MEASURED AT J1.2 SHOULD BE LESS THAN 0.9 VDC.

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FROM W2 P2-2 (UUT J1-2) FROM ID J2A-6B FROM ID A1P15.26	TO W2 P1A-6B TO ID A1J15.26 TO ID P13-73 (S701-25)
FROM ID P12-76 (S701-1) FROM ID A1J12.50 FROM ID A1P10.3 FROM ID P11-129 (S506-8) FROM ID A1J9.30	TO ID A1J10.3 TO ID P11-194 (S506-1)
FROM ID BUS 6 FROM ID A1P8.48 FROM ID P10-42 (S301-49) FROM ID A1J7.24 FROM ID A1P4.16 FROM ID R109.2 FROM ID A1J4.9	TO ID A1J8.48 TO ID P10-171 (S301-50) TO ID A1P7.24 TO ID A1J4.16 TO ID R109.1 TO ID A1P4.9 TO +28V
FROM W2 P2-3 (UUT J1-3) FROM ID J2B-8F FROM ID A1P12.43	TO W2 P1B-8F TO ID A1J12.43 TO ID P12-64 (S202-37)
FROM ID P12-76 (S701-1) FROM ID A1J12.50 FROM ID A1P10.3 FROM ID P11-164 (S506-3) FROM ID A1J9.23	TO ID A1J10.3 TO ID P11-194 (S506-1)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 229

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.5. THE VOLTAGE MEASURED AT J1.4 SHOULD BE LESS THAN 0.9 VDC.

FROM W2 P2-4 (UUT J1-4)	TO W2 P1B-9B
FROM ID J2B-9B	TO ID A1J13.29
FROM ID A1P13.29	TO ID P12-8 (S701-27)

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FROM ID FROM ID FROM ID	P11-129 (S506-8)	TO ID A1J10.3 TO ID P11-194 (S506-1)
FROM ID FROM ID FROM ID FROM ID	BUS 6 A1P8.48 P10-42 (S301-49) A1J7.24 A1P4.16 R109.2 A1J4.9	TO ID A1J8.48 TO ID P10-171 (S301-50) TO ID A1P7.24 TO ID A1J4.16 TO ID R109.1 TO ID A1P4.9 TO +28V
	P2-5 (UUT J1-5) J2B-6F A1P12.47	TO W2 P1B-6F TO ID A1J12.47 TO ID P12-94 (S202-39)
FROM ID FROM ID FROM ID	P12-76 (S701-1) A1J12.50 A1P10.3 P11-164 (S506-3) A1J9.23	TO ID A1P12.50 TO ID A1J10.3 TO ID P11-194 (S506-1) TO ID A1P9.23 TO ID BUS 1
FROM ID FROM ID FROM ID	P20-2 (DMM-HI) A1J15.49 A1P8.28 P10-77 (S503-3) A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID	P10-229 (S301-24)	10 12 110 100 (2001 10)

STEP 230

DESCRIPTION:

THIS STEP APPLIES 28.0 VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0 VDC IS APPLIED TO PIN AND J1.49. THE VOLTAGE MEASURED AT PIN J1.9 SHOULD BE GREATER THAN 27.0 VDC.

FROM	W2	P2-9 (UUT J1-9)	TO	W2	P1B-10B
FROM	ID	J2B-10B	ТО	ID	A1J13.33
FROM	ID	A1P13.33	ТО	ID	P12-10 (S701-45)
FROM	ID	P12-76 (S701-1)	TO	ID	A1P12.50
FROM	ID	A1J12.50	ТО	ID	A1J10.3
FROM	ID	A1P10.3	ТО	ID	P11-194 (S506-1)

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FROM	ID	P11-129 (S506-8)	то	ID A1P9.30
		A1J9.30		ID BUS 6
		BUS 6		ID A1J8.50
		A1P8.50		ID P10-138 (S301-54)
FROM	ID	P10-9 (S301-53)	TO	ID A1P7.26
		A1J7.26	_	ID A1J4.18
		A1P4.18		ID R108.1
		R108.2	TO	ID A1P4.10
FROM	ID	A1J4.10	TO	GROUND
		- 0 40 (40)		
		P2-49 (UUT J1-49)		
		J2B-6A		ID A1J12.21
FROM	ID	A1P12.21	TO	ID P12-61 (S202-21)
EDOM	TD	D12 76 (C701 1)	ШΟ	TD 31D10 E0
FROM	TD	P12-76 (S701-1)		ID A1J10.3
		A1J12.50 A1P10.3		ID P11-194 (S506-1)
		P11-164 (S506-3)		
		A1J9.23		ID BUS 1
FROM	Tυ	A109.23	10	ID B05 I
FROM	ID	P20-2 (DMM-HI)	ТО	ID A1P15.49
		A1J15.49		ID A1J8.28
		A1P8.28		ID P10-203 (S503-1)
				ID A1P6.13
		A1J6.13		ID BUS 1
		· ·	ТО	ID A1P15.50
FROM	ID	A1J15.50	TO	ID A1J7.38
FROM	ID	A1P7.38	TO	ID P10-130 (S301-23)
FROM	ID	P10-229 (S301-24)	TO	ID A1P7.36
FROM	ID	A1J7.36	TO	GROUND

STEP 231

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN AND J1.49. THE VOLTAGE MEASURED AT PIN J1.46 SHOULD BE GREATER THAN 27.0VDC.

FROM	W2	P2-46 (UUT J1-46)	TO	W2	PIA-8B
FROM	ID	J2A-8B	TO	ID	A1J15.20
FROM	ID	A1P15.20	TO	ID	P13-46 (S701-48)
FROM	ID	P12-44 (S701-2)	ТО	ID	A1P12.48
FROM	ID	A1J12.48	TO	ID	A1J10.1
FROM	ID	A1P10.1	ТО	ID	P11-162 (S506-2)
FROM	ID	P11-129 (S506-8)	ТО	ID	A1P9.30
FROM	ID	A1J9.30	ТО	ID	BUS 6
FROM	ID	BUS 6	ТО	ID	A1J8.50

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FROM ID A1P8.50 FROM ID P10-9 (S301-53) FROM ID A1J7.26 FROM ID A1P4.18 FROM ID R108.2 FROM ID A1J4.10	TO ID P10-138 (S301-54) TO ID A1P7.26 TO ID A1J4.18 TO ID R108.1 TO ID A1P4.10 TO GROUND
FROM W2 P2-49 (UUT J1-4 FROM ID J2B-6A FROM ID A1P12.21	·
FROM ID P12-44 (S701-2) FROM ID A1J12.48 FROM ID A1P10.1 FROM ID P11-164 (S506-3 FROM ID A1J9.23	TO ID A1J10.1 TO ID P11-162 (S506-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-2 FROM ID A1J7.36	TO ID A1J7.38 TO ID P10-130 (S301-23)

STEP 231

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN AND J1.49. THE VOLTAGE MEASURED AT PIN J1.46 SHOULD BE GREATER THAN 27.0VDC.

FROM I	2 P2-46 (UUT J1-46) D J2A-8B D A1P15.20	TO W2 P1A-8B TO ID A1J15.20 TO ID P13-46 (S701-48)
_	D P12-44 (S701-2)	TO ID A1P12.48
FROM I	D A1J12.48	TO ID A1J10.1
FROM I	D A1P10.1	TO ID P11-162 (S506-2)
FROM I	D P11-129 (S506-8)	TO ID A1P9.30
FROM I	D A1J9.30	TO ID BUS 6
FROM I	D BUS 6	TO ID A1J8.50
FROM I	D A1P8.50	TO ID P10-138 (S301-54)
FROM I	D P10-9 (S301-53)	TO ID A1P7.26
FROM I	D A1J7.26	TO ID A1J4.18
FROM I	D A1P4.18	TO ID R108.1

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FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM W2 P2-49 (UUT J1-49)	TO W2 P1B-6A
FROM ID J2B-6A	TO ID A1J12.21
FROM ID A1P12.21	TO ID P12-61 (S202-21)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 232

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT J1.63 SHOULD BE LESS THAN 0.9 VDC.

FROM W2 P2-63 (UUT J1-63) TO W2 P1B-5F

CONNECTION PATH IS AS FOLLOWS:

FROM ID J2B-5F	TO ID A1J12.49
FROM ID A1P12.49	TO ID P12-30 (S202-40)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-20 (S510-8)	TO ID A1P9.28
FROM ID A1J9.28	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P13-29 (S202-4)	TO ID A1P14.50

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FROM	ID	A1J14.50	TO	ID	A1J10.50	
FROM	ID	A1P10.50	TO	ID	P11-244	(S510-2)
FROM	ID	P11-180 (S510-3)	TO	ID	A1P9.21	
FROM	ID	A1J9.21	ТО	ID	BUS 1	
FROM	ID	P20-2 (DMM-HI)	TO	ID	A1P15.49	
FROM	ID	A1J15.49	ТО	ID	A1J8.28	
FROM	ID	A1P8.28	TO	ID	P10-203	(S503-1)
FROM	ID	P10-77 (S503-3)	ТО	ID	A1P6.13	
FROM	ID	A1J6.13	ТО	ID	BUS 1	
FROM	ID	P20-3 (DMM-LO)	ТО	ID	A1P15.50	
FROM	ID	A1J15.50	ТО	ID	A1J7.38	
FROM	ID	A1P7.38	ТО	ID	P10-130	(S301-23)
FROM	ID	P10-229 (S301-24)	ТО	ID	A1P7.36	
FROM	ID	A1J7.36	ТО	GRO	OUND	

STEP 233

DESCRIPTION:

THIS STEP APPLIES 28.0 VDC TO PINS J1.70 WITH RESPECT TO J1.51. 28.0 VDC IS APPLIED TO PIN J1.40. THE VOLTAGE MEASURED AT PINS J1.63 SHOULD BE GREATER THAN 27.0 VDC.

FROM W2	P2-63 (UUT J1-63)	TO W2	P1B-5F
FROM ID	J2B-5F	TO ID	A1J12.49
FROM ID	A1P12.49	TO ID	P12-30 (S202-40)
	P13-29 (S202-4)		
-	A1J14.50	_	A1J10.50
	A1P10.50		P11-244 (S510-2)
FROM ID	P11-20 (S510-8)	TO ID	A1P9.28
FROM ID	A1J9.28	TO ID	BUS 6
FROM ID	BUS 6	TO ID	A1J8.50
-	A1P8.50		P10-138 (S301-54)
FROM ID	P10-9 (S301-53)	TO ID	A1P7.26
FROM ID	A1J7.26	TO ID	A1J4.18
FROM ID	A1P4.18	TO ID	R108.1
FROM ID	R108.2	TO ID	A1P4.10
FROM ID	A1J4.10	TO GR	OUND
FROM W2	P2-40 (UUT J1-40)	TO W2	P1B-13E
FROM ID	J2B-13E	TO ID	A1J12.31
FROM ID	A1P12.31	TO ID	P12-57 (S202-13)
FROM ID	P13-29 (S202-4)	TO ID	A1P14.50
FROM ID	A1J14.50	TO ID	A1J10.50
FROM ID	A1P10.50	TO ID	P11-244 (S510-2)
FROM ID	P11-180 (S510-3)	TO ID	A1P9.21
	A1J9.21		BUS 1

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FROM ID	P20-2 (DMM-HI)	TO	ID A1P15.49
FROM ID	A1J15.49	ТО	ID A1J8.28
FROM ID	A1P8.28	ТО	ID P10-203 (S503-1)
FROM ID	P10-77 (S503-3)	TO	ID A1P6.13
FROM ID	A1J6.13	TO	ID BUS 1
FROM ID	P20-3 (DMM-LO)	TO	ID A1P15.50
FROM ID	A1J15.50	TO	ID A1J7.38
FROM ID	A1P7.38	TO	ID P10-130 (S301-23)
FROM ID	P10-229 (S301-24)	TO	ID A1P7.36
FROM ID	A1J7.36	TO	GROUND

STEP 234

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.40. THE VOLTAGE MEASURED AT PINS J1.9 SHOULD BE GREATER THAN 27.0VDC.

FROM W2 P2-9 (UUT J1-9) FROM ID J2B-10B FROM ID A1P13.33	
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1D10.3	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6 FROM ID A1P8.50 FROM ID P10-9 (S301-53)	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J7.26 FROM ID A1P4.18 FROM ID R108.2 FROM ID A1J4.10	TO GROUND
FROM W2 P2-40 (UUT J1-40)	
FROM ID J2B-13E FROM ID A1P12.31	TO ID A1J12.31
FROM ID A1P12.31	TO ID P12-57 (S202-13)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49 FROM ID A1P8.28	TO ID P10-203 (S503-1)

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FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-2	24) TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 235

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51. 28.0 VDC IS APPLIED TO PIN J1.40. THE VOLTAGE MEASURED AT PINS J1.46 SHOULD BE GREATER THAN 27.0VDC.

FROM W2 P2-46 (UUT J1-46) FROM ID J2A-8B FROM ID A1P15.20	TO W2 P1A-8B TO ID A1J15.20 TO ID P13-46 (S701-48)
FROM ID P12-44 (S701-2) FROM ID A1J12.48 FROM ID A1P10.1 FROM ID P11-129 (S506-8) FROM ID A1J9.30	TO ID A1J10.1 TO ID P11-162 (S506-2)
FROM ID A1P8.50 FROM ID P10-9 (S301-53) FROM ID A1J7.26 FROM ID A1P4.18 FROM ID R108.2	TO ID A1P7.26
FROM W2 P2-40 (UUT J1-40) FROM ID J2B-13E FROM ID A1P12.31	TO ID A1J12.31 TO ID P12-57 (S202-13)
FROM ID J2B-13E FROM ID A1P12.31 FROM ID P12-44 (S701-2) FROM ID A1J12.48	TO ID A1J12.31 TO ID P12-57 (S202-13) TO ID A1P12.48 TO ID A1J10.1 TO ID P11-162 (S506-2)
FROM ID J2B-13E FROM ID A1P12.31 FROM ID P12-44 (S701-2) FROM ID A1J12.48 FROM ID A1P10.1 FROM ID P11-164 (S506-3)	TO ID A1J12.31 TO ID P12-57 (S202-13) TO ID A1P12.48 TO ID A1J10.1 TO ID P11-162 (S506-2) TO ID A1P9.23 TO ID BUS 1 TO ID A1P15.49

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FROM ID A1J15.50 TO ID A1J7.38
FROM ID A1P7.38 TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24) TO ID A1P7.36
FROM ID A1J7.36 TO GROUND

STEP 236

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51. 28.0 VDC IS APPLIED TO PIN J1.40. THE VOLTAGE MEASURED AT J1.4 SHOULD BE LESS THAN 0.9 VDC.

FROM W2 P2-4 (UUT J1-4) FROM ID J2B-9B FROM ID A1P13.29	TO W2 P1B-9B TO ID A1J13.29 TO ID P12-8 (S701-27)
FROM ID P12-76 (S701-1) FROM ID A1J12.50 FROM ID A1P10.3 FROM ID P11-129 (S506-8) FROM ID A1J9.30	TO ID A1J10.3 TO ID P11-194 (S506-1)
FROM ID BUS 6 FROM ID A1P8.48 FROM ID P10-42 (S301-49) FROM ID A1J7.24 FROM ID A1P4.16 FROM ID R109.2 FROM ID A1J4.9	TO TO 3107 04
FROM W2 P2-40 (UUT J1-40) FROM ID J2B-13E FROM ID A1P12.31	TO W2 P1B-13E TO ID A1J12.31 TO ID P12-57 (S202-13)
FROM ID P12-44 (S701-2) FROM ID A1J12.48 FROM ID A1P10.1 FROM ID P11-164 (S506-3) FROM ID A1J9.23	TO ID A1J10.1 TO ID P11-162 (S506-2)
FROM ID P20-2 (DMM-HI) FROM ID A1J15.49 FROM ID A1P8.28 FROM ID P10-77 (S503-3) FROM ID A1J6.13	TO ID A1J8.28 TO ID P10-203 (S503-1)
FROM ID P20-3 (DMM-LO) FROM ID A1J15.50 FROM ID A1P7.38 FROM ID P10-229 (S301-24) FROM ID A1J7.36	TO ID P10-130 (S301-23)

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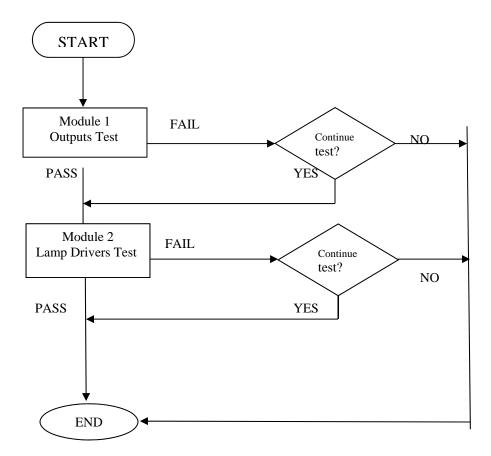
STEP 237

DESCRIPTION:

THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.40. THE VOLTAGE MEASURED AT J1.2 SHOULD BE LESS THAN 0.9 VDC.

FROM W2 P2-2 (UUT J1-2)	TO W2 P1A-6B
FROM W2 P2-2 (UUT J1-2) FROM ID J2A-6B FROM ID A1P15.26	TO ID A1J15.26
FROM ID A1P15.26	TO ID P13-73 (S701-25)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8) TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49	
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
11011 12 1110 1.9	10 1201
FROM W2 P2-40 (UUT J1-4	0) TO W2 P1B-13E
FROM ID J2B-13E	TO ID A1J12.31
FROM ID A1P12 31	TO ID P12-57 (S202-13)
FROM ID J2B-13E FROM ID A1P12.31	10 10 112 37 (5202 13)
FROM ID P12-76 (S701-1)	
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3	
FROM ID A1J9.23	TO ID BUS 1
11011 12 1110 3 . 23	10 10 200 1
FROM ID P20-2 (DMM-HI)	TO TD A1P15 49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	
FROM ID A1J6.13	TO ID BUS 1
FROM ID AIOU.13	10 10 505 1
FROM ID P20-3 (DMM-LO)	TO TO A1P15 50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1013.30 FROM ID A107.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-2	4) TO TD A1D7 36
FROM ID F10-229 (5301-2)	TO GROUND
LIVOM TO WIO 1.20	IO GROOND

3.0 Functional Flow Chart (FFC)



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