Date: 04 March 2016

English Language Test Description

MIPR # M9545012MP24797 CDRL F001

for

Unit Under Test

UUT Nomenclature: Interface Device
UUT Part Number: 13020A1000 and associated TPH
from

Light Armored Vehicle - 25A2 (LAV-25A2)

ATE SYSTEM

AN/USM-657B Third Echelon Test System (TETS)
AN/USM-717 Virtual Instrument Portable Equipment Repair/Test (VIPER/T) **Developed by**

U.S. Army RDECOM
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Date: 04 March 2016

ELTD REVISION SUMMARY

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Table of Contents

1.0 Reference Documents	1
1.1 Virtual Instrument Portable Equipment Repair/Test (VIPER/T)	1
1.2 Third Echelon Test System (TETS)	1
1.3 Unit Under Test	1
1.4 Reference Drawings	2
2.0 English Language Test Description Steps	2
2.1 Common Procedures	2
2.2 Interface ID	3
2.3 Safe to Turn On	4
2.4 Module 1- Interface Device Test	10
2.5 Module 2- W1 CDA Logic A Specific ID Test	57
2.6 Module 3- W2 CDA Logic B Specific ID Tests	97
2.7 Module 4- W3 CDA Grenade Relay Specific ID Tests	134
2.8 Module 5 – W4 GCU Logic Specific ID Tests	149
2.9 Module 6 – W5 GCU Power Drive Specific ID Tests	201
2.10 Module 7 – W6 GTD Power Supply Specific ID Tests	212
2.11 Module 8 – W7/W8 GTD Processor Specific ID Tests	242
3.0 Functional Flow Chart	293

Date: 04 March 2016

1.0 Reference Documents

1.1 Virtual Instrument Portable Equipment Repair/Test (VIPER/T)

IEEE Std 716-1989 IEEE Standard Common

Abbreviated Test Language

for All Systems

TM 10530A-CD TETS IETM (Interactive

Electronic Technical Manual)

System Design Description Third Echelon Test System

(TETS)

Doc # 93006A0018 AN/USM-657(V)

1.2 Third Echelon Test System (TETS)

TETS P/N 93006A0026 TETS CPM (Computer

Programming Manual)

TM TBD-CD VIPER/T IETM (Interactive

Electronic Technical Manual)

System Design Document Doc # 7992008 VIPER/T AN/USM-717

VIPER/T P/N 7992021 VIPER/T CPM (Computer

Programming Manual)

1.3 Unit Under Test

UUT P/N: 13020A1000

UUT Nomenclature: Interface Device – ASSY – LAVA2- SRU

UUT Type: Interface Device

UUT P/N: 13020A7100

UUT Nomenclature: CABLE ASSEMBLY, CDA LOGIC A, W1

UUT Type: Cable

UUT P/N: 13020A7200

UUT Nomenclature: CABLE ASSEMBLY, CDA LOGIC B,W2

UUT Type: Cable

UUT P/N: 13020A7300

UUT Nomenclature: CABLE ASSEMBLY, CDA GRENADE RELAY, W3

UUT Type: Cable

Date: 04 March 2016

UUT P/N: 13020A7400

UUT Nomenclature: CABLE ASSEMBLY, GCU LOGIC, W4

UUT Type: Cable

UUT P/N: 13020A7500

UUT Nomenclature: CABLE ASSEMBLY, GCU POWER DRIVE, W5

UUT Type: Cable

UUT P/N: 13020A7600

UUT Nomenclature: CABLE ASSEMBLY, GTD POWER SUPPLY, W6

UUT Type: Cable

UUT P/N: 13020A7700

UUT Nomenclature: CABLE ASSEMBLY, GTD PROCESSOR, W7

UUT Type: Cable

UUT P/N: 08014A3800

UUT Nomenclature: CABLE ASSEMBLY, RS422, W8

UUT Type: Cable

UUT P/N: 13020A9015

UUT Nomenclature: FIXTURE ASSEMBLY, HOLDING

UUT Type: Holding Fixture

UUT P/N: 13020A6000

UUT Nomenclature: FIXTURE ASSEMBLY, SELF TEST

UUT Type: Test Fixture

1.4 Reference Drawings

Refer to the following schematics when diagnosing connection paths.











13020A0001 (SYSTEM 13020A6004 (SELF 13020A7101 (CABLE, 13020A7201 (CABLE, 13020A7301 (CABLE, INTERCONNECT).pdf TEST PWB, A2).pdf W1, SCHEMATIC).pdf W2, SCHEMATIC).pdf W3, SCHEMATIC).pdf











13020A7401 (CABLE, 13020A7501 (CABLE, 13020A7601 (CABLE, 13020A7701 (CABLE, 08014A3800 (CABLE W4, SCHEMATIC).pdf W5, SCHEMATIC).pdf W6, SCHEMATIC).pdf W7, SCHEMATIC).pdf W8, RS422).pdf

2.0 English Language Test Description Steps

2.1 Common Procedures

The following procedure is used to support the termination of the CAN bus Interface

Date: 04 March 2016

CAN 2 TERMINATE

The following setup procedure properly terminates the CAN bus with a resistive termination as required.

Connection Path is as follows:

From	W7	P3-62 (ST J10-62)	to	W7	P1B-1A
From	ID	J1B-1A	to	ID	A1J12.21
From	ID	A1P12.21	to	ID	P12-61 (S202-21)
From	W7	P3-38 (ST J10-38)	to	W7	P1B-5A
From	ID	J1B-5A	to	ID	A1J12.13
From	ID	A1P12.13	to	ID	P12-92 (S202-22)
	T.D.	D10 F0 (G200 1)		TD	71010 20
		P12-59 (S202-1)			
					A1J10.10
_		A1P10.10			P11-177 (S509-1)
		P11-207 (S509-7)			
F'rom	TD	A1J9.36	to	TD	BUS 5
From	ID	P12-90 (S202-2)	to	ID	A1P12.36
		· · · · · · · · · · · · · · · · · · ·			A1J10.12
					P11-242 (S509-2)
From	ID	P11-144 (S509-8)			
		A1J9.26			BUS 6
From	ID	BUS 5	to	ID	A1J8.45
From	ID	A1P8.45	to	ID	P10-148 (S301-96)
From	ID	P10-50 (S301-95)	to	ID	A1P8.25
From	ID	A1J8.25	to	ID	R11.1
From	TD	BUS 6	t 0	TD	A1J8.46
_		A1P8.46			P10-145 (S301-93)
_		P10-179 (S301-94)			
		A1J8.23			R11.2
T T OIL	エレ	ATU U • AJ		エレ	1/11.0

2.2 Interface ID

Refer to Reference Drawings when diagnosing connection paths.

Step 1 TPH (ID) Identification:

Description:

Connect R111 (324 ohms) to Bus 5. Connect R109 (698 ohms) to Bus 6. R109 and R111 are now in series between Bus 5 and 6. Connect DMM HI to Bus 5. Connect DMM LO to Bus 6. Expected Resistance: 1022 ohms +/- 5%.

From	ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From	ID	A1J15.49	to	ID	A1J8.28
From	ID	A1P8.28	to	ID	P10-203 (S503-1)

Date: 04 March 2016

From ID P10-137 (S503-7) From ID A1J6.47	to ID A1P6.47 to ID BUS 5
From ID BUS 5 From ID A1P8.47 From ID P10-7 (S301-47) From ID A1J7.23 From ID A1P4.15 From ID R111.2 From ID A1J4.9	to ID A1J8.47 to ID P10-73 (S301-48) to ID A1P7.23 to ID A1J4.15 to ID R111.1 to ID A1P4.9 to +28V
From +28V From ID A1P4.9 From ID R109.1 From ID A1J4.16 From ID A1P7.24 From ID P10-171 (S301-50) From ID A1J8.48	to ID A1J4.9 to ID R109.2 to ID A1P4.16 to ID A1J7.24 to ID P10-42 (S301-49) to ID A1P8.48 to ID BUS 6
From ID BUS 6 From ID A1P6.38 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.38 to ID P10-170 (S503-8) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

2.3 Safe to Turn On

Step 2 STTO Test:

Description:

Verify that no low impedance/shorts exist on PS DC2 leads - minimum resistance of 1E3 ohms

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P7.42	to ID A1P15.49 to ID A1J7.42 to ID P10-34 (S301-2)
From ID P10-65 (S301-1)	to ID A1P7.3
From ID A1J7.3	to ID A1J2.15
From ID A1P2.15	to ID P10-86 (S101-2)
From ID P10-22 (S101-1)	to ID A1P2.23
From ID A1J2.23	to ID A1J1.2
From ID A1P1.2	to ID P1-4 (DC2-HI)
From ID P10-86 (S101-2)	to ID A1P2.15
From ID A1J2.15	to ID A1J10.14
From ID A1P10.14	to ID P11-140 (S301-161)
From ID P11-206 (S301-162)	to ID A1P10.16
From ID A1J10.16	to ID J1A-13E
From ID P10-86 (S101-2)	to ID A1P2.15
From ID A1J2.15	to ID A1J11.11

Date: 04 March 2016

From ID A1P11.11 to ID P11-233 (S301-153) From ID P11-43 (S301-154) to ID A1P11.9 From ID A1J11.9 to ID J1A-12D

From ID P20-3 (DMM-LO) to ID A1P15.50 From ID A1J15.50 to ID A1J7.38 From ID A1P7.38 to ID P10-130 (S301-23) From ID P10-229 (S301-24) to ID A1P7.36 From ID A1J7.36 to GROUND

Step 3 STTO Test:

Description:

Verify that no low impedance/shorts exist on PS DC4 leads - minimum resistance of 1E3 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J7.44
From ID A1P7.44	to ID P10-99 (S301-4)
From ID P10-226 (S301-3)	to ID A1P7.13
From ID A1J7.13	to ID A1J2.31
From ID A1P2.31	to ID P10-87 (S101-6)
From ID P10-23 (S101-5)	to ID A1P2.4
From ID A1J2.4	to ID A1J1.3
From ID A1P1.3	to ID P1-10 (DC4-HI)
From ID P10-87 (S101-6)	to ID A1P2.31
From ID A1J2.31	to ID A1J2.30
From ID A1P2.30	to ID P11-216 (S101-32)
From ID P11-152 (S101-31)	to ID A1P2.37
From ID A1J2.37	to ID A1J3.20
From ID A1P3.20	to ID J3-28
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID A1J7.36	to GROUND

Step 4 STTO Test:

Description:

Verify that no low impedance/shorts exist on PS DC5 leads - minimum resistance of 1E3 ohms.

From P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J7.46
From ID A1P7.46	to ID P10-35 (S301-8)
From ID P10-1 (S301-7)	to ID A1P7.1
From ID AlJ7.1	to ID A1J2.14

Date: 04 March 2016

From ID A1P2.14 From ID P10-24 (S101-9) From ID A1J2.2 From ID A1P1.14	to ID P10-88 (S101-10) to ID A1P2.2 to ID A1J1.14 to ID P1-13 (DC5-HI)
From ID P20-3 (DMM-LO) From ID A1J15.50	to ID A1P15.50 to ID A1J7.40
From ID A1P7.40 From ID P10-66 (S301-6) From ID A1J7.12	to ID P10-33 (S301-5) to ID A1P7.12 to ID A1J1.7
From ID A1P1.7	to ID P1-14 (DC5-LO)

Step 5 STTO Test:

Description:

Verify that no low impedance/shorts exist on PS DC6 leads - minimum resistance of 1E3 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J7.48
From ID A1P7.48	to ID P10-2 (S301-10)
From ID P10-97 (S301-9)	to ID A1P7.5
From ID A1J7.5	to ID A1J2.33
From ID A1P2.33	to ID P10-89 (S101-14)
From ID P10-25 (S101-13)	to ID A1P2.20
From ID A1J2.20	to ID A1J1.15
From ID A1P1.15	to ID P1-16 (DC6-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 6 STTO Test:

Description:

Verify that no low impedance/shorts exist on PS DC10 leads - minimum resistance of 1E3 ohms.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.34
From ID	A1P8.34	to	ID	P10-201 (S301-46)
From ID	P10-167 (S301-45)	to	ID	A1P7.22
From ID	A1J7.22	to	ID	A1J4.14
From ID	A1P4.14	to	ID	R102.1
From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.32
From ID	A1P8.32	to	ID	P10-136 (S301-44)

Date: 04 March 2016

0 . 1,1011 011 1	-010			
From ID	P10-135 (S301-43)	to	ID	A1P7.7
	A1J7.7			A1J4.13
	A1P4.13			R103.1
From ID	R103.1	to	ID	A1P4.13
From ID	A1J4.13	to	ID	A1J2.12
From ID	A1P2.12	to	ID	P10-150 (S101-3)
From ID	P10-214 (S101-4)			A1P2.35
	A1J2.35			A1J3.22
From ID	A1P3.22	to	ID	J3-18
		to	ID	A1P15.49
	A1J15.49	to	ID	A1J8.30
	A1P8.30			P10-41 (S301-42)
From ID	P10-199 (S301-41)	to	ID	A1P7.2
From ID	A1J7.2	to	ID	A1J2.29
From ID	A1P2.29	to	ID	P10-153 (S101-15)
	P1-28 (DC10-HI)			
	A1J1.6			A1J7.11
				P10-4 (S301-18)
	P10-67 (S301-17)			
	A1J7.27			A1J4.19
From ID	A1P4.19	to	ID	R102.2
	D1 00 (DC10 HT)		T D	31D1 C
	P1-28 (DC10-HI)			
				A1J2.1
From ID				P10-90 (S101-18)
	P10-26 (S101-17)			
	A1J2.11			A1J4.1
From ID	AIP4.I	to	ID	R103.2
From ID	P1-28 (DC10-HI)	tο	TD	Δ1D1 6
				A1J2.21
				P10-217 (S101-16)
	P10-153 (S101-15)			
	A1J2.29			
	A1P2.32			P10-218 (S101-20)
	P10-154 (S101-19)			A1P2.16
FIOIII ID	A1J2.16	LO	ΙD	J1B-3D
From ID	P1-29 (DC10-LO)	to	ID	A1P1.4
From ID				A1J7.20
	A1P7.20			P10-228 (S301-14)
	P10-100 (S301-13)			A1P7.6
	A1J7.6			A1J3.4
From ID				J3-24
		- 0		
From ID	J3-24	to	ID	A1P3.4
From ID	A1J3.4	to	ID	J1A-12C
Erow In	T2 24	+ -	TD	ת 1 ה מרו <i>ה</i>
From ID				A1P3.4
From ID	A1U3.4	τo	ΤŊ	J2A-10E

Date: 04 March 2016

From ID P20-3 (DMM-LO) to ID A1P15.50 to ID A1J7.38 From ID A1P7.38 to ID P10-130 (S301-23) From ID P10-229 (S301-24) to ID A1P7.36 from ID A1J7.36 to GROUND

Step 7 STTO Test:

Description:

Verify that no low impedance/shorts exist on PS DC9 leads - minimum resistance of 1E3 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
	to ID AlJ8.28
	to ID P10-203 (S503-1)
From ID P10-173 (S503-9)	
From ID A1J6.28	to ID BUS 7
11011120.20	66 12 265 7
From ID P1-25 (DC9-HI)	to ID A1P1.13
From ID A1J1.13	to ID A1J7.14
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-173 (S503-9)	to ID A1P6.28
From ID A1J6.28	to ID BUS 7
From ID P1-25 (DC9-HI)	to ID A1P1.13
From ID A1J1.13	to ID A1J7.14
From ID A1P7.14	to ID P10-197 (S301-29)
From ID P10-198 (S301-30)	to ID A1P6.24
From ID A1J6.24	to ID BUS 7
From ID P1-26 (DC9-LO)	to ID A1P1.5
From ID A1J1.5	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From ID P1-26 (DC9-LO)	to ID A1P1.5
From ID AlJ1.5	to ID AlJ7.16
From ID A1P7.16	to ID P10-163 (S301-12)
From ID P10-98 (S301-11)	to ID A1P7.32
From ID A1J7.32	to GROUND
D TD D20 2 /D355 T2)	L. TD 31D15 50
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-205 (S503-10)	to ID AlP6.16
From ID A1J6.16	to ID BUS 8

Step 8 STTO Test:

Date: 04 March 2016

Description:

Verify that no low impedance/shorts exist on PS DC1 - minimum resistance of 1E3 ohms.

From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID AlJ1.1 (+5V)	to U4.1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2 (+15V)	to R12.2
From R12.1	to U4.3
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID AlJ1.10	to GROUND
	11 2
From ID J2A-13A	to ID A1U4.3
From ID A1U4.2	to ID A1J5.36
From ID A1P5.36	to ID P6-17 (DTS CH23)
From ID P1-1 (DC1-HI)	to ID AlP1.1
From ID AlJ1.1 (+5V)	to ID Alul.1
From ID AlJ1.1 (+5V)	to ID AlU1.13
From ID AlJ1.1 (+5V)	to ID A1U2.1
From ID AlJ1.1 (+5V)	to ID A1U2.13
From ID AlJ1.1 (+5V)	to ID A1U3.1
From ID AlJ1.1 (+5V)	to ID A1U3.13
From ID AlJ1.1 (+5V)	to ID A1U4.1
From ID AlJ1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID AlC11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID AlJ1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID AlU1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID Alu3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID AlC4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID AlP1.10

Date: 04 March 2016

From	ID	A1J1.10	to	GROUND
${\tt From}$	ID	A1C1.2	to	GROUND
${\tt From}$	ID	A1C2.2	to	GROUND
${\tt From}$	ID	A1C3.2	to	GROUND
${\tt From}$	ID	A1C4.2	to	GROUND
${\tt From}$	ID	A1C5.2	to	GROUND
${\tt From}$	ID	A1C6.2	to	GROUND
${\tt From}$	ID	A1C7.2	to	GROUND
${\tt From}$	ID	A1C8.2	to	GROUND
${\tt From}$	ID	A1C9.2	to	GROUND
${\tt From}$	ID	A1C10.2	to	GROUND
${\tt From}$	ID	A1C11.2	to	GROUND
${\tt From}$	ID	A1U1.8	to	GROUND
${\tt From}$	ID	A1U2.8	to	GROUND
${\tt From}$	ID	A1U3.8	to	GROUND
${\tt From}$	ID	A1U4.8	to	GROUND
${\tt From}$	ID	A1U5.8	to	GROUND
${\tt From}$	ID	A1U6.8	to	GROUND
${\tt From}$	ID	A1U7.8	to	GROUND
${\tt From}$	ID	A1U8.8	to	GROUND
${\tt From}$	ID	P7-24 (DTS GCH 40)	to	ID A1P5.3
${\tt From}$	ID	A1J5.3	to	GROUND
${\tt From}$	ID	P6-64 (DTS GCH 7)	to	ID A1P5.1
From	ID	A1J5.1	to	GROUND

2.4 MODULE 1- INTERFACE DEVICE TEST

Refer to Reference Drawings when diagnosing connection paths.

Step 101

Description:

This step verifies DCPS2 connections to the DMM. DCPS2 will be set to 15VDC and the DMM is used to measure output voltage UL= $15.5\ \text{VDC}$, LL= $14.5\ \text{VDC}$.

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J2.23
From ID A1P2.23	to ID P10-22 (S101-1)
From ID P10-86 (S101-2)	to ID A1P2.15
From ID A1J2.15	to ID A1J7.3
From ID A1P7.3	to ID P10-65 (S301-1)
From ID P10-34 (S301-2)	to ID A1P7.42
From ID A1J7.42	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Date: 04 March 2016

From GROUND to ID AlJ1.10 From ID AlP1.10 to ID P1-5 (DC2-LO)

Step 102

Description:

This step verifies DCPS2 connections to BUS1. DCPS2 will be set to 15VDC and the DMM is used to measure output voltage UL= $15.5\ \text{VDC}$, LL= $14.5\ \text{VDC}$.

Connection Path is as follows:

From ID P1-4 (DC2-HI) From ID A1J1.2 From ID A1P8.4 From ID P10-204 (S301-67) From ID A1J8.29	to ID A1P1.2 to ID A1J8.4 to ID P10-174 (S301-68) to ID A1P8.29 to ID BUS 1
From ID BUS 1 From ID A1P6.13 From ID P10-203 (S503-1) From ID A1J8.28	to ID A1J6.13 to ID P10-77 (S503-3) to ID A1P8.28 to ID A1J15.49
From ID A1P15.49 From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P7.38 From ID P10-229 (S301-24) From ID A1J7.36	to ID P20-2 (DMM-HI) to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 103

Description:

This step verifies DCPS4 connections to the DMM. DCPS4 will be set to 28VDC and the DMM is used to measure output voltage UL= $28.5\ \text{VDC}$, LL= $27.5\ \text{VDC}$.

From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID AlJ1.3	to ID A1J2.4
From ID A1P2.4	to ID P10-23 (S101-5)
From ID P10-87 (S101-6)	to ID A1P2.31
From ID A1J2.31	to ID A1J7.13
From ID A1P7.13	to ID P10-226 (S301-3)
From ID P10-99 (S301-4)	to ID A1P7.44
From ID AlJ7.44	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Date: 04 March 2016

From GROUND

to ID A1J1.11 to ID P1-11 (DC4-LO) From ID A1P1.11

Step 104

Description:

This step verifies DCPS4 connections to BUS2. DCPS4 will be set to 15VDC and the DMM is used to measure output voltage UL= 28.5 VDC, LL= 27.5 VDC.

Connection Path is as follows:

From ID P1-10 (DC4-HI) From ID A1J1.3			A1P1.3 A1J8.2
From ID A1P8.2	to	ID	P10-141 (S301-70)
From ID P10-44 (S301-69)	to	ID	A1P8.31
From ID A1J8.31	to	ID	BUS 2
From ID BUS 2	to	ID	A1J6.23
From ID A1P6.23	to	ID	P10-12 (S503-4)
From ID P10-203 (S503-1)	to	ID	A1P8.28
From ID A1J8.28	to	ID	A1J15.49
From ID A1P15.49	to	ID	P20-2 (DMM-HI)
From ID P20-3 (DMM-LO)	to	ID	A1P15.50

From ID	P20-3 (DMM-LO)	to	ID	A1P15.50	
From ID	A1J15.50	to	ID	A1J7.38	
T TD	7107 20	L .	TD	D10 130 /	

From ID AlJ15.50

From ID AlP7.38

From ID P10-229 (S301-24)

From ID AlJ7.36

to ID P10-130 (S301-23)

to ID AlP7.36

to GROUND

Step 105

Description:

This step verifies DCPS5 connections to the DMM. DCPS5 will be set to 15VDC and the DMM is used to measure output voltage UL= 15.5 VDC, LL= 14.5 VDC.

From ID	P1-13 (DC5-HI)	to	ID AlP1.14
From ID	A1J1.14	to	ID A1J2.2
From ID	A1P2.2	to	ID P10-24 (S101-9)
From ID	P10-88 (S101-10)	to	ID A1P2.14
From ID	A1J2.14	to	ID A1J7.1
From ID	A1P7.1	to	ID P10-1 (S301-7)
From ID	P10-35 (S301-8)	to	ID A1P7.46
From ID	A1J7.46	to	ID A1J15.49
From ID	A1P15.49	to	P20-2 (DMM-HI)
From ID	P1-14 (DC5-LO)	to	ID A1P1.7
From ID	A1J1.7	to	ID A1J7.12
From ID	A1P7.12	to	ID P10-66 (S301-6)
From ID	P10-33 (S301-5)	to	ID A1P7.40
From ID	A1J7.40	to	ID A1J15.50

Date: 04 March 2016

From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 106

Description:

This step verifies DCPS6 connections to the DMM. DCPS6 will be set to 15VDC and the DMM is used to measure output voltage UL= $15.5\ \text{VDC}$, LL= $14.5\ \text{VDC}$.

Connection Path is as follows:

From ID P1-16 (DC6-HI) From ID A1J1.15 From ID A1P2.20 From ID P10-89 (S101-14)	to ID A1P1.15 to ID A1J2.20 to ID P10-25 (S101-13) to ID A1P2.33
From ID A1J2.33 From ID A1P7.5	to ID A1J7.5 to ID P10-97 (S301-9) to ID A1P7.48
From ID P10-2 (S301-10) From ID A1J7.48 From ID A1P15.49	to ID AIP7.48 to ID A1J15.49 to ID P20-2 (DMM-HI)
From ID P1-17 (DC6-LO) From ID A1J1.12	to ID AlP1.12 to GROUND
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P7.38 From ID P10-229 (S301-24) From ID A1J7.36	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 107

Description:

This step verifies DCPS10 connections to the DMM. DCPS10 will be set to 15VDC and the DMM is used to measure output voltage UL= 15.5 VDC, LL= 14.5 VDC.

From ID P1-28 (DC10-HI)	to ID A1P1.6
From ID A1J1.6	to ID A1J2.11
From ID A1P2.11	to ID P10-217 (S101-16)
From ID P10-153 (S101-15)	to ID A1P2.29
From ID A1J2.29	to ID A1J7.2
From ID A1P7.2	to ID P10-199 (S301-41)
From ID P10-41 (S301-42)	to ID A1P8.30
From ID A1J8.30	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID P1-29 (DC10-LO)	to ID A1P1.4
From ID A1J1.4	to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)

Date: 04 March 2016

From ID P10-229 (S301-24) to ID A1P7.36 From ID A1J7.36 to GROUND

Step 108

Description:

This step verifies DCPS10 through resistor R103. DCPS10 will be set to 15VDC and the DMM is used to measure output voltage UL= 15.5 VDC, LL= 14.5 VDC.

Connection Path is as follows:

From ID P1-28 (DC10-HI)	to ID A1P1.6
From ID A1J1.6	to ID A1J2.1
From ID A1P2.1	to ID P10-90 (S101-18)
From ID P10-26 (S101-17)	to ID A1P2.11
From ID A1J2.11	to ID A1J4.1
From ID A1P4.1	to ID R103.2
From ID P1-29 (DC10-LO)	to ID A1P1.4
From ID A1J1.4	to GROUND
From ID R103.1	to ID AlP4.13
From ID AlJ4.13	to ID A1J7.7
From ID A1P7.7	to ID P10-135 (S301-43)
From ID P10-136 (S301-44)	to ID A1P8.32
From ID A1J8.32	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 109

Description:

This step verifies DCPS10 through resistor R102. DCPS10 will be set to 15VDC and the DMM is used to measure output voltage UL= 15.5 VDC, LL= 14.5 VDC.

From ID P1-28 (DC10-HI)	to ID A1P1.6
From ID A1J1.6	to ID A1J7.11
From ID A1P7.11	to ID P10-4 (S301-18)
From ID P10-67 (S301-17)	to ID A1P7.27
From ID A1J7.27	to ID A1J4.19
From ID A1P4.19	to ID R102.2
From ID P1-29 (DC10-LO)	to ID A1P1.4
From ID AlJ1.4	to GROUND

Date: 04 March 2016

From ID R102.1	to ID A1P4.14
From ID A1J4.14	to ID A1J7.22
From ID A1P7.22	to ID P10-167 (S301-45)
From ID P10-201 (S301-46)	to ID A1P8.34
From ID A1J8.34	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 110

Description:

This step verifies DCPS9 connections to BUS7. DCPS9 will be set to 15VDC and the DMM is used to measure output voltage UL= $15.5\ \text{VDC}$, LL= $14.5\ \text{VDC}$.

Connection Path is as follows:

From ID P1-25 (DC9-HI) From ID A1J1.13 From ID A1P7.14 From ID P10-198 (S301-30) From ID A1J6.24	to ID A1P1.13 to ID A1J7.14 to ID P10-197 (S301-29) to ID A1P6.24 to ID BUS 7
From ID P1-26 (DC9-LO) From ID A1J1.5 From ID A1P7.16 From ID P10-98 (S301-11) From ID A1J7.32	to ID A1P1.5 to ID A1J7.16 to ID P10-163 (S301-12) to ID A1P7.32 to GROUND
From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-173 (S503-9) From ID A1J6.28	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.28 to ID BUS 7
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P7.38 From ID P10-229 (S301-24) From ID A1J7.36	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 111

Description:

This step verifies DCPS9 connections to BUS7 and BUS 8. DCPS9 will be set to 15VDC and the DMM is used to measure output voltage UL= $15.5\ \text{VDC}$, LL= $14.5\ \text{VDC}$.

Date: 04 March 2016

From ID P1-25 (DC9-HI) From ID A1J1.13 From ID A1P7.14 From ID P10-198 (S301-30) From ID A1J6.24	to ID A1J7.14 to ID P10-197 (S301-29)
From ID P1-26 (DC9-LO)	to ID A1P1.5
From ID A1J1.5	to ID A1J7.18
From ID A1P7.18	to ID P10-133 (S301-27)
From ID P10-70 (S301-28)	to ID A1P6.12
From ID A1J6.12	to ID BUS 8
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-173 (S503-9)	to ID A1P6.28
From ID A1J6.28	to ID BUS 7
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-205 (S503-10)	to ID A1P6.16
From ID A1J6.16	to ID BUS 8

Step 112

Description:

This step verifies the value of ITA resistor R1 equals 10K Ohms +/-5% with one end connected to ground. The DMM resource will be used to measure resistance UL= 10500 OHM, LL= 9500 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-137 (S503-7) From ID A1J6.47	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.47 to ID BUS 5
From ID BUS 5 From ID A1P8.33 From ID P10-142 (S301-72) From ID A1J8.1 From ID R1.2	to ID A1J8.33 to ID P10-76 (S301-71) to ID A1P8.1 to ID R1.1 to GROUND
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P7.38 From ID P10-229 (S301-24) From ID A1J7.36	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 113

Description:

Date: 04 March 2016

This step verifies the value of ITA resistor R2 equals 15K Ohms+/-5% with one end connected to ground. The DMM resource will be used to measure resistance UL= 15750 OHM, LL= 14250 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-137 (S503-7) From ID A1J6.47	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.47 to ID BUS 5
From ID BUS 6 From ID A1P8.36 From ID P10-15 (S301-74) From ID A1J8.3 From ID R2.2	to ID A1J8.36 to ID P10-14 (S301-73) to ID A1P8.3 to ID R2.1 to GROUND
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P7.38 From ID P10-229 (S301-24) From ID A1J7.36	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 114

Description:

This step verifies the value of ITA resistor R3 equals 15K Ohms+/-5% with one end connected to +15VDC. The DMM resource will be used to measure resistance UL= 15750 OHM, LL= 14250 OHM.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1	to ID A1J8.29
From ID A1P8.29	to ID P10-204 (S301-67)
From ID P10-174 (S301-68)	to ID A1P8.4
From ID A1J8.4	to ID A1J1.2
From ID A1P1.2	to ID P1-4 (DC2-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID BUS 5	to ID A1J8.35
From ID A1P8.35	to ID P10-79 (S301-75)
From ID P10-80 (S301-76)	to ID A1P8.11

Date: 04 March 2016

From ID AlJ8.11 to ID R3.1 From ID R3.2 to +15V

Step 115

Description:

This step verifies the value of ITA resistor R4 equals 909 Ohms+/-5% with one end connected to +15VDC. The DMM resource will be used to measure resistance UL= 956 Ohm, LL= 864 Ohm.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J8.29
From ID A1P8.29	to ID P10-204 (S301-67)
From ID P10-174 (S301-68)	to ID A1P8.4
From ID A1J8.4	to ID A1J1.2
From ID A1P1.2	to ID P1-4 (DC2-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
110111 12 120 3 (2111 20)	
From ID A1J15.50	to ID A1J8.26
From ID A1J15.50	to ID A1J8.26
From ID A1J15.50 From ID A1P8.26	to ID A1J8.26 to ID P10-139 (S503-2)
From ID A1J15.50 From ID A1P8.26 From ID P10-170 (S503-8)	to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.38
From ID A1J15.50 From ID A1P8.26 From ID P10-170 (S503-8)	to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.38
From ID A1J15.50 From ID A1P8.26 From ID P10-170 (S503-8) From ID A1J6.38	to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.38 to ID BUS 6
From ID A1J15.50 From ID A1P8.26 From ID P10-170 (S503-8) From ID A1J6.38 From ID BUS 6	to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.38 to ID BUS 6 to ID A1J8.38 to ID P10-47 (S301-77)
From ID A1J15.50 From ID A1P8.26 From ID P10-170 (S503-8) From ID A1J6.38 From ID BUS 6 From ID A1P8.38	to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.38 to ID BUS 6 to ID A1J8.38 to ID P10-47 (S301-77)

Step 116

Description:

This step verifies the value of ITA resistor R5 equals 2.2K Ohms+/-5% with one end connected to +15VDC. The DMM resource will be used to measure resistance UL= 2310 OHM, LL= 2090 OHM.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49)
From ID	A1J15.49	to	ID	A1J8.28	
From ID	A1P8.28	to	ID	P10-203	(S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13	
From ID	A1J6.13	to	ID	BUS 1	
From ID	BUS 1	to	ID	A1J8.29	
From ID	A1P8.29	to	ID	P10-204	(S301-67)
From ID	P10-174 (S301-68)	to	ID	A1P8.4	

Date: 04 March 2016

From ID A1J8.4 From ID A1P1.2	to ID AlJ1.2 to ID P1-4 (DC2-HI)
110111111111111111111111111111111111111	
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID BUS 5	to ID A1J8.37
From ID A1P8.37	to ID P10-143 (S301-79)
From ID P10-209 (S301-80)	to ID A1P8.15
From ID A1J8.15	to ID R5.1
From ID R5.2	to +15V

Step 117

Description:

This step verifies the value of ITA resistor R6 equals 5.6 K Ohms+/- 5 W with one end connected to +15VDC. The DMM resource will be used to measure resistance UL= 5880 OHM, LL= 5320 OHM.

Connection Path is as follows:

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13
From ID	A1J6.13	to	ID	BUS 1
D TD	DIIG 1		TD	7170 20
From ID				A1J8.29
_	A1P8.29			P10-204 (S301-67)
	P10-174 (S301-68)			A1P8.4
From ID				A1J1.2
From ID	A1P1.2	to	ID	P1-4 (DC2-HI)
	DOO 2 (DIGG TO)			31D1E E0
				A1P15.50
From ID	A1J15.50	to	ID	A1J8.26
From ID From ID	A1J15.50 A1P8.26	to to	ID ID	A1J8.26 P10-139 (S503-2)
From ID From ID	A1J15.50	to to	ID ID	A1J8.26
From ID From ID From ID	A1J15.50 A1P8.26	to to to	ID ID ID	A1J8.26 P10-139 (S503-2)
From ID From ID From ID	A1J15.50 A1P8.26 P10-170 (S503-8) A1J6.38	to to to	ID ID ID	A1J8.26 P10-139 (S503-2) A1P6.38 BUS 6
From ID From ID From ID From ID	A1J15.50 A1P8.26 P10-170 (S503-8) A1J6.38	to to to to	ID ID ID ID	A1J8.26 P10-139 (S503-2) A1P6.38 BUS 6
From ID From ID From ID From ID From ID	A1J15.50 A1P8.26 P10-170 (S503-8) A1J6.38 BUS 6 A1P8.40	to to to to	ID ID ID ID ID	A1J8.26 P10-139 (S503-2) A1P6.38 BUS 6 A1J8.40 P10-175 (S301-81)
From ID From ID From ID From ID From ID	A1J15.50 A1P8.26 P10-170 (S503-8) A1J6.38	to to to to to	ID ID ID ID ID ID	A1J8.26 P10-139 (S503-2) A1P6.38 BUS 6 A1J8.40 P10-175 (S301-81) A1P8.17
From ID	A1J15.50 A1P8.26 P10-170 (S503-8) A1J6.38 BUS 6 A1P8.40	to to to to to	ID ID ID ID ID ID	A1J8.26 P10-139 (S503-2) A1P6.38 BUS 6 A1J8.40 P10-175 (S301-81)
From ID	A1J15.50 A1P8.26 P10-170 (S503-8) A1J6.38 BUS 6 A1P8.40 P10-48 (S301-82) A1J8.17	to to to to to	ID ID ID ID ID ID	A1J8.26 P10-139 (S503-2) A1P6.38 BUS 6 A1J8.40 P10-175 (S301-81) A1P8.17 R6.1

Step 118

Description:

This step verifies the value of ITA resistor R8 equals 5.1K Ohms+/- 5% with one end connected to +15VDC. The DMM resource will be used to measure resistance UL= 5355 OHM, LL= 4845 OHM.

Date: 04 March 2016

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P8.29 From ID P10-174 (S301-68) From ID A1J8.4 From ID A1P1.2	to ID A1J8.29 to ID P10-204 (S301-67) to ID A1P8.4 to ID A1J1.2 to ID P1-4 (DC2-HI)
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-137 (S503-7) From ID A1J6.47	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.47 to ID BUS 5
From ID BUS 5 From ID A1P8.39 From ID P10-243 (S301-86) From ID A1J8.19 From ID R8.2	to ID A1J8.39 to ID P10-178 (S301-85) to ID A1P8.19 to ID R8.1 to +15V

Step 119

Description:

This step verifies the value of ITA diode CR1 equals 5.1VDC+/-5% with one end connected to +15VDC. Power Supply DC2 will be used to apply +15VDC, and the DMM resource will be used to measure voltage across CR1. UL= 5.4 VOLTS, LL= 4.8 VOLTS

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-137 (S503-7) From ID A1J6.47	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.47 to ID BUS 5
From ID BUS 5 From ID A1P8.39 From ID P10-243 (S301-86) From ID A1J8.19 From ID R8.2	to ID A1J8.39 to ID P10-178 (S301-85) to ID A1P8.19 to ID R8.1 to +15V
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-170 (S503-8) From ID A1J6.38	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.38 to ID BUS 6

Date: 04 March 2016

From ID BUS 6 From ID A1P8.44 From ID P10-210 (S301-87) From ID A1J8.21 From ID CR1.K	to ID A1J8.44 to ID P10-83 (S301-88) to ID A1P8.21 to ID CR1.A to ID R8.1
From ID BUS 6 From ID AlP6.44 From ID P10-94 (S402-1) From ID AlJ6.9	to ID A1J6.44 to ID P10-222 (S402-3) to ID A1P6.9 to INSTR-RTN
From INSTR-RTN From ID A1P6.11 From ID P10-102 (S301-25) From ID A1J7.34	to ID A1J6.11 to ID P10-166 (S301-26) to ID A1P7.34 to GROUND

Step 120

Description:

This step verifies the value of ITA resistor R9 equals 2.2K Ohms+/-5% with one end connected to Ground. The DMM resource will be used to measure resistance UL= 2310 OHM, LL= 2090 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-137 (S503-7) From ID A1J6.47	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.47 to ID BUS 5
From ID BUS 5 From ID A1P8.41 From ID P10-82 (S301-90) From ID A1J8.7 From ID R9.2	to ID A1J8.41 to ID P10-49 (S301-89) to ID A1P8.7 to ID R9.1 to GROUND
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P7.38 From ID P10-229 (S301-24) From ID A1J7.36	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 121

Description:

This step verifies the value of ITA resistor R7 equals 5.6K Ohms+/-5% with one end connected to Ground. The DMM resource will be used to measure resistance UL= 5880 OHM, LL= 5320 OHM.

From	ID	P20-2	(DMM-HI)	to	ID	A1P15.49
From	ID	A1J15.	49	to	ID	A1J8.28

Date: 04 March 2016

From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID BUS 6 From ID A1P8.42 From ID P10-81 (S301-84) From ID A1J8.5 From ID R7.2	to ID A1J8.42 to ID P10-78 (S301-83) to ID A1P8.5 to ID R7.1 to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 122

Description:

This step verifies the value of ITA resistor R10 equals 11.5K Ohms+/-5% with one end connected to Ground. The DMM resource will be used to measure resistance UL= 12075 OHM, LL= 10925 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-137 (S503-7) From ID A1J6.47	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.47 to ID BUS 5
From ID BUS 5 From ID A1P8.43 From ID P10-51 (S301-92) From ID A1J8.9 From ID R10.2	to ID A1J8.43 to ID P10-114 (S301-91) to ID A1P8.9 to ID R10.1 to GROUND
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P7.38 From ID P10-229 (S301-24) From ID A1J7.36	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 123

Description:

This step verifies the value of ITA resistor R111 is $324 \, \text{Ohms} + /-5\%$, with one end connected to $+28 \, \text{VDC}$. The DMM resource will be used to measure resistance UL= $340 \, \text{OHM}$, LL= $308 \, \text{OHM}$.

From ID P	20-2 (DMM-HI)	to	ID	A1P15.49
From ID A	.1J15.49	to	ID	A1J8.28

Date: 04 March 2016

From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID AlJ6.23	to ID BUS 2
From ID BUS 2	to ID A1J8.31
From ID A1P8.31	to ID P10-44 (S301-69)
From ID P10-141 (S301-70)	to ID A1P8.2
From ID A1J8.2	to ID A1J1.3
From ID A1P1.3	to ID P1-10 (DC4-HI)
From +28V	to ID A1J4.9
From ID A1P4.9	to ID R111.2
From ID R111.1	to ID A1P4.15
From ID AlJ4.15	to ID A1J7.23
From ID A1P7.23	to ID P10-7 (S301-47)
From ID P10-73 (S301-48)	to ID A1P8.47
From ID AlJ8.47	to ID BUS 5
From ID BUS 5	to ID A1J6.47
From ID A1P6.47	to ID P10-137 (S503-7)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 124

Description:

This step verifies the value of ITA resistor R109 is 698 Ohms+/-5%, with one end connected to +28VDC. The DMM resource will be used to measure resistance UL= 733 OHM, LL= 663 OHM.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From ID BUS 2	to ID A1J8.31
From ID A1P8.31	to ID P10-44 (S301-69)
From ID P10-141 (S301-70)	to ID A1P8.2
From ID A1J8.2	to ID A1J1.3
From ID A1P1.3	to ID P1-10 (DC4-HI)
From +28V	to ID A1J4.9
From ID A1P4.9	to ID R109.2
From ID R109.1	to ID A1P4.16
From ID A1J4.16	to ID A1J7.24
From ID A1P7.24	to ID P10-42 (S301-49)
From ID P10-171 (S301-50)	to ID A1P8.48
From ID A1J8.48	to ID BUS 6
From ID BUS 6	to ID AlJ6.38

Date: 04 March 2016

From ID A1P6.38 to ID P10-170 (S503-8) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 125

Description:

This step verifies the value of ITA resistor R110 is $80.6 \, \text{Ohms+/-5}$, with one end connected to Ground. The DMM resource will be used to measure resistance UL= $84.6 \, \text{OHM}$, LL= $76.6 \, \text{OHM}$.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-137 (S503-7) From ID A1J6.47	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.47 to ID BUS 5
From ID BUS 5 From ID A1P8.49 From ID P10-234 (S301-51) From ID A1J7.25 From ID A1P4.17 From ID R110.2 From ID R110.2 From ID A1J4.21 From ID A1J4.22	to ID A1J8.49 to ID P10-202 (S301-52) to ID A1P7.25 to ID A1J4.17 to ID R110.1 to ID A1P4.21 to ID A1P4.22 to GROUND to GROUND
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P7.38 From ID P10-229 (S301-24) From ID A1J7.36	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 126

Description:

This step verifies the value of ITA resistor R108 is $698 \, \text{Ohms} + /-5\%$, with one end connected to Ground. The DMM resource will be used to measure resistance UL= $733 \, \text{OHM}$, LL= $663 \, \text{OHM}$.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID BUS 6	to ID A1J8.50
From ID A1P8.50	to ID P10-138 (S301-54)
From ID P10-9 (S301-53)	to ID A1P7.26
From ID A1J7.26	to ID A1J4.18

Date: 04 March 2016

From ID A1P4.18 to ID R108.1 from ID R108.2 to ID A1P4.10 from ID A1J4.10 to GROUND

From ID P20-3 (DMM-LO) to ID A1P15.50 from ID A1J15.50 to ID A1J7.38 from ID A1P7.38 to ID P10-130 (S301-23) from ID P10-229 (S301-24) to ID A1P7.36 from ID A1J7.36 to GROUND

Step 127

Description:

This step verifies the value of ITA resistor R11 is $120 \, \text{Ohms+/-5}$ %. The DMM resource will be used to measure resistance UL= $126 \, \text{OHM}$, LL= $114 \, \text{OHM}$.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From ID R11.2	to ID A1J8.23
From ID A1P8.23	to ID P10-179 (S301-94)
From ID P10-145 (S301-93)	to ID A1P8.46
From ID A1J8.46	to ID BUS 6
From ID BUS 6	to ID A1J6.38
From ID A1P6.38	to ID P10-170 (S503-8)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 128

Description:

This step verifies the connection from S501-3 to BUS 1 and from S501-4 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13

Date: 04 March 2016

From ID AlJ6.13	to ID BUS 1
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-12 (S503-4) From ID A1J6.23	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.23 to ID BUS 2
From ID P23-12 (ARB-OUT) From ID A1J24.1 From ID A1P6.5 From ID P10-164 (S501-3) From ID A1J7.29	to ID A1P24.1 to ID A1J6.5 to ID P10-194 (S501-1) to ID A1P7.29 to ID BUS 1
From ID P23-12 (ARB-OUT) From ID A1J24.1 From ID A1P6.5 From ID P10-195 (S501-4) From ID A1J7.33	to ID A1P24.1 to ID A1J6.5 to ID P10-194 (S501-1) to ID A1P7.33 to ID BUS 2

Step 129

Description:

This step verifies the connection from S501-5 to BUS 3 and from S501-6 to BUS 4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.31 to ID BUS 3
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-74 (S503-6) From ID A1J6.39	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.39 to ID BUS 4
From ID P23-12 (ARB-OUT) From ID A1J24.1 From ID A1P6.5 From ID P10-225 (S501-5) From ID A1J7.37	to ID A1P24.1 to ID A1J6.5 to ID P10-194 (S501-1) to ID A1P7.37 to ID BUS 3
From ID P23-12 (ARB-OUT) From ID A1J24.1 From ID A1P6.5 From ID P10-193 (S501-6) From ID A1J7.41	to ID A1P24.1 to ID A1J6.5 to ID P10-194 (S501-1) to ID A1P7.41 to ID BUS 4

Date: 04 March 2016

Step 130

Description:

This step verifies the connection from S501-7 to BUS 5 and from S501-8 to BUS 6. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-137 (S503-7) From ID A1J6.47	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.47 to ID BUS 5
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-170 (S503-8) From ID A1J6.38	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.38 to ID BUS 6
From ID P23-12 (ARB-OUT) From ID A1J24.1 From ID A1P6.5 From ID P10-161 (S501-7) From ID A1J7.45	to ID A1P24.1 to ID A1J6.5 to ID P10-194 (S501-1) to ID A1P7.45 to ID BUS 5
From ID P23-12 (ARB-OUT) From ID A1J24.1 From ID A1P6.5 From ID P10-129 (S501-8) From ID A1J7.47	to ID A1P24.1 to ID A1J6.5 to ID P10-194 (S501-1) to ID A1P7.47 to ID BUS 6

Step 131

Description:

This step verifies the connection from S501-9 to BUS 7 and from S501-10 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-173 (S503-9)	to ID A1P6.28
From ID A1J6.28	to ID BUS 7
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-205 (S503-10)	to ID A1P6.16
From ID A1J6.16	to ID BUS 8

Date: 04 March 2016

From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID A1J24.1	to ID A1J6.5
From ID A1P6.5	to ID P10-194 (S501-1)
From ID P10-68 (S501-9)	to ID A1P7.49
From ID A1J7.49	to ID BUS 7
From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID A1J24.1	to ID A1J6.5
From ID A1P6.5	to ID P10-194 (S501-1)
From ID P10-36 (S501-10)	to ID A1P7.50
From ID A1J7.50	to ID BUS 8

Step 132

Description:

This step verifies the connection from S502-3 to BUS 1 and from S502-4 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From	ID P20-2 (DMM-HI)	to	ID	A1P15.49
	ID A1J15.49	to	ID	A1J8.28
	ID A1P8.28			P10-203 (S503-1)
From	ID P10-77 (S503-3)			A1P6.13
	ID A1J6.13	to	ID	BUS 1
From	ID P20-3 (DMM-LO)	to	ID	A1P15.50
From	ID A1J15.50	to	ID	A1J8.26
From	ID A1P8.26	to	ID	P10-139 (S503-2)
From	ID P10-12 (S503-4)	to	ID	A1P6.23
From	ID A1J6.23	to	ID	BUS 2
_				-1-10 1
	ID P19-2 (DSO-IN2)			
_	ID A1J18.1			R25.1
	ID R25.2			A1J6.2
_	ID A1P6.2			P10-39 (S502-1)
From	ID P10-168 (S502-3)	to	ID	A1P7.31
From	ID A1J7.31	to	ID	BUS 1
From	ID P19-2 (DSO-IN2)	to	ID	A1P18.1
	ID A1J18.1			A1J6.2
	ID A1P6.2			P10-39 (S502-1)
_	ID P10-72 (S502-4)			A1P7.35
	ID A1J7.35			BUS 2
_		_		

Step 133

Description:

This step verifies the connection from S502-5 to BUS 3 and from S502-6 to BUS 4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Date: 04 March 2016

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.31 to ID BUS 3
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-74 (S503-6) From ID A1J6.39	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.39 to ID BUS 4
From ID P19-2 (DSO-IN2) From ID A1J18.1 From ID A1P6.2 From ID P10-6 (S502-5) From ID A1J7.39	to ID A1P18.1 to ID A1J6.2 to ID P10-39 (S502-1) to ID A1P7.39 to ID BUS 3
From ID P19-2 (DSO-IN2) From ID A1J18.1 From ID A1P6.2 From ID P10-38 (S502-6) From ID A1J7.43	to ID A1P18.1 to ID A1J6.2 to ID P10-39 (S502-1) to ID A1P7.43 to ID BUS 4

Step 134

Description:

This step verifies the connection from S502-7 to BUS 5 and from S502-8 to BUS 6. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P19-2 (DSO-IN2)	to ID A1P18.1
From ID A1J18.1	to ID A1J6.2
From ID A1P6.2	to ID P10-39 (S502-1)
From ID P10-230 (S502-7)	to ID A1P6.46
From ID A1J6.46	to ID BUS 5
From ID P19-2 (DSO-IN2)	to ID A1P18.1
From ID AlJ18.1	to ID A1J6.2
From ID A1P6.2	to ID P10-39 (S502-1)
From ID P10-5 (S502-8)	to ID A1P6.36

Date: 04 March 2016

From ID AlJ6.36 to ID BUS 6

Step 135

Description:

This step verifies the connection from S502-9 to BUS 7 and from S502-10 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-173 (S503-9) From ID A1J6.28	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.28 to ID BUS 7
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-205 (S503-10) From ID A1J6.16	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.16 to ID BUS 8
From ID P19-2 (DSO-IN2) From ID A1J18.1 From ID A1P6.2 From ID P10-232 (S502-9) From ID A1J6.26	to ID A1P18.1 to ID A1J6.2 to ID P10-39 (S502-1) to ID A1P6.26 to ID BUS 7
From ID P19-2 (DSO-IN2) From ID A1J18.1 From ID A1P6.2 From ID P10-105 (S502-10) From ID A1J6.14	to ID A1P18.1 to ID A1J6.2 to ID P10-39 (S502-1) to ID A1P6.14 to ID BUS 8

Step 136

Description:

This step verifies the connection from S504-3 to BUS 1 and from S504-4 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23

Date: 04 March 2016

From ID	A1J6.23	to	ID	BUS 2	
From ID	P19-1 (DSO-IN1)	to	ID	A1P17.1	
From ID	A1J17.1	to	ID	A1J6.1	
From ID	A1P6.1	to	ID	P10-177	(S504-1)
From ID	P10-18 (S504-3)	to	ID	A1P6.15	
From ID	A1J6.15	to	ID	BUS 1	
From ID	P19-1 (DSO-IN1)	to	ID	A1P17.1	
From ID	A1J17.1	to	ID	A1J6.1	
From ID	A1P6.1	to	ID	P10-177	(S504-1)
From ID	P10-17 (S504-4)	to	ID	A1P6.25	
From ID	A1J6.25	to	ID	BUS 2	

Step 137

Description:

This step verifies the connection from S504-5 to BUS 3 and from S504-6 to BUS 4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
,	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-74 (S503-6)	to ID A1P6.39
From ID A1J6.39	to ID BUS 4
From ID P19-1 (DSO-IN1)	to ID A1P17.1
From ID A1J17.1	to ID A1J6.1
From ID A1P6.1	to ID P10-177 (S504-1)
From ID P10-176 (S504-5)	to ID A1P6.33
From ID A1J6.33	to ID BUS 3
From ID P19-1 (DSO-IN1)	to ID A1P17.1
From ID A1J17.1	to ID A1J6.1
From ID A1P6.1	to ID P10-177 (S504-1)
From ID P10-16 (S504-6)	to ID A1P6.41
From ID A1J6.41	to ID BUS 4

Step 138

Description:

This step verifies the connection from S504-7 to BUS 5 and from S504-8 to BUS 6. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Date: 04 March 2016

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-137 (S503-7) From ID A1J6.47	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.47 to ID BUS 5
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-170 (S503-8) From ID A1J6.38	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.38 to ID BUS 6
From ID P19-1 (DSO-IN1) From ID A1J17.1 From ID A1P6.1 From ID P10-207 (S504-7) From ID A1J6.49	to ID A1P17.1 to ID A1J6.1 to ID P10-177 (S504-1) to ID A1P6.49 to ID BUS 5
From ID P19-1 (DSO-IN1) From ID A1J17.1 From ID A1P6.1 From ID P10-144 (S504-8) From ID A1J6.40	to ID A1P17.1 to ID A1J6.1 to ID P10-177 (S504-1) to ID A1P6.40 to ID BUS 6

Step 139

Description:

This step verifies the connection from S504-9 to BUS 7 and from S504-10 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-173 (S503-9) From ID A1J6.28	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.28 to ID BUS 7
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-205 (S503-10) From ID A1J6.16	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.16 to ID BUS 8
From ID P19-1 (DSO-IN1) From ID A1J17.1 From ID A1P6.1 From ID P10-211 (S504-9) From ID A1J6.30	to ID A1P17.1 to ID A1J6.1 to ID P10-177 (S504-1) to ID A1P6.30 to ID BUS 7
From ID P19-1 (DSO-IN1)	to ID A1P17.1

Date: 04 March 2016

From ID A1J17.1 to ID A1J6.1 From ID A1P6.1 to ID P10-177 (S504-1) From ID P10-146 (S504-10) to ID A1P6.18 From ID A1J6.18 to ID BUS 8

Step 140

Description:

This step verifies the connection from S505-3 to BUS 1 and from S505-4 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From ID P19-17 (CT-ARM) From ID A1P20.1 From ID A1P6.6 From ID P10-180 (S505-3) From ID A1J6.17	to ID A1P20.1 to ID A1J6.6 to ID P10-52 (S505-1) to ID A1P6.17 to ID BUS 1
From ID P19-17 (CT-ARM) From ID A1P20.1 From ID A1P6.6 From ID P10-147 (S505-4) From ID A1J6.27	to ID A1P20.1 to ID A1J6.6 to ID P10-52 (S505-1) to ID A1P6.27 to ID BUS 2

Step 141

Description:

This step verifies the connection from S505-5 to BUS 3 and from S505-6 to BUS 4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI)	to ID AlP15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID P20-3 (DMM-LO)	to ID A1P15.50

Date: 04 March 2016

From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-74 (S503-6)	to ID A1P6.39
From ID A1J6.39	to ID BUS 4
From ID P19-17 (CT-ARM)	to ID A1P20.1
From ID A1P20.1	to ID AlJ6.6
From ID A1P6.6	to ID P10-52 (S505-1)
From ID P10-212 (S505-5)	to ID A1P6.35
From ID A1J6.35	to ID BUS 3
From ID P19-17 (CT-ARM)	to ID A1P20.1
From ID A1P20.1	to ID A1J6.6
From ID A1P6.6	to ID P10-52 (S505-1)
From ID P10-84 (S505-6)	to ID A1P6.43
From ID A1J6.43	to ID BUS 4

Step 142

Description:

This step verifies the connection from S505-7 to BUS 5 and from S505-8 to BUS 6. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From I	D P20-2 (DMM-HI)	to	ID	A1P15.49
From I	D A1J15.49	to	ID	A1J8.28
From I	D A1P8.28	to	ID	P10-203 (S503-1)
From I	D P10-137 (S503-7)	to	ID	A1P6.47
From I	D A1J6.47	to	ID	BUS 5
From I	D P20-3 (DMM-LO)	to	ID	A1P15.50
From I	D A1J15.50	to	ID	A1J8.26
From I	D A1P8.26	to	ID	P10-139 (S503-2)
From I	D P10-170 (S503-8)	to	ID	A1P6.38
From I	D A1J6.38	to	ID	BUS 6
From I	D P19-17 (CT-ARM)	to	ID	A1P20.1
From I	D A1P20.1	to	ID	A1J6.6
From I	D A1P6.6	to	ID	P10-52 (S505-1)
From I	D P10-115 (S505-7)	to	ID	A1P6.50
From I	D A1J6.50	to	ID	BUS 5
From I	D P19-17 (CT-ARM)	to	ID	A1P20.1
From I	D A1P20.1	to	ID	A1J6.6
From I	D A1P6.6	to	ID	P10-52 (S505-1)
From I	D P10-20 (S505-8)	to	ID	A1P6.42
From I	D A1J6.42	to	ID	BUS 6

Step 143

Description:

This step verifies the connection from S505-9 to BUS 7 and from

Date: 04 March 2016

\$5505--10\$ to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From I	D P20-2 (DMM-HI)	to	ID	A1P15.49
From I	D A1J15.49	to	ID	A1J8.28
From I	D A1P8.28	to	ID	P10-203 (S503-1)
From I	D P10-173 (S503-9)	to	ID	A1P6.28
From I	D A1J6.28	to	ID	BUS 7
From I	D P20-3 (DMM-LO)	to	ID	A1P15.50
From I	D A1J15.50	to	ID	A1J8.26
From I	D A1P8.26	to	ID	P10-139 (S503-2)
From I	D P10-205 (S503-10)	to	ID	A1P6.16
From I	D A1J6.16	to	ID	BUS 8
From I	D P19-17 (CT-ARM)	to	ID	A1P20.1
From I	D A1P20.1	to	ID	A1J6.6
From I	D A1P6.6	to	ID	P10-52 (S505-1)
From I	D P10-116 (S505-9)	to	ID	A1P6.32
From I	D A1J6.32	to	ID	BUS 7
From I	D P19-17 (CT-ARM)	to	ID	A1P20.1
From I	D A1P20.1	to	ID	A1J6.6
From I	D A1P6.6	to	ID	P10-52 (S505-1)
From I	D P10-19 (S505-10)	to	ID	A1P6.20
From I	D A1J6.20	to	ID	BUS 8

Step 144

Description:

This step verifies the connection from S506-3 to BUS 1 and from S506-4 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-12 (S503-4) From ID A1J6.23	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.23 to ID BUS 2
From ID P12-76 (S701-1) From ID A1J12.50 From ID A1P10.3 From ID P11-164 (S506-3)	to ID A1P12.50 to ID A1J10.3 to ID P11-194 (S506-1) to ID A1P9.23

Date: 04 March 2016

From ID A1J9.23 to ID BUS 1

From ID P12-76 (S701-1) to ID A1P12.50

From ID A1J12.50 to ID A1J10.3

From ID A1P10.3 to ID P11-194 (S506-1)

From ID P11-195 (S506-4) to ID A1P9.33

From ID A1J9.33 to ID BUS 2

Step 145

Description:

This step verifies the connection from S506-5 to BUS 3 and from S506-6 to BUS 4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From	ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From	ID	A1J15.49	to	ID	A1J8.28
From	ID	A1P8.28	to	ID	P10-203 (S503-1)
From	ID	P10-10 (S503-5)	to	ID	A1P6.31
From	ID	A1J6.31	to	ID	BUS 3
From	ID	P20-3 (DMM-LO)	to	ID	A1P15.50
From	ID	A1J15.50	to	ID	A1J8.26
From	ID	A1P8.26	to	ID	P10-139 (S503-2)
From	ID	P10-74 (S503-6)	to	ID	A1P6.39
From	ID	A1J6.39	to	ID	BUS 4
From	ID	P12-76 (S701-1)	to	ID	A1P12.50
From	ID	A1J12.50	to	ID	A1J10.3
From	ID	A1P10.3	to	ID	P11-194 (S506-1)
From	ID	P11-225 (S506-5)	to	ID	A1P9.45
From	ID	A1J9.45	to	ID	BUS 3
From	ID	P12-76 (S701-1)	to	ID	A1P12.50
From	ID	A1J12.50	to	ID	A1J10.3
From	ID	A1P10.3	to	ID	P11-194 (S506-1)
From	ID	P11-193 (S506-6)	to	ID	A1P9.50
From	ID	A1J9.50	to	ID	BUS 4

Step 146

Description:

This step verifies the connection from S506-7 to BUS 5 and from S506-8 to BUS 6. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49	
From ID	A1J15.49	to	ID	A1J8.28	
From ID	A1P8.28	to	ID	P10-203	(S503-1)
From ID	P10-137 (S503-7)	to	ID	A1P6.47	

Date: 04 March 2016

From ID AlJ6.47	to ID BUS 5
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-170 (S503-8) From ID A1J6.38	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.38 to ID BUS 6
From ID P12-76 (S701-1) From ID A1J12.50 From ID A1P10.3 From ID P11-161 (S506-7) From ID A1J9.40	to ID A1P12.50 to ID A1J10.3 to ID P11-194 (S506-1) to ID A1P9.40 to ID BUS 5
From ID P12-76 (S701-1) From ID A1J12.50 From ID A1P10.3 From ID P11-129 (S506-8) From ID A1J9.30	to ID A1P12.50 to ID A1J10.3 to ID P11-194 (S506-1) to ID A1P9.30 to ID BUS 6

Step 147

Description:

This step verifies the connection from S506-9 to BUS 7 and from S506-10 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-173 (S503-9) From ID A1J6.28	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.28 to ID BUS 7
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-205 (S503-10) From ID A1J6.16	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.16 to ID BUS 8
From ID P12-76 (S701-1) From ID A1J12.50 From ID A1P10.3 From ID P11-68 (S506-9) From ID A1J9.20	to ID A1P12.50 to ID A1J10.3 to ID P11-194 (S506-1) to ID A1P9.20 to ID BUS 7
From ID P12-76 (S701-1) From ID A1J12.50 From ID A1P10.3 From ID P11-36 (S506-10) From ID A1J9.10	to ID A1P12.50 to ID A1J10.3 to ID P11-194 (S506-1) to ID A1P9.10 to ID BUS 8

Date: 04 March 2016

Step 148

Description:

This step verifies the connection from S507-3 to BUS 1 and from S507-4 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-12 (S503-4) From ID A1J6.23	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.23 to ID BUS 2
From ID P12-20 (S201-3) From ID A1J12.46 From ID A1P10.2 From ID P11-168 (S507-3) From ID A1J9.17	to ID A1P12.46 to ID A1J10.2 to ID P11-39 (S507-1) to ID A1P9.17 to ID BUS 1
From ID P12-20 (S201-3) From ID A1J12.46 From ID A1P10.2 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.46 to ID A1J10.2 to ID P11-39 (S507-1) to ID A1P9.27 to ID BUS 2

Step 149

Description:

This step verifies the connection from S507-5 to BUS 3 and from S507-6 to BUS 4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-10 (S503-5)	to	ID	A1P6.31
From ID	A1J6.31	to	ID	BUS 3
From ID	P20-3 (DMM-LO)	to	ID	A1P15.50
From ID	A1J15.50	to	ID	A1J8.26
From ID	A1P8.26	to	ID	P10-139 (S503-2)
From ID	P10-74 (S503-6)	to	ID	A1P6.39
From ID	A1J6.39	to	ID	BUS 4

Date: 04 March 2016

From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-6 (S507-5)	to ID A1P9.39
From ID A1J9.39	to ID BUS 3
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-38 (S507-6)	to ID A1P9.44
From ID A1J9.44	to ID BUS 4

Step 150

Description:

This step verifies the connection from S507-7 to BUS 5 and from S507-8 to BUS 6. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-230 (S507-7)	to ID A1P9.34
From ID A1J9.34	to ID BUS 5
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-5 (S507-8)	to ID A1P9.24
From ID A1J9.24	to ID BUS 6

Step 151

Description:

This step verifies the connection from S507-9 to BUS 7 and from S507-10 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI) to ID A1P15.49

Date: 04 March 2016

From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-173 (S503-9)	to ID A1P6.28
From ID A1J6.28	to ID BUS 7
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-205 (S503-10)	to ID A1P6.16
From ID A1J6.16	to ID BUS 8
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-232 (S507-9)	to ID A1P9.14
From ID A1J9.14	to ID BUS 7
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-105 (S507-10)	to ID A1P9.4
From ID A1J9.4	to ID BUS 8

Step 152

Description:

This step verifies the connection from S508-3 to BUS 1 and from S508-4 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID P12-16 (S201-1) From ID A1J12.42 From ID A1P10.6 From ID P11-12 (S508-4)	to ID A1P12.42 to ID A1J10.6 to ID P11-203 (S508-1) to ID A1P9.25

Date: 04 March 2016

From ID AlJ9.25 to ID BUS 2

Step 153

Description:

This step verifies the connection from S508-5 to BUS 3 and from S508-6 to BUS 4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.31 to ID BUS 3
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-74 (S503-6)	to ID A1P6.39
From ID A1J6.39	to ID BUS 4
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-10 (S508-5)	to ID A1P9.37
From ID A1J9.37	to ID BUS 3
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-74 (S508-6)	to ID A1P9.42
From ID A1J9.42	to ID BUS 4

Step 154

Description:

This step verifies the connection from S508-7 to BUS 5 and from S508-8 to BUS 6. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-137 (S503-7)	to	ID	A1P6.47
From ID	A1J6.47	to	ID	BUS 5
From ID	P20-3 (DMM-LO)	to	ID	A1P15.50
From ID	A1J15.50	to	ID	A1J8.26
From ID	A1P8.26	to	ID	P10-139 (S503-2)
D TD	D10 170 /CE02 0\	+ ~	TD	71DC 20
From ID	P10-170 (S503-8)	LO	TD	A1P6.38

Date: 04 March 2016

From ID A1J6.38	to ID BUS 6
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-137 (S508-7)	to ID A1P9.32
From ID AlJ9.32	to ID BUS 5
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-170 (S508-8)	to ID A1P9.22
From ID A1J9.22	to ID BUS 6

Step 155

Description:

This step verifies the connection from S508-9 to BUS 7 and from S508-10 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-173 (S503-9)	to ID A1P6.28
From ID A1J6.28	to ID BUS 7
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-205 (S503-10)	to ID A1P6.16
From ID AlJ6.16	to ID BUS 8
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	
110 12 1111110.0	to ID P11-203 (S508-1)
From ID P11-173 (S508-9)	to ID P11-203 (S508-1) to ID A1P9.12
From ID P11-173 (S508-9)	to ID A1P9.12
From ID P11-173 (S508-9)	to ID A1P9.12
From ID P11-173 (S508-9) From ID A1J9.12	to ID A1P9.12 to ID BUS 7
From ID P11-173 (S508-9) From ID A1J9.12 From ID P12-16 (S201-1)	to ID A1P9.12 to ID BUS 7 to ID A1P12.42
From ID P11-173 (S508-9) From ID A1J9.12 From ID P12-16 (S201-1) From ID A1J12.42	to ID A1P9.12 to ID BUS 7 to ID A1P12.42 to ID A1J10.6 to ID P11-203 (S508-1)

Step 156

Description:

This step verifies the connection from S507-1 to S201-3 and from S508-1 to S201-1. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Date: 04 March 2016

Connection Path is as follows:

From ID From ID From ID	P20-2 (DMM-HI) AlJ15.49 AlP8.28 P10-77 (S503-3) AlJ6.13	to to to	ID ID ID	A1P15.49 A1J8.28 P10-203 (S503-1) A1P6.13 BUS 1
From ID From ID From ID	P20-3 (DMM-LO) A1J15.50 A1P8.26 P10-12 (S503-4) A1J6.23	to to to	ID ID ID	A1P15.50 A1J8.26 P10-139 (S503-2) A1P6.23 BUS 2
From ID From ID From ID	P12-20 (S201-3) A1J12.46 A1P10.2 P11-168 (S507-3) A1J9.17	to to to	ID ID ID	A1P12.46 A1J10.2 P11-39 (S507-1) A1P9.17 BUS 1
From ID From ID From ID	P12-16 (S201-1) A1J12.42 A1P10.6 P11-12 (S508-4) A1J9.25	to to to	ID ID ID	A1P12.42 A1J10.6 P11-203 (S508-1) A1P9.25 BUS 2
	P12-79 (S201-5) A1J13.1			A1P13.1 J1B-14A
	P12-79 (S201-5) A1J13.1			A1P13.1 J1B-14A

Step 157

Description:

This step verifies the connection from S507-2 to S201-4 and from S508-2 to S201-2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From 1	ID P20-2 (DMM-HI)	to	ID	A1P15.49	
From 1	ID A1J15.49	to	ID	A1J8.28	
From 1	ID A1P8.28	to	ID	P10-203 ((S503-1)
From 1	ID P10-77 (S503-3)	to	ID	A1P6.13	
From 1	ID A1J6.13	to	ID	BUS 1	
From 1	ID P20-3 (DMM-LO)	to	ID	A1P15.50	
From 1	ID A1J15.50	to	ID	A1J8.26	
From 1	ID A1P8.26	to	ID	P10-139 ((S503-2)
From 1	ID P10-12 (S503-4)	to	ID	A1P6.23	
From 1	ID A1J6.23	to	ID	BUS 2	
From 1	ID P12-52 (S201-4)	to	ID	A1P12.44	

Date: 04 March 2016

From ID A1J12.44	to ID A1J10.4
From ID A1P10.4	to ID P11-71 (S507-2)
From ID P11-168 (S507-3)	to ID A1P9.17
From ID A1J9.17	to ID BUS 1
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID P12-47 (S201-6)	to ID A1P13.2
From ID AlJ13.2	to ID J1B-13A
From ID P12-47 (S201-6)	to ID A1P13.2
From ID A1J13.2	to ID J1B-13A

Step 158

Description:

This step verifies the connection from S509-3 to BUS 1 and from S509-4 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-12 (S503-4) From ID A1J6.23	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.23 to ID BUS 2
From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10 From ID P11-18 (S509-3) From ID A1J9.19	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.19 to ID BUS 1
From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2

Step 159

Description:

Date: 04 March 2016

This step verifies the connection from S509-5 to BUS 3 and from S509-6 to BUS 4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID AlJ15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID AlJ6.31	to ID BUS 3
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-74 (S503-6)	to ID A1P6.39
From ID AlJ6.39	to ID BUS 4
From ID P12-59 (S202-1)	to ID A1P12.38
From ID P12-59 (S202-1) From ID A1J12.38	to ID A1P12.38 to ID A1J10.10
From ID A1J12.38	to ID A1J10.10
From ID A1J12.38 From ID A1P10.10	to ID A1J10.10 to ID P11-177 (S509-1)
From ID A1J12.38 From ID A1P10.10 From ID P11-176 (S509-5)	to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.41
From ID A1J12.38 From ID A1P10.10 From ID P11-176 (S509-5)	to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.41
From ID A1J12.38 From ID A1P10.10 From ID P11-176 (S509-5) From ID A1J9.41	to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.41 to ID BUS 3
From ID A1J12.38 From ID A1P10.10 From ID P11-176 (S509-5) From ID A1J9.41 From ID P12-59 (S202-1)	to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.41 to ID BUS 3 to ID A1P12.38
From ID A1J12.38 From ID A1P10.10 From ID P11-176 (S509-5) From ID A1J9.41 From ID P12-59 (S202-1) From ID A1J12.38	to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.41 to ID BUS 3 to ID A1P12.38 to ID A1J10.10

Step 160

Description:

This step verifies the connection from S509-7 to BUS 5 and from S509-8 to BUS 6. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-137 (S503-7) From ID A1J6.47	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.47 to ID BUS 5
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-170 (S503-8) From ID A1J6.38	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.38 to ID BUS 6
From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1)

Date: 04 March 2016

From ID P11-207 (S509-7) to ID A1P9.36 from ID A1J9.36 to ID BUS 5

From ID P12-59 (S202-1) to ID A1P12.38 from ID A1J12.38 to ID A1J10.10 from ID A1P10.10 to ID P11-177 (S509-1) from ID P11-144 (S509-8) to ID A1P9.26 from ID A1J9.26 to ID BUS 6

Step 161

Description:

This step verifies the connection from S509-9 to BUS 7 and from S509-10 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49	to ID A1P15.49 to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-173 (S503-9)	to ID A1P6.28
From ID AlJ6.28	to ID BUS 7
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-205 (S503-10)	to ID A1P6.16
From ID AlJ6.16	to ID BUS 8
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-211 (S509-9)	to ID A1P9.16
From ID A1J9.16	to ID BUS 7
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-146 (S509-10)	to ID A1P9.6
From ID A1J9.6	to ID BUS 8

Step 162

Description:

This step verifies the connection from S510-3 to BUS 1 and from S510-4 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49	
From ID	A1J15.49	to	ID	A1J8.28	
From ID	A1P8.28	to	ID	P10-203	(S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13	

Date: 04 March 2016

From ID AlJ6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From ID P13-93 (S202-3)	to ID A1P14.49
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-180 (S510-3)	to ID A1P9.21
From ID A1J9.21	to ID BUS 1
From ID P13-93 (S202-3)	to ID A1P14.49
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2

Step 163

Description:

This step verifies the connection from S510-5 to BUS 3 and from S510-6 to BUS 4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI) to ID A1P15.49

From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-10 (S503-5)	to	ID	A1P6.31
From ID	A1J6.31	to	ID	BUS 3
From ID	P20-3 (DMM-LO)	to	ID	A1P15.50
From ID	A1J15.50	to	ID	A1J8.26
From ID	A1P8.26	to	ID	P10-139 (S503-2)
From ID	P10-74 (S503-6)	to	ID	A1P6.39
From ID	A1J6.39	to	ID	BUS 4
From ID	P13-93 (S202-3)	to	ID	A1P14.49
From ID	A1J14.49	to	ID	A1J10.48
From ID	A1P10.48	to	ID	P11-52 (S510-1)
From ID	P11-212 (S510-5)	to	ID	A1P9.43
From ID	A1J9.43	to	ID	BUS 3
From ID	P13-93 (S202-3)	to	ID	A1P14.49
From ID	A1J14.49	to	ID	A1J10.48
From ID	A1P10.48	to	ID	P11-52 (S510-1)
From ID	P11-84 (S510-6)	to	ID	A1P9.48
From ID	A1J9.48	to	ID	BUS 4

Date: 04 March 2016

Step 164

Description:

This step verifies the connection from S510-7 to BUS 5 and from S510-8 to BUS 6. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-137 (S503-7) From ID A1J6.47	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.47 to ID BUS 5
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-170 (S503-8) From ID A1J6.38	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.38 to ID BUS 6
From ID P13-93 (S202-3) From ID A1J14.49 From ID A1P10.48 From ID P11-115 (S510-7) From ID A1J9.38	to ID A1P14.49 to ID A1J10.48 to ID P11-52 (S510-1) to ID A1P9.38 to ID BUS 5
From ID P13-93 (S202-3) From ID A1J14.49 From ID A1P10.48 From ID P11-20 (S510-8) From ID A1J9.28	to ID A1P14.49 to ID A1J10.48 to ID P11-52 (S510-1) to ID A1P9.28 to ID BUS 6

Step 165

Description:

This step verifies the connection from S510-9 to BUS 7 and from S510-10 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-173 (S503-9)	to ID A1P6.28
From ID A1J6.28	to ID BUS 7
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-205 (S503-10)	to ID A1P6.16
From ID A1J6.16	to ID BUS 8

Date: 04 March 2016

From ID P13-93 (S202-3)	to ID A1P14.49
From ID AlJ14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-116 (S510-9)	to ID A1P9.18
From ID AlJ9.18	to ID BUS 7
From ID P13-93 (S202-3)	to ID A1P14.49
From ID AlJ14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-19 (S510-10)	to ID A1P9.8
From ID A1J9.8	to ID BUS 8

Step 166

Description:

This step verifies the connection from S509-1 to S202-1 and from S510-1 to S202-3. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1J8.28 to ID P10-203 (S503-1)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From ID AlJ12.38	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.19 to ID BUS 1
From ID P13-93 (S202-3) From ID A1J14.49 From ID A1P10.48 From ID P11-147 (S510-4) From ID A1J9.31	to ID A1P14.49 to ID A1J10.48 to ID P11-52 (S510-1) to ID A1P9.31 to ID BUS 2
From ID P12-89 (S202-5)	to ID A1P12.25
From ID A1J12.25	to ID J2B-8C
From ID P12-89 (S202-5)	to ID A1P12.25
From ID A1J12.25	to ID J2B-8C

Date: 04 March 2016

Step 167

Description:

This step verifies the connection from S509-2 to S202-2 and from S510-2 to S202-4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID A1J15.49	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.23 to ID BUS 2
From ID A1J12.36	to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.19 to ID BUS 1
	to ID A1J10.50 to ID P11-244 (S510-2)
From ID P12-25 (S202-6) From ID A1J12.26	to ID A1P12.26 to ID J2B-8B
From ID P12-25 (S202-6) From ID A1J12.26	to ID A1P12.26 to ID J2B-8B

Step 168

Description:

This step verifies the connection from S401-2 to BUS 1 and from S401-3 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

Date: 04 March 2016

From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-12 (S503-4) From ID A1J6.23	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.23 to ID BUS 2
From INSTR-RTN From ID A1P6.7 From ID P10-190 (S401-2) From ID A1J6.19	to ID A1J6.7 to ID P10-93 (S401-1) to ID A1P6.19 to ID BUS 1
From INSTR-RTN From ID A1P6.7 From ID P10-254 (S401-3) From ID A1J6.29	to ID A1J6.7 to ID P10-93 (S401-1) to ID A1P6.29 to ID BUS 2

Step 169

Description:

This step verifies the connection from S401-4 to BUS 3 and from S401-5 to BUS 4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-74 (S503-6)	to ID A1P6.39
From ID A1P6.7	to ID P10-93 (S401-1)
From ID P10-28 (S401-4)	to ID A1P6.37
From ID A1J6.37	to ID BUS 3
From INSTR-RTN	to ID A1J6.7
From ID A1P6.7	to ID P10-93 (S401-1)
From ID P10-220 (S401-5)	to ID A1P6.45
From ID A1J6.45	to ID BUS 4

Step 170

Description:

This step verifies the connection from S402-2 to BUS 5 and from S402-4 to BUS 7. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Date: 04 March 2016

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-137 (S503-7) From ID A1J6.47	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.47 to ID BUS 5
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-173 (S503-9)	to ID A1P6.28
From ID A1J6.28	to ID BUS 7
From INSTR-RTN	to ID A1J6.9
From ID A1P6.9	to ID P10-94 (S402-1)
From ID P10-158 (S402-2)	to ID A1P6.48
From ID A1J6.48	to ID BUS 5
From INSTR-RTN	to ID A1J6.9
From ID A1P6.9	to ID P10-94 (S402-1)
From ID P10-61 (S402-4)	to ID A1P6.34
From ID A1J6.34	to ID BUS 7

Step 171

Description:

This step verifies the connection from S401-2 to BUS 1 and from S402-5 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P8.26 From ID P10-205 (S503-10) From ID A1J6.16	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.16 to ID BUS 8
From INSTR-RTN From ID A1P6.7 From ID P10-190 (S401-2) From ID A1J6.19	to ID A1J6.7 to ID P10-93 (S401-1) to ID A1P6.19 to ID BUS 1
From INSTR-RTN From ID A1P6.9 From ID P10-253 (S402-5) From ID A1J6.22	to ID A1J6.9 to ID P10-94 (S402-1) to ID A1P6.22 to ID BUS 8

Date: 04 March 2016

Step 172

Description:

This step verifies the connection from ARB-OUT to S501-1 and ARB-RTN to INSTR-RTN. The DMM resource will be used to measure voltage generated by the ARB-OUT UL= $1.86~\rm Vrms$, LL= $1.68\rm Vrms$.

Connection Path is as follows:

From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID A1J24.1	to ID A1J6.5
From ID A1P6.5	to ID P10-194 (S501-1)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ARB-RTN	to ID AlJ6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID AlJ6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 173

Description:

This step verifies the connection from CT-IN1 to S501-2 and CT-RTN1 to ARB-RTN. The CT-IN1 resource will be used to measure frequency generated by the ARB-OUT UL= $10.5 \, \mathrm{kHz}$, LL= $9.5 \, \mathrm{kHz}$.

From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID A1J24.1	to ID A1J6.5
From ID A1P6.5	to ID P10-194 (S501-1)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1

Date: 04 March 2016

Step 174

Description:

This step verifies the connection from CT-IN2 to S502-2 and CT-RTN2 to ARB-RTN. The CT-IN2 resource will be used to measure frequency generated by the ARB-OUT UL= $10.5 \, \mathrm{kHz}$, LL= $9.5 \, \mathrm{kHz}$.

Connection Path is as follows:

From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID AlJ24.1	to ID A1J6.5
From ID A1P6.5	to ID P10-194 (S501-1)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1

Step 175

Description:

This step verifies the connection from DSO-IN2 to S502-1 and DSO-RTN1 to ARB-RTN. The DSO-IN2 resource will be used to measure voltage generated by the ARB-OUT UL= 6 Vpp, LL= 4 Vpp.

Connection Path is as follows:

From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID A1J24.1	to ID A1J6.5
From ID A1P6.5	to ID P10-194 (S501-1)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-2 (DSO-IN2)	to ID A1P18.1
From ID A1J18.1	to ID R25.1
From ID R25.2	to ID A1J6.2
From ID A1P6.2	L - TD D10 20 (GE00 1)
FION ID AIPO.Z	to ID P10-39 (S502-1)
From ID P10-168 (S502-3)	to ID A1P7.31

Step 176

Description:

This step verifies the RC filter on DSO-IN2 input. The DSO-IN2 resource will be used to measure voltage generated by the ARB-OUT UL= $1.8~\rm Vpp$, LL= $1.4~\rm Vpp$.

From I	D P23-12 (ARB-OUT)	to	ID	A1P24.1
From I	D A1J24.1	to	ID	A1J6.5

Date: 04 March 2016

From ID A1P6.5 From ID P10-164 (S501-3) From ID A1J7.29	to ID P10-194 (S501-1) to ID A1P7.29 to ID BUS 1
From ID P19-2 (DSO-IN2) From ID A1J18.1 From ID R25.2	to ID A1P18.1 to ID R25.1 to ID A1J6.2
From ID A1P6.2	to ID P10-39 (S502-1)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1

Step 177

Description:

This step verifies the connection from DSO-IN1 to S504-1 and DSO-RTN1 to ARB-RTN. The DSO-IN1 resource will be used to measure voltage generated by the ARB-OUT UL= $6\ \text{Vpp}$, LL= $4\ \text{Vpp}$.

Connection Path is as follows:

From ID P23-12 (ARB-OUT) From ID A1J24.1 From ID A1P6.5 From ID P10-164 (S501-3) From ID A1J7.29	to ID A1P24.1 to ID A1J6.5 to ID P10-194 (S501-1) to ID A1P7.29 to ID BUS 1
From ID P19-1 (DSO-IN1) From ID A1J17.1 From ID A1P6.1 From ID P10-18 (S504-3) From ID A1J6.15	to ID A1P17.1 to ID A1J6.1 to ID P10-177 (S504-1) to ID A1P6.15 to ID BUS 1

Step 178

Description:

This step verifies the connection from DSO-TRIGIN to S504-2. The DSO-IN1 resource will be used to measure voltage generated by the ARB-OUT UL= 6 Vpp, LL= 4 Vpp.

From ID P23-12 (ARB-OUT) From ID A1J24.1 From ID A1P6.5 From ID P10-164 (S501-3) From ID A1J7.29	to ID A1P24.1 to ID A1J6.5 to ID P10-194 (S501-1) to ID A1P7.29 to ID BUS 1
From ID P19-1 (DSO-IN1) From ID A1J17.1 From ID A1P6.1 From ID P10-18 (S504-3) From ID A1J6.15	to ID A1P17.1 to ID A1J6.1 to ID P10-177 (S504-1) to ID A1P6.15 to ID BUS 1
From ID P19-3 (DSO-TRIGIN)	to ID A1P19.1

Date: 04 March 2016

From ID A1J19.1 to ID A1J6.3 From ID A1P6.3 to ID P10-242 (S504-2) From ID P10-18 (S504-3) to ID A1P6.15 From ID A1J6.15 to ID BUS 1

Step 179

Description:

This step verifies the connection from CT-ARM to S505-1. This step uses DC2 to gate the CT (previously did not). DC2 is connected to BUS1 for 1 second. The low to high to low transition is used to gate the CT. The CT-IN1 resource will be used to measure frequency generated by the ARB-OUT UL= $10.5 \, \mathrm{kHz}$, LL= $9.5 \, \mathrm{kHz}$.

Connection Path is as follows:

From ID P23-12 (ARB-OUT) From ID A1J24.1 From ID A1P6.5 From ID P10-195 (S501-4) From ID A1J7.33	to ID A1P24.1 to ID A1J6.5 to ID P10-194 (S501-1) to ID A1P7.33 to ID BUS 2
From ID P19-18 (CT-IN1) From ID A1J21.1 From ID A1P6.8 From ID P10-195 (S501-4) From ID A1J7.33	to ID A1P21.1 to ID A1J6.8 to ID P10-162 (S501-2) to ID A1P7.33 to ID BUS 2
From ID P19-17 (CT-ARM) From ID A1P20.1 From ID A1P6.6 From ID P10-180 (S505-3) From ID A1J6.17	to ID A1P20.1 to ID A1J6.6 to ID P10-52 (S505-1) to ID A1P6.17 to ID BUS 1
From ID P1-4 (DC2-HI) From ID A1J1.2 From ID A1P8.4 From ID P10-204 (S301-67) From ID A1J8.29	to ID A1P1.2 to ID A1J8.4 to ID P10-174 (S301-68) to ID A1P8.29 to ID BUS 1

Step 180

Description:

This step verifies the connection from ARB-START-ARM to S505-2. The DSO-IN1 resource will be used to measure voltage generated by the ARB-OUT UL= 6 Vpp, LL= 4 Vpp.

From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID A1J24.1	to ID A1J6.5
From ID A1P6.5	to ID P10-194 (S501-1)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1

Date: 04 March 2016

From ID P19-1 (DSO-IN1)	to ID A1P17.1
From ID A1J17.1	to ID A1J6.1
From ID A1P6.1	to ID P10-177 (S504-1)
From ID P10-18 (S504-3)	to ID A1P6.15
From ID A1J6.15	to ID BUS 1
From ID P23-10 (ARB-STARTARM)	to ID A1P23.1
From ID A1J23.1	to ID A1J6.4
From ID A1P6.4	to ID P10-244 (S505-2)
From ID P10-147 (S505-4)	to ID A1P6.27
From ID A1J6.27	to ID BUS 2

Step 181

Description:

This step verifies the connection from APROBE to DSO-IN1 and APROBE-RTN to DSO-RTN. The DSO-IN1 resource will be used to measure voltage generated by the ARB-OUT UL= $6\ \text{Vpp}$, LL= $4\ \text{Vpp}$.

Connection Path is as follows:

From ID P23-12 (ARB-OUT) From ID A1J24.1 From ID A1P6.5 From ID P10-164 (S501-3) From ID A1J7.29	to ID A1P24.1 to ID A1J6.5 to ID P10-194 (S501-1) to ID A1P7.29 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-79 (S201-5) From ID AlJ13.1	to ID A1P13.1 to ID J1B-14A

2.5 MODULE 2- W1 CDA LOGIC A SPECIFIC ID TEST

Refer to Reference Drawings when diagnosing connection paths.

Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF TEST PWB, A2).pdf and 13020A7101 (CABLE, W1, SCHEMATIC).pdf in section 1.4 during review of the following steps.

Step 201

Description:

This step verifies the wire path from W1 P2-1 to W1 P2-32. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) to ID A1P15.49

Date: 04 March 2016

From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
,	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.1 to ID A1J12.48 to ID P12-44 (S701-2)
From ID P12-68 (S701-4) From ID A1J13.8 From W1 P1B-13D	to ID A1P13.8 to ID J1B-13D to W1 P2-1 (ST J1-1)
From ST_J1-1	to ST_J1-32
From W1 P2-32 (ST J1-32) From ID J1B-6B From ID A1P12.11	to W1 P1B-6B to ID A1J12.11 to ID P12-22 (S201-37)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 202

Description:

This step verifies the wire path from W1 P2-2 to W1 P2-59. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-12 (S503-4) From ID A1J6.23	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.23 to ID BUS 2
From ID BUS 2 From ID A1P9.33 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.33 to ID P11-195 (S506-4) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P12-4 (S701-3) From ID A1J13.7 From W1 P1B-14D From ST_J1-2	to ID A1P13.7 to ID J1B-14D to W1 P2-2 (ST J1-2) to ST_J1-59
From W1 P2-59 (ST J1-59)	to W1 P1A-4C

Date: 04 March 2016

From ID J1A-4C	to ID A1J15.6
From ID A1P15.6	to ID P13-44 (S701-42)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-225 (S506-5)	to ID A1P9.45
From ID A1J9.45	to ID BUS 3
From ID BUS 3 From ID A1P6.31 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.31 to ID P10-10 (S503-5) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 203

Description:

This step verifies the wire path from W1 P2-3 to W1 P2-57. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-74 (S503-6) From ID A1J6.39	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.39 to ID BUS 4
From ID BUS 4 From ID A1P9.50 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.50 to ID P11-193 (S506-6) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P12-36 (S701-5) From ID A1J13.9 From W1 P1B-14E	to ID A1P13.9 to ID J1B-14E to W1 P2-3 (ST J1-3)
From ST_J1-3	to ST_J1-57
From ST_J1-3 From W1 P2-57 (ST J1-57) From ID J1A-7B From ID A1P15.14	to ST_J1-57 to W1 P1A-7B to ID A1J15.14 to ID P13-12 (S701-40)
From W1 P2-57 (ST J1-57) From ID J1A-7B	to W1 P1A-7B to ID A1J15.14

Date: 04 March 2016

From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 204

Description:

This step verifies the wire path from W1 P2-4 to W1 P2-5. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-47 (S201-6) From ID A1J13.2 From W1 P1B-13A	to ID A1P13.2 to ID J1B-13A to W1 P2-4 (ST J1-4)
From ST_J1-4	to ST_J1-5
From W1 P2-5 (ST J1-5) From ID J1B-14A From ID A1P13.1	to W1 P1B-14A to ID A1J13.1 to ID P12-79 (S201-5)
From ID P12-16 (S201-1) From ID A1J12.42 From ID A1P10.6 From ID P11-12 (S508-4) From ID A1J9.25	to ID A1P12.42 to ID A1J10.6 to ID P11-203 (S508-1) to ID A1P9.25 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID AlJ6.23 to ID P10-12 (S503-4) to ID AlP8.26 to ID AlJ15.50 to ID P20-3 (DMM-LO)

Step 205

Description:

This step verifies the wire path from W1 P2-6 to W1 P2-7. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) to ID A1P15.49

Date: 04 March 2016

From ID AlJ15.49 From ID AlP8.28 From ID P10-170 (S503-8) From ID AlJ6.38	to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.38 to ID BUS 6
From ID BUS 6 From ID A1P9.30 From ID P11-162 (S506-2) From ID A1J10.1 From ID A1P12.48	to ID A1J9.30 to ID P11-129 (S506-8) to ID A1P10.1 to ID A1J12.48 to ID P12-44 (S701-2)
From ID P12-3 (S701-6) From ID A1J13.10 From W1 P1B-13E	to ID A1P13.10 to ID J1B-13E to W1 P2-6 (ST J1-6)
From ST_J1-6	to ST_J1-7
From W1 P2-7 (ST J1-7) From ID J1A-8F From ID A1P14.24	to W1 P1A-8F to ID A1J14.24 to ID P13-62 (S202-42)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1J10.12 to ID P11-242 (S509-2)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 206

Description:

This step verifies the wire path from W1 P2-8 to W1 P2-9. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13
From ID	A1J6.13	to	ID	BUS 1
From ID	BUS 1	to	ID	A1J9.21
From ID	A1P9.21	to	ID	P11-180 (S510-3)
From ID	P11-244 (S510-2)	to	ID	A1P10.50
From ID	A1J10.50	to	ID	A1J14.50
From ID	A1P14.50	to	ID	P13-29 (S202-4)
From ID	P13-87 (S202-10)	to	ID	A1P14.17

Date: 04 March 2016

From ID AlJ14.17 From W1 P1A-5E		ID J1A-5E W1 P2-8 (ST J1-8)
From ST_J1-8	to	ST_J1-9
From W1 P2-9 (ST J1-9	•	W1 P1A-1A
From ID J1A-1A		ID A1J14.1
From ID A1P14.1	to	ID P13-47 (S201-9)
From ID P12-20 (S201-	-3) to	ID A1P12.46
From ID A1J12.46	to	ID A1J10.2
From ID A1P10.2	to	ID P11-39 (S507-1)
From ID P11-72 (S507-	-4) to	ID A1P9.27
From ID AlJ9.27	to	ID BUS 2
From ID BUS 2	to	ID A1J6.23
From ID A1P6.23	to	ID P10-12 (S503-4)
From ID P10-139 (S503	3-2) to	ID A1P8.26
From ID AlJ8.26	to	ID A1J15.50
From ID A1P15.50	to	ID P20-3 (DMM-LO)

Step 207

Description:

This step verifies the wire path from W1 P2-10 to W1 P2-12. The DMM resource will be used to measure resistance UL= 10 ohms.

From	ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From	ID	A1J15.49	to	ID	A1J8.28
From	ID	A1P8.28	to	ID	P10-203 (S503-1)
From	ID	P10-77 (S503-3)	to	ID	A1P6.13
From	ID	A1J6.13	to	ID	BUS 1
_					A1J8.29
					P10-204 (S301-67)
		P10-174 (S301-68)	to	ID	A1P8.4
From	ID	A1J8.4	to	ID	A1J1.2
From	ID	A1P1.2	to	ID	P1-4 (DC2-HI)
_		-1 4 (-CO)			-1-1 0
		P1-4 (DC2-HI)			
_		A1J1.2			A1J2.23
_		A1P2.23			P10-22 (S101-1)
			to	ID	A1P2.15
From	ID	A1J2.15	to	ID	A1J10.14
From	ID	A1P10.14	to	ID	P11-140 (S301-161)
From	ID	P11-206 (S301-162)	to	ID	A1P10.16
From	ID	A1J10.16	to	ID	J1A-13E
From	W1	P1A-13E	to	W1	P2-10 (ST J1-10)
Enom	СШ	T1 10	٠.	αш	T1 10
rrom	ST_	_J1-10	ΕÖ	ST_	_J1-12
From	W1	P2-12 (ST J1-12)	to	W1	P1B-8A

Date: 04 March 2016

From ID J1B-8A	to ID A1J12.4
From ID A1P12.4	to ID P12-18 (S201-22)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 208

Description:

This step verifies the wire path from W1 P2-11 to W1 P2-26. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.28 to ID BUS 7
From ID BUS 7 From ID A1P9.20 From ID P11-162 (S506-2) From ID A1J10.1 From ID A1P12.48	to ID A1J9.20 to ID P11-68 (S506-9) to ID A1P10.1 to ID A1J12.48 to ID P12-44 (S701-2)
From ID P13-74 (S701-34) From ID A1J15.12 From W1 P1A-6C From ST_J1-26	to ID A1P15.12 to ID J1A-6C to W1 P2-26 (ST J1-26) to ST_J1-11
From W1 P2-11 (ST J1-11) From ID J1B-1F From ID A1P1.11	to W1 P1B-1F to ID A1J1.11 to ID P1-11 (DC4-LO)
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P7.38 From ID P10-229 (S301-24) From ID A1J7.36	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 209

Description:

Date: 04 March 2016

This step verifies the wire path from W1 P2-13 to W1 P2-14. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1J8.28 to ID P10-203 (S503-1)
From ID BUS 1 From ID A1P9.17 From ID P11-39 (S507-1) From ID A1J10.2 From ID A1P12.46	to ID A1J9.17 to ID P11-168 (S507-3) to ID A1P10.2 to ID A1J12.46 to ID P12-20 (S201-3)
From ID P12-46 (S201-7) From ID A1J13.3 From W1 P1B-14B	to ID A1P13.3 to ID J1B-14B to W1 P2-13 (ST J1-13)
From ST_J1-13 From W1 P2-14 (ST J1-14)	to ST_J1-14 to W1 P1A-3A
From ID AlP15.1	to ID P13-39 (S701-7)
	to ID A1J10.3 to ID P11-194 (S506-1)
From ID BUS 8 From ID AlP6.16 From ID P10-139 (S503-2) From ID AlJ8.26 From ID AlP15.50	to ID A1J6.16 to ID P10-205 (S503-10) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 210

Description:

This step verifies the wire path from W1 P2-15 to W1 P2-16. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J7.42
From ID	A1P7.42	to	ID	P10-34 (S301-2)
From ID	P10-65 (S301-1)	to	ID	A1P7.3
From ID	A1J7.3	to	ID	A1J11.11
From ID	A1P11.11	to	ID	P11-233 (S301-153)
From ID	P11-43 (S301-154)	to	ID	A1P11.9

Date: 04 March 2016

From ID AlJ11.9	to ID J1A-12D
From W1 P1A-12D	to W1 P2-16 (ST J1-16)
From ST_J1-16	to ST_J1-15
From W1 P2-15 (ST J1-15)	to W1 P1B-8C
From ID J1B-8C	to ID A1J12.6
From ID A1P12.6	to ID P12-81 (S201-24)
From ID P12-80 (S201-2) From ID A1J12.40 From ID A1P10.8 From ID P11-12 (S508-4) From ID A1J9.25	to ID A1P12.40 to ID A1J10.8 to ID P11-139 (S508-2) to ID A1P9.25 to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 211

Description:

This step verifies the wire path from W1 P2-17 to W1 P2-18. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID AlJ9.17
From ID A1P9.17	to ID P11-168 (S507-3)
From ID P11-71 (S507-2)	to ID A1P10.4
From ID A1J10.4	to ID AlJ12.44
From ID A1P12.44	to ID P12-52 (S201-4)
10 40 (5001 10)	
,	to ID AlP14.4
From ID A1J14.4	to ID J1A-2B
From W1 P1A-2B	to W1 P2-17 (ST J1-17)
D	- CT 11 10
From ST_J1-17	to ST_J1-18
From W1 P2-18 (ST J1-18)	to W1 P1A-3C
From ID J1A-3C	to ID A1J15.3
From ID A1P15.3	to ID P13-70 (S701-9)
FIOM ID AIFIS.S	CO ID F13 70 (8701 9)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID AlJ10.3
From ID A1P10.3	to ID P11-194 (S506-1)
110 12 111110.0	33 12 111 171 (8300 1)

Date: 04 March 2016

From ID P11-195 (S506-4) to ID A1P9.33 to ID BUS 2

From ID BUS 2 to ID A1J6.23 to ID P10-12 (S503-4) From ID A1P6.23 to ID A1P8.26 to ID A1J5.50 to ID A1J15.50

Step 212

Description:

This step verifies the wire path from W1 P2-19 to W1 P2-20. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID AlJ15.49	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-78 (S201-14) From ID A1J13.6 From W1 P1B-13C	to ID A1P13.6 to ID J1B-13C to W1 P2-19 (ST J1-19)
From ST_J1-19	to ST_J1-20
From W1 P2-20 (ST J1-20) From ID J1B-4A From ID A1P12.16	to W1 P1B-4A to ID A1J12.16 to ID P12-63 (S202-46)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1J10.12 to ID P11-242 (S509-2)
	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 213

Description:

Date: 04 March 2016

This step verifies the wire path from W1 P2-21 to W1 P2-22. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.21 From ID P11-52 (S510-1) From ID A1J10.48 From ID A1P14.49	to ID A1J9.21 to ID P11-180 (S510-3) to ID A1P10.48 to ID A1J14.49 to ID P13-93 (S202-3)
From ID P12-31 (S202-45) From ID A1J12.15 From W1 P1B-5C	to ID A1P12.15 to ID J1B-5C to W1 P2-21 (ST J1-21)
From ST_J1-21	to ST_J1-22
From W1 P2-22 (ST J1-22) From ID J1A-1B From ID A1P14.3	to W1 P1A-1B to ID A1J14.3 to ID P13-80 (S201-11)
From ID P12-16 (S201-1) From ID A1J12.42 From ID A1P10.6 From ID P11-12 (S508-4) From ID A1J9.25	to ID A1J10.6 to ID P11-203 (S508-1)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 214

Description:

This step verifies the wire path from W1 P2-23 to W1 P2-25. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23

Date: 04 March 2016

From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-162 (S506-2)	to ID A1P10.1
From ID A1J10.1	to ID A1J12.48
From ID A1P12.48	to ID P12-44 (S701-2)
From ID P13-43 (S701-32)	to ID A1P15.10
From ID A1J15.10	to ID J1A-6A
From W1 P1A-6A	to W1 P2-23 (ST J1-23)
From ST_J1-23	to ST_J1-25
From W1 P2-25 (ST J1-25) From ID J1A-5C From ID A1P15.9	to W1 P1A-5C to ID A1J15.9 to ID P13-75 (S701-31)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 215

Description:

This step verifies the wire path from W1 P2-24 to W1 P2-28. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P12-40 (S701-29) From ID A1J13.18 From W1 P1B-11F From ST_J1-24	to ID A1P13.18 to ID J1B-11F to W1 P2-24 (ST J1-24) to ST_J1-28
From W1 P2-28 (ST J1-28)	to W1 P1B-9A

Date: 04 March 2016

From ID J1B-9A	to ID A1J12.1
From ID A1P12.1	to ID P12-48 (S201-15)
From ID P12-16 (S201-1) From ID A1J12.42 From ID A1P10.6 From ID P11-12 (S508-4) From ID A1J9.25	to ID A1P12.42 to ID A1J10.6 to ID P11-203 (S508-1) to ID A1P9.25 to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 216

Description:

This step verifies the wire path from W1 P2-27 to W1 P2-29. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-14 (S201-13) From ID A1J13.5 From W1 P1B-14C	to ID A1P13.5 to ID J1B-14C to W1 P2-27 (ST J1-27)
From ST_J1-27	to ST_J1-29
From ST_J1-27 From W1 P2-29 (ST J1-29) From ID J1A-1C From ID A1P14.5	to ST_J1-29 to W1 P1A-1C to ID A1J14.5 to ID P13-49 (S201-17)
From W1 P2-29 (ST J1-29) From ID J1A-1C From ID A1P14.5	to W1 P1A-1C to ID A1J14.5

Date: 04 March 2016

From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 217

Description:

This step verifies the wire path from W1 P2-30 to W1 P2-31. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID P10-203 (S503-1)
From ID BUS 1 From ID A1P9.21 From ID P11-52 (S510-1) From ID A1J10.48 From ID A1P14.49	to ID A1J9.21 to ID P11-180 (S510-3) to ID A1P10.48 to ID A1J14.49 to ID P13-93 (S202-3)
From ID P12-96 (S202-47) From ID A1J12.17 From W1 P1B-4B	to ID A1P12.17 to ID J1B-4B to W1 P2-30 (ST J1-30)
From ST_J1-30	to ST_J1-31
From W1 P2-31 (ST J1-31) From ID J1A-2C From ID A1P14.6	to W1 P1A-2C to ID A1J14.6 to ID P13-15 (S201-19)
From ID P12-16 (S201-1) From ID A1J12.42 From ID A1P10.6 From ID P11-12 (S508-4) From ID A1J9.25	to ID A1J10.6 to ID P11-203 (S508-1)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID AlJ6.23 to ID P10-12 (S503-4) to ID AlP8.26 to ID AlJ15.50 to ID P20-3 (DMM-LO)

Step 218

Description:

This step verifies the wire path from W1 P2-30 to W1 P2-36. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) to ID A1P15.49

Date: 04 March 2016

From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID AlJ9.21
From ID A1P9.21	to ID P11-180 (S510-3)
From ID P11-52 (S510-1)	to ID A1P10.48
From ID A1J10.48	to ID A1J14.49
From ID A1P14.49	to ID P13-93 (S202-3)
From ID P12-96 (S202-47)	to ID A1P12.17
From ID A1J12.17	to ID J1B-4B
From W1 P1B-4B	to W1 P2-30 (ST J1-30)
From ST_J1-30	to ST_J1-36
T 11 D0 26 (GT 71 26)	515 15
From W1 P2-36 (ST J1-36)	to W1 P1B-1F
From ID J1B-1F	to ID AlJ1.11
From ID A1P1.11	to ID P1-11 (DC4-LO)

Step 219

Description:

This step verifies the wire path from W1 P2-34 to W1 P2-37. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-242 (S509-2) From ID A1J10.12 From ID A1P12.36	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.12 to ID A1J12.36 to ID P12-90 (S202-2)
From ID P13-89 (S202-18) From ID A1J14.18 From W1 P1A-5F	to ID A1P14.18 to ID J1A-5F to W1 P2-34 (ST J1-34)
From ST_J1-34	to ST_J1-37
From W1 P2-37 (ST J1-37) From ID J1B-14F From ID A1P13.11	to W1 P1B-14F to ID A1J13.11 to ID P12-38 (S701-11)
From ID P12-76 (S701-1) From ID A1J12.50 From ID A1P10.3	to ID A1P12.50 to ID A1J10.3 to ID P11-194 (S506-1)

Date: 04 March 2016

From ID P11-195 (S506-4) to ID A1P9.33 to ID BUS 2

From ID BUS 2 to ID A1J6.23 to ID P10-12 (S503-4) From ID A1P6.23 to ID A1P8.26 From ID A1J8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 220

Description:

This step verifies the wire path from W1 P2-35 to W1 P2-47. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-51 (S201-21) From ID A1J12.3 From W1 P1B-9C	to ID A1P12.3 to ID J1B-9C to W1 P2-35 (ST J1-35)
From ST_J1-35	to ST_J1-47
` ,	to W1 P1A-3F to ID A1J14.14 to ID P13-18 (S201-35)
	to ID A1P12.46 to ID A1J10.2 to ID P11-39 (S507-1) to ID A1P9.27 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 221

Description:

Date: 04 March 2016

This step verifies the wire path from W1 P2-35 to W1 P2-68. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-51 (S201-21) From ID A1J12.3 From W1 P1B-9C	to ID A1P12.3 to ID J1B-9C to W1 P2-35 (ST J1-35)
From ST_J1-35	to ST_J1-68
From W1 P2-68 (ST J1-68) From ID J1B-5A From ID A1P12.13	to W1 P1B-5A to ID A1J12.13 to ID P12-92 (S202-22)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1J10.12 to ID P11-242 (S509-2)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 222

Description:

This step verifies the wire path from W1 P2-38 to W1 P2-39. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15

Date: 04 March 2016

From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-17 (S201-23) From ID A1J12.5 From W1 P1B-8B	to ID A1P12.5 to ID J1B-8B to W1 P2-38 (ST J1-38)
From ST_J1-38	to ST_J1-39
From W1 P2-39 (ST J1-39) From ID J1A-1E From ID A1P14.9	to W1 P1A-1E to ID A1J14.9 to ID P13-17 (S201-26)
From ID P12-52 (S201-4) From ID A1J12.44 From ID A1P10.4 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.44 to ID A1J10.4 to ID P11-71 (S507-2) to ID A1P9.27 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 223

Description:

This step verifies the wire path from W1 P2-40 to W1 P2-41. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20)-2 (DMM-HI)	to I	D A1P15.49
From ID AlJ	15.49	to I	D A1J8.28
From ID A1P	98.28	to I	D P10-203 (S503-1)
From ID P10)-77 (S503-3)	to I	D A1P6.13
From ID A1J	6.13	to I	D BUS 1
From ID BUS	3 1	to I	D A1J9.15
From ID A1P	9.15	to I	D P11-77 (S508-3)
From ID P11	139 (S508-2)	to I	D A1P10.8
From ID AlJ	10.8	to I	D A1J12.40
From ID A1P	12.40	to I	D P12-80 (S201-2)
From ID P13	3-51 (S201-28)	to I	D A1P14.11
From ID AlJ	14.11	to I	D J1A-1F
From W1 P1A	1-1F	to W	1 P2-40 (ST J1-40)
From ST_J1-	40	to S	T_J1-41
From W1 P2-	-41 (ST J1-41)	to W	1 P1B-10E

Date: 04 March 2016

_	J1B-10E A1P13.20			A1J13.20 P12-9 (S701-36)
From ID From ID From ID	P12-44 (S701-2) A1J12.48 A1P10.1 P11-195 (S506-4) A1J9.33	to to to	ID ID ID	A1P12.48 A1J10.1 P11-162 (S506-2) A1P9.33 BUS 2
From ID	BUS 2 A1P6.23 P10-139 (S503-2) A1J8.26 A1P15.50	to to to	ID ID ID	A1J6.23 P10-12 (S503-4) A1P8.26 A1J15.50 P20-3 (DMM-LO)

Step 224

Description:

This step verifies the wire path from W1 P2-42 to W1 P2-43. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
•	to ID A1J8.28
From ID Alp8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	
· · · · · · · · · · · · · · · · · · ·	to ID BUS 1
110 15 11100.13	00 12 202 1
From ID BUS 1	to ID A1J9.21
From ID A1P9.21	to ID P11-180 (S510-3)
From ID P11-244 (S510-2)	to ID A1P10.50
	to ID A1J14.50
From ID A1P14.50	to ID P13-29 (S202-4)
From ID P13-31 (S202-44)	to ID A1P14.25
From ID AlJ14.25	to ID J1A-9E
From W1 P1A-9E	to W1 P2-42 (ST J1-42)
From ST_J1-42	to ST_J1-43
From ST_J1-42	to ST_J1-43
_	to ST_J1-43 to W1 P1B-9D
From W1 P2-43 (ST J1-43)	_
From W1 P2-43 (ST J1-43)	to W1 P1B-9D
From W1 P2-43 (ST J1-43) From ID J1B-9D	to W1 P1B-9D to ID A1J13.17
From W1 P2-43 (ST J1-43) From ID J1B-9D From ID A1P13.17	to W1 P1B-9D to ID A1J13.17
From W1 P2-43 (ST J1-43) From ID J1B-9D From ID A1P13.17	to W1 P1B-9D to ID A1J13.17 to ID P12-41 (S701-38)
From W1 P2-43 (ST J1-43) From ID J1B-9D From ID A1P13.17 From ID P12-44 (S701-2)	to W1 P1B-9D to ID A1J13.17 to ID P12-41 (S701-38) to ID A1P12.48
From W1 P2-43 (ST J1-43) From ID J1B-9D From ID A1P13.17 From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1	to W1 P1B-9D to ID A1J13.17 to ID P12-41 (S701-38) to ID A1P12.48 to ID A1J10.1
From W1 P2-43 (ST J1-43) From ID J1B-9D From ID A1P13.17 From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1	to W1 P1B-9D to ID A1J13.17 to ID P12-41 (S701-38) to ID A1P12.48 to ID A1J10.1 to ID P11-162 (S506-2)
From W1 P2-43 (ST J1-43) From ID J1B-9D From ID A1P13.17 From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4)	to W1 P1B-9D to ID A1J13.17 to ID P12-41 (S701-38) to ID A1P12.48 to ID A1J10.1 to ID P11-162 (S506-2) to ID A1P9.33
From W1 P2-43 (ST J1-43) From ID J1B-9D From ID A1P13.17 From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4) From ID A1J9.33	to W1 P1B-9D to ID A1J13.17 to ID P12-41 (S701-38) to ID A1P12.48 to ID A1J10.1 to ID P11-162 (S506-2) to ID A1P9.33
From W1 P2-43 (ST J1-43) From ID J1B-9D From ID A1P13.17 From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4) From ID A1J9.33	to W1 P1B-9D to ID A1J13.17 to ID P12-41 (S701-38) to ID A1P12.48 to ID A1J10.1 to ID P11-162 (S506-2) to ID A1P9.33 to ID BUS 2
From W1 P2-43 (ST J1-43) From ID J1B-9D From ID A1P13.17 From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4) From ID A1J9.33 From ID BUS 2 From ID A1P6.23	to W1 P1B-9D to ID A1J13.17 to ID P12-41 (S701-38) to ID A1P12.48 to ID A1J10.1 to ID P11-162 (S506-2) to ID A1P9.33 to ID BUS 2

Date: 04 March 2016

From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 225

Description:

This step verifies the wire path from W1 P2-46 to W1 P2-48. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID AlJ9.15 to ID P11-77 (S508-3) to ID AlP10.8 to ID AlJ12.40 to ID P12-80 (S201-2)
From ID P12-50 (S201-30) From ID A1J12.8 From W1 P1B-7B	to ID A1P12.8 to ID J1B-7B to W1 P2-46 (ST J1-46)
From ST_J1-46	to ST_J1-48
From W1 P2-48 (ST J1-48) From ID J1B-12D From ID A1P13.13	to W1 P1B-12D to ID A1J13.13 to ID P12-69 (S701-13)
From ID P12-76 (S701-1) From ID A1J12.50 From ID A1P10.3 From ID P11-195 (S506-4) From ID A1J9.33	to ID A1J10.3 to ID P11-194 (S506-1)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 226

Description:

This step verifies the wire path from W1 P2-49 to W1 P2-50. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) to ID A1P15.49

Date: 04 March 2016

From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID A1J15.5 From W1 P1B-4B	to ID A1P15.5 to ID J1A-4B to W1 P2-49 (ST J1-49)
From ST_J1-49 From W1 P2-50 (ST J1-50) From ID J1B-6A	to ST_J1-50 to W1 P1B-6A to ID A1J12.10
,	to ID P12-83 (S201-32) to ID A1P12.44
From ID A1J12.44 From ID A1P10.4 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1J10.4 to ID P11-71 (S507-2) to ID A1P9.27 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50

Step 227

Description:

This step verifies the wire path from W1 P2-51 to W1 P2-52. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-15 (S201-16)	to ID A1P12.2

Date: 04 March 2016

From ID A1J12.2	to ID J1B-9B
From W1 P1B-9B	to W1 P2-51 (ST J1-51)
From ST_J1-51	to ST_J1-52
From W1 P2-52 (ST J1-52)	to W1 P1A-5A
From ID J1A-5A	to ID A1J15.7
From ID A1P15.7	to ID P13-7 (S701-17)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID AlJ6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 228

Description:

This step verifies the wire path from W1 P2-53 to W1 P2-54. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1J8.28 to ID P10-203 (S503-1)
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID AlJ9.23 to ID P11-164 (S506-3) to ID AlP10.3 to ID AlJ12.50 to ID P12-76 (S701-1)
From ID P12-71 (S701-19) From ID A1J13.15 From W1 P1B-12F	to ID A1P13.15 to ID J1B-12F to W1 P2-53 (ST J1-53)
From ST_J1-53	to ST_J1-54
From W1 P2-54 (ST J1-54) From ID J1B-4C From ID A1P12.18	to W1 P1B-4C to ID A1J12.18 to ID P12-32 (S202-48)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12	to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2)

Date: 04 March 2016

From ID P11-17 (S509-4) to ID A1P9.29 to ID BUS 2

From ID BUS 2 to ID A1J6.23 to ID P10-12 (S503-4) From ID A1P6.23 to ID A1P8.26 From ID A1J8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 229

Description:

This step verifies the wire path from W1 P2-55 to W1 P2-56. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.17 From ID P11-39 (S507-1) From ID A1J10.2 From ID A1P12.46	to ID A1J9.17 to ID P11-168 (S507-3) to ID A1P10.2 to ID A1J12.46 to ID P12-20 (S201-3)
From ID P13-83 (S201-27) From ID A1J14.10 From W1 P1A-2E	to ID A1P14.10 to ID J1A-2E to W1 P2-55 (ST J1-55)
From ST_J1-55	to ST_J1-56
From W1 P2-56 (ST J1-56) From ID J1B-11D From ID A1P13.16	to W1 P1B-11D to ID A1J13.16 to ID P12-6 (S701-21)
	to ID A1P12.50 to ID A1J10.3 to ID P11-194 (S506-1) to ID A1P9.33 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 230

Description:

Date: 04 March 2016

This step verifies the wire path from W1 P2-58 to W1 P2-60. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P12-42 (S701-35) From ID A1J13.19 From W1 P1B-10D	to ID A1P13.19 to ID J1B-10D to W1 P2-58 (ST J1-58)
From ST_J1-58	to ST_J1-60
From W1 P2-60 (ST J1-60) From ID J1A-1D From ID A1P14.7	to W1 P1A-1D to ID A1J14.7 to ID P13-79 (S201-20)
From ID P12-80 (S201-2) From ID A1J12.40 From ID A1P10.8 From ID P11-12 (S508-4) From ID A1J9.25	to ID A1J10.8 to ID P11-139 (S508-2)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 231

Description:

This step verifies the wire path from W1 P2-61 to W1 P2-62. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15

Date: 04 March 2016

From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P12-82 (S201-29)	to ID A1P12.7
From ID A1J12.7	to ID J1B-7A
From W1 P1B-7A	to W1 P2-61 (ST J1-61)
From ST_J1-61	to ST_J1-62
From W1 P2-62 (ST J1-62)	to W1 P1B-7C
From ID J1B-7C	to ID A1J12.9
From ID A1P12.9	to ID P12-19 (S201-31)
From ID P12-20 (S201-3) From ID A1J12.46 From ID A1P10.2 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.46 to ID A1J10.2 to ID P11-39 (S507-1) to ID A1P9.27 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 232

Description:

This step verifies the wire path from W1 P2-63 to W1 P2-67. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P13-10 (S701-33) From ID A1J15.11 From W1 P1A-6B From ST_J1-63	to ID A1P15.11 to ID J1A-6B to W1 P2-63 (ST J1-63) to ST_J1-67
From W1 P2-67 (ST J1-67)	to W1 P1A-6F

Date: 04 March 2016

From ID J1A-6F	to ID A1J14.20
From ID A1P14.20	to ID P13-24 (S202-20)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 233

Description:

This step verifies the wire path from W1 P2-64 to W1 P2-11. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID AlJ6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P12-73 (S701-37)	to ID A1P13.21
From ID A1J13.21	to ID J1B-10F
From W1 P1B-10F	to W1 P2-64 (ST J1-64)
From ST_J1-64	to ST_J1-69
From W1 P2-69 (ST J1-69)	to W1 P1B-1F
From ID J1B-1F	to ID A1J1.11
From ID AlP1.11	to ID P1-11 (DC4-LO)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 234

Description:

Date: 04 March 2016

This step verifies the wire path from W1 P2-65 to W1 P2-70. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID A1J15.49 From ID A1P8.28	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID P11-194 (S506-1)	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P13-45 (S701-39) From ID A1J15.13 From W1 P1A-7A	to ID A1P15.13 to ID J1A-7A to W1 P2-65 (ST J1-65)
From ST_J1-65	to ST_J1-70
· · · · · · · · · · · · · · · · · · ·	to W1 P1A-4A to ID A1J15.4 to ID P13-38 (S701-10)
	to ID A1J10.1 to ID P11-162 (S506-2)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID AlJ6.23 to ID P10-12 (S503-4) to ID AlP8.26 to ID AlJ15.50 to ID P20-3 (DMM-LO)

Step 235

Description:

This step verifies the wire path from W1 P2-66 to W1 P2-68. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23

Date: 04 March 2016

From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P13-76 (S701-41) From ID A1J15.15 From W1 P1A-7C	to ID A1P15.15 to ID J1A-7C to W1 P2-66 (ST J1-66)
From ST_J1-66	to ST_J1-68
From W1 P2-68 (ST J1-68) From ID J1B-5A From ID A1P12.13	to ID A1J12.13 to ID P12-92 (S202-22)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12 From ID P11-17 (S509-4)	to ID P11-242 (S509-2) to ID A1P9.29
From ID A1J9.29	to ID BUS 2
FIOM ID A109.29	CO 1D B05 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 236

Description:

This step verifies the wire path from W1 P4-A1 to W1 P4-A10. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.31 to ID BUS 3
From ID BUS 3 From ID A1P15.45 From ID P12-34 (S751-3) From ID A1J13.35 From W1 P1B-6E	to ID A1J15.45 to ID P13-2 (S751-1) to ID A1P13.35 to ID J1B-6E to W1 P4-A1 (ST J3-1)
From ST_J3-A1	to ST_J3-A10
From W1 P4-A10 (ST J3-19) From ID J1B-6F From ID A1P13.37 From ID P13-66 (S751-2) From ID A1J15.47	to W1 P1B-6F to ID A1J13.37 to ID P12-1 (S751-4) to ID A1P15.47 to ID BUS 4

Date: 04 March 2016

From ID BUS 4 to ID A1J6.39
From ID A1P6.39 to ID P10-74 (S503-6)
From ID P10-139 (S503-2) to ID A1P8.26
From ID A1J8.26 to ID A1J15.50
From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 237

Description:

This step verifies the wire path from W1P4-A2 to W1P4-B5. The DMM resource will be used to measure resistance UL=10 ohms.

Connection Path is as follows:

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-10 (S503-5)	to	ID	A1P6.31
From ID	A1J6.31	to	ID	BUS 3
From ID	BUS 3	to	ID	A1J15.45
From ID	A1P15.45	to	ID	P13-2 (S751-1)
From ID	P12-65 (S751-5)	to	ID	A1P13.39
From ID	A1J13.39	to	ID	J1B-5E
From W1	P1B-5E	to	W1	P4-A2 (ST J3-3)
From ST_	J3-A2	to	ST_	_J3-B5
From W1	P4-B5 (ST J3-10)	to	W1	P1B-5F
From ID	J1B-5F			A1J13.41
From ID		to	ID	P12-33 (S751-6)
From ID	P13-66 (S751-2)	to	ID	A1P15.47
From ID	A1J15.47	to	ID	BUS 4
From ID	BUS 4	to	ID	A1J6.39
From ID	A1P6.39	to	ID	P10-74 (S503-6)
From ID	P10-139 (S503-2)	to	ID	A1P8.26
From ID	A1J8.26	to	ID	A1J15.50
From ID	A1P15.50	to	ID	P20-3 (DMM-LO)

Step 238

Description:

This step verifies the wire path from W1P4-B2 to W1P4-A5. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3

Date: 04 March 2016

From ID	A1P15.45 P13-68 (S751-7)	to to	ID ID	A1J15.45 P13-2 (S751-1) A1P15.21
_	A1J15.21			J1A-10A
From WI	P1A-10A	to	W⊥	P4-B2 (ST J3-4)
From ST_	_J3-B2	to	ST_	_J3-A5
From W1	P4-A5 (ST J3-9)	to	W1	P1A-11A
From ID	J1A-11A	to	ID	A1J15.23
From ID	A1P15.23	to	ID	P13-36 (S751-8)
From ID	P13-66 (S751-2)	to	ID	A1P15.47
From ID	A1J15.47	to	ID	BUS 4
From ID	BUS 4	to	ID	A1J6.39
From ID	A1P6.39	to	ID	P10-74 (S503-6)
From ID	P10-139 (S503-2)	to	ID	A1P8.26
From ID	A1J8.26	to	ID	A1J15.50
From ID	A1P15.50	to	ID	P20-3 (DMM-LO)

Step 239

Description:

This step verifies the wire path from W1P4-A3 to W1P4-B7. The DMM resource will be used to measure resistance UL=10 ohms.

From ID From ID From ID	P20-2 (DMM-HI) A1J15.49 A1P8.28 P10-10 (S503-5) A1J6.31	to to to	ID ID ID	A1P15.49 A1J8.28 P10-203 (S503-1) A1P6.31 BUS 3
From ID	BUS 3 A1P15.45 P13-67 (S751-9) A1J15.25 P1A-12A	to to to	ID ID ID	A1J15.45 P13-2 (S751-1) A1P15.25 J1A-12A P4-A3 (ST J3-5)
From ST		to	ST_	_J3-B7
From ID	J1A-13A	to	ID	A1J15.27
_	A1P15.27 P13-66 (S751-2)			P13-35 (S751-10) A1P15.47
From ID	A1J15.47	to	ID	BUS 4
From ID	A1P6.39	to to to	ID ID ID	A1J6.39 P10-74 (S503-6) A1P8.26 A1J15.50 P20-3 (DMM-LO)

Date: 04 March 2016

Step 240

Description:

This step verifies the wire path from W1P4-B3 to W1P4-A6. The DMM resource will be used to measure resistance UL=10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
	to ID A1J15.45
	to ID P13-2 (S751-1)
From ID P12-67 (S751-11)	to ID A1P13.43
From ID A1J13.43	to ID J1B-4E
From W1 P1B-4E	to W1 P4-B3 (ST J3-6)
- CF 72 - D2	
From ST_J3-B3	to ST_J3-A6
From W1 P4-A6 (ST J3-11)	to W1 P1B-4F
	to ID A1J13.45
	to ID P12-35 (S751-12)
	to ID A1P15.47
From ID AlJ15.47	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
	CO ID AII 0:20
From ID A1J8.26	to ID A1J15.50

Step 241

Description:

This step verifies the wire path from W1P4-A4 to W1P4-A8. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J15.45
From ID A1P15.45	to ID P13-2 (S751-1)
From ID P12-2 (S751-13)	to ID A1P13.47
From ID A1J13.47	to ID J1B-3E
From W1 P1B-3E	to W1 P4-A4 (ST J3-7)

Date: 04 March 2016

From ST_J3-A4	to ST_J3-A8
From W1 P4-A8 (ST J3-15) From ID J1B-3F From ID A1P13.49 From ID P13-66 (S751-2) From ID A1J15.47	to W1 P1B-3F to ID A1J13.49 to ID P12-66 (S751-14) to ID A1P15.47 to ID BUS 4
From ID BUS 4 From ID A1P6.39 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.39 to ID P10-74 (S503-6) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 242

Description:

This step verifies the wire path from W1P4-B4 to W1P4-B8. The DMM resource will be used to measure resistance UL=10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1J8.28 to ID P10-203 (S503-1)
From ID BUS 3 From ID A1P15.45 From ID P13-5 (S751-15) From ID A1J15.30 From W1 P1A-13C	to ID A1J15.45 to ID P13-2 (S751-1) to ID A1P15.30 to ID J1A-13C to W1 P4-B4 (ST J3-8)
From ST_J3-B4	to ST_J3-B8
From W1 P4-B8 (ST J3-16) From ID J1A-14A From ID A1P15.32 From ID P13-66 (S751-2) From ID A1J15.47	to W1 P1A-14A to ID A1J15.32 to ID P13-69 (S751-16) to ID A1P15.47 to ID BUS 4
From ID BUS 4 From ID A1P6.39 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.39 to ID P10-74 (S503-6) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 243

Description:

This step verifies the wire path from W1P4-B6 to W1P4-B9. The DMM resource will be used to measure resistance UL= 10 ohms.

Date: 04 March 2016

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.31 to ID BUS 3
From ID BUS 3 From ID A1P15.45 From ID P13-37 (S751-17) From ID A1J15.34 From W1 P1A-14B	to ID A1J15.45 to ID P13-2 (S751-1) to ID A1P15.34 to ID J1A-14B to W1 P4-B6 (ST J3-12)
From ST_J3-B6	to ST_J3-B9
From W1 P4-B9 (ST J3-18) From ID J1A-14C From ID A1P15.36 From ID P13-66 (S751-2) From ID A1J15.47	to W1 P1A-14C to ID A1J15.36 to ID P13-4 (S751-18) to ID A1P15.47 to ID BUS 4
From ID BUS 4 From ID A1P6.39 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.39 to ID P10-74 (S503-6) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 244

Description:

This step verifies the wire path from W1 P3-1 to W1 P3-2. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P12-75 (S701-43) From ID A1J13.22 From W1 P1B-11E From ST J2-1	to ID A1P13.22 to ID J1B-11E to W1 P3-1 (ST J2-1)

Date: 04 March 2016

From W1 P3-2 (ST J2-2) From ID J1A-2F	to W1 P1A-2F to ID A1J14.12
From ID A1P14.12	to ID P13-52 (S201-33)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID AlJ12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID AlJ8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 245

Description:

This step verifies the wire path from W1 P3-4 to W1 P3-3. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A138.28 to ID P10-203 (S503-1)
From ID BUS 1 From ID A1P9.23 From ID P11-162 (S506-2) From ID A1J10.1 From ID A1P12.48	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.1 to ID A1J12.48 to ID P12-44 (S701-2)
From ID P13-6 (S701-8) From ID A1J15.2 From W1 P1A-3B	to ID A1P15.2 to ID J1A-3B to W1 P3-4 (ST J2-4)
From ST_J2-4 From W1 P3-3 (ST J2-3) From ID J1A-5B From ID A1P15.8	to ST_J2-3 to W1 P1A-5B to ID A1J15.8 to ID P13-42 (S701-23)
From W1 P3-3 (ST J2-3) From ID J1A-5B From ID A1P15.8 From ID P12-76 (S701-1) From ID A1J12.50 From ID A1P10.3	to W1 P1A-5B to ID A1J15.8 to ID P13-42 (S701-23)

Date: 04 March 2016

From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 246

Description:

This step verifies the wire path from W1 P3-6 to W1 P3-7. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-242 (S509-2) From ID A1J10.12 From ID A1P12.36	to ID AlJ9.19 to ID P11-18 (S509-3) to ID AlP10.12 to ID AlJ12.36 to ID P12-90 (S202-2)
From ID P12-27 (S202-24) From ID A1J12.14 From W1 P1B-5B	to ID A1P12.14 to ID J1B-5B to W1 P3-6 (ST J2-6)
From ST_J2-6	to ST_J2-7
From W1 P3-7 (ST J2-7) From ID J1A-9F From ID A1P14.26	to W1 P1A-9F to ID A1J14.26 to ID P13-64 (S202-50)
From ID P13-29 (S202-4) From ID A1J14.50 From ID A1P10.50 From ID P11-147 (S510-4) From ID A1J9.31	to ID A1P14.50 to ID A1J10.50 to ID P11-244 (S510-2) to ID A1P9.31 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 247

Description:

This step verifies the wire path from W1 P3-8 to W1 P3-11. The DMM resource will be used to measure resistance UL= 10 ohms.

Date: 04 March 2016

From	ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From	ID	A1J15.49	to	ID	A1J8.28
From	ID	A1P8.28	to	ID	P10-203 (S503-1)
From	ID	P10-77 (S503-3)	to	ID	A1P6.13
From	ID	A1J6.13	to	ID	BUS 1
From	ID	BUS 1	to	ID	A1J9.15
From	ID	A1P9.15	to	ID	P11-77 (S508-3)
From	ID	P11-203 (S508-1)	to	ID	A1P10.6
From	ID	A1J10.6	to	ID	A1J12.42
From	ID	A1P12.42	to	ID	P12-16 (S201-1)
From	ID	P13-50 (S201-25)			
From	ID	A1J14.8	to	ID	J1A-2D
From	W1	P1A-2D	to	W1	P3-8 (ST J2-8)
From	$ST_{_}$	_J2-8	to	$ST_{_}$	_J2-11
		- ,			P1A-7E
					A1J14.21
From	ID	A1P14.21	to	ID	P13-90 (S202-26)
_		-10 00 (-000 0)			-1-10 01
					A1P12.36
		A1J12.36			A1J10.12
					P11-242 (S509-2)
		· · · · · · · · · · · · · · · · · · ·			A1P9.29
From	ID	A1J9.29	to	ID	BUS 2
	TD	DIIG 0		TD	7176 00
					A1J6.23
					P10-12 (S503-4)
					A1P8.26
					A1J15.50
rrom	TD	A1P15.50	CO	TD	P20-3 (DMM-LO)

Step 248

Description:

This step verifies the wire path from W1 P3-18 to W1 P3-10. The DMM resource will be used to measure resistance UL= 10 ohms.

From	ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From	ID	A1J15.49	to	ID	A1J8.28
From	ID	A1P8.28	to	ID	P10-203 (S503-1)
From	ID	P10-77 (S503-3)	to	ID	A1P6.13
From	ID	A1J6.13	to	ID	BUS 1
From	ID	BUS 1	to	ID	A1J9.23
From	ID	A1P9.23	to	ID	P11-164 (S506-3)
From	ID	P11-162 (S506-2)	to	ID	A1P10.1
From	ID	A1J10.1	to	ID	A1J12.48
From	ID	A1P12.48	to	ID	P12-44 (S701-2)

Date: 04 March 2016

From ID P12-37 (S701-14) From ID A1J13.14 From W1 P1B-12E	to ID A1P13.14 to ID J1B-12E to W1 P3-18 (ST J2-18)
From ST_J2-18	to ST_J2-10
From W1 P3-10 (ST J2-10)	to W1 P1B-1E
From ID J1B-1E	to ID A1J1.11
From ID A1P1.11	to ID P1-11 (DC4-LO)
11011111111111	
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND
- · · · · · · · · · · · · · · · · · · ·	

Step 249

Description:

This step verifies the wire path from W1 P3-14 to W1 P3-16. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID P10-203 (S503-1)
From ID BUS 1 From ID A1P9.19 From ID P11-242 (S509-2) From ID A1J10.12 From ID A1P12.36	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.12 to ID A1J12.36 to ID P12-90 (S202-2)
From ID P13-94 (S202-52) From ID A1J14.27 From W1 P1A-10E	to ID A1P14.27 to ID J1A-10E to W1 P3-14 (ST J2-14)
From ST_J2-14 From W1 P3-16 (ST J2-16) From ID J1B-6C	to ST_J2-16 to W1 P1B-6C to ID A1J12.12
From W1 P3-16 (ST J2-16)	to W1 P1B-6C to ID A1J12.12 to ID P12-53 (S201-39)
From W1 P3-16 (ST J2-16) From ID J1B-6C From ID A1P12.12 From ID P12-16 (S201-1) From ID A1J12.42 From ID A1P10.6	to W1 P1B-6C to ID A1J12.12 to ID P12-53 (S201-39) to ID A1P12.42

Date: 04 March 2016

From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 250

Description:

This step verifies the wire path from W1 P3-17 to W1 P3-20. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-162 (S506-2) From ID A1J10.1 From ID A1P12.48	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.1 to ID A1J12.48 to ID P12-44 (S701-2)
From ID P12-5 (S701-12)	to ID A1P13.12
From ID A1J13.12	to ID J1B-13F
From W1 P1B-13F	to W1 P3-17 (ST J2-17)
From ST_J2-17	to ST_J2-20
From W1 P3-20 (ST J2-20)	to W1 P1A-4E
From ID J1A-4E	to ID A1J14.15
From ID A1P14.15	to ID P13-21 (S201-41)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 251

Description:

This step verifies the wire path from W1 P3-19 to W1 P3-21. The DMM resource will be used to measure resistance UL= 10 ohms.

Date: 04 March 2016

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13
From ID	A1J6.13	to	ID	BUS 1
From ID	BUS 1	to	ID	A1J9.23
From ID	A1P9.23	to	ID	P11-164 (S506-3)
From ID	P11-162 (S506-2)	to	ID	A1P10.1
		to	ID	A1J12.48
From ID	A1P12.48	to	ID	P12-44 (S701-2)
From ID	P12-43 (S701-44)	to	ID	A1P13.24
From ID	A1J13.24	to	ID	J1B-9F
From W1	P1B-9F	to	W1	P3-19 (ST J2-19)
From ST_	_J2-19	to	ST_	_J2-21
From W1	P3-21 (ST J2-21)	to	W1	P1A-4F
From ID	J1A-4F	to	ID	A1J14.16
From ID	A1P14.16	to	ID	P13-86 (S201-43)
From ID	P12-16 (S201-1)	to	ID	A1P12.42
From ID	A1J12.42	to	ID	A1J10.6
	A1P10.6	to	ID	P11-203 (S508-1)
From ID	P11-12 (S508-4)	to	ID	A1P9.25
From ID	A1J9.25	to	ID	BUS 2
From ID	BUS 2			A1J6.23
	A1P6.23			P10-12 (S503-4)
				A1P8.26
	A1J8.26			A1J15.50
From ID	A1P15.50	to	ID	P20-3 (DMM-LO)

Step 252

Description:

This step verifies the wire path from W1 P3-22 to W1 P3-24. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
I I O	CO ID AIOD.IS
From ID A1P9.15	to ID P11-77 (S508-3)
From ID A1P9.15	to ID P11-77 (S508-3)

Date: 04 March 2016

From ID P13-19 (S201-34) From ID A1J14.13 From W1 P1A-3E	to ID A1P14.13 to ID J1A-3E to W1 P3-22 (ST J2-22)
From ST_J2-22	to ST_J2-24
From W1 P3-24 (ST J2-24) From ID J1A-7F From ID A1P14.22	to W1 P1A-7F to ID A1J14.22 to ID P13-92 (S202-34)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 253

Description:

This step verifies the wire path from W1 P3-29 to W1 P3-25. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P7.48 From ID P10-97 (S301-9) From ID A1J7.5 From W1 P1A-14D	to ID A1P15.49 to ID A1J7.48 to ID P10-2 (S301-10) to ID A1P7.5 to ID J1A-14D to W1 P3-29 (ST J2-29)
From ST_J2-29	to ST_J2-25
From W1 P3-25 (ST J2-25) From ID J1A-8E From ID A1P14.23	to W1 P1A-8E to ID A1J14.23 to ID P13-91 (S202-35)
From ID A1J12.38 From ID A1P10.10	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Date: 04 March 2016

Step 254

Description:

This step verifies the wire path from W1 P3-29 to W1 P3-30. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P7.42 From ID P10-65 (S301-1) From ID A1J7.3 From W1 P1A-14E	to ID A1P15.49 to ID A1J7.42 to ID P10-34 (S301-2) to ID A1P7.3 to ID J1A-14E to W1 P3-30 (ST J2-30)
From ST_J2-30	to ST_J2-40
From W1 P3-40 (ST J2-40)	to W1 P1A-9A
From ID J1A-9A	to ID A1J15.19
From ID A1P15.19	to ID P13-77 (S701-50)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

2.6 MODULE 3 W2 CDA LOGIC B SPECIFIC ID TESTS

Refer to Reference Drawings when diagnosing connection paths.

Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF TEST PWB, A2).pdf and 13020A7201 (CABLE, W2, SCHEMATIC).pdf in section 1.4 during review of the following steps.

Step 301

Description:

This step verifies the wire path from W2 P2-1 to W2 P2-32. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID AlJ15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)

Date: 04 March 2016

From ID P10-77 (S503-3)	to ID AlP6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-162 (S506-2) From ID A1J10.1 From ID A1P12.48	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.1 to ID A1J12.48 to ID P12-44 (S701-2)
From ID P13-40 (S701-16)	to ID A1P15.16
From ID A1J15.16	to ID J2A-7C
From W2 P1A-7C	to W2 P2-1 (ST J1-1)
From ST_J1-1	to ST_J1-32
From W2 P2-32 (ST J1-32)	to W2 P1B-7A
From ID J2B-7A	to ID A1J12.24
From ID A1P12.24	to ID P12-21 (S201-46)
From ID P12-80 (S201-2) From ID A1J12.40 From ID A1P10.8 From ID P11-12 (S508-4) From ID A1J9.25	to ID A1P12.40 to ID A1J10.8 to ID P11-139 (S508-2) to ID A1P9.25 to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 302

Description:

This step verifies the wire path from W2 P2-2 to W2 P2-59. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P13-73 (S701-25) From ID A1J15.26 From W2 P1A-6B	to ID A1P15.26 to ID J2A-6B to W2 P2-2 (ST J1-2)

Date: 04 March 2016

From ST_J1-2	to ST_J1-59
From W2 P2-59 (ST J1-59) From ID J2B-13D From ID A1P12.30	to W2 P1B-13D to ID A1J12.30 to ID P12-56 (S202-8)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 303

Description:

This step verifies the wire path from W2 P2-3 to W2 P2-57. The DMM resource will be used to measure resistance UL= 10 ohms.

From	ID	P20-2 (DMM-HI) A1J15.49	to	ID	A1P15.49 A1J8.28
		A1P8.28	to	ID	P10-203 (S503-1)
From	ID	P10-77 (S503-3)	to	ID	A1P6.13
From	ID	A1J6.13	to	ID	BUS 1
_		BUS 1			A1J9.19
		A1P9.19	to	ID	P11-18 (S509-3)
From	ID	P11-177 (S509-1)	to	ID	A1P10.10
From	ID	A1J10.10	to	ID	A1J12.38
From	ID	A1P12.38	to	ID	P12-59 (S202-1)
From	ID	P12-64 (S202-37)	to	ID	A1P12.43
From	ID	A1J12.43	to	ID	J2B-8F
From	W2	P1B-8F	to	W2	P2-3 (ST J1-3)
From	ST_	_J1-3	to	ST_	_J1-57
From	W2	P2-57 (ST J1-57)	to	W2	P1B-10A
From	ID	J2B-10A	to	ID	A1J13.31
From	ID	A1P13.31	to	ID	P12-7 (S701-30)
From	ID	P12-44 (S701-2)	to	ID	A1P12.48
From	ID	A1J12.48	to	ID	A1J10.1
From	ID	A1P10.1	to	ID	P11-162 (S506-2)
From	ID	P11-195 (S506-4)	to	ID	A1P9.33
From	ID	A1J9.33	to	ID	BUS 2

Date: 04 March 2016

From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 304

Description:

This step verifies the wire path from W2 P2-4 to W2 P2-5. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID A1J15.49 From ID A1P8.28	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID P11-194 (S506-1)	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P12-8 (S701-27) From ID A1J13.29 From W2 P1B-9B	to ID A1P13.29 to ID J2B-9B to W2 P2-4 (ST J1-4)
From ST_J1-4	to ST_J1-5
From W2 P2-5 (ST J1-5) From ID J2B-6F From ID A1P12.47	to W2 P1B-6F to ID A1J12.47 to ID P12-94 (S202-39)
	to ID A1J10.10 to ID P11-177 (S509-1)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 305

Description:

This step verifies the wire path from W2 P2-6 to W2 P2-7. The DMM resource will be used to measure resistance UL= 10 ohms.

Date: 04 March 2016

Connection Path is as follows:

From ID From ID From ID	P20-2 (DMM-HI) A1J15.49 A1P8.28 P10-77 (S503-3) A1J6.13	to to to	ID ID ID	A1P15.49 A1J8.28 P10-203 (S503-1) A1P6.13 BUS 1
From ID From ID	BUS 1 A1P9.15 P11-139 (S508-2) A1J10.8 A1P12.40	to to to	ID ID ID	A1J9.15 P11-77 (S508-3) A1P10.8 A1J12.40 P12-80 (S201-2)
	P13-16 (S201-18) A1J14.28 P1A-1A	to	ID	A1P14.28 J2A-1A P2-6 (ST J1-6)
From ST	_J1-6	to	ST_	_J1-7
From ID	P2-7 (ST J1-7) J2B-8A A1P13.25	to	ID	P1B-8A A1J13.25 P12-39 (S701-20)
From ID From ID From ID	P12-44 (S701-2) A1J12.48 A1P10.1 P11-195 (S506-4) A1J9.33	to to to	ID ID ID	A1P12.48 A1J10.1 P11-162 (S506-2) A1P9.33 BUS 2
From ID From ID	BUS 2 A1P6.23 P10-139 (S503-2) A1J8.26 A1P15.50	to to to	ID ID ID	A1J6.23 P10-12 (S503-4) A1P8.26 A1J15.50 P20-3 (DMM-LO)

Step 306

Description:

This step verifies the wire path from W2 P2-8 to W2 P2-9. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-177 (S509-1)	to ID A1P10.10
From ID A1J10.10	to ID A1J12.38

Date: 04 March 2016

From ID A1P12.38	to ID P12-59 (S202-1)
From ID P13-30 (S202-41)	to ID A1P14.31
From ID A1J14.31	to ID J2A-2B
From W2 P1A-2B	to W2 P2-8 (ST J1-8)
From ST_J1-8	to ST_J1-9
From W2 P2-9 (ST J1-9)	to W2 P1B-10B
From ID J2B-10B	to ID A1J13.33
From ID A1P13.33	to ID P12-10 (S701-45)
From ID P12-76 (S701-1) From ID A1J12.50 From ID A1P10.3 From ID P11-195 (S506-4) From ID A1J9.33	to ID A1P12.50 to ID A1J10.3 to ID P11-194 (S506-1) to ID A1P9.33 to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 307

Description:

This step verifies the wire path from W2 P2-10 to W2 P2-12. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-162 (S506-2) From ID A1J10.1 From ID A1P12.48	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.1 to ID A1J12.48 to ID P12-44 (S701-2)
From ID P12-70 (S701-22) From ID A1J13.27 From W2 P1B-9A	to ID A1P13.27 to ID J2B-9A to W2 P2-10 (ST J1-10)
From ST_J1-10	to ST_J1-12
From W2 P2-12 (ST J1-12) From ID J2B-9F From ID A1P12.41	to W2 P1B-9F to ID A1J12.41 to ID P12-29 (S202-32)

Date: 04 March 2016

From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 308

Description:

This step verifies the wire path from W2 P2-13 to W2 P2-14. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P13-82 (S201-36) From ID A1J14.29 From W2 P1A-2A	to ID A1P14.29 to ID J2A-2A to W2 P2-13 (ST J1-13)
From ST_J1-13	to ST_J1-14
From W2 P2-14 (ST J1-14) From ID J2B-6C From ID A1P12.19	to W2 P1B-6C to ID A1J12.19 to ID P12-54 (S201-38)
From ID P12-52 (S201-4) From ID A1J12.44 From ID A1P10.4 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.44 to ID A1J10.4 to ID P11-71 (S507-2) to ID A1P9.27 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Date: 04 March 2016

Step 309

Description:

This step verifies the wire path from W2 P2-15 to W2 P2-16. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-85 (S201-45) From ID A1J12.23 From W2 P1B-7B	to ID A1P12.23 to ID J2B-7B to W2 P2-15 (ST J1-15)
From ST_J1-15	to ST_J1-16
From W2 P2-16 (ST J1-16) From ID J2B-11F From ID A1P12.37	to W2 P1B-11F to ID A1J12.37 to ID P12-60 (S202-30)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1J10.12 to ID P11-242 (S509-2)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 310

Description:

This step verifies the wire path from W2 P2-17 to W2 P2-18. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13

Date: 04 March 2016

From ID AlJ6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P13-78 (S701-47)	to ID A1P15.18
From ID A1J15.18	to ID J2A-8C
From W2 P1A-8C	to W2 P2-17 (ST J1-17)
From ST_J1-17	to ST_J1-18
From W2 P2-18 (ST J1-18)	to W2 P1A-3C
From ID J2A-3C	to ID A1J14.32
From ID A1P14.32	to ID P13-53 (S201-42)
From ID P12-52 (S201-4)	to ID A1P12.44
From ID AlJ12.44	to ID A1J10.4
From ID A1P10.4	to ID P11-71 (S507-2)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 311

Description:

This step verifies the wire path from W2 P2-19 to W2 P2-20. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13
From ID	A1J6.13	to	ID	BUS 1
From ID	BUS 1	to	ID	A1J9.15
From ID	A1P9.15	to	ID	P11-77 (S508-3)
From ID	P11-139 (S508-2)	to	ID	A1P10.8
From ID	A1J10.8	to	ID	A1J12.40
From ID	A1P12.40	to	ID	P12-80 (S201-2)
	P13-22 (S201-44)	to	ID	A1P14.33
From ID	A1J14.33	to	ID	J2A-2C
From W2	P1A-2C	to	W2	P2-19 (ST J1-19)

Date: 04 March 2016

From ST_J1-19	to ST_J1-20
From W2 P2-20 (ST J1-20)	to W2 P1B-14D
From ID J2B-14D	to ID A1J12.27
From ID A1P12.27	to ID P12-55 (S201-47)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID $P20-3$ (DMM-LO)

Step 312

Description:

This step verifies the wire path from W2 P2-21 to W2 P2-22. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P13-23 (S201-49) From ID A1J14.35 From W2 P1A-3D From ST_J1-21	to ID A1P14.35 to ID J2A-3D to W2 P2-21 (ST J1-21) to ST_J1-22
From W2 P2-22 (ST J1-22) From ID J2A-4F From ID A1P14.46	to W2 P1A-4F to ID A1J14.46 to ID P13-95 (S202-43)
From ID A1J12.38 From ID A1P10.10	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2

Date: 04 March 2016

From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 313

Description:

This step verifies the wire path from W2 P2-23 to W2 P2-25. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID A1J15.49	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID P11-162 (S506-2)	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.1 to ID A1J12.48 to ID P12-44 (S701-2)
From ID P13-9 (S701-24) From ID A1J15.24 From W2 P1A-7B	to ID A1P15.24 to ID J2A-7B to W2 P2-23 (ST J1-23)
From ST_J1-23	to ST_J1-25
From W2 P2-25 (ST J1-25) From ID J2A-5D From ID A1P14.47	to W2 P1A-5D to ID A1J14.47 to ID P13-32 (S202-49)
	to ID A1J10.10 to ID P11-177 (S509-1)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID AlJ6.23 to ID P10-12 (S503-4) to ID AlP8.26 to ID AlJ15.50 to ID P20-3 (DMM-LO)

Step 314

Description:

This step verifies the wire path from W2 P2-24 to W2 P2-28. The DMM resource will be used to measure resistance UL= 10 ohms.

Date: 04 March 2016

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-177 (S509-1) From ID A1J10.10 From ID A1P12.38	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.10 to ID A1J12.38 to ID P12-59 (S202-1)
From ID P12-89 (S202-5) From ID A1J12.25 From W2 P1B-8C	to ID A1P12.25 to ID J2B-8C to W2 P2-24 (ST J1-24)
From ST_J1-24	to ST_J1-28
From W2 P2-28 (ST J1-28) From ID J2B-14F From ID A1P12.29	to W2 P1B-14F to ID A1J12.29 to ID P12-24 (S202-7)
From ID P13-93 (S202-3) From ID A1J14.49 From ID A1P10.48 From ID P11-147 (S510-4) From ID A1J9.31	to ID A1P14.49 to ID A1J10.48 to ID P11-52 (S510-1) to ID A1P9.31 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 315

Description:

This step verifies the wire path from W2 P2-33 to W2 P2-36. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40

Date: 04 March 2016

From ID A1P12.40	to ID P12-80 (S201-2)
From ID P12-86 (S201-48)	to ID A1P12.28
From ID A1J12.28	to ID J2B-14E
From W2 P1B-14E	to W2 P2-33 (ST J1-33)
From ST_J1-33	to ST_J1-36
From W2 P2-36 (ST J1-36)	to W2 P1A-2E
From ID J2A-2E	to ID A1J14.39
From ID A1P14.39	to ID P13-56 (S202-9)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 316

Description:

This step verifies the wire path from W2 P2-34 to W2 P2-37. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-242 (S509-2) From ID A1J10.12 From ID A1P12.36	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.12 to ID A1J12.36 to ID P12-90 (S202-2)
From ID P13-27 (S202-36) From ID A1J14.45 From W2 P1A-4E From ST_J1-34	to ID A1P14.45 to ID J2A-4E to W2 P2-34 (ST J1-34) to ST_J1-37
From W2 P2-37 (ST J1-37) From ID J2A-8A From ID A1P15.22	to W2 P1A-8A to ID A1J15.22 to ID P13-71 (S701-18)

Date: 04 March 2016

From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4) From ID A1J9.33	to ID A1P12.48 to ID A1J10.1 to ID P11-162 (S506-2) to ID A1P9.33 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 317

Description:

This step verifies the wire path from W2 P2-38 to W2 P2-39. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-177 (S509-1) From ID A1J10.10 From ID A1P12.38	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.10 to ID A1J12.38 to ID P12-59 (S202-1)
From ID P13-25 (S202-11) From ID A1J14.40 From W2 P1A-1E	to ID A1P14.40 to ID J2A-1E to W2 P2-38 (ST J1-38)
From ST_J1-38	to ST_J1-39
From W2 P2-39 (ST J1-39) From ID J2A-5E From ID A1P14.48	to W2 P1A-5E to ID A1J14.48 to ID P13-63 (S202-51)
From ID P13-93 (S202-3) From ID A1J14.49 From ID A1P10.48 From ID P11-147 (S510-4) From ID A1J9.31	to ID A1P14.49 to ID A1J10.48 to ID P11-52 (S510-1) to ID A1P9.31 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Date: 04 March 2016

Step 318

Description:

This step verifies the wire path from W2 P2-40 to W2 P2-41. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-177 (S509-1) From ID A1J10.10 From ID A1P12.38	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.10 to ID A1J12.38 to ID P12-59 (S202-1)
From ID P12-57 (S202-13) From ID A1J12.31 From W2 P1B-13E	to ID A1P12.31 to ID J2B-13E to W2 P2-40 (ST J1-40)
From ST_J1-40	to ST_J1-41
From W2 P2-41 (ST J1-41) From ID J2B-12D From ID A1P12.33	to W2 P1B-12D to ID A1J12.33 to ID P12-26 (S202-15)
From ID P13-93 (S202-3) From ID A1J14.49 From ID A1P10.48 From ID P11-147 (S510-4) From ID A1J9.31	to ID A1J10.48 to ID P11-52 (S510-1)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 319

Description:

This step verifies the wire path from W2 P2-42 to W2 P2-43. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13

Date: 04 March 2016

From ID A	AlJ6.13	to	ID	BUS 1
From ID H	BUS 1	to	ID	A1J9.19
From ID A	A1P9.19	to	ID	P11-18 (S509-3)
From ID I	P11-177 (S509-1)	to	ID	A1P10.10
From ID A	A1J10.10	to	ID	A1J12.38
From ID A	A1P12.38	to	ID	P12-59 (S202-1)
	,	to	ID	A1P14.34
From ID A				J2A-1C
From W2 I	P1A-1C	to	W2	P2-42 (ST J1-42)
From ST_3	J1-42	to	ST_	_J1-43
From W2 I	P2-43 (ST J1-43)	to	W2	P1A-2D
From ID 3	J2A-2D	to	ID	A1J14.36
From ID A	A1P14.36	to	ID	P13-55 (S201-50)
	P12-80 (S201-2)			
From ID A				A1J10.8
From ID A				P11-139 (S508-2)
	,			A1P9.25
From ID A	A1J9.25	to	ID	BUS 2
From ID H				A1J6.23
From ID A				P10-12 (S503-4)
	, , , , , , , , , , , , , , , , , , , ,			A1P8.26
From ID A				A1J15.50
From ID A	A1P15.50	to	ID	P20-3 (DMM-LO)

Step 320

Description:

This step verifies the wire path from W2 P2-44 to W2 P2-47. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13
From ID	A1J6.13	to	ID	BUS 1
From ID	BUS 1	to	ID	A1J9.23
From ID	A1P9.23	to	ID	P11-164 (S506-3)
From ID	P11-162 (S506-2)	to	ID	A1P10.1
From ID	A1J10.1	to	ID	A1J12.48
From ID	A1P12.48	to	ID	P12-44 (S701-2)
From ID	P13-41 (S701-26)	to	ID	A1P15.28
From ID	A1J15.28	to	ID	J2A-5B
From W2	P1A-5B	to	W2	P2-44 (ST J1-44)

Date: 04 March 2016

From ST_J1-44	to ST_J1-47
From W2 P2-47 (ST J1-47)	to W2 P1B-7F
From ID J2B-7F	to ID A1J12.45
From ID A1P12.45	to ID P12-95 (S202-38)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 321

Description:

This step verifies the wire path from W2 P2-46 to W2 P2-48. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49	to ID A1P15.49
From ID AlJ15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
	10
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-162 (S506-2)	
From ID A1J10.1	to ID A1J12.48
From ID A1P12.48	to ID P12-44 (S701-2)
	to ID A1P15.20
From ID A1J15.20	to ID J2A-8B
From W2 P1A-8B	to W2 P2-46 (ST J1-46)
T	- GE T1 40
From ST_J1-46	to ST_J1-48
From W2 P2-48 (ST J1-48)	to W2 P1A-1D
From ID J2A-1D	to ID A1J14.37
From ID A1P14.37	to ID P13-54 (S201-51)
11011 12 111111.07	66 12 113 31 (8201 31)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

Date: 04 March 2016

From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 322

Description:

This step verifies the wire path from W2 P2-49 to W2 P2-50. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID A1J15.49 From ID A1P8.28	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID P11-177 (S509-1)	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.10 to ID A1J12.38 to ID P12-59 (S202-1)
From ID P12-61 (S202-21) From ID A1J12.21 From W2 P1B-6A	to ID A1P12.21 to ID J2B-6A to W2 P2-49 (ST J1-49)
From ST_J1-49	to ST_J1-50
	to W2 P1B-8B to ID A1J12.26 to ID P12-25 (S202-6)
	to ID A1P14.50 to ID A1J10.50 to ID P11-244 (S510-2) to ID A1P9.31 to ID BUS 2
	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 323

Description:

This step verifies the wire path from W2 P2-52 to W1 P2-69. The DMM resource will be used to measure resistance UL= 10 ohms.

Date: 04 March 2016

Connection Path is as follows:

	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-177 (S509-1) From ID A1J10.10 From ID A1P12.38	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.10 to ID A1J12.38 to ID P12-59 (S202-1)
From ID P12-91 (S202-23) From ID A1J12.22 From W2 P1B-7C From ST_J1-52	to ID A1P12.22 to ID J2B-7C to W2 P2-52 (ST J1-52) to ST_J1-51
From W2 P2-51 (ST J1-51) From ID J2B-4F From ID A1P1.11	to W2 P1B-4F to ID A1J1.11 to ID P1-11 (DC4-LO)
From ID A1J15.50 From ID A1P7.38	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 324

Description:

This step verifies the wire path from W2 P2-53 to W2 P2-54. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13
From ID	A1J6.13	to	ID	BUS 1
From ID	BUS 1	to	ID	A1J9.19
From ID	A1P9.19	to	ID	P11-18 (S509-3)
From ID	P11-177 (S509-1)	to	ID	A1P10.10
From ID	A1J10.10	to	ID	A1J12.38
From ID	A1P12.38	to	ID	P12-59 (S202-1)
	P13-59 (S202-25)			A1P14.30
From ID	A1J14.30	to	ID	J2A-1B
From W2	P1A-1B	to	W2	P2-53 (ST J1-53)

Date: 04 March 2016

From ST_J1-53	to ST_J1-54
From W2 P2-54 (ST J1-54) From ID J2A-2F From ID A1P14.42	to W2 P1A-2F to ID A1J14.42 to ID P13-28 (S202-27)
From ID P13-93 (S202-3)	to ID A1P14.49
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 325

Description:

This step verifies the wire path from W2 P2-55 to W2 P2-56. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
	to ID A1J8.28
	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID AlJ6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
	to ID P11-18 (S509-3)
From ID P11-177 (S509-1)	
	to ID A1J12.38
From ID A1912.38	to ID P12-59 (S202-1)
FIOM ID AIFIZ.30	CO ID FIZ 35 (5202 I)
From ID P12-28 (S202-29)	to ID A1P12.35
From ID A1J12.35	to ID J2B-12F
From W2 P1B-12F	to W2 P2-55 (ST J1-55)
From ST_J1-55	to ST_J1-56
From W2 P2-56 (ST J1-56)	to W2 P1A-3E
From ID J2A-3E	to ID A1J14.38
From ID AlP14.38	to ID P13-85 (S201-52)
From ID P12-80 (S201-2)	to ID A1D12 40
From ID A1J12.40	to ID AlJ10.8
From ID A1012.40	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	· · · · · · · · · · · · · · · · · · ·
From ID A1J9.25	to ID BUS 2
110m 10 A107.23	CO 1D DOD 2

Date: 04 March 2016

From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 326

Description:

This step verifies the wire path from W2 P2-58 to W2 P2-60. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID AlJ15.49	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P13.13 (S701-49) From ID A1J15.17	to ID A1P15.17 to ID J1A-11E
From W2 P1A-11E	to W2 P2-58 (ST J1-58)
From ST_J1-58	to ST_J1-60
From W2 P2-60 (ST J1-60)	to W2 P1A-1F
From ID J2A-1F	to ID A1J14.43
From ID A1P14.43	to ID P13-60 (S202-28)
From ID P12-90 (S202-2)	to ID A1P12.36
	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 327

Description:

This step verifies the wire path from W2 P2-61 to W2 P2-62. The DMM resource will be used to measure resistance UL= 10 ohms.

Date: 04 March 2016

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-242 (S509-2) From ID A1J10.12 From ID A1P12.36	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.12 to ID A1J12.36 to ID P12-90 (S202-2)
From ID P13-57 (S202-12) From ID A1J14.41 From W2 P1A-3F	to ID A1P14.41 to ID J2A-3F to W2 P2-61 (ST J1-61)
From ST_J1-61	to ST_J1-62
From W2 P2-62 (ST J1-62)	to W2 P1B-13F
From ID J2B-13F	to ID A1J12.32
From ID A1P12.32	to ID P12-88 (S202-14)
From ID P13-29 (S202-4)	to ID A1P14.50
From ID A1J14.50	to ID A1J10.50
From ID A1P10.50	to ID P11-244 (S510-2)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID AlJ9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 328

Description:

This step verifies the wire path from W2 P2-63 to W2 P2-67. The DMM resource will be used to measure resistance UL= 10 ohms.

Ε	rom I	ID P20-2 (DMM-HI)	to	ID	A1P15.49
E	rom I	ID A1J15.49	to	ID	A1J8.28
E	rom I	ID A1P8.28	to	ID	P10-203 (S503-1)
E	rom I	D P10-77 (S503-3)	to	ID	A1P6.13
Ε	rom I	D A1J6.13	to	ID	BUS 1
E	rom I	D BUS 1	to	ID	A1J9.19
E	rom I	ID A1P9.19	to	ID	P11-18 (S509-3)
E	rom I	ID P11-242 (S509-2)	to	ID	A1P10.12
Ε	rom I	D A1J10.12	to	ID	A1J12.36

Date: 04 March 2016

From ID A1P12.36	to ID P12-90 (S202-2)
From ID P12-30 (S202-40) From ID A1J12.49	to ID A1P12.49 to ID J2B-5F
From W2 P1B-5F	to W2 P2-63 (ST J1-63)
From ST_J1-63	to ST_J1-67
From W2 P2-67 (ST J1-67)	to W2 P1B-12E
From ID J2B-12E	to ID A1J12.34
From ID A1P12.34	to ID P12-58 (S202-16)
From ID P13-29 (S202-4)	to ID A1P14.50
From ID A1J14.50	to ID A1J10.50
From ID A1P10.50	to ID P11-244 (S510-2)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 329

Description:

This step verifies the wire path from W2 P2-64 to W2 P2-69. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-177 (S509-1) From ID A1J10.10 From ID A1P12.38	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.10 to ID A1J12.38 to ID P12-59 (S202-1)
From ID P12-93 (S202-31) From ID A1J12.39 From W2 P1B-10F From ST_J1-64	to ID A1P12.39 to ID J2B-10F to W2 P2-64 (ST J1-64) to ST_J1-69
From W2 P2-69 (ST J1-69) From ID J2A-4D From ID A1P14.44	to W2 P1A-4D to ID A1J14.44 to ID P13-61 (S202-33)

Date: 04 March 2016

From ID P13-93 (S202-3)	to ID A1P14.49
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 330

Description:

This step verifies the wire path from W2 P2-65 to W2 P2-70. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID AlJ15.50	to ID A1P15.50 to ID A1J8.26 to ID P10-139 (S503-2) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-162 (S506-2) From ID A1J10.1 From ID A1P12.48	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.1 to ID A1J12.48 to ID P12-44 (S701-2)
From ID P12-72 (S701-28) From ID A1J13.23 From W2 P1B-9C	to ID A1P13.23 to ID J2B-9C to W2 P2-65 (ST J1-65)
From ST_J1-65	to ST_J1-70
From W2 P2-70 (ST J1-70) From ID J2B-14C From ID A1P2.31 From ID P10-23 (S101-5) From ID A1J2.4 From ID A1P1.3	to W2 P1B-14C to ID A1J2.31 to ID P10-87 (S101-6) to ID A1P2.4 to ID A1J1.3 to ID P1-10 (DC4-HI)
From ID P1-10 (DC4-HI) From ID A1J1.3 From ID A1P8.2 From ID P10-44 (S301-69) From ID A1J8.31	to ID A1J8.2 to ID P10-141 (S301-70)
From ID BUS 2 From ID A1P6.23 From ID P10-203 (S503-1)	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.28

Date: 04 March 2016

From ID A1J8.28 to ID A1J15.49 from ID A1P15.49 to ID P20-2 (DMM-HI)

Step 331

Description:

This step verifies the wire path from W2P3-A1 to W2P3-A10. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-12 (S503-4)	to	ID	A1P6.23
From ID	A1J6.23	to	ID	BUS 2
	DVG 0			31.70. 25
_	BUS 2			A1J9.35
_	A1P9.35			P11-93 (S404-1)
	P11-28 (S404-4)			A1P10.9
From ID	A1J10.9			J2B-3D
From W2	P1B-3D	to	W2	P3-A1 (ST J3-1)
From ST	т.т3-Л1	+0	СП	_J3-A10
1.10111 2.1	_03-A1	CO	51_	_0 3 - A1 0
From W2	P3-A10 (ST J3-19)	to	W2	P1A-5F
From ID	J2A-5F	to	ID	A1J9.7
From ID	A1P9.7	to	ID	P11-221 (S406-5)
From ID	P11-92 (S406-1)	to	ID	A1P9.49
From ID	A1J9.49	to	ID	BUS 4
From ID	DIIC A	+ 0	TD	A1J6.39
_				
	A1P6.39			P10-74 (S503-6)
	P10-139 (S503-2)			A1P8.26
	A1J8.26			A1J15.50
From ID	A1P15.50	to	ID	P20-3 (DMM-LO)

Step 332

Description:

This step verifies the wire path from W2 P3-A2 to W2 P3-B5. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From ID BUS 2	to ID A1J9.35
From ID A1P9.35	to ID P11-93 (S404-1)
From ID P11-220 (S404-5)	to ID A1P10.11

Date: 04 March 2016

From ID A1J10.11 From W2 P1B-2E	to ID J2B-2E to W2 P3-A2 (ST J3-3)
From ST_J3-A2	to ST_J3-B5
From W2 P3-B5 (ST J3-10) From ID J2A-14B From ID A1P10.17 From ID P11-94 (S405-1) From ID A1J9.47	to W2 P1A-14B to ID A1J10.17 to ID P11-61 (S405-4) to ID A1P9.47 to ID BUS 3
From ID BUS 3 From ID A1P6.31 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.31 to ID P10-10 (S503-5) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 333

Description:

This step verifies the wire path from W2P3-A3 to W2P3-B7. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.31 to ID BUS 3
From ID BUS 3 From ID A1P9.47 From ID P11-158 (S405-2) From ID A1J10.13 From W2 P1B-2D	to ID A1J9.47 to ID P11-94 (S405-1) to ID A1P10.13 to ID J2B-2D to W2 P3-A3 (ST J3-5)
From ST_J3-A3	to ST_J3-B7
From ID J2A-8F From ID A1P9.1	to W2 P1A-8F to ID A1J9.1 to ID P11-191 (S406-2) to ID A1P9.49 to ID BUS 4
From ID BUS 4 From ID A1P6.39 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.39 to ID P10-74 (S503-6) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 334

Description:

Date: 04 March 2016

This step verifies the wire path from W2P3-B4 to W2P3-B8. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J9.47
From ID A1P9.47	to ID P11-94 (S405-1)
From ID P11-222 (S405-3)	
From ID A1J10.15	to ID J2B-14A
From W2 P1B-14A	to W2 P3-B4 (ST J3-8)
From ST_J3-B4	to ST_J3-B8
From W2 P3-B8 (ST J3-16)	to W2 P1A-7F
From ID J2A-7F	to ID A1J9.3
From ID A1P9.3	to ID P11-95 (S406-3)
From ID P11-92 (S406-1)	to ID A1P9.49
From ID A1J9.49	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	
From ID A1J8.26	to ID A1J15.50
From ID A108.20 From ID A1P15.50	
FIOU ID AIPIO.30	to ID $P20-3$ (DMM-LO)

Step 335

Description:

This step verifies the wire path from W2P3-B6 to W2P3-B9. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.31 to ID BUS 3
From ID BUS 3 From ID A1P9.47 From ID P11-253 (S405-5) From ID A1J10.19 From W2 P1A-14C	to ID A1J9.47 to ID P11-94 (S405-1) to ID A1P10.19 to ID J2A-14C to W2 P3-B6 (ST J3-12)
From ST_J3-B6	to ST_J3-B9
From W2 P3-B9 (ST J3-18)	to W2 P1A-6F

Date: 04 March 2016

From ID J2A-6F	to ID A1J9.5
From ID A1P9.5	to ID P11-60 (S406-4)
From ID P11-92 (S406-1)	to ID A1P9.49
From ID A1J9.49	to ID BUS 4
From ID BUS 4 From ID A1P6.39 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.39 to ID P10-74 (S503-6) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 336

Description:

This step verifies the wire path from W2P4-A1 to W2P4-A10. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.31 to ID BUS 3
From ID BUS 3 From ID A1P15.45 From ID P12-34 (S751-3) From ID A1J13.35 From W2 P1B-11D	to ID A1J15.45 to ID P13-2 (S751-1) to ID A1P13.35 to ID J2B-11A to W2 P4-A1 (ST J4-1)
From ST_J4-A1	to ST_J4-A10
From W2 P4-A10 (ST J4-19) From ID J2B-11B From ID A1P13.37 From ID P13-66 (S751-2) From ID A1J15.47	to ID A1J13.37 to ID P12-1 (S751-4)
From ID BUS 4 From ID A1P6.39 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.39 to ID P10-74 (S503-6) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 337

Description:

This step verifies the wire path from W2P4-B1 to W2P4-A11. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) to ID A1P15.49

Date: 04 March 2016

From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13
From ID	A1J6.13	to	ID	BUS 1
From ID				A1J6.21
From ID	A1P6.21	to	ID	P10-92 (S403-1)
From ID	P10-191 (S403-2)	to	ID	A1P7.15
From ID	A1J7.15	to	ID	J2A-12F
From W2	P1A-12F	to	W2	P4-B1 (ST J4-2)
From ST_	_J4-B1	to	ST_	_J4-A11
Enom WO	D4 311 (CE T4 21)	ـ ـ	T.T ()	D1D 2D
	(/			P1B-3D
From ID				A1J10.9
	A1P10.9			P11-28 (S404-4)
	(/			A1P9.35
From ID	A1J9.35	to	ID	BUS 2
T TD	DIIG 0			7176 02
From ID				A1J6.23
_	A1P6.23			P10-12 (S503-4)
	P10-139 (S503-2)			A1P8.26
	A1J8.26			A1J15.50
From ID	A1P15.50	to	ID	P20-3 (DMM-LO)

Step 338

Description:

This step verifies the wire path from W2P4-A2 to W2P4-B5. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.31 to ID BUS 3
From ID BUS 3 From ID A1P15.45 From ID P12-65 (S751-5) From ID A1J13.39 From W2 P1B-12A	to ID A1J15.45 to ID P13-2 (S751-1) to ID A1P13.39 to ID J2B-12A to W2 P4-A2 (ST J4-3)
From ST_J4-A2	to ST_J4-B5
From W2 P4-B5 (ST J4-10) From ID J2B-12B From ID A1P13.41 From ID P13-66 (S751-2) From ID A1J15.47	to W2 P1B-12B to ID A1J13.41 to ID P12-33 (S751-6) to ID A1P15.47 to ID BUS 4
From ID BUS 4	to ID A1J6.39

Date: 04 March 2016

From ID A1P6.39 to ID P10-74 (S503-6) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 339

Description:

This step verifies the wire path from W2P4-B2 to W2P4-A5. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID AlJ15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID AlJ6.31	to ID BUS 3
From ID BUS 3	to ID A1J15.45
From ID A1P15.45	to ID P13-2 (S751-1)
From ID P13-68 (S751-7)	to ID A1P15.21
From ID AlJ15.21	to ID J2A-9A
From W2 P1A-9A	to W2 P4-B2 (ST J4-4)
From ST_J4-B2	to ST_J4-A5
From W2 P4-A5 (ST J4-9)	to W2 P1A-7A
From ID J2A-7A	to ID A1J15.23
From ID A1P15.23	to ID P13-36 (S751-8)
From ID P13-66 (S751-2)	to ID A1P15.47
From ID A1J15.47	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2	
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID $P20-3$ (DMM-LO)

Step 340

Description:

This step verifies the wire path from W2P4-A3 to W2P4-B6. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J15.45

Date: 04 March 2016

From ID A1P15.45	to ID P13-2 (S751-1)
From ID P13-67 (S751-9)	to ID A1P15.25
From ID A1J15.25	to ID J2A-6A
From W2 P1A-6A	to W2 P4-A3 (ST J4-5)
From ST_J4-A3	to ST_J4-B6
From W2 P4-B6 (ST J4-12)	to W2 P1A-5A
From ID J2A-5A	to ID A1J15.27
From ID A1P15.27	to ID P13-35 (S751-10)
From ID P13-66 (S751-2)	to ID A1P15.47
From ID A1J15.47	to ID BUS 4
From ID BUS 4	to ID AlJ6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 341

Description:

This step verifies the wire path from W2P4-B3 to W2P4-A6. The DMM resource will be used to measure resistance UL=10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.31 to ID BUS 3
From ID BUS 3 From ID A1P15.45 From ID P12-67 (S751-11) From ID A1J13.43 From W2 P1B-13A	to ID A1J15.45 to ID P13-2 (S751-1) to ID A1P13.43 to ID J2B-13A to W2 P4-B3 (ST J4-6)
From ST_J4-B3	to ST_J4-A6
From W2 P4-A6 (ST J4-11) From ID J2B-13B From ID A1P13.45 From ID P13-66 (S751-2) From ID A1J15.47	to W2 P1B-13B to ID A1J13.45 to ID P12-35 (S751-12) to ID A1P15.47 to ID BUS 4
From ID BUS 4 From ID A1P6.39 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.39 to ID P10-74 (S503-6) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Date: 04 March 2016

Step 342

Description:

This step verifies the wire path from W2P4-A4 to W2P4-A8. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID AlJ15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID AlJ6.31	to ID BUS 3
From ID BUS 3	to ID A1J15.45
From ID A1P15.45	to ID P13-2 (S751-1)
From ID P12-2 (S751-13)	to ID A1P13.47
From ID A1J13.47	to ID J2B-14A
From W2 P1B-14A	to W2 P4-A4 (ST J4-7)
From ST_J4-A4	to ST_J4-A8
From W2 P4-A8 (ST J4-15)	to W2 P1B-14B
From ID J2B-14B	to ID A1J13.49
From ID A1P13.49	to ID P12-66 (S751-14)
From ID P13-66 (S751-2)	to ID A1P15.47
From ID A1J15.47	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 343

Description:

This step verifies the wire path from W2P4-B4 to W2P4-B8. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.31 to ID BUS 3
From ID BUS 3 From ID A1P15.45 From ID P13-5 (S751-15) From ID A1J15.30 From W2 P1A-4A	to ID A1J15.45 to ID P13-2 (S751-1) to ID A1P15.30 to ID J2A-4A to W2 P4-B4 (ST J4-8)

Date: 04 March 2016

From ST_J4-B4	to ST_J4-B8			
From W2 P4-B8 (ST J4-16) From ID J2A-4B From ID A1P15.32 From ID P13-66 (S751-2) From ID A1J15.47	to W2 P1A-4B to ID A1J15.32 to ID P13-69 (S751-16) to ID A1P15.47 to ID BUS 4			
From ID BUS 4 From ID A1P6.39 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.39 to ID P10-74 (S503-6) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)			

Step 344

Description:

This step verifies the wire path from W2P4-A7 to W2P4-B11. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J6.21
From ID A1P6.21	to ID P10-92 (S403-1)
From ID P10-95 (S403-3)	to ID A1P7.17
From ID AlJ7.17	to ID J2A-11F
From W2 P1A-11F	to W2 P4-A7 (ST J4-13)
From ST_J4-A7	to ST_J4-B11
From W2 P4-B11 (ST J4-22)	to W2 P1B-3F
From W2 P4-B11 (ST J4-22) From ID J2B-3F	to W2 P1B-3F to ID A1J10.5
From ID J2B-3F	to ID A1J10.5
From ID J2B-3F From ID A1P10.5	to ID A1J10.5 to ID P11-190 (S404-2)
From ID J2B-3F From ID A1P10.5 From ID P11-93 (S404-1)	to ID A1J10.5 to ID P11-190 (S404-2) to ID A1P9.35
From ID J2B-3F From ID A1P10.5 From ID P11-93 (S404-1) From ID A1J9.35	to ID A1J10.5 to ID P11-190 (S404-2) to ID A1P9.35 to ID BUS 2
From ID J2B-3F From ID A1P10.5 From ID P11-93 (S404-1) From ID A1J9.35 From ID BUS 2	to ID A1J10.5 to ID P11-190 (S404-2) to ID A1P9.35 to ID BUS 2 to ID A1J6.23 to ID P10-12 (S503-4)
From ID J2B-3F From ID A1P10.5 From ID P11-93 (S404-1) From ID A1J9.35 From ID BUS 2 From ID A1P6.23	to ID A1J10.5 to ID P11-190 (S404-2) to ID A1P9.35 to ID BUS 2 to ID A1J6.23 to ID P10-12 (S503-4)
From ID J2B-3F From ID A1P10.5 From ID P11-93 (S404-1) From ID A1J9.35 From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2)	to ID A1J10.5 to ID P11-190 (S404-2) to ID A1P9.35 to ID BUS 2 to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26

Step 345

Description:

This step verifies the wire path from W2P4-B7 to W2P4-B9. The DMM resource will be used to measure resistance UL= 10 ohms.

Date: 04 March 2016

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.31 to ID BUS 3
From ID BUS 3 From ID A1P15.45 From ID P13-37 (S751-17) From ID A1J15.34 From W2 P1A-3A	to ID A1J15.45 to ID P13-2 (S751-1) to ID A1P15.34 to ID J2A-3A to W2 P4-B7 (ST J4-14)
From ST_J4-B7	to ST_J4-B9
From W2 P4-B9 (ST J4-18) From ID J2A-3B From ID A1P15.36 From ID P13-66 (S751-2) From ID A1J15.47	to W2 P1A-3B to ID A1J15.36 to ID P13-4 (S751-18) to ID A1P15.47 to ID BUS 4
From ID BUS 4 From ID A1P6.39 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.39 to ID P10-74 (S503-6) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 346

Description:

This step verifies the wire path from W2P4-A9 to W2P4-A12. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1	to ID A1J6.21
From ID A1P6.21	to ID P10-92 (S403-1)
From ID P10-60 (S403-4)	to ID A1P7.19
From ID A1J7.19	to ID J2A-10F
From W2 P1A-10F	to W2 P4-A9 (ST J4-17)
From ST_J4-A9	to ST_J4-A12
From W2 P4-A12 (ST J4-23)	to W2 P1B-2E
From ID J2B-2E	to ID A1J10.11
From ID A1P10.11	to ID P11-220 (S404-5)
From ID P11-93 (S404-1)	to ID A1P9.35

Date: 04 March 2016

From ID A1	J9.35	to	ID	BUS 2
From ID BU	S 2	to	ID	A1J6.23
From ID A1	P6.23	to	ID	P10-12 (S503-4)
From ID P1	0-139 (S503-2)	to	ID	A1P8.26
From ID A1	J8.26	to	ID	A1J15.50
From ID A1	P15.50	to	ID	P20-3 (DMM-LO)

Step 347

Description:

This step verifies the wire path from W2P4-B10 to W2P4-B14. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J6.21
From ID A1P6.21	to ID P10-92 (S403-1)
From ID P10-221 (S403-5)	to ID A1P7.21
From ID A1J7.21	to ID J2A-9F
From W2 P1A-9F	to W2 P4-B10 (ST J4-20)
From ST_J4-B10	to ST_J4-B14
From W2 P4-B14 (ST J4-28)	to W2 P1B-3E
From ID J2B-3E	to ID A1J10.7
From ID A1P10.7	to ID P11-254 (S404-3)
From ID P11-93 (S404-1)	to ID A1P9.35
From ID A1J9.35	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 348

Description:

This step verifies the wire path from W2P4-A13 to W2P4-A17. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49	
From ID	A1J15.49	to	ID	A1J8.28	
From ID	A1P8.28	to	ID	P10-203	(S503-1)
From ID	P10-10 (S503-5)	to	ID	A1P6.31	

Date: 04 March 2016

From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J9.47
From ID A1P9.47	to ID P11-94 (S405-1)
From ID P11-158 (S405-2)	to ID A1P10.13
From ID AlJ10.13	to ID J2B-2D
From W2 P1B-2D	to W2 P4-A13 (ST J4-25)
From ST_J4-A13	to ST_J4-A17
From W2 P4-A17 (ST J4-33)	to W2 P1A-8F
From ID J2A-8F	to ID A1J9.1
From ID A1P9.1	to ID P11-191 (S406-2)
From ID P11-92 (S406-1)	to ID A1P9.49
From ID AlJ9.49	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 349

Description:

This step verifies the wire path from W2P4-A14 to W2P4-A18. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-10 (S503-5) From ID A1J6.31	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.31 to ID BUS 3
From ID BUS 3 From ID A1P9.47 From ID P11-222 (S405-3) From ID A1J10.15 From W2 P1A-14A	to ID A1J9.47 to ID P11-94 (S405-1) to ID A1P10.15 to ID J2A-14A to W2 P4-A14 (ST J4-27)
From ST_J4-A14	to ST_J4-A18
From W2 P4-A18 (ST J4-35) From ID J2A-7F From ID A1P9.3 From ID P11-92 (S406-1) From ID A1J9.49	to W2 P1A-7F to ID A1J9.3 to ID P11-95 (S406-3) to ID A1P9.49 to ID BUS 4
From ID BUS 4 From ID A1P6.39 From ID P10-139 (S503-2) From ID A1J8.26	to ID A1J6.39 to ID P10-74 (S503-6) to ID A1P8.26 to ID A1J15.50

Date: 04 March 2016

From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 350

Description:

This step verifies the wire path from W2P4-A15 to W2P4-A19. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J9.47
From ID A1P9.47	to ID P11-94 (S405-1)
From ID P11-61 (S405-4)	
From ID A1J10.17	to ID J2A-14B
From W2 P1A-14B	to W2 P4-A15 (ST J4-29)
From ST_J4-A15	to ST_J4-A19
From W2 P4-A19 (ST J4-37)	to W2 P1A-6F
From ID J2A-6F	to ID A1J9.5
From ID A1P9.5	to ID P11-60 (S406-4)
From ID P11-92 (S406-1)	to ID A1P9.49
From ID A1J9.49	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 351

Description:

This step verifies the wire path from W2P4-A16 to W2P4-A20. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J9.47
From ID A1P9.47	to ID P11-94 (S405-1)
From ID P11-253 (S405-5)	to ID A1P10.19
From ID A1J10.19	to ID J2A-14C

Date: 04 March 2016

From W2 P1A-14C	to W2 P4-A16 (ST J4-31)
From ST_J4-A16	to ST_J4-A20
From W2 P4-A20 (ST J4-39)	to W2 P1A-5F
From ID J2A-5F	to ID A1J9.7
From ID A1P9.7	to ID P11-221 (S406-5)
From ID P11-92 (S406-1)	to ID A1P9.49
From ID A1J9.49	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

2.7 MODULE 4 W3 CDA GRENADE RELAY SPECIFIC ID TESTS

Refer to Reference Drawings when diagnosing connection paths.

Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF TEST PWB, A2).pdf and 13020A7301 (CABLE, W3, SCHEMATIC).pdf in section 1.4 during review of the following steps.

Step 401

Description:

This step verifies the wire path from W3 P2-1 to W3 P2-2. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-14 (S201-13) From ID A1J13.5 From W3 P1B-14C From ST_J2-1	to ID A1P13.5 to ID J1B-14C to W3 P2-1 (ST J2-1) to ST_J2-2
From W3 P2-2 (ST J2-2) From ID J1B-13C From ID A1P13.6	to W3 P1B-13C to ID A1J13.6 to ID P12-78 (S201-14)

Date: 04 March 2016

From ID P12-52 (S201-4) From ID A1J12.44 From ID A1P10.4 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.44 to ID A1J10.4 to ID P11-71 (S507-2) to ID A1P9.27 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 402

Description:

This step verifies the wire path from W3 P2-3 to W3 P2-4. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-79 (S201-5) From ID A1J13.1 From W3 P1B-14A	to ID A1P13.1 to ID J1B-14A to W3 P2-3 (ST J2-3)
From ST_J2-3	to ST_J2-4
From W3 P2-4 (ST J2-4) From ID J1A-2E From ID A1P14.10	to W3 P1A-2E to ID A1J14.10 to ID P13-83 (S201-27)
From ID P12-20 (S201-3) From ID A1J12.46 From ID A1P10.2 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.46 to ID A1J10.2 to ID P11-39 (S507-1) to ID A1P9.27 to ID BUS 2
	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Date: 04 March 2016

Step 403

Description:

This step verifies the wire path from W3 P2-5 to W3 P2-9. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-242 (S509-2)	to ID A1P10.12
From ID A1J10.12	to ID A1J12.36
From ID A1P12.36	to ID P12-90 (S202-2)
From ID P12-63 (S202-46)	to ID A1P12.16
From ID A1J12.16	to ID J1B-4A
From W3 P1B-4A	to W3 P2-5 (ST J2-5)
From ST_J2-5	to ST_J2-9
From W3 P2-9 (ST J2-9) From ID J1B-13A From ID A1P13.2	to W3 P1B-13A to ID A1J13.2 to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 404

Description:

This step verifies the wire path from W3 P2-6 to W3 P2-7. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13

Date: 04 March 2016

From ID AlJ6.13	to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-82 (S201-29) From ID A1J12.7 From W3 P1B-7A	to ID A1P12.7 to ID J1B-7A to W3 P2-6 (ST J2-6)
From ST_J2-6	to ST_J2-7
From W3 P2-7 (ST J2-7) From ID J1B-13B From ID A1P13.4	to W3 P1B-13B to ID A1J13.4 to ID P12-13 (S201-8)
From ID P12-52 (S201-4) From ID A1J12.44 From ID A1P10.4 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.44 to ID A1J10.4 to ID P11-71 (S507-2) to ID A1P9.27 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 405

Description:

This step verifies the wire path from W3 P2-8 to W3 P2-11. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P12-46 (S201-7)	to ID A1P13.3
From ID A1J13.3	to ID J1B-14B
From W3 P1B-14B	to W3 P2-8 (ST J2-8)

Date: 04 March 2016

From ST_J2-8	to ST_J2-11
From W3 P2-11 (ST J2-11) From ID J1B-5C	to W3 P1B-5C to ID A1J12.15
From ID A1P12.15	to ID P12-31 (S202-45)
From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 406

Description:

This step verifies the wire path from W3 P2-10 to W3 P2-12. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID AlJ6.13	to ID BUS 1
T T DWG 1	
From ID BUS 1	to ID A1J9.19
	to ID P11-18 (S509-3)
From ID P11-177 (S509-1)	
	to ID A1J12.38
From ID A1P12.38	to ID P12-59 (S202-1)
F TD D12 00 (G200 10)	L. TD 71D14 10
	to ID A1P14.19
From ID A1J14.19	to ID J1A-6E
From W3 P1A-6E	to W3 P2-10 (ST J2-10)
From ST_J2-10	to ST_J2-12
From W3 P2-12 (ST J2-12)	to W3 P1A-1C
From ID J1A-1C	to ID AlJ14.5
From ID A1P14.5	
From ID AIP14.5	to ID P13-49 (S201-17)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID AlJ12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-12 (S508-4)	
From ID A1J9.25	to ID BUS 2
1100.10	

Date: 04 March 2016

From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 407

Description:

This step verifies the wire path from W3 P2-13 to W3 P2-15. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P13-15 (S201-19) From ID A1J14.6 From W3 P1A-2C	to ID A1P14.6 to ID J1A-2C to W3 P2-13 (ST J2-13)
From ST_J2-13	to ST_J2-15
From W3 P2-15 (ST J2-15) From ID J1A-2A From ID A1P14.2	to W3 P1A-2A to ID A1J14.2 to ID P13-14 (S201-10)
From ID P12-52 (S201-4) From ID A1J12.44 From ID A1P10.4 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.44 to ID A1J10.4 to ID P11-71 (S507-2) to ID A1P9.27 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID AlJ6.23 to ID P10-12 (S503-4) to ID AlP8.26 to ID AlJ15.50 to ID P20-3 (DMM-LO)

Step 408

Description:

This step verifies the wire path from W3 P2-14 to W3 P2-16. The DMM resource will be used to measure resistance UL= 10 ohms.

Date: 04 March 2016

Connection Path is as follows:

From I	ID ID ID	P20-2 (DMM-HI) A1J15.49 A1P8.28 P10-77 (S503-3) A1J6.13	to to to	ID ID ID	A1P15.49 A1J8.28 P10-203 (S503-1) A1P6.13 BUS 1
From :	ID ID ID	BUS 1 A1P9.15 P11-139 (S508-2) A1J10.8 A1P12.40	to to to	ID ID ID	A1J9.15 P11-77 (S508-3) A1P10.8 A1J12.40 P12-80 (S201-2)
From 3	ID	P12-50 (S201-30) A1J12.8 P1B-7B	to	ID	A1P12.8 J1B-7B P2-14 (ST J2-14)
From S	ST_	_J2-14	to	ST_	_J2-16
From 3	ID	P2-16 (ST J2-16) J1A-5F A1P14.18	to	ID	P1A-5F A1J14.18 P13-89 (S202-18)
From I	ID ID ID	P12-90 (S202-2) A1J12.36 A1P10.12 P11-17 (S509-4) A1J9.29	to to to	ID ID ID	A1P12.36 A1J10.12 P11-242 (S509-2) A1P9.29 BUS 2
From :	ID ID ID	BUS 2 A1P6.23 P10-139 (S503-2) A1J8.26 A1P15.50	to to to	ID ID ID	A1J6.23 P10-12 (S503-4) A1P8.26 A1J15.50 P20-3 (DMM-LO)

Step 409

Description:

This step verifies the wire path from W3 P2-17 to W3 P2-20. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-242 (S509-2)	to ID A1P10.12
From ID A1J10.12	to ID A1J12.36

Date: 04 March 2016

From ID A1P12.36	to ID P12-90 (S202-2)
From ID P13-24 (S202-20) From ID A1J14.20 From W3 P1A-6F	to ID A1P14.20 to ID J1A-6F to W3 P2-17 (ST J2-17)
From ST_J2-17	to ST_J2-20
From W3 P2-20 (ST J2-20) From ID J1A-1D From ID A1P14.7	to W3 P1A-1D to ID A1J14.7 to ID P13-79 (S201-20)
From ID P12-80 (S201-2) From ID A1J12.40 From ID A1P10.8 From ID P11-12 (S508-4) From ID A1J9.25	to ID A1P12.40 to ID A1J10.8 to ID P11-139 (S508-2) to ID A1P9.25 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID AlJ6.23 to ID P10-12 (S503-4) to ID AlP8.26 to ID AlJ15.50 to ID P20-3 (DMM-LO)

Step 410

Description:

This step verifies the wire path from W3 P2-19 to W3 P2-21. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P13-16 (S201-18) From ID A1J14.28 From W3 P1A-10F From ST_J2-19	to ID A1P14.28 to ID J1A-10F to W3 P2-19 (ST J2-19) to ST_J2-21
From W3 P2-21 (ST J2-21) From ID J1B-9A From ID A1P12.1	to W3 P1B-9A to ID A1J12.1 to ID P12-48 (S201-15)

Date: 04 March 2016

From ID P12-20 (S201-3) From ID A1J12.46 From ID A1P10.2 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.46 to ID A1J10.2 to ID P11-39 (S507-1) to ID A1P9.27 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 411

Description:

This step verifies the wire path from W3 P2-22 to W3 P2-24. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-15 (S201-16) From ID A1J12.2 From W3 P1B-9B	to ID J1B-9B to W3 P2-22 (ST J2-22)
	to ST_J2-24 to W3 P1B-14D to ID A1J13.7 to ID P12-4 (S701-3)
From ID P12-76 (S701-1) From ID A1J12.50 From ID A1P10.3 From ID P11-195 (S506-4) From ID A1J9.33	to ID A1P12.50 to ID A1J10.3 to ID P11-194 (S506-1) to ID A1P9.33 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Date: 04 March 2016

Step 412

Description:

This step verifies the wire path from W3 P2-23 to W3 P2-26. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P13-51 (S201-28) From ID A1J14.11 From W3 P1A-1F	to ID A1P14.11 to ID J1A-1F to W3 P2-23 (ST J2-23)
From ST_J2-23	to ST_J2-26
From W3 P2-26 (ST J2-26) From ID J1B-7C From ID A1P12.9	to W3 P1B-7C to ID A1J12.9 to ID P12-19 (S201-31)
From ID P12-20 (S201-3) From ID A1J12.46 From ID A1P10.2 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1J10.2 to ID P11-39 (S507-1)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 413

Description:

This step verifies the wire path from W3 P2-25 to W3 P2-29. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13

Date: 04 March 2016

From ID AlJ6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P13-52 (S201-33)	to ID A1P14.12
From ID AlJ14.12	to ID J1A-2F
From W3 P1A-2F	to W3 P2-25 (ST J2-25)
From ST_J2-25	to ST_J2-29
From W3 P2-29 (ST J2-29)	to W3 P1B-13D
From ID J1B-13D	to ID A1J13.8
From ID A1P13.8	to ID P12-68 (S701-4)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 414

Description:

This step verifies the wire path from W3 P2-28 to W3 P2-31. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P13-47 (S201-9)	to ID A1P14.1
From ID A1J14.1	to ID J1A-1A
From W3 P1A-1A	to W3 P2-28 (ST J2-28)

Date: 04 March 2016

From ST_J2-28	to ST_J2-31
From W3 P2-31 (ST J2-31)	to W3 P1B-9C
From ID J1B-9C	to ID A1J12.3
From ID A1P12.3	to ID P12-51 (S201-21)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 415

Description:

This step verifies the wire path from W3 P2-30 to W3 P2-40. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-83 (S201-32) From ID A1J12.10 From W3 P1B-6A From ST_J2-30	to ID A1P12.10 to ID J1B-6A to W3 P2-30 (ST J2-30) to ST_J2-40
From W3 P2-40 (ST J2-40) From ID J1A-1E From ID A1P14.9	to W3 P1A-1E to ID A1J14.9 to ID P13-17 (S201-26)
From ID P12-52 (S201-4) From ID A1J12.44 From ID A1P10.4 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1J10.4 to ID P11-71 (S507-2)

Date: 04 March 2016

From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 416

Description:

This step verifies the wire path from W3 P2-32 to W3 P2-33. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-17 (S201-23)	
From ID A1J12.5	to ID J1B-8B
From W3 P1B-8B	to W3 P2-32 (ST J2-32)
From ST_J2-32	to ST_J2-33
From W3 P2-33 (ST J2-33)	to W3 P1A-2D
From ID J1A-2D	to ID AlJ14.8
From ID A1P14.9	to ID P13-50 (S201-25)
From ID P12-20 (S201-3) From ID A1J12.46 From ID A1P10.2 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.46 to ID A1J10.2 to ID P11-39 (S507-1) to ID A1P9.27 to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 417

Description:

This step verifies the wire path from W3 P2-34 to W3 P2-35. The DMM resource will be used to measure resistance UL= 10 ohms.

Date: 04 March 2016

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1J8.28 to ID P10-203 (S503-1)
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P13-19 (S201-34)	to ID A1P14.13
From ID A1J14.13	to ID J1A-3E
From W3 P1A-3E	to W3 P2-34 (ST J2-34)
From ST_J2-34	to ST_J2-35
From W3 P2-35 (ST J2-35) From ID J1A-1B From ID A1P14.3	to W3 P1A-1B to ID A1J14.3 to ID P13-80 (S201-11)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 418

Description:

This step verifies the wire path from W3 P2-36 to W3 P2-38. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40

Date: 04 March 2016

From ID A1P12.40	to ID P12-80 (S201-2)
From ID P13-48 (S201-12) From ID A1J14.4 From W3 P1A-2B	to ID A1P14.4 to ID J1A-2B to W3 P2-36 (ST J2-36)
From ST_J2-36	to ST_J2-38
From W3 P2-38 (ST J2-38) From ID J1B-8A From ID A1P12.4	to W3 P1B-8A to ID A1J12.4 to ID P12-18 (S201-22)
From ID P12-52 (S201-4) From ID A1J12.44 From ID A1P10.4 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.44 to ID A1J10.4 to ID P11-71 (S507-2) to ID A1P9.27 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 419

Description:

This step verifies the wire path from W3 P2-39 to W3 P2-40. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-177 (S509-1) From ID A1J10.10 From ID A1P12.38	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.10 to ID A1J12.38 to ID P12-59 (S202-1)
From ID P13-91 (S202-35) From ID A1J14.23 From W3 P1A-8E From ST_J2-39	to ID A1P14.23 to ID J1A-8E to W3 P2-39 (ST J2-39) to ST_J2-40
From W3 P2-40 (ST J2-40) From ID J1A-1E From ID A1P14.9	to W3 P1A-1E to ID A1J14.9 to ID P13-17 (S201-26)

Date: 04 March 2016

From ID P12-52 (S201-4) to ID A1P12.44
From ID A1J12.44 to ID A1J10.4
From ID A1P10.4 to ID P11-71 (S507-2)
From ID P11-72 (S507-4) to ID A1P9.27
From ID A1J9.27 to ID BUS 2

From ID BUS 2 to ID A1J6.23
From ID A1P6.23 to ID P10-12 (S503-4)
From ID P10-139 (S503-2) to ID A1P8.26
From ID A1J8.26 to ID A1J15.50
From ID A1P15.50 to ID P20-3 (DMM-LO)

2.8 MODULE 5 W4 GCU LOGIC SPECIFIC ID TESTS

Refer to Reference Drawings when diagnosing connection paths.

Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF TEST PWB, A2).pdf and 13020A7401 (CABLE, W4, SCHEMATIC).pdf in section 1.4 during review of the following steps.

Step 501

Description:

This step verifies proper receive path components associated with DTS ch 23 while connecting W4 with loopback.

From ID P12-29 (S202-32)	to ID A1P12.41
From ID A1J12.41	to ID J2B-9F
From W4 P1B-9F	to W4 P3-B4 (ST J6-8)
From ST_J6-B4	to ST_J5-1
From W4 P2-1 (ST J5-1)	to W4 P1A-13A
From ID J2A-13A	to ID A1U4.3
From ID J2A-13A	to ID A1R12.1
From ID A1R12.2	to ID A1J1.2 (+15V)
From ID A1U4.2	to ID A1J5.36
From ID A1P5.36	to ID P6-17 (DTS CH23)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V) From ID A1J1.1 (+5V)	to ID A1U4.1 to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U5.1

Date: 04 March 2016

From ID A1J1.1 (+5V)	to ID AlC1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID AlJ1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID AlC11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID AlU1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 502

Description:

This step verifies connectivity from S202-32 out through the proper receive path components associated with DTS ch 23 while connecting W4 with loopback and applying 0 volts out on ID 2B-9F.

From ID	P1-10 (DC4-HI)	to	ID	A1P1.3
From ID	A1J1.3	to	ID	A1J8.2

Date: 04 March 2016

O+ Iviai	C11 Z	.010			
From	TD	A1P8.2	tο	TD	P10-141 (S301-70)
		P10-44 (S301-69)			
From	TD	A1J8.31	to	TD	BUS 2
		-			
_					A1J9.29
			to	ID	P11-17 (S509-4)
From	ID	P11-242 (S509-2)	to	ID	A1P10.12
From	ID	A1J10.12	to	ID	A1J12.36
From	ID	A1P12.36	to	ID	P12-90 (S202-2)
From	ID	P12-29 (S202-32)	to	ID	A1P12.41
		A1J12.41			J2B-9F
		P1B-9F			P3-B4 (ST J6-8)
I' I OIII	w	FIB-91	LU	w	F3-B4 (S1 00-0)
Enom	СШ	16 D4	۰.	СШ	TE 1
FLOIII	21_	_J6-B4	LO	21_	_J5-1
	T.7 A	DO 1 (CF TF 1)		T.T. 4	D1 7 1 2 7
					P1A-13A
		J2A-13A			A1U4.3
		J2A-13A			A1R12.1
From	ID	A1R12.2	to	ID	A1J1.2 (+15V)
From	ID	A1U4.2	to	ID	A1J5.36
From	ID	A1P5.36	to	ID	P6-17 (DTS CH23)
From	ID	P1-1 (DC1-HI)	to	ID	A1P1.1
		AlJ1.1 (+5V)			A1U1.1
		AlJ1.1 (+5V)			A1U1.13
		AlJ1.1 (+5V)			A1U2.1
		A1J1.1 (+5V)			
					A1U2.13
		A1J1.1 (+5V)			A1U3.1
		A1J1.1 (+5V)			A1U3.13
		A1J1.1 (+5V)			A1U4.1
From	ID	A1J1.1 (+5V)			A1U5.1
From	ID	A1J1.1 (+5V)	to	ID	A1U6.1
From	ID	A1J1.1 (+5V)	to	ID	A1U7.1
From	ID	A1J1.1 (+5V)	to	ID	Alu8.1
From	ID	A1J1.1 (+5V)	to	ID	AlC1.1
		A1J1.1 (+5V)			A1C3.1
		AlJ1.1 (+5V)			A1C5.1
		AlJ1.1 (+5V)			A1C7.1
		•			
		A1J1.1 (+5V)			A1C8.1
		A1J1.1 (+5V)			A1C9.1
		A1J1.1 (+5V)			A1C10.1
		A1J1.1 (+5V)			A1C11.1
From	ID	P1-2 (DC1-LO)	to	ID	A1P1.9
From	ID	A1J1.9	to	GRO	DUND
From	ID	P1-4 (DC2-HI)	to	ID	A1P2.2
From	ID	A1J2.2 (+15V)	to	ID	A1U1.16
		A1J2.2 (+15V)			A1U2.16
		AlJ2.2 (+15V)			A1U3.16
		A1J2.2 (+15V)			A1C2.1
		A1J2.2 (+15V)			A1C4.1
		A1J2.2 (+15V)			A1C6.1
		P1-5 (DC2-LO)			A1P1.10
From	ID	A1J1.10	to	GRO	DUND

Date: 04 March 2016

From II	D A1C1.2	to	GROUND
From II	D A1C2.2	to	GROUND
From II	D A1C3.2	to	GROUND
From II	D A1C4.2	to	GROUND
From II	D A1C5.2	to	GROUND
From II	D A1C6.2	to	GROUND
From II	D A1C7.2	to	GROUND
From II	D A1C8.2	to	GROUND
From II	D A1C9.2	to	GROUND
From II	D A1C10.2	to	GROUND
From II	D A1C11.2	to	GROUND
From II	A1U1.8	to	GROUND
From II	D A1U2.8	to	GROUND
From II	D A1U3.8	to	GROUND
From II	D A1U4.8	to	GROUND
From II	D A1U5.8	to	GROUND
From II	D A1U6.8	to	GROUND
From II	D A1U7.8	to	GROUND
From II		to	GROUND
	D P7-24 (DTS GCH 40)	to	ID A1P5.3
From II			GROUND
	D P6-64 (DTS GCH 7)	to	ID A1P5.1
From II	D A1J5.1	to	GROUND

Step 503

Description:

This step verifies the wire path from W4 P2-1 to W4 P3-B4. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-86 (S201-48) From ID A1J12.28 From W4 P1B-14E From ST_J5-1	to ID A1P12.28 to ID J2B-14E to W4 P2-1 (ST J5-1) to ST_J6-B4
From W4 P3-B4 (ST J6-8) From ID J2B-9F From ID A1P12.41	to W4 P1B-9F to ID A1J12.41 to ID P12-29 (S202-32)

Date: 04 March 2016

From ID P12-90 (S202-2)	to ID AlP12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID AlJ9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 504

Description:

This step verifies the wire path from DC4-LO through W4P2-2 to W4P2-19 via ST board and back to S201-52. The DMM will be used to measure resistance UL= 10 ohms as DC4-LO will tie the path to GND.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P13-85 (S201-52) From ID A1J14.38 From W4 P1A-3E From ST_J5-19	to ID A1P14.38 to ID J2A-3E to W4 P2-19 (ST J5-19) to ST_J5-2
From W4 P2-2 (ST J5-2) From ID J2B-4F From ID A1P1.11	to W4 P1B-4F to ID A1J1.11 to ID P1-11 (DC4-LO)
	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 505

Description:

Date: 04 March 2016

This step verifies the wire path from W4 P2-3 to W4 P3-A5. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID A1P8.28	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-177 (S509-1) From ID A1J10.10 From ID A1P12.38	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.10 to ID A1J12.38 to ID P12-59 (S202-1)
From ID P13-25 (S202-11) From ID A1J14.40 From W4 P1A-1E	to ID A1P14.40 to ID J2A-1E to W4 P2-3 (ST J5-3)
From ST_J5-3	to ST_J6-A5
From W4 P3-B3 (ST J6-9) From ID J2A-2E From ID A1P14.39	to W4 P1A-2E to ID A1J14.39 to ID P13-56 (S202-9)
	to ID A1P14.49 to ID A1J10.48 to ID P11-52 (S510-1) to ID A1P9.31 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 506

Description:

This step verifies connectivity from DC4-HI out through W4 and to the proper receive path associated with DTS ch 41 while connecting W4 with loopback and applying 15 volts out on ID 2B-14C.

From ID P1-10 (DC4-HI)	to ID AlP1.3
From ID AlJ1.3	to ID A1J2.4
From ID A1P2.4	to ID P10-23 (S101-5)
From ID P10-87 (S101-6)	to ID A1P2.31
From ID A1J2.31	to ID J2B-14C
From W4 P1B-14C	to W4 P2-4 (ST J5-4)

Date: 04 March 2016

From ST_J5-4	to ST_J6-B1
From W4 P3-B1 (ST J6-2)	to W4 P1B-1C
From ID J2B-1C	to ID A1U6.5
From ID A1U6.4	to ID A1J5.26
From ID A1P5.26	to ID P7-55 (DTS CH41)
110 12 11113.20	20 12 17 33 (218 0111)
From ID P1-1 (DC1-HI)	to ID AlP1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID AlJ1.1 (+5V)	to ID A1U6.1
From ID AlJ1.1 (+5V)	to ID A1U7.1
From ID AlJ1.1 (+5V)	to ID A1U8.1
From ID AlJ1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID AlJ1.9 From ID P1-4 (DC2-HI)	to GROUND to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID AlC6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID AlU1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND

Date: 04 March 2016

From ID A1U6.8 to GROUND
From ID A1U7.8 to GROUND
From ID A1U8.8 to GROUND
From ID P7-24 (DTS GCH 40) to ID A1P5.3
From ID A1J5.3 to GROUND
From ID P6-64 (DTS GCH 7) to ID A1P5.1
From ID A1J5.1 to GROUND

Step 507

Description:

This step verifies the wire path from W4P2-5 to W4P3-A10. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1J8.28 to ID P10-203 (S503-1)
From ID BUS 1 From ID A1P9.19 From ID P11-177 (S509-1) From ID A1J10.10 From ID A1P12.38	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.10 to ID A1J12.38 to ID P12-59 (S202-1)
From ID P12-24 (S202-7) From ID A1J12.29 From W4 P1B-14F	to ID A1P12.29 to ID J2B-14F to W4 P2-5 (ST J5-5)
From ST_J5-5	to ST_J6-A10
From W4 P3-A10 (ST J6-19) From ID J2A-3F From ID A1P14.41	to W4 P1A-3F to ID A1J14.41 to ID P13-57 (S202-12)
From ID P13-29 (S202-4) From ID A1J14.50 From ID A1P10.50 From ID P11-147 (S510-4) From ID A1J9.31	to ID A1P14.50 to ID A1J10.50 to ID P11-244 (S510-2) to ID A1P9.31 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 508

Description:

Date: 04 March 2016

This step verifies the wire path from W4 P2-6 to W4 P2-8. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.12 to ID A1J12.36 to ID P12-90 (S202-2)
From ID P12-56 (S202-8) From ID A1J12.30 From W4 P1B-13D	to ID A1P12.30 to ID J2B-13D to W4 P2-6 (ST J5-6)
From ST_J5-6	to ST_J5-8
From W4 P2-8 (ST J5-8) From ID J2B-11F From ID A1P12.37	to W4 P1B-11F to ID A1J12.37 to ID P12-60 (S202-30)
	to ID A1J10.50 to ID P11-244 (S510-2)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 509

Description:

This step will verify the connections from the S701 switch out through W4 to FET#1 and FET#2 Gate inputs. Functionality of the FETs will be verified.

From ID P13-73 (S701-25) From ID A1J15.26 From W4 P1A-6B	to ID A1P15.26 to ID J2A-6B to W4 P2-28 (ST J5-28)
From W4 P2-28 (ST J5-28)	to W4 P1B-4E
From ID J2B-4E	to ID A1D1.A

Date: 04 March 2016

From ID AlD1.K	to ID A1Q1.1
From ID AlD1.K	to ID A1CR2.K
From ID A1D1.K	to ID A1R20.1
From ID P13-41 (S701-26)	to ID A1P15.28
From ID A1J15.28	to ID J2A-5B
From W4 P1A-5B	to W4 P2-9 (ST J5-9)
	,
From W4 P2-9 (ST J5-9)	to W4 P1B-5E
From ID J2B-5E	to ID A1Q2.1
From ID J2B-5E	to ID A1CR3.K
From ID J2B-5E	to ID A1R21.1
From W4 P2-8 (UUT J1-8)	to W4 P1B-6E
From ID J2B-6E	to IDA1Q2.3
From ID J2B-6E	to ID A1R22.1
From ID J2B-6E	to ID A1CR3.A
From ID J2B-6E	to ID A1R21.2
From ID J2B-6E	to ID A1R23.1
From ID A1R23.2	to ID J2B-8E
From ID A1R23.2	to ID A1R24.1
From ID A1R23.2	to ID A1C12.1
From ID A1C12.2	to GROUND
From ID A1R24.2	to GROUND
From ID A1R22.2	to GROUND
From ID A1J1.3	to ID A1Q1.2
From ID A1Q1.3	to ID A1Q2.2
From ID A1Q1.3	to ID A1CR2.A
From ID A1Q1.3	to ID A1 R20.2
From W4 P1B-8E	to W4 P1A-14D
From ID J1A-14D	to ID A1U5.3
From ID A1U5.2	to ID A1J5.32
From ID A1P5.32	to ID P6-13 (DTS CH27)

Step 510

Description:

This step verifies the wire path from W4 P2-9 to W4 P2-24. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID $P20-2$ (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-162 (S506-2)	to ID A1P10.1
From ID A1J10.1	to ID A1J12.48
From ID A1P12.48	to ID P12-44 (S701-2)

Date: 04 March 2016

From ID P13-41 (S701-26) From ID A1J15.28 From W4 P1A-5B	to ID A1P15.28 to ID J2A-5B to W4 P2-9 (ST J5-9)
From ST_J5-9	to ST_J5-24
From W4 P2-24 (ST J5-24) From ID J2B-13F From ID A1P12.32	to W4 P1B-13F to ID A1J12.32 to ID P12-88 (S202-14)
From ID P13-29 (S202-4) From ID A1J14.50 From ID A1P10.50 From ID P11-147 (S510-4) From ID A1J9.31	to ID A1P14.50 to ID A1J10.50 to ID P11-244 (S510-2) to ID A1P9.31 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 511

Description:

This step verifies connectivity from DC4-HI out through W4 and to the proper receive path associated with DTS ch 28 while connecting W4 with loopback and applying 0 volts out on W4P2-23 then driving 15 volts with DC4 to detect that CH28 receives High level and reads "1".

From ID P1-10 (DC4-HI) From ID A1J1.3 From ID A1P8.2 From ID P10-44 (S301-69) From ID A1J8.31	to ID A1J8.2 to ID P10-141 (S301-70)
From ID BUS 2	to ID A1J9.29
From ID A1P9.29	to ID P11-17 (S509-4)
From ID P11-177 (S509-1)	to ID A1P10.10
From ID A1J10.10	to ID A1J12.38
From ID A1P12.38	to ID P12-59 (S202-1)
From ID P12-57 (S202-13) From ID A1J12.31 From W4 P1B-13E From ST_J5-23	to ID A1P12.31 to ID J2B-13E to W4 P2-23 (ST J5-23) to ST_J5-13
From W4 P2-13 (ST J5-13)	to W4 P1A-14E
From ID J2A-14E	to ID A1U5.5
From ID A1U5.4	to ID A1J5.29

Date: 04 March 2016

From ID Alps	5.29	to	ID	P6-12	(DTS	CH28)
From ID P1-1	1 (DC1-HT)	t.o	TD	A1P1.1		
From ID A1J1				A1U1.1		
From ID A1J1				A1U1.1		
From ID A1J1				A1U2.1		
From ID A1J1				A1U2.1		
From ID A1J1				A1U3.1		
From ID A1J1				A1U3.1		
From ID A1J1				A1U4.1		
From ID A1J1		to	ID	A1U5.1	_	
From ID A1J1		to	ID	A1U6.1	_	
From ID A1J1		to	ID	A1U7.1	_	
From ID A1J1		to	ID	A1U8.1	_	
From ID A1J1				A1C1.1		
From ID A1J1		to	ID	A1C3.1	_	
From ID A1J1		to	ID	A1C5.1	_	
From ID A1J1	l.1 (+5V)	to	ID	A1C7.1	_	
From ID A1J1	l.1 (+5V)	to	ID	A1C8.1	_	
From ID A1J1	l.1 (+5V)	to	ID	A1C9.1	_	
From ID A1J1	1.1 (+5V)	to	ID	A1C10.	1	
From ID A1J1	1.1 (+5V)	to	ID	A1C11.	1	
From ID P1-2	2 (DC1-LO)	to	ID	A1P1.9)	
From ID A1J1	1.9	to	GRO	DUND		
From ID P1-4	4 (DC2-HI)	to	ID	A1P2.2	2	
From ID A1J2	2.2 (+15V)	to	ID	A1U1.1	.6	
From ID A1J2	2.2 (+15V)	to	ID	A1U2.1	.6	
From ID A1J2	2.2 (+15V)	to	ID	A1U3.1	.6	
From ID A1J2	2.2 (+15V)	to	ID	A1C2.1	-	
From ID A1J2	2.2 (+15V)	to	ID	A1C4.1	-	
From ID A1J2	2.2 (+15V)	to	ID	A1C6.1	-	
From ID P1-5	5 (DC2-LO)	to	ID	A1P1.1	.0	
From ID A1J1	1.10	to	GRO	DUND		
From ID A1C1		to	GRO	DUND		
From ID A1C2				DUND		
From ID A1C3		to	GRO	DUND		
From ID A1C4		to	GRO	DUND		
From ID A1C5				DUND		
From ID A1C6				DUND		
From ID A1C7	· · · =			DUND		
From ID A1C8				DUND		
From ID A1C9				DUND		
From ID A1C1				DUND		
From ID A1C1				DUND		
From ID A1U1				OUND		
From ID A1U2				OUND		
From ID A1U3				OUND		
From ID A1U4				OUND		
From ID A1U5				OUND		
From ID A1U6				OUND		
From ID A1U7				OUND		
From ID A1U8				OUND)	
	24 (DTS GCH 40)			A1P5.3	•	
From ID A1J5	J. J	LO	GK	DUND		

Date: 04 March 2016

From ID P6-64 (DTS GCH 7) to ID A1P5.1 From ID A1J5.1 to GROUND

Step 512

Description:

This step verifies connectivity from CH6 (ID J2A=11B) out through W4 and to the proper receive path associated with DTS ch 42 while connecting W4 with loopback and applying 0 volts out on W4P2-23 then driving 15 volts with DC4 to detect that CH42 receives High level and reads "1".

From ID P6-31 (DTS CH6)	to ID A1P5.43
From ID A1J5.43	to ID A1U2.3
From ID A1U2.2	to ID A1J10.34
From ID A1P10.34	to ID P11-143 (S301-175)
From ID P11-209 (S301-176)	to ID A1P10.37
From ID A1J10.37	to ID J2A-11B
From W4 P1A-11B	to W4 P2-10 (ST J5-10)
From ST_J5-10	to ST_J6-A2
From W4 P3-A2 (ST J6-3)	to W4 P1B-1B
From ID J2B-1B	to ID A1U6.7
From ID A1U6.6	to ID A1J5.23
From ID A1P5.23	to ID P7-54 (DTS CH42)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
Erom ID 31 T1 1 (+577)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID AlJ1.1 (+5V) From ID P1-2 (DC1-LO) From ID AlJ1.9 From ID P1-4 (DC2-HI) From ID AlJ2.2 (+15V) From ID AlJ2.2 (+15V)	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID AlU1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
·	

Date: 04 March 2016

From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID AlP1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 513

Description:

This step verifies connectivity from CH7 (ID J2A=11B) out through W4 and to the proper receive path associated with DTS ch 43 while connecting W4 with loopback and applying 0 volts out on W4P2-11 then driving 15 volts with DC4 to detect that CH43 receives High level and reads "1".

From ID From ID	P6-32 (DTS CH7) A1J5.44			A1P5.44 A1U2.5
From ID .	A1U2.4	to	ID	A1J10.32
From ID .	A1P10.32	to	ID	P11-175 (S301-177)
From ID	P11-48 (S301-178)	to	ID	A1P10.35
From ID .	A1J10.35			J2A-11A
From W4	P1A-11A	to	W4	P2-11 (ST J5-11)
From ST_	J5-11	to	ST_	_J6-B2
From W4	P3-B2 (ST J6-4)	to	W4	P1B-1A
From ID	J2B-1A	to	ID	A1U6.9
From ID .	A1U6.10	to	ID	A1J5.24

Date: 04 March 2016

From	ID A1P5.24	to	ID	P7-53	(DTS	CH43)
From	ID P1-1 (DC1-HI)	t.o	TD	A1P1.1		
	ID A1J1.1 (+5V)			A1U1.1		
	ID A1J1.1 (+5V)			A1U1.1		
	ID A1J1.1 (+5V)			A1U2.1		
	ID A1J1.1 (+5V)			A1U2.1		
	ID A1J1.1 (+5V)			A1U3.1		
	ID A1J1.1 (+5V)			A1U3.1		
	ID A1J1.1 (+5V)			A1U4.1		
	ID A1J1.1 (+5V)			A1U5.1		
	ID A1J1.1 (+5V)			A1U6.1		
	ID A1J1.1 (+5V)			A1U7.1		
	ID A1J1.1 (+5V)			A1U8.1		
	ID A1J1.1 (+5V)			A1C1.1		
	ID A1J1.1 (+5V)	to	ID	A1C3.1	_	
	ID A1J1.1 (+5V)	to	ID	A1C5.1	_	
From	ID A1J1.1 (+5V)	to	ID	A1C7.1	_	
From	ID A1J1.1 (+5V)	to	ID	A1C8.1		
From	ID A1J1.1 (+5V)	to	ID	A1C9.1		
From	ID A1J1.1 (+5V)	to	ID	A1C10.	1	
From	ID A1J1.1 (+5V)	to	ID	A1C11.	1	
From	ID P1-2 (DC1-LO)	to	ID	A1P1.9)	
From	ID A1J1.9	to	GRO	DUND		
From	ID P1-4 (DC2-HI)	to	ID	A1P2.2	?	
	ID A1J2.2 (+15V)	to	ID	A1U1.1	.6	
	ID A1J2.2 (+15V)	to	ID	A1U2.1	.6	
	ID A1J2.2 (+15V)			A1U3.1		
	ID A1J2.2 (+15V)			A1C2.1		
	ID A1J2.2 (+15V)			A1C4.1		
	ID A1J2.2 (+15V)			A1C6.1		
	ID P1-5 (DC2-LO)			A1P1.1	.0	
_	ID A1J1.10			DUND		
	ID A1C1.2			OUND		
	ID A1C2.2			DUND		
	ID A1C3.2			OUND		
	ID A1C4.2			OUND		
	ID A1C5.2 ID A1C6.2			OUND OUND		
	ID A1C0.2 ID A1C7.2			DUND		
	ID A1C7.2 ID A1C8.2			DUND		
	ID A1C8.2 ID A1C9.2			DUND		
	ID A1C10.2			DUND		
	ID A1C11.2			DUND		
	ID A1U1.8			DUND		
	ID A1U2.8			DUND		
	ID A1U3.8			DUND		
	ID A1U4.8			DUND		
	ID A1U5.8			DUND		
	ID A1U6.8			DUND		
	ID A1U7.8			DUND		
	ID A1U8.8			DUND		
	ID P7-24 (DTS GCH 40)			A1P5.3	}	
	ID A1J5.3			DUND		

Date: 04 March 2016

From ID P6-64 (DTS GCH 7) to ID A1P5.1 From ID A1J5.1 to GROUND

Step 514

Description:

This step verifies connectivity from CH016 (ID J2A=12E) out through W4 and to the proper receive path associated with DTS ch 44 while connecting W4 with loopback and applying "0" out on W4P2-14 then driving "1" and detecting CH44 receives High level and reads "1".

From ID P6-24 (DTS CH16) From ID A1J5.41 From ID A1U2.6 From ID A1P10.30 From ID P11-81 (S301-180) From ID A1J10.33 From W4 P1A-12E	to ID AIJIU.30 to ID P11-78 (S301-179)
From ST_J5-14	to ST_J6-A3
From W4 P3-A3 (ST J6-5) From ID J2B-2F From ID A1U6.12 From ID A1P5.21	to W4 P1B-2F to ID A1U6.11 to ID A1J5.21 to ID P7-52 (DTS CH44)
From ID P1-1 (DC1-HI) From ID AlJ1.1 (+5V)	to ID A1P1.1 to ID A1U1.1 to ID A1U1.13 to ID A1U2.1 to ID A1U2.13 to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13 to ID A1U4.1 to ID A1U5.1 to ID A1U6.1 to ID A1U7.1 to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1 to ID A1C3.1 to ID A1C5.1 to ID A1C7.1 to ID A1C8.1
From ID A1J1.1 (+5V) From ID A1J1.1 (+5V) From ID A1J1.1 (+5V) From ID P1-2 (DC1-LO) From ID A1J1.9 From ID P1-4 (DC2-HI) From ID A1J2.2 (+15V) From ID A1J2.2 (+15V) From ID A1J2.2 (+15V)	to ID A1C9.1 to ID A1C10.1 to ID A1C11.1 to ID A1P1.9 to GROUND to ID A1P2.2 to ID A1U1.16 to ID A1U2.16 to ID A1U3.16

Date: 04 March 2016

From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID AlU1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 515

Description:

This step verifies connectivity from CH017 (ID J2A-12D) out through W4 and to the proper receive path associated with DTS ch 45 while connecting W4 with loopback and applying "0" out on W4P2-15 then driving "1" and detecting CH45 receives High level and reads "1".

From ID Alu From ID Alu From ID Alu	J2.10 P10.28 L-243 (S301-182)	to to to to	ID ID ID ID	A1P5.42 A1U2.9 A1J10.28 P11-178 (S301-181) A1P10.31 J2A-12D
From W4 P1A	1-12D	to	W4	P2-15 (ST J5-15)
From ST_J5-	-15	to	ST_	_J6-B3
From W4 P3-	B3 (ST J6-6)	to	W4	P1B-2C
From ID J2B	3-2C	to	ID	A1U6.14
From ID A1U	J6.15	to	ID	A1J5.22
From ID A1P	5.22	to	ID	P7-51 (DTS CH45)
From W4 P3- From ID J2B From ID A1U	B3 (ST J6-6) 3-2C J6.15	to to to	W4 ID ID	P1B-2C A1U6.14 A1J5.22

Date: 04 March 2016

From II	P1-1 (DC1-HI)	to	ID A1P1.1
) A1J1.1 (+5V)	to	ID A1U1.1
) A1J1.1 (+5V)	to	ID A1U1.13
) A1J1.1 (+5V)	to	ID A1U2.1
) A1J1.1 (+5V)	to	ID A1U2.13
From II	A1J1.1 (+5V)	to	ID A1U3.1
From II	A1J1.1 (+5V)	to	ID A1U3.13
From II	A1J1.1 (+5V)	to	ID A1U4.1
From II	A1J1.1 (+5V)	to	ID A1U5.1
From II	A1J1.1 (+5V)	to	ID A1U6.1
From II	A1J1.1 (+5V)	to	ID A1U7.1
From II	A1J1.1 (+5V)	to	ID A1U8.1
From II	A1J1.1 (+5V)	to	ID A1C1.1
From II	A1J1.1 (+5V)	to	ID A1C3.1
From II	A1J1.1 (+5V)	to	ID A1C5.1
	A1J1.1 (+5V)		ID A1C7.1
	A1J1.1 (+5V)	to	ID A1C8.1
	A1J1.1 (+5V)		ID A1C9.1
	A1J1.1 (+5V)		ID A1C10.1
	A1J1.1 (+5V)		ID A1C11.1
	P1-2 (DC1-LO)		ID A1P1.9
	A1J1.9		GROUND
	P1-4 (DC2-HI)		ID A1P2.2
	A1J2.2 (+15V)		ID A1U1.16
	A1J2.2 (+15V)		ID A1U2.16
	A1J2.2 (+15V)		ID A1U3.16
	A1J2.2 (+15V)		ID A103.10
	A1J2.2 (+15V)		ID A1C4.1
	A102.2 (+15V) A1J2.2 (+15V)		ID AlC4.1
	P1-5 (DC2-LO)		ID A1P1.10
	A1J1.10		GROUND
	A101.10 A1C1.2		
			GROUND GROUND
	A1C2.2		
	A1C3.2		GROUND
	A1C4.2		GROUND
_	A1C5.2		GROUND
	A1C6.2		GROUND
	A1C7.2		GROUND
_	A1C8.2		GROUND
	A1C9.2		GROUND
	A1C10.2		GROUND
	A1C11.2		GROUND
	A1U1.8		GROUND
	A1U2.8		GROUND
	A1U3.8		GROUND
	A1U4.8	to	GROUND
	A1U5.8		GROUND
	A1U6.8	to	GROUND
	A1U7.8	to	GROUND
	A1U8.8		GROUND
From II	P7-24 (DTS GCH 40)	to	ID A1P5.3
From II	A1J5.3	to	GROUND
From II	P6-64 (DTS GCH 7)	to	ID A1P5.1
From II	A1J5.1	to	GROUND

Date: 04 March 2016

Step 516

Description:

This step verifies connectivity from CH18 (ID J2A-12C) out through W4 and to the proper receive path associated with DTS ch 46 while connecting W4 with loopback and applying "0" out on W4P2-16 then driving "1" and detecting CH46 receives High level and reads "1".

From ID P6-22 (DTS CH18)	to ID A1P5.39
From ID A1J5.39	to ID A1U2.11
From ID A1J5.39 From ID A1U2.12 From ID A1P10.26 From ID P11-83 (S301-184) From ID A1J10.29	to ID A1J10.26
From ID A1P10.26	to ID P11-210 (S301-183)
From ID P11-83 (S301-184)	to ID A1P10.29
	to ID J2A-12C
From W4 P1A-12C	to W4 P2-16 (ST J5-16)
From ST_J5-16	to ST_J6-A4
From W4 P3-A4 (ST J6-7)	to W4 P1B-2B
From ID J2B-2B	to ID A1U7.3
From ID A1U7.2	to ID A1J5.19
From ID A1P5.19	to ID P7-50 (DTS CH46)
From ID P1-1 (DC1-HI) From ID A1J1.1 (+5V)	
From ID P1-1 (DC1-HI)	to ID AlP1.1
From ID AlJ1.1 (+5V)	to ID AlU1.1
From ID AlJ1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.1 (+5V) From ID P1-2 (DC1-LO) From ID A1J1.9 From ID P1-4 (DC2-HI)	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID AlU1.16
From ID A1J2.2 (+15V)	to ID AlU2.16
From ID A1J2.2 (+15V)	to ID Alu3.16
From ID AlJ2.2 (+15V)	to ID A1C2.1
From ID AlJ2.2 (+15V)	to ID A1C4.1
1100.0 (1100)	00 ID 11IC1.I

Date: 04 March 2016

_		- 1 - 0 0 / 1 - \		
		A1J2.2 (+15V)	to	ID A1C6.1
From	ID	P1-5 (DC2-LO)	to	ID A1P1.10
From	ID	A1J1.10	to	GROUND
From	ID	A1C1.2	to	GROUND
From	ID	A1C2.2	to	GROUND
From	ID	A1C3.2	to	GROUND
From	ID	A1C4.2	to	GROUND
From	ID	A1C5.2	to	GROUND
From	ID	A1C6.2	to	GROUND
From	ID	A1C7.2	to	GROUND
From	ID	A1C8.2	to	GROUND
From	ID	A1C9.2	to	GROUND
From	ID	A1C10.2	to	GROUND
From	ID	A1C11.2	to	GROUND
From	ID	A1U1.8	to	GROUND
From	ID	A1U2.8	to	GROUND
From	ID	A1U3.8	to	GROUND
From	ID	A1U4.8	to	GROUND
From	ID	A1U5.8	to	GROUND
From	ID	A1U6.8	to	GROUND
From	ID	A1U7.8	to	GROUND
From	ID	A1U8.8	to	GROUND
From	ID	P7-24 (DTS GCH 40)	to	ID A1P5.3
From	ID	A1J5.3	to	GROUND
From	ID	P6-64 (DTS GCH 7)	to	ID A1P5.1
From	ID	A1J5.1	to	GROUND

Step 517

Description:

This step verifies proper receive path components associated with DTS ch 26 while connecting W4 with loopback.

to ID A1P14.43 to ID J2A-1F to W4 P2-17 (ST J5-17)
to ST_J5-27
to W4 P1A-13D
to ID A1U4.9
to ID A1R15.1
to ID A1J1.2 (+15V)
to ID A1J5.31
to ID P6-14 (DTS CH26)
to ID A1P1.1
to ID A1U1.1
to ID AlU1.13
to ID A1U2.1
to ID A1U2.13
to ID A1U3.1

Date: 04 March 2016

	A1J1.1 (+5V)		ID A1U3.13
From ID			ID A1U4.1
From ID	- ' ' - ' '		ID A1U5.1
From ID			ID A1U6.1
From ID		to	
From ID	,	to	
From ID			ID A1C7.1
From ID			ID A1C8.1
From ID			ID A1C9.1
From ID		to	
From ID			ID A1C11.1
	P1-2 (DC1-LO)	to	
From ID			GROUND
	P1-4 (DC2-HI)		ID A1P2.2
	A1J2.2 (+15V)		ID A1U1.16
	A1J2.2 (+15V)		ID A1U2.16
	A1J2.2 (+15V)		ID A1U3.16
	A1J2.2 (+15V)		ID A1C2.1
	A1J2.2 (+15V)		ID A1C4.1
	A1J2.2 (+15V)		ID A1C6.1
	P1-5 (DC2-LO)		ID A1P1.10
	A1J1.10		GROUND
From ID			GROUND
	A1C10.2	LO	GROUND
From ID		+ ~	
Enom ID	A1C11.2		GROUND
From ID	Alul.8	to	GROUND
From ID	A1U1.8 A1U2.8	to to	GROUND GROUND
From ID From ID	A1U1.8 A1U2.8 A1U3.8	to to to	GROUND GROUND GROUND
From ID From ID From ID	A1U1.8 A1U2.8 A1U3.8 A1U4.8	to to to	GROUND GROUND GROUND GROUND
From ID From ID From ID From ID	A1U1.8 A1U2.8 A1U3.8 A1U4.8 A1U5.8	to to to to	GROUND GROUND GROUND GROUND GROUND
From ID From ID From ID From ID From ID	A1U1.8 A1U2.8 A1U3.8 A1U4.8 A1U5.8 A1U6.8	to to to to to	GROUND GROUND GROUND GROUND GROUND GROUND
From ID From ID From ID From ID From ID From ID	A1U1.8 A1U2.8 A1U3.8 A1U4.8 A1U5.8 A1U6.8 A1U7.8	to to to to to to to	GROUND GROUND GROUND GROUND GROUND GROUND GROUND GROUND
From ID	A1U1.8 A1U2.8 A1U3.8 A1U4.8 A1U5.8 A1U6.8 A1U7.8 A1U8.8	to to to to to to to to	GROUND GROUND GROUND GROUND GROUND GROUND GROUND GROUND GROUND
From ID	A1U1.8 A1U2.8 A1U3.8 A1U4.8 A1U5.8 A1U6.8 A1U7.8 A1U8.8 P7-24 (DTS GCH 40)	to to to to to to to to to	GROUND GROUND GROUND GROUND GROUND GROUND GROUND GROUND GROUND ID A1P5.3
From ID	A1U1.8 A1U2.8 A1U3.8 A1U4.8 A1U5.8 A1U6.8 A1U7.8 A1U8.8 P7-24 (DTS GCH 40) A1J5.3	to	GROUND ID A1P5.3 GROUND
From ID	A1U1.8 A1U2.8 A1U3.8 A1U4.8 A1U5.8 A1U6.8 A1U7.8 A1U8.8 P7-24 (DTS GCH 40) A1J5.3 P6-64 (DTS GCH 7)	to to to to to to to	GROUND GROUND GROUND GROUND GROUND GROUND GROUND GROUND GROUND ID A1P5.3

Step 518

Description:

Date: 04 March 2016

This step verifies connectivity from S202-28 out through the proper receive path components associated with DTS ch 26 while connecting W4 with loopback and applying 0 volts out on ID J2A-1F.

Connection Path is as follows:

From ID P13-60 (S202-28) to ID A1P14.43 From ID A1J14.43 to ID J2A-1F to W4 P2-17 (ST J5-17) From W4 P1A-1F From ST_J5-17 to ST_J5-27 From W4 P2-27 (ST J5-27) to W4 P1A-13D to ID A1U4.9 from ID J2A-13D to ID A1R15.1 from ID A1R15.2 to ID A1J1.2 (+15V) from ID A1U4.10 to ID A1J5.31 from ID A1P5.31 to ID P6-14 (DTS CH26) From ID A1P5.31

From ID P1-1 (DC1-HI)

From ID AJJ.1.1 (+5V)

From ID AJJ.1.2 (+15V)

From ID AJJ.2.2 (+15V)

From ID AJJ.1.10

From ID AJJ.1.10 From ID A1J1.10
From ID A1C1.2
From ID A1C2.2
From ID A1C3.2
From ID A1C4.2 to GROUND to GROUND to GROUND to GROUND to GROUND

Date: 04 March 2016

From	ID	A1C5.2			to	GRO	DUND
From	ID	A1C6.2			to	GRO	DUND
${\tt From}$	ID	A1C7.2			to	GRO	DUND
${\tt From}$	ID	A1C8.2			to	GRO	DUND
${\tt From}$	ID	A1C9.2			to	GRO	DUND
${\tt From}$	ID	A1C10.2			to	GRO	DUND
${\tt From}$	ID	A1C11.2			to	GRO	DUND
${\tt From}$	ID	A1U1.8			to	GRO	DUND
${\tt From}$	ID	A1U2.8			to	GRO	DUND
From	ID	A1U3.8			to	GRO	DUND
From	ID	A1U4.8			to	GRO	DUND
From	ID	A1U5.8			to	GRO	DUND
From	ID	A1U6.8			to	GRO	DUND
From	ID	A1U7.8			to	GRO	DUND
From	ID	A1U8.8			to	GRO	DUND
From	ID	P7-24 (DTS GCI	Η	40)	to	ID	A1P5.3
From	ID	A1J5.3				-	DUND
From	ID	P6-64 (DTS GCI	Η	7)	to	ID	A1P5.1
From	ID	A1J5.1			to	GRO	DUND

Step 519

Description:

This step verifies the wire path from S201-2/W4P2-17 to S201-1/W4P2-27. The DMM will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1J8.28 to ID P10-203 (S503-1)
From ID P11-242 (S509-2)	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.12 to ID A1J12.36 to ID P12-90 (S202-2)
From ID P13-60 (S202-28) From ID A1J14.43 From W4 P1A-1F	to ID A1P14.43 to ID J2A-1F to W4 P2-17 (ST J5-17)
From ST_J5-17 From W4 P2-27 (ST J5-27) From ID J2B-14D From ID A1P12.27	to ST_J5-27 to W4 P1B-14D to ID A1J12.27 to ID P12-55 (S201-47)
From ID P12-16 (S201-1) From ID A1J12.42 From ID A1P10.6	to ID A1P12.42 to ID A1J10.6 to ID P11-203 (S508-1)

Date: 04 March 2016

From ID P11-12 (S508-4) to ID A1P9.25 from ID A1J9.25 to ID BUS 2

From ID BUS 2 to ID A1J6.23 from ID A1P6.23 to ID P10-12 (S503-4) from ID P10-139 (S503-2) to ID A1P8.26 from ID A1J8.26 to ID A1J15.50 from ID A1P15.50 to ID P20-3 (DMM-LO)

Step 520

Description:

This step verifies connectivity from CH19 (ID J2A-12B) out through W4 and to the proper receive path associated with DTS ch 47 while connecting W4 with loopback and applying "0" out on W4P2-18 then driving "1" and detecting CH 47 receives High level and reads "1".

From ID P6-21 (DTS CH19)	to	ID	A1P5.40
From ID A1J5.40	to	ID	A1U2.14
From ID A1U2.15			A1J10.24
From ID A1P10.24	to	ID	P11-49 (S301-185)
From ID P11-82 (S301-186)			
From ID A1J10.27			J2A-12B
From W4 P1A-12B	to	W4	P2-18 (ST J5-18)
From ST_J5-18	to	ST_	_J6-B5
From W4 P3-B5 (ST J6-10)	to	W4	P1B-2A
From ID J2B-2A			A1U7.5
From ID A1U7.4	to	ID	A1J5.20
From ID A1P5.20	to	ID	P7-49 (DTS CH47)
From ID P1-1 (DC1-HI)	to	ID	A1P1.1
From ID A1J1.1 (+5V)	to	ID	Alu1.1
From ID A1J1.1 (+5V)			A1U1.13
From ID A1J1.1 (+5V)	to	ID	Alu2.1
From ID A1J1.1 (+5V)	to	ID	A1U2.13
From ID A1J1.1 (+5V)	to	ID	Alu3.1
From ID A1J1.1 (+5V)	to	ID	A1U3.13
From ID A1J1.1 (+5V)	to	ID	A1U4.1
From ID A1J1.1 (+5V)	to	ID	A1U5.1
From ID A1J1.1 (+5V)	to	ID	A1U6.1
From ID A1J1.1 (+5V)	to	ID	A1U7.1
From ID A1J1.1 (+5V)	to	ID	A1U8.1
From ID A1J1.1 (+5V)	to	ID	A1C1.1
From ID A1J1.1 (+5V)	to	ID	A1C3.1
From ID A1J1.1 (+5V)	to	ID	A1C5.1
From ID A1J1.1 (+5V)	to	ID	A1C7.1
From ID A1J1.1 (+5V)	to	ID	A1C8.1
From ID A1J1.1 (+5V)	to	ID	A1C9.1
From ID A1J1.1 (+5V)	to	ID	A1C10.1
From ID A1J1.1 (+5V)	to	ID	A1C11.1

Date: 04 March 2016

From ID P1-2 (DC1-LO)		to	ID A1P1.9
From ID A1J1.9		to	GROUND
From ID P1-4 (DC2-HI)		to	ID A1P2.2
From ID A1J2.2 (+15V)		to	ID A1U1.16
From ID A1J2.2 (+15V)		to	ID A1U2.16
From ID A1J2.2 (+15V)		to	ID A1U3.16
From ID A1J2.2 (+15V)		to	ID A1C2.1
From ID A1J2.2 (+15V)		to	ID A1C4.1
From ID A1J2.2 (+15V)		to	ID A1C6.1
From ID P1-5 (DC2-LO)		to	ID A1P1.10
From ID AlJ1.10		to	GROUND
From ID A1C1.2		to	GROUND
From ID A1C2.2			GROUND
From ID A1C3.2		to	GROUND
From ID A1C4.2		to	GROUND
From ID A1C5.2		to	GROUND
From ID A1C6.2		to	GROUND
From ID A1C7.2			GROUND
From ID A1C8.2		to	GROUND
From ID A1C9.2		to	GROUND
From ID A1C10.2			GROUND
From ID A1C11.2		to	GROUND
From ID A1U1.8		to	GROUND
From ID A1U2.8			GROUND
From ID A1U3.8		to	GROUND
From ID A1U4.8		to	GROUND
From ID A1U5.8		to	GROUND
From ID A1U6.8			GROUND
From ID A1U7.8		to	GROUND
From ID A1U8.8			GROUND
From ID P7-24 (DTS GCH	40)		ID A1P5.3
From ID A1J5.3			GROUND
From ID P6-64 (DTS GCH	7)		ID A1P5.1
From ID A1J5.1		to	GROUND

Step 521

Description:

This step verifies connectivity from CH000 (ID J2A-9C) out through W4 and to the proper receive path associated with DTS ch 48 while connecting W4 with loopback and applying "0" out on W4P2-20 then driving "1" and detecting CH 48 receives High level and reads "1".

From ID P6-25 (DTS CH0)	to ID A1P5.49
From ID A1J5.49	to ID A1U1.3
From ID A1U1.2	to ID A1J10.46
From ID A1P10.46	to ID P11-204 (S301-163)
From ID P11-174 (S301-164)	to ID A1P10.49
From ID A1J10.49	to ID J2A-9C
From W4 P1A-9C	to W4 P2-20 (ST J5-20)
From ST_J5-20	to ST_J6-A6

Date: 04 March 2016

From V	N4 P3-A6 (ST J6-11)	to	W4	P1B-3C
	ID J2B-3C	to	ID	A1U7.7
From]	ID A1U7.6	to	ID	A1J5.17
From]	ID A1P5.17	to	ID	P7-48 (DTS CH48)
	ID P1-1 (DC1-HI)	to	ID	A1P1.1
From]	ID A1J1.1 (+5V)	to	ID	A1U1.1
	ID A1J1.1 (+5V)	to	ID	A1U1.13
From]	ID A1J1.1 (+5V)	to	ID	A1U2.1
	ID A1J1.1 (+5V)	to	ID	A1U2.13
From 1	ID A1J1.1 (+5V)	to	ID	A1U3.1
	ID A1J1.1 (+5V)			A1U3.13
From 1	ID A1J1.1 (+5V)	to	ID	A1U4.1
From 1	ID A1J1.1 (+5V)	to	ID	A1U5.1
From 1	ID A1J1.1 (+5V)	to	ID	A1U6.1
From 1	ID A1J1.1 (+5V)	to	ID	A1U7.1
From 1	ID A1J1.1 (+5V)	to	ID	A1U8.1
From]	ID A1J1.1 (+5V)	to	ID	A1C1.1
From]	ID A1J1.1 (+5V)	to	ID	A1C3.1
From 1	ID A1J1.1 (+5V)	to	ID	A1C5.1
From]	ID A1J1.1 (+5V)	to	ID	A1C7.1
From]	ID A1J1.1 (+5V)	to	ID	A1C8.1
From]	ID A1J1.1 (+5V)	to	ID	A1C9.1
From]	ID A1J1.1 (+5V)	to	ID	A1C10.1
From]	ID A1J1.1 (+5V)	to	ID	A1C11.1
From]	ID P1-2 (DC1-LO)	to	ID	A1P1.9
From]	ID A1J1.9	to	GRO	DUND
From]	ID P1-4 (DC2-HI)	to	ID	A1P2.2
	ID A1J2.2 (+15V)	to	ID	A1U1.16
	ID A1J2.2 (+15V)	to	ID	A1U2.16
	ID A1J2.2 (+15V)	to	ID	A1U3.16
From 1	ID A1J2.2 (+15V)	to	ID	A1C2.1
From]	ID A1J2.2 (+15V)			A1C4.1
From 1	ID A1J2.2 (+15V)	to	ID	A1C6.1
From 1	ID P1-5 (DC2-LO)	to	ID	A1P1.10
From]	ID A1J1.10	to	GRO	DUND
From 1	ID A1C1.2	to	GRO	DUND
	ID A1C2.2	to	GRO	DUND
	ID A1C3.2	to	GRO	DUND
	ID A1C4.2	to	GRO	DUND
From 1	ID A1C5.2	to	GRO	DUND
	ID A1C6.2	to	GRO	DUND
	ID A1C7.2	to	GRO	DUND
	ID A1C8.2	to	GRO	DUND
	ID A1C9.2	to	GRO	DUND
	ID A1C10.2			DUND
	ID A1C11.2			DUND
	ID A1U1.8			DUND
	ID A1U2.8			DUND
	ID A1U3.8			DUND
	ID A1U4.8			DUND
	ID A1U5.8			DUND
From 1	ID A1U6.8	to	GRO	DUND

Date: 04 March 2016

From ID A1U7.8 to GROUND
From ID A1U8.8 to GROUND
From ID P7-24 (DTS GCH 40) to ID A1P5.3
From ID A1J5.3 to GROUND
From ID P6-64 (DTS GCH 7) to ID A1P5.1
From ID A1J5.1 to GROUND

Step 522

Description:

This step verifies proper receive path components associated with DTS ch 24 while connecting W4 with loopback.

From ID P13-54 (S201-51) From ID A1J14.37 From W4 P1A-1D	to ID A1P14.37 to ID J2A-1D to W4 P2-22 9ST J5-22)
From ST_J5-22	to ST_J5-21
From W4 P2-21 (ST J5-21)	to W4 P1A-13B
From ID J2A-13B	to ID A1U4.5
From ID J2A-13B	to ID A1R13.1
From ID A1R13.2	to ID A1J1.2 (+15V)
From ID AlDE 33	to ID A1J5.33
From ID A1P5.33	to ID P6-16 (DTS CH24)
From ID P1-1 (DC1-HI)	to ID AlP1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID AlJ1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID AlJ1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID AlJ1.1 (+5V)	to ID A1C7.1
From ID AlJ1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID AlJ1.1 (+5V)	to ID A1C10.1
From ID AlJ1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID AlJ1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16

Date: 04 March 2016

From	ID	A1J2.2 (+15V)	to	ID A1C2.1
From	ID	A1J2.2 (+15V)	to	ID A1C4.1
${\tt From}$	ID	A1J2.2 (+15V)	to	ID A1C6.1
${\tt From}$	ID	P1-5 (DC2-LO)	to	ID A1P1.10
${\tt From}$	ID	A1J1.10	to	GROUND
${\tt From}$	ID	A1C1.2	to	GROUND
${\tt From}$	ID	A1C2.2	to	GROUND
${\tt From}$	ID	A1C3.2	to	GROUND
${\tt From}$	ID	A1C4.2	to	GROUND
${\tt From}$	ID	A1C5.2	to	GROUND
${\tt From}$	ID	A1C6.2	to	GROUND
${\tt From}$	ID	A1C7.2	to	GROUND
${\tt From}$	ID	A1C8.2	to	GROUND
${\tt From}$	ID	A1C9.2	to	GROUND
${\tt From}$	ID	A1C10.2	to	GROUND
${\tt From}$	ID	A1C11.2	to	GROUND
From	ID	A1U1.8	to	GROUND
${\tt From}$	ID	A1U2.8	to	GROUND
${\tt From}$	ID	A1U3.8	to	GROUND
${\tt From}$	ID	A1U4.8	to	GROUND
From	ID	A1U5.8	to	GROUND
${\tt From}$	ID	A1U6.8	to	GROUND
${\tt From}$	ID	A1U7.8	to	GROUND
From	ID	A1U8.8	to	GROUND
${\tt From}$	ID	P7-24 (DTS GCH 40)	to	ID A1P5.3
${\tt From}$	ID	A1J5.3	to	GROUND
From	ID	P6-64 (DTS GCH 7)	to	ID A1P5.1
From	ID	A1J5.1	to	GROUND

Step 523

Description:

This step verifies connectivity from S201-51 out through the proper receive path components associated with DTS ch 24 while connecting W4 with loopback and applying 0 volts out on ID J2A-1D.

From ID P13-54 (S201-51) From ID A1J14.37 From W4 P1A-1D	to ID A1P14.37 to ID J2A-1D to W4 P2-22 9ST J5-22)
From ST_J5-22	to ST_J5-21
From W4 P2-21 (ST J5-21)	to W4 P1A-13B
From ID J2A-13B	to ID A1U4.5
From ID J2A-13B	to ID A1R13.1
From ID A1R13.2	to ID A1J1.2 (+15V)
From ID A1U4.4	to ID A1J5.33
From ID A1P5.33	to ID P6-16 (DTS CH24)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID AlU1.13

Date: 04 March 2016

From ID	A1J1.1 (+5V)		to	ID A1U2.1
_	A1J1.1 (+5V)		to	ID A1U2.13
	A1J1.1 (+5V)		to	ID A1U3.1
From ID	A1J1.1 (+5V)		to	ID A1U3.13
From ID	A1J1.1 (+5V)		to	ID A1U4.1
From ID	A1J1.1 (+5V)		to	ID A1U5.1
From ID	A1J1.1 (+5V)		to	ID A1U6.1
From ID	A1J1.1 (+5V)		to	ID A1U7.1
From ID	A1J1.1 (+5V)		to	ID A1U8.1
From ID	A1J1.1 (+5V)		to	ID A1C1.1
From ID	A1J1.1 (+5V)		to	ID A1C3.1
From ID	A1J1.1 (+5V)		to	ID A1C5.1
From ID	A1J1.1 (+5V)		to	ID A1C7.1
From ID			to	ID A1C8.1
From ID			to	ID A1C9.1
From ID	A1J1.1 (+5V)		to	ID A1C10.1
From ID	A1J1.1 (+5V)		to	ID A1C11.1
From ID	P1-2 (DC1-LO)		to	ID A1P1.9
From ID	A1J1.9		to	GROUND
From ID	P1-4 (DC2-HI)		to	ID A1P2.2
From ID	A1J2.2 (+15V)		to	ID A1U1.16
From ID	A1J2.2 (+15V)		to	ID A1U2.16
From ID	A1J2.2 (+15V)		to	ID A1U3.16
From ID	A1J2.2 (+15V)		to	ID A1C2.1
From ID	A1J2.2 (+15V)		to	ID A1C4.1
From ID	A1J2.2 (+15V)		to	ID A1C6.1
From ID	P1-5 (DC2-LO)		to	ID A1P1.10
From ID	A1J1.10		to	GROUND
From ID	A1C1.2		to	GROUND
From ID	A1C2.2		to	GROUND
From ID	A1C3.2		to	GROUND
From ID	A1C4.2		to	GROUND
From ID	A1C5.2		to	GROUND
From ID	A1C6.2		to	GROUND
From ID	A1C7.2		to	GROUND
From ID	A1C8.2		to	GROUND
From ID	A1C9.2		to	GROUND
From ID	A1C10.2		to	GROUND
From ID	A1C11.2		to	GROUND
From ID	A1U1.8		to	GROUND
From ID	A1U2.8		to	GROUND
From ID	A1U3.8		to	GROUND
From ID	A1U4.8		to	GROUND
From ID	A1U5.8		to	GROUND
From ID	A1U6.8		to	GROUND
From ID	A1U7.8		to	GROUND
From ID	A1U8.8		to	GROUND
From ID	P7-24 (DTS GCH	40)	to	ID A1P5.3
From ID	A1J5.3			GROUND
	P6-64 (DTS GCH	7)	to	ID A1P5.1
From ID			to	GROUND

Step 524

Date: 04 March 2016

Description:

This step verifies proper receive path components associated with DTS ch 25 while connecting W4 with loopback.

From ID P13-23 (S201-49)	to ID A1P14.35
From ID A1J14.35	to ID J2A-3D
From W4 P1A-3D	to W4 P2-21 (ST J5-21)
From ST_J5-21	to ST_J5-22
From W4 P2-22 (ST J5-22)	to W4 P1A-13C
From ID J2A-13C	to ID A1U4.7
From ID J2A-13C	to ID A1R14.1
From ID A1R14.2	to ID A1J1.2 (+15V)
From ID A1U4.6	to ID A1J5.34
From ID A1P5.34	to ID P6-15 (DTS CH25)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND

Date: 04 March 2016

From	ID	A1C5.2		to	GROUND
From	ID	A1C6.2		to	GROUND
From	ID	A1C7.2		to	GROUND
From	ID	A1C8.2		to	GROUND
From	ID	A1C9.2		to	GROUND
From	ID	A1C10.2		to	GROUND
From	ID	A1C11.2		to	GROUND
From	ID	A1U1.8		to	GROUND
From	ID	A1U2.8		to	GROUND
From	ID	A1U3.8		to	GROUND
From	ID	A1U4.8		to	GROUND
From	ID	A1U5.8		to	GROUND
From	ID	A1U6.8		to	GROUND
From	ID	A1U7.8		to	GROUND
From	ID	A1U8.8		to	GROUND
From	ID	P7-24 (DTS GCH	40)	to	ID A1P5.3
From	ID	A1J5.3		to	GROUND
From	ID	P6-64 (DTS GCH	7)	to	ID A1P5.1
From	ID	A1J5.1		to	GROUND

Step 525

Description:

This step verifies connectivity from S201-49 out through the proper receive path components associated with DTS ch 25 while connecting W4 with loopback and applying 0 volts out on ID J2A-3D.

From ID P13-23 (S201-49) From ID A1J14.35 From W4 P1A-3D	to ID A1P14.35 to ID J2A-3D to W4 P2-21 (ST J5-21)
From ST_J5-21	to ST_J5-22
From W4 P2-22 (ST J5-22) From ID J2A-13C From ID J2A-13C From ID A1R14.2 From ID A1U4.6 From ID A1P5.34	to W4 P1A-13C to ID A1U4.7 to ID A1R14.1 to ID A1J1.2 (+15V) to ID A1J5.34 to ID P6-15 (DTS CH25)
From ID P1-1 (DC1-HI) From ID A1J1.1 (+5V)	to ID A1P1.1 to ID A1U1.1 to ID A1U1.13 to ID A1U2.1 to ID A1U2.13 to ID A1U3.1 to ID A1U3.1 to ID A1U3.13 to ID A1U4.1 to ID A1U5.1 to ID A1U6.1 to ID A1U7.1

Date: 04 March 2016

From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID AlC11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID AlU1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID AlC4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID AlP1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 526

Description:

This step verifies the wire path from W4P2-23 to W4P2-13. The DMM resource will be used to measure resistance UL= 10 ohms.

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From ID P20-2 (DMM-HI) to ID A1P15.49 From ID A1J15.49 to ID A1J8.28
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Date: 04 March 2016

From ID BUS 1 From ID Alp9.19 From ID Alp9.19 From ID P11-177 (S509-1) From ID AlJ10.10 From ID AlJ10.10 From ID Alp12.38 From ID P12-57 (S202-13) From ID AlJ12.31 From ID AlJ12.31 From W4 P1B-13E From W4 P2-23 (ST J5-23) From ID J2B-12E From ID Alp12.34 From ID Alp12.34 From ID Alp12.34 From ID Alp12.34 From ID Alp12.35 From ID Alp12.36 From ID Alp14.50 From ID Alp10.50 From ID P11-147 (S510-4) From ID BUS 2 From ID Alp6.23 From ID Alp6.23 From ID Aly8.26 From ID Aly8.26 From ID Aly8.26 From ID Aly15.50 From ID Aly8.26 From ID Aly15.50 From ID Aly8.26 From ID Aly15.50 From ID Aly15.50 From ID Aly8.26 From ID Aly15.50 From ID Aly8.26 From ID Aly8.26 From ID Aly15.50 From ID Aly15.50 From ID Aly15.50 From ID Aly8.26 From ID Aly8.26 From ID Aly15.50	From ID Al From ID Pl From ID Al	10-77 (S503-3)	to :	ID	P10-203 (S503-1) A1P6.13 BUS 1
From ID A1J12.31 to ID J2B-13E to W4 P1B-13E to W4 P2-23 (ST J5-23) From ST_J5-23 to ST_J5-13 From W4 P2-13 (ST J5-13) to W4 P1B-12E to ID A1J12.34 to ID P12-58 (S202-16) From ID A1P12.34 to ID A1P14.50 to ID A1J10.50 From ID A1J14.50 to ID A1J10.50 From ID A1P10.50 to ID A1P1-244 (S510-2) From ID A1J9.31 to ID BUS 2 From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID A1P8.26 From ID A1J8.26 to ID A1J15.50	From ID Al From ID Al From ID Al	1P9.19 11-177 (S509-1) LJ10.10	to : to :	ID ID ID	P11-18 (S509-3) A1P10.10 A1J12.38
From W4 P1B-13E to W4 P2-23 (ST J5-23) From ST_J5-23 to ST_J5-13 From W4 P2-13 (ST J5-13) to W4 P1B-12E From ID J2B-12E to ID A1J12.34 From ID A1P12.34 to ID P12-58 (S202-16) From ID P13-29 (S202-4) to ID A1P14.50 From ID A1J14.50 to ID A1J10.50 From ID A1P10.50 to ID P11-244 (S510-2) From ID P11-147 (S510-4) to ID A1P9.31 From ID A1J9.31 to ID BUS 2 From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50	From ID P1	12-57 (S202-13)	to :	ID	A1P12.31
From ST_J5-23 to ST_J5-13 From W4 P2-13 (ST J5-13) to W4 P1B-12E From ID J2B-12E to ID A1J12.34 From ID A1P12.34 to ID P12-58 (S202-16) From ID P13-29 (S202-4) to ID A1P14.50 From ID A1J14.50 to ID A1J10.50 From ID A1P10.50 to ID P11-244 (S510-2) From ID P11-147 (S510-4) to ID A1P9.31 From ID BUS 2 to ID BUS 2 From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50	From ID A1	LJ12.31	to :	ID	J2B-13E
From W4 P2-13 (ST J5-13) to W4 P1B-12E From ID J2B-12E to ID A1J12.34 From ID A1P12.34 to ID P12-58 (S202-16) From ID P13-29 (S202-4) to ID A1J10.50 From ID A1J14.50 to ID A1J10.50 From ID A1P10.50 to ID P11-244 (S510-2) From ID P11-147 (S510-4) to ID A1P9.31 From ID A1J9.31 to ID BUS 2 From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50	From W4 P1	LB-13E	to I	W4	P2-23 (ST J5-23)
From ID J2B-12E to ID A1J12.34 From ID A1P12.34 to ID P12-58 (S202-16) From ID P13-29 (S202-4) to ID A1P14.50 From ID A1J14.50 to ID A1J10.50 From ID A1P10.50 to ID P11-244 (S510-2) From ID P11-147 (S510-4) to ID A1P9.31 From ID A1J9.31 to ID BUS 2 From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50	From ST_J5	5-23	to :	ST_	_J5-13
From ID A1P12.34 to ID P12-58 (S202-16) From ID P13-29 (S202-4) to ID A1P14.50 From ID A1J14.50 to ID A1J10.50 From ID A1P10.50 to ID P11-244 (S510-2) From ID P11-147 (S510-4) to ID A1P9.31 From ID A1J9.31 to ID BUS 2 From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50					
From ID P13-29 (S202-4) to ID A1P14.50 From ID A1J14.50 to ID A1J10.50 From ID A1P10.50 to ID P11-244 (S510-2) From ID P11-147 (S510-4) to ID A1P9.31 From ID A1J9.31 to ID BUS 2 From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50	From W4 P2	2-13 (ST J5-13)	to 1	W4	P1B-12E
From ID AlJ14.50 to ID AlJ10.50 From ID AlP10.50 to ID P11-244 (S510-2) From ID P11-147 (S510-4) to ID AlP9.31 From ID AlJ9.31 to ID BUS 2 From ID BUS 2 to ID AlJ6.23 From ID AlP6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID AlP8.26 From ID AlJ8.26 to ID AlJ15.50					
From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50	From ID J2	2B-12E	to :	ID	A1J12.34
From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50	From ID J2 From ID A1 From ID A1 From ID A1 From ID A1 From ID D	2B-12E 1P12.34 13-29 (S202-4) 1J14.50 1P10.50 11-147 (S510-4)	to : to : to : to : to : to :	ID ID ID ID ID ID	A1J12.34 P12-58 (S202-16) A1P14.50 A1J10.50 P11-244 (S510-2) A1P9.31
From ID A1J8.26 to ID A1J15.50	From ID J2 From ID A1 From ID A1 From ID A1 From ID A1 From ID P1 From ID A1	2B-12E 1P12.34 13-29 (S202-4) 1J14.50 1P10.50 11-147 (S510-4) 1J9.31	to :	ID ID ID ID ID ID ID ID	AlJ12.34 P12-58 (S202-16) AlP14.50 AlJ10.50 P11-244 (S510-2) AlP9.31 BUS 2
	From ID J2 From ID A1 From ID B1	2B-12E 1P12.34 13-29 (S202-4) LJ14.50 1P10.50 L1-147 (S510-4) LJ9.31 JS 2	to :	ID ID ID ID ID ID ID ID ID	AlJ12.34 P12-58 (S202-16) AlP14.50 AlJ10.50 P11-244 (S510-2) AlP9.31 BUS 2 AlJ6.23
From ID A1P15.50 to ID P20-3 (DMM-LO)	From ID J2 From ID A1 From ID B1 From ID A1	2B-12E 1P12.34 13-29 (S202-4) 1J14.50 1P10.50 11-147 (S510-4) 1J9.31 JS 2 1P6.23	to :	ID	AlJ12.34 P12-58 (S202-16) AlP14.50 AlJ10.50 P11-244 (S510-2) AlP9.31 BUS 2 AlJ6.23 P10-12 (S503-4)
	From ID J2 From ID A1 From ID B1 From ID A1 From ID A1 From ID A1 From ID A1	2B-12E 1P12.34 13-29 (S202-4) 1J14.50 1P10.50 11-147 (S510-4) 1J9.31 JS 2 1P6.23 10-139 (S503-2)	to :	ID	A1J12.34 P12-58 (S202-16) A1P14.50 A1J10.50 P11-244 (S510-2) A1P9.31 BUS 2 A1J6.23 P10-12 (S503-4) A1P8.26

Step 527

Description:

This step verifies connectivity from DC4-HI out through W4 and to the proper receive path associated with DTS ch 28 while connecting W4 with loopback and applying 0 volts out on W4P2-23 then driving 15 volts with DC4 to detect that CH28 receives High level and reads "1".

From	ID	P1-10 (DC4-HI)	to	ID	A1P1.3
From	ID	A1J1.3	to	ID	A1J8.2
From	ID	A1P8.2	to	ID	P10-141 (S301-70)
From	ID	P10-44 (S301-69)	to	ID	A1P8.31
From	ID	A1J8.31	to	ID	BUS 2
From	ID	BUS 2	to	ID	A1J9.29
From	ID	A1P9.29	to	ID	P11-17 (S509-4)
From	ID	P11-177 (S509-1)	to	ID	A1P10.10
From	ID	A1J10.10	to	ID	A1J12.38
From	ID	A1P12.38	to	ID	P12-59 (S202-1)

Date: 04 March 2016

From ID	P12-28 (S202-29)	to	ID	A1P12.35
	A1J12.35	to	ID	J2B-12F
From W4	P1B-12F	to	W4	P2-25 (ST J5-25)
From ST	'_J5-25	to	ST_	_J6-A1
T 1/1/4	D2 31 /CE TC 1)		T.T /	D1D 1D
	P3-A1 (ST J6-1)			P1B-1D
) J2B-1D) A1U6.2			A1U6.3 A1J5.25
	A106.2 A1P5.25			P7-56 (DTS CH40)
FIOII ID	AIPS.25	LO	דט	P7-30 (DIS CH40)
From ID	P1-1 (DC1-HI)	to	ID	A1P1.1
	AlJ1.1 (+5V)			A1U1.1
	AlJ1.1 (+5V)			A1U1.13
	AlJ1.1 (+5V)			A1U2.1
	AlJ1.1 (+5V)			A1U2.13
	AlJ1.1 (+5V)			A1U3.1
	AlJ1.1 (+5V)			A1U3.13
	A1J1.1 (+5V)			A1U4.1
	A1J1.1 (+5V)			A1U5.1
	A1J1.1 (+5V)			A1U6.1
	A1J1.1 (+5V)			A1U7.1
	A1J1.1 (+5V)			A1U8.1
	A1J1.1 (+5V)			A1C1.1
	A1J1.1 (+5V)			A1C3.1
	A1J1.1 (+5V)			A1C5.1
				A1C7.1
) AlJ1.1 (+5V)) AlJ1.1 (+5V)			A1C8.1
	A1J1.1 (+5V)			A1C9.1
	A1J1.1 (+5V)			A1C10.1
	AlJ1.1 (+5V)			A1C11.1
	P1-2 (DC1-LO)			A1P1.9
	A1J1.9			DUND
	P1-4 (DC2-HI)			A1P2.2
	A1J2.2 (+15V)			A1U1.16
	A1J2.2 (+15V)			A1U2.16
	A1J2.2 (+15V)			A1U3.16
	A1J2.2 (+15V)			A1C2.1
	A1J2.2 (+15V)			A1C4.1
	A1J2.2 (+15V)			A1C6.1
	P1-5 (DC2-LO)			A1P1.10
	A1J1.10			DUND
From ID				DUND
From ID				OUND
From ID				DUND
From ID				DUND
From ID				OUND
From ID				OUND
From ID				DUND
From ID				DUND
From ID				DUND
	A1C10.2			DUND
From ID	A1C11.2	to	GR(DUND

Date: 04 March 2016

From	ID	A1U1.8	to	GROUND
From	ID	A1U2.8	to	GROUND
${\tt From}$	ID	A1U3.8	to	GROUND
From	ID	A1U4.8	to	GROUND
From	ID	A1U5.8	to	GROUND
From	ID	A1U6.8	to	GROUND
${\tt From}$	ID	A1U7.8	to	GROUND
From	ID	A1U8.8	to	GROUND
From	ID	P7-24 (DTS GCH 40)	to	ID A1P5.3
${\tt From}$	ID	A1J5.3	to	GROUND
${\tt From}$	ID	P6-64 (DTS GCH 7)	to	ID A1P5.1
From	ID	A1J5.1	to	GROUND

Step 528

Description:

This step verifies connectivity from CH22 (ID J2A-13E) out through W4 and to the proper receive path associated with DTS ch 49 while connecting W4 with loopback and applying 0 volts out on W4P2-26 then driving 15 volts with DC4 to detect that CH49 receives High level and reads "1".

From ID P6-18 (DTS CH22) From ID A1J5.35 From ID A1U3.6 From ID A1P10.18 From ID P11-148 (S301-192) From ID A1J10.21 From W4 P1A-13E	to ID A1P5.35 to ID A1U3.7 to ID A1J10.18 to ID P11-50 (S301-191) to ID A1P10.21 to ID J2A-13E to W4 P2-26 (ST J5-26)
From ST_J5-26	to ST_J6-B6
From ID AlU7.10 From ID AlP5.18	to ID A1U7.9 to ID A1J5.18 to ID P7-47 (DTS CH49)
From ID P1-1 (DC1-HI) From ID A1J1.1 (+5V)	to ID AlP1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID $A1J1.1 (+5V)$	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID AlJ1.1 (+5V) From ID AlJ1.1 (+5V)	to ID A1U2.13
	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
· · · · · · · · · · · · · · · · · · ·	to ID A1U7.1
	to ID A1U8.1
	to ID A1C1.1
From ID A1J1.1 (+5V) From ID A1J1.1 (+5V) From ID A1J1.1 (+5V)	to ID A1C3.1
From ID AlJ1.1 (+5V)	to ID A1C5.1
From ID AlJ1.1 (+5V)	to ID A1C7.1

Date: 04 March 2016

From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID AlC11.1
From ID P1-2 (DC1-LO)	to ID AlP1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID AlU1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID AlP1.10
From ID AlJ1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 529

Description:

This step verifies the wire path from W4 P2-28 to W4 P2-40. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23

Date: 04 March 2016

From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
T TD D12 F2 (CF01 OF)	
,	to ID A1P15.26
From ID A1J15.26	to ID J2A-6B
From W4 P1A-6B	to W4 P2-28 (ST J5-28)
From ST_J5-28	to ST_J5-40
	00 21_00 10
From W4 P2-40 (ST J5-40)	to W4 P1A-2F
From ID J2A-2F	to ID A1J14.42
From ID A1P14.42	to ID P13-28 (S202-27)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1012.30	to ID P11-177 (S509-1)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
FIUM ID AIPI3.30	CO ID PZO-3 (DMM-TO)

Step 530

Description:

This step verifies connectivity from DC4-HI out through W4 and to the proper receive path associated with DTS CH30 while connecting W4 with loopback and applying 0 volts out on W4P2-35 then driving 15 volts with DC4 to detect that CH30 receives High level and reads "1".

From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID AlJ1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2
From ID BUS 2	to ID A1J9.25
From ID A1P9.25	to ID P11-12 (S508-4)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P13-55 (S201-50)	to ID A1P14.36
From ID A1J14.36	to ID J2A-2D
From W4 P1A-2D	to W4 P2-35 (ST J5-35)

Date: 04 March 2016

From ST_J5-35	to ST_J5-29
T 174 P2 20 / GE TE 20 \	6- WA DID 10
From W4 P2-29 (ST J5-29)	
From ID J2B-1F	to ID A1U5.9
From ID A1U5.10	to ID A1J5.27
From ID A1P5.27	to ID P6-10 (DTS CH30)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID AlU1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID Algl 2	to GROUND
From ID A1C1.2 From ID A1C2.2	to GROUND to GROUND
From ID A1C3.2	to GROUND
From ID AlC4.2	to GROUND
From ID AlC5.2	to GROUND
From ID AlC6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID AlC9.2	to GROUND
From ID AlC10.2	to GROUND
From ID AlC11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND

Date: 04 March 2016

From ID A1U6.8 to GROUND
From ID A1U7.8 to GROUND
From ID A1U8.8 to GROUND
From ID P7-24 (DTS GCH 40) to ID A1P5.3
From ID A1J5.3 to GROUND
From ID P6-64 (DTS GCH 7) to ID A1P5.1
From ID A1J5.1 to GROUND

Step 531

Description:

This step verifies connectivity from CH001 (ID J2A-9B) out through W4 and to the proper receive path associated with DTS ch 50 while connecting W4 with loopback and applying "0" out on W4P2-30 then driving "1" and detecting CH050 receives High level and reads "1".

From ID P6	5-26 (DTS CH1)	to	ID	A1P5.50
From ID A1	.J5.50	to	ID	A1U1.5
From ID A1	.U1.4	to	ID	A1J10.44
From ID A1	.P10.44	to	ID	P11-44 (S301-165)
From ID P1	.1-141 (S301-166)	to	ID	A1P10.47
From ID A1	J10.47	to	ID	J2A-9B
From W4 P1	.A-9B	to	W4	P2-30 (ST J5-30)
From ST_J5	5-30	to	ST_	_J6-A7
	3-A7 (ST J6-13)			
From ID J2				A1U7.11
From ID A1				A1J5.15
From ID A1	.P5.15	to	ID	P7-46 (DTS CH50)
U TD D1	1 (DG1 III)		TD	7171 1
	,			A1P1.1
	J1.1 (+5V)			A1U1.1
	J1.1 (+5V)			A1U1.13
	J1.1 (+5V)			A1U2.1
	J1.1 (+5V)			A1U2.13
	J1.1 (+5V)			A1U3.1
	J1.1 (+5V)			A1U3.13
	J1.1 (+5V)			A1U4.1
	J1.1 (+5V)			A1U5.1
	J1.1 (+5V)			A1U6.1
	.J1.1 (+5V)			A1U7.1
	J1.1 (+5V)			A1U8.1
	.J1.1 (+5V)			A1C1.1
	.J1.1 (+5V)			A1C3.1
	.J1.1 (+5V)			A1C5.1
From ID A1	.J1.1 (+5V)	to	ID	A1C7.1
From ID A1	.J1.1 (+5V)			A1C8.1
	.J1.1 (+5V)			A1C9.1
	.J1.1 (+5V)	to	ID	A1C10.1
	.J1.1 (+5V)			A1C11.1
From ID P1	-2 (DC1-LO)	to	ID	A1P1.9

Date: 04 March 2016

From ID Al			to	GROUND
_	-4 (DC2-HI)		to	ID A1P2.2
	J2.2 (+15V)		to	ID A1U1.16
	J2.2 (+15V)		to	ID A1U2.16
	J2.2 (+15V)		to	ID A1U3.16
	J2.2 (+15V)		to	ID A1C2.1
From ID Al	J2.2 (+15V)			ID A1C4.1
From ID Al	J2.2 (+15V)		to	ID A1C6.1
From ID P1	-5 (DC2-LO)		to	ID A1P1.10
From ID Al	J1.10		to	GROUND
From ID A10			to	GROUND
From ID A10	C2.2		to	GROUND
From ID A10			to	GROUND
From ID A10	C4.2		to	GROUND
From ID A10	C5.2		to	GROUND
From ID A10	C6.2		to	GROUND
From ID A10	C7.2		to	GROUND
From ID A10	C8.2		to	GROUND
From ID A10	C9.2		to	GROUND
From ID A10	C10.2		to	GROUND
From ID A10	C11.2		to	GROUND
From ID All	U1.8		to	GROUND
From ID All	U2.8		to	GROUND
From ID All	U3.8		to	GROUND
From ID All	U4.8		to	GROUND
From ID A1	U5.8		to	GROUND
From ID All	U6.8		to	GROUND
From ID All	U7.8		to	GROUND
From ID All	U8.8		to	GROUND
From ID P7	-24 (DTS GCH	40)	to	ID A1P5.3
From ID Al	J5.3		to	GROUND
From ID P6	-64 (DTS GCH	7)	to	ID A1P5.1
From ID Al	J5.1		to	GROUND

Step 532

Description:

This step verifies connectivity from CH002 (ID J2A-10C) out through W4 and to the proper receive path associated with DTS ch 51 while connecting W4 with loopback and applying "0" out on W4P2-31 then driving "1" and detecting CH051 receives High level and reads "1".

From ID P6-27 (DTS CH2)	to ID A1P5.47
From ID A1J5.47	to ID A1U1.7
From ID A1U1.6	to ID A1J10.42
From ID A1P10.42	to ID P11-76 (S301-167)
From ID P11-142 (S301-168)	to ID A1P10.45
From ID A1J10.45	to ID J2A-10C
From W4 P1A-10C	to W4 P2-31 (ST J5-31)
From ST_J5-31	to ST_J6-B7

Date: 04 March 2016

From W4 P3-B7 (ST J6-14) to W4 P1B-4C From ID J2B-4C From ID A1U7.15 to ID A1U7.14 to ID A1J5.16 to ID P7-45 (DTS CH51) From ID AID 1.15
From ID AIP5.16
From ID P1-1 (DC1-HI)
From ID AIJ1.1 (+5V)
From ID AIJ1.2 (+15V)
From ID AIJ1.1 (+5V)
From ID AIJ2.2 (+15V)
From ID AIJ From ID A1P5.16 From ID A1C10.2
From ID A1C11.2
From ID A1U1.8
From ID A1U2.8
From ID A1U3.8
From ID A1U4.8
From ID A1U4.8
From ID A1U5.8
From ID A1U5.8
From ID A1U6.8
From ID A1U7.8 to GROUND From ID A1U7.8 to GROUND

Date: 04 March 2016

From ID A1U8.8 to GROUND
From ID P7-24 (DTS GCH 40) to ID A1P5.3
From ID A1J5.3 to GROUND
From ID P6-64 (DTS GCH 7) to ID A1P5.1
From ID A1J5.1 to GROUND

Step 533

Description:

This step verifies connectivity from DC4-HI out through W4 and to the proper receive path associated with DTS ch 31 while connecting W4 with loopback and applying 0 volts out on W4P2-35 then driving 15 volts with DC4 to detect that CH31 receives High level and reads "1".

From ID P13-55 (S201-50) From ID A1J14.36 From W4 P1A-2D	to ID A1P14.36 to ID J2A-2D to W4 P2-35 (ST J5-35)
From ST_J5-35	to ST_J5-32
From W4 P2-32 (ST J5-32) From ID J2B-1E From ID A1U5.12 From ID A1P5.28	to W4 P1B-1E to ID A1U5.11 to ID A1J5.28 to ID P6-9 (DTS CH31)
From ID P1-1 (DC1-HI) From ID A1J1.1 (+5V) From ID A1J1.1 (+5V) From ID A1J1.1 (+5V) From ID A1J1.1 (+5V)	to ID A1P1.1 to ID A1U1.1 to ID A1U1.13 to ID A1U2.1 to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1 to ID A1U3.13 to ID A1U4.1 to ID A1U5.1 to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1 to ID A1U8.1 to ID A1C1.1 to ID A1C3.1 to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1 to ID A1C8.1 to ID A1C9.1 to ID A1C10.1 to ID A1C11.1
From ID P1-2 (DC1-LO) From ID A1J1.9 From ID P1-4 (DC2-HI) From ID A1J2.2 (+15V) From ID A1J2.2 (+15V) From ID A1J2.2 (+15V)	to ID A1P1.9 to GROUND to ID A1P2.2 to ID A1U1.16 to ID A1U2.16 to ID A1U3.16

Date: 04 March 2016

From II	D A1J2.2 (+15V)	to	ID A1C2.1
From II	D A1J2.2 (+15V)	to	ID A1C4.1
From II	D A1J2.2 (+15V)	to	ID A1C6.1
From II	D P1-5 (DC2-LO)	to	ID A1P1.10
From II	A1J1.10	to	GROUND
From II	A1C1.2	to	GROUND
From II	D A1C2.2	to	GROUND
From II	D A1C3.2	to	GROUND
From II	D A1C4.2	to	GROUND
From II	D A1C5.2	to	GROUND
From II	D A1C6.2	to	GROUND
From II	D A1C7.2	to	GROUND
From II	D A1C8.2	to	GROUND
From II	A1C9.2	to	GROUND
From II	A1C10.2	to	GROUND
From II	D A1C11.2	to	GROUND
From II	A1U1.8	to	GROUND
From II	A1U2.8	to	GROUND
From II	A1U3.8	to	GROUND
From II	A1U4.8	to	GROUND
From II	A1U5.8	to	GROUND
From II	A1U6.8	to	GROUND
From II	A1U7.8	to	GROUND
From II	A1U8.8	to	GROUND
From II	D P7-24 (DTS GCH 40)	to	ID A1P5.3
From II	A1J5.3	to	GROUND
From II	D P6-64 (DTS GCH 7)	to	ID A1P5.1
From II	D A1J5.1	to	GROUND

Step 534

Description:

This step verifies the wire path from W4 P2-32 to W4 P2-35. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P12-26 (S202-15)	to ID A1P12.33
From ID AlJ12.33	to ID J2B-12D
From W4 P1B-12D	to W4 P2-32 (ST J5-32)
From ST_J5-32	to ST_J5-35
From W4 P2-35 (ST J5-35)	to W4 P1A-2D
From ID J2A-2D	to ID A1J14.36
From ID A1P14.36	to ID P13-55 (S201-50)

Step 535

Description:

This step verifies connectivity from CH003 (ID J2A-10B) out through W4 and to the proper receive path associated with DTS ch 52 while connecting W4 with loopback and applying "0" out on W4P2-34 then driving "1" and detecting CH052 receives High level and reads "1".

Date: 04 March 2016

From ID P6-28 (DTS CH3)	to ID A1P5.48
From ID A1J5.48	to ID A1U1.9
From ID AlU1.10	to ID A1J10.40
From ID A1P10.40	to ID P11-14 (S301-169)
From ID P11-15 (S301-170)	to ID A1P10.43
From ID A1J10.43	to ID J2A-10B
From W4 P1A-10B	to W4 P2-34 (ST J5-34)
From ST_J5-34	to ST_J6-A8
From W4 P3-A8 (ST J6-15)	to W4 P1B-4B
From ID J2B-4B	to ID A1U8.3
From ID A1U8.2	to ID A1J5.13
From ID A1P5.13	to ID P7-44 (DTS CH52)
	,
From ID P1-1 (DC1-HI)	to ID AlP1.1
From ID P1-1 (DC1-HI) From ID A1J1.1 (+5V)	to ID AlU1.1
From ID A1J1.1 (+5V)	to ID AlU1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID AlJ1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID AlJ1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND

Date: 04 March 2016

From	ID	A1C6.2			to	GRO	DUND	
From	ID	A1C7.2			to	GRO	DUND	
From	ID	A1C8.2			to	GRO	DUND	
From	ID	A1C9.2			to	GRO	DUND	
From	ID	A1C10.2			to	GRO	DUND	
From	ID	A1C11.2			to	GRO	DUND	
${\tt From}$	ID	A1U1.8			to	GRO	DUND	
From	ID	A1U2.8			to	GRO	DUND	
From	ID	A1U3.8			to	GRO	DUND	
${\tt From}$	ID	A1U4.8			to	GRO	DUND	
${\tt From}$	ID	A1U5.8			to	GRO	DUND	
${\tt From}$	ID	A1U6.8			to	GRO	DUND	
${\tt From}$	ID	A1U7.8			to	GRO	DUND	
${\tt From}$	ID	A1U8.8			to	GRO	DUND	
${\tt From}$	ID	P7-24 (DTS	S GCH	40)	to	ID	A1P5	. 3
${\tt From}$	ID	A1J5.3			to	GRO	DUND	
${\tt From}$	ID	P6-64 (DTS	S GCH	7)	to	ID	A1P5	. 1
From	ID	A1J5.1			to	GRO	DUND	

Step 536

Description:

This step verifies the wire path from W4 P2-35 to W4 P2-29. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1J8.28 to ID P10-203 (S503-1)
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2)	to ID A1J9.15 to ID P11-77 (S508-3)
From ID P13-55 (S201-50) From ID A1J14.36 From W4 P1A-2D	to ID A1P14.36 to ID J2A-2D to W4 P2-35 (ST J5-35)
From ST_J5-35 From W4 P2-29 (ST J5-29) From ID J2B-10F From ID A1P12.39	to ST_J5-29 to W4 P1B-10F to ID A1J12.39 to ID P12-93 (S202-31)
From ID P12-16 (S201-1) From ID A1J12.42 From ID A1P10.6	to ID A1P12.42 to ID A1J10.6 to ID P11-203 (S508-1)

Date: 04 March 2016

From ID P11-12 (S508-4) to ID A1P9.25 to ID BUS 2

From ID BUS 2 to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 537

Description:

This step verifies connectivity from CH004 (ID J2A-10A) out through W4 and to the proper receive path associated with DTS ch 53 while connecting W4 with loopback and applying "0" out on W4P2-36 then driving "1" and detecting CH053 receives High level and reads "1".

Fr	om ID P6-29 (DTS CH4)	to ID A1P5.45
	om ID A1J5.45	to ID A1U1.11
Fr	om ID A1U1.12	to ID A1J10.38
	om ID A1P10.38	to ID P11-79 (S301-171)
Fr	om ID P11-80 (S301-172)	to ID AlP10.41
	om ID A1J10.41	to ID J2A-10A
Fr	om W4 P1A-10A	to W4 P2-36 (ST J5-36)
Fr	om ST_J5-36	to ST_J6-B8
	om W4 P3-B8 (ST J6-16)	
	om ID J2B-4A	to ID A1U8.5
	om ID A1U8.4	to ID A1J5.11
Fr	om ID A1P5.11	to ID P7-43 (DTS CH53)
E-m	om ID P1-1 (DC1-HI)	to ID AlP1.1
	om ID A1J1.1 (+5V)	to ID AIPI.1
	· · · · · · · · · · · · · · · · · · ·	
	om ID A1J1.1 (+5V) om ID A1J1.1 (+5V)	to ID AlU1.13 to ID AlU2.1
	om ID A1J1.1 (+5V)	to ID A1U2.1
	om ID A1J1.1 (+5V)	to ID A1U3.1 to ID A1U3.13
	om ID A1J1.1 (+5V) om ID A1J1.1 (+5V)	to ID Alu4.1
		to ID Alu4.1
	om ID A1J1.1 (+5V) om ID A1J1.1 (+5V)	to ID Alus.1
	om ID AlJ1.1 (+5V)	to ID A100.1
	om ID A1J1.1 (+5V)	to ID Alu7.1
	om ID AlJ1.1 (+5V)	to ID AlC1.1
	om ID AlJ1.1 (+5V)	to ID AlC1.1
	· · · · · · · · · · · · · · · · · · ·	
	om ID A1J1.1 (+5V)	to ID AlC5.1 to ID AlC7.1
	om ID A1J1.1 (+5V)	
	om ID A1J1.1 (+5V)	to ID A1C8.1
	om ID A1J1.1 (+5V)	to ID A1C9.1
	om ID A1J1.1 (+5V)	to ID A1C10.1
FΥ	om ID A1J1.1 (+5V)	to ID A1C11.1

Date: 04 March 2016

From ID P1-2 (DC1-LO)		to	ID A1P1.9
From ID A1J1.9		to	GROUND
From ID P1-4 (DC2-HI)		to	ID A1P2.2
From ID A1J2.2 (+15V)		to	ID A1U1.16
From ID A1J2.2 (+15V)		to	ID A1U2.16
From ID A1J2.2 (+15V)		to	ID A1U3.16
From ID A1J2.2 (+15V)		to	ID A1C2.1
From ID A1J2.2 (+15V)		to	ID A1C4.1
From ID A1J2.2 (+15V)		to	ID A1C6.1
From ID P1-5 (DC2-LO)		to	ID A1P1.10
From ID AlJ1.10		to	GROUND
From ID A1C1.2		to	GROUND
From ID A1C2.2			GROUND
From ID A1C3.2		to	GROUND
From ID A1C4.2		to	GROUND
From ID A1C5.2		to	GROUND
From ID A1C6.2		to	GROUND
From ID A1C7.2			GROUND
From ID A1C8.2		to	GROUND
From ID A1C9.2		to	GROUND
From ID A1C10.2			GROUND
From ID A1C11.2		to	GROUND
From ID A1U1.8		to	GROUND
From ID A1U2.8			GROUND
From ID A1U3.8		to	GROUND
From ID A1U4.8		to	GROUND
From ID A1U5.8		to	GROUND
From ID A1U6.8			GROUND
From ID A1U7.8		to	GROUND
From ID A1U8.8			GROUND
From ID P7-24 (DTS GCH	40)		ID A1P5.3
From ID A1J5.3			GROUND
From ID P6-64 (DTS GCH	7)		ID A1P5.1
From ID A1J5.1		to	GROUND

Step 538

Description:

This step verifies connectivity from CH005 (ID J2A-11C) out through W4 and to the proper receive path associated with DTS ch 54 while connecting W4 with loopback and applying "0" out on W4P2-37 then driving "1" and detecting CH054 receives High level and reads "1".

From ID P6-30 (DTS CH5)	to ID AlP5.46
From ID A1J5.46	to ID A1U1.14
From ID A1U1.15	to ID A1J10.36
From ID A1P10.36	to ID P11-47 (S301-173)
From ID P11-208 (S301-174)	to ID A1P10.39
From ID A1J10.39	to ID J2A-11C
From W4 P1A-11C	to W4 P2-37 (ST J5-37)
From ST_J5-37	to ST_J6-A9

Date: 04 March 2016

From W4 P3-A9 (ST J6-17)	to W4 P1B-5C
From ID J2B-5C	to ID A1U8.7
From ID A1U8.6	to ID A1J5.9
From ID A1P5.9	to ID P7-42 (DTS CH54)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID AlJ1.1 $(+5V)$	to ID A1U4.1
From ID AlJ1.1 $(+5V)$	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID AlJ1.1 $(+5V)$	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID AlC9.2	to GROUND
From ID AlC10.2	to GROUND
From ID AlC11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U4.8	to GROUND
From ID Alue 9	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND

Date: 04 March 2016

From ID A1U7.8 to GROUND From ID A1U8.8 to GROUND From ID P7-24 (DTS GCH 40) to ID A1P5.3 From ID A1J5.3 to GROUND From ID P6-64 (DTS GCH 7) to ID A1P5.1 From ID A1J5.1 to GROUND

Step 539

Description:

This step verifies connectivity from CH020 (ID J2A-12A) out through W4 and to the proper receive path associated with DTS ch 55 while connecting W4 with loopback and applying "0" out on W4P2-38 then driving "1" and detecting CH055 receives High level and reads "1".

From ID	P6-20 (DTS CH20)	to	ID	A1P5.37
From ID	P6-20 (DTS CH20) A1J5.37	to	ID	A1U3.3
From ID	A1U3.2	to		A1J10.22
From ID	A1P10.22			P11-114 (S301-187)
From ID	P11-51 (S301-188)			A1P10.25
	A1J10.25		ID	J2A-12A
From W4	P1A-12A	to	W4	P2-38 (ST J5-38)
From ST	_J5-38	to	ST_	_J6-B9
	P3-B9 (ST J6-18)			
From ID	J2B-5B			A1U8.9
	A1U8.10	to	ID	A1J5.7
From ID	A1P5.7	to	ID	P7-41 (DTS CH55)
	P1-1 (DC1-HI)			A1P1.1
	A1J1.1 (+5V)			A1U1.1
	A1J1.1 (+5V)			A1U1.13
	A1J1.1 (+5V)			A1U2.1
	A1J1.1 (+5V)			A1U2.13
	A1J1.1 (+5V)			A1U3.1
	A1J1.1 (+5V)			A1U3.13
	A1J1.1 (+5V)			A1U4.1
	A1J1.1 (+5V)			A1U5.1
	A1J1.1 (+5V)			A1U6.1
	A1J1.1 (+5V)			A1U7.1
	A1J1.1 (+5V)			A1U8.1
	A1J1.1 (+5V)	to	ID	A1C1.1
	A1J1.1 (+5V)	to	ID	A1C3.1
	A1J1.1 (+5V)	to	ID	A1C5.1
From ID	A1J1.1 (+5V)	to	ID	A1C7.1
From ID	A1J1.1 (+5V)	to	ID	A1C8.1
From ID	A1J1.1 (+5V)	to	ID	A1C9.1
From ID	A1J1.1 (+5V)	to	ID	A1C10.1
From ID	A1J1.1 (+5V)	to	ID	A1C11.1
From ID	P1-2 (DC1-LO)	to	ID	A1P1.9
From ID	A1J1.9	to	GRO	DUND

Date: 04 March 2016

From ID	P1-4 (DC2-HI)	to	ID A1P2.2
From ID	A1J2.2 (+15V)	to	ID A1U1.16
From ID	A1J2.2 (+15V)	to	ID A1U2.16
From ID	A1J2.2 (+15V)	to	ID A1U3.16
From ID	A1J2.2 (+15V)	to	ID A1C2.1
From ID	A1J2.2 (+15V)	to	ID A1C4.1
From ID	A1J2.2 (+15V)	to	ID A1C6.1
From ID	P1-5 (DC2-LO)	to	ID A1P1.10
From ID	A1J1.10	to	GROUND
From ID	A1C1.2	to	GROUND
From ID	A1C2.2	to	GROUND
From ID	A1C3.2	to	GROUND
From ID	A1C4.2	to	GROUND
From ID	A1C5.2	to	GROUND
From ID	A1C6.2	to	GROUND
From ID	A1C7.2	to	GROUND
From ID	A1C8.2	to	GROUND
From ID	A1C9.2	to	GROUND
From ID	A1C10.2	to	GROUND
From ID	A1C11.2	to	GROUND
From ID	A1U1.8	to	GROUND
From ID	A1U2.8	to	GROUND
From ID	A1U3.8	to	GROUND
From ID	A1U4.8	to	GROUND
From ID	A1U5.8	to	GROUND
From ID		to	GROUND
From ID	A1U7.8	to	GROUND
From ID	A1U8.8	to	GROUND
From ID	P7-24 (DTS GCH 40)	to	ID A1P5.3
From ID			GROUND
	P6-64 (DTS GCH 7)	to	ID A1P5.1
From ID	A1J5.1	to	GROUND

Step 540

Description:

This step verifies connectivity from CH021 (ID J2A-13F) out through W4 and to the proper receive path associated with DTS ch 56 while connecting W4 with loopback and applying "0" out on W4P2-39 then driving "1" and detecting CH056 receives High level and reads "1".

From ID	P6-19 (DTS CH21)	to	ID	A1P5.38
From ID	A1J5.38	to	ID	A1U3.5
From ID	A1U3.4	to	ID	A1J10.20
From ID	A1P10.20	to	ID	P11-145 (S301-189)
From ID	P11-179 (S301-190)	to	ID	A1P10.23
From ID	A1J10.23	to	ID	J2A-13F
From W4	P1A-13F	to	W4	P2-39 (ST J5-39)
From ST_	J5-39	to	ST_	_J6-B10
From W4	P3-B10 (ST J6-20)	to	W4	P1B-5A

Date: 04 March 2016

From ID J2B-5A	to ID A1U8.11
From ID A1U8.12	to ID A1J5.5
From ID A1P5.5	to ID P7-40 (DTS CH56)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID AlJ1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID AlJ1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID AlJ1.1 (+5V)	to ID AlC8.1
From ID AlJ1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID AlC10.1
From ID AlJ1.1 (+5V)	to ID AlC11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID AlJ1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A102.2 (+15V) From ID A1J2.2 (+15V)	to ID A1C3.10
From ID A102.2 (+15V) From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A102.2 (+15V) From ID A1J2.2 (+15V)	to ID AlC4.1
	to ID A1C0.1
From ID P1-5 (DC2-LO)	
From ID Algl 2	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID AlC6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND

Date: 04 March 2016

From	ID	P7-24 (DTS GCH	40)	to	ID A1P5.3
From	ID	A1J5.3		to	GROUND
From	ID	P6-64 (DTS GCH	7)	to	ID A1P5.1
From	ID	A1J5.1		to	GROUND

Step 541

Description:

This step will verify the connections from the S701 switch out through W4 to FET#1 and FET#2 Gate inputs. This step will validate that the Q2 source loopback path is connected and received at the input of S202-30.

From ID A1J1.3	to ID A1P1.3 to ID A1J8.2 to ID P10-141 (S301-70) to ID A1P8.31 to ID BUS 2
From ID BUS 2 From ID Alp9.33 From ID P11-194 (S506-1) From ID AlJ10.3 From ID Alp12.50	to ID A1J9.33 to ID P11-195 (S506-4) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID BUS 2 From ID A1P9.33 From ID P11-162 (S506-2) From ID A1J10.1 From ID A1P12.48	to ID A1J9.33 to ID P11-195 (S506-4) to ID A1P10.1 to ID A1J12.48 to ID P12-44 (S701-2)
From ID P13-73 (S701-25) From ID A1J15.26 From W4 P1A-6B	to ID A1P15.26 to ID J2A-6B to W4 P2-28 (ST J5-28)
From W4 P2-28 (ST J5-28) From ID J2B-4E From ID A1D1.K From ID A1D1.K From ID A1D1.K	to W4 P1B-4E to ID A1D1.A to ID A1Q1.1 to ID A1CR2.K to ID A1R20.1
From ID P13-41 (S701-26) From ID A1J15.28 From W4 P1A-5B	to ID A1P15.28 to ID J2A-5B to W4 P2-9 (ST J5-9)
From W4 P2-9 (ST J5-9) From ID J2B-5E From ID A1Q2.3 From ID_J2B-6E	to W4 P1B-5E to ID A1Q2.1 to ID A1R23.1 to W4P2-8
From W4 P2-8 (ST J5-8) From ID J2B-11F	to W4 P1B-11F to ID A1J12.37

Date: 04 March 2016

From ID A1P12.37	to ID P12-60 (S202-30)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-18 (S509-3) From ID A1J9.19	to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.19 to ID BUS 1
From ID BUS 1 From ID A1P6.13 From ID P10-203 (S503-1) From ID A1J8.28 From ID A1P15.49	to ID AlJ6.13 to ID P10-77 (S503-3) to ID AlP8.28 to ID AlJ15.49 to ID P20-2 (DMM-HI)
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P7.38 From ID P10-229 (S301-24) From ID A1J7.36	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

2.9 MODULE 6 W5 GCU POWER DRIVE SPECIFIC ID TESTS

Refer to Reference Drawings when diagnosing connection paths.

Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF TEST PWB, A2).pdf and 13020A7501 (CABLE, W5, SCHEMATIC).pdf in section 1.4 during review of the following steps.

Step 601

Description:

This step verifies the wire path from W5 P2-1 to W5 P2-12. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-85 (S201-45) From ID A1J12.23 From ID A1P3.27 From W5 P1-1	to ID A1P12.23 to ID A1J3.27 to ID J3-1 to W5 P2-1 (ST J7-1)

Date: 04 March 2016

From ST_J7-1	to ST_J7-12	
From W5 P2-12 (ST J7-12)	to W5 P1-12	
From ID J3-12	to ID A1P3.36	
From ID A1J3.36	to ID A1J12.34	
From ID A1P12.34	to ID P12-58 (S202-16	5)
From ID P12-90 (S202-2)	to ID A1P12.36	
From ID A1J12.36	to ID A1J10.12	
From ID A1P10.12	to ID P11-242 (S509-2	3)
From ID P11-17 (S509-4)	to ID A1P9.29	
From ID AlJ9.29	to ID BUS 2	
From ID BUS 2	to ID A1J6.23	
From ID A1P6.23	to ID P10-12 (S503-4))
From ID P10-139 (S503-2)	to ID A1P8.26	
From ID AlJ8.26	to ID A1J15.50	
From ID A1P15.50	to ID P20-3 (DMM-LO)	

Step 602

Description:

This step verifies the wire path from W5 P2-1 to W5 P2-12. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID AlJ8.32
From ID A1P8.32	to ID P10-136 (S301-44)
From ID P10-135 (S301-43)	
From ID A1J7.7	to ID A1J2.12
From ID A1P2.12	to ID P10-150 (S101-3)
From ID P10-214 (S101-4)	to ID A1P2.35
From ID A1J2.35	to ID A1J3.22
From ID A1P3.22	to ID J3-18
From W5 P1-12	to W5 P2-12 (ST J7-12)
From ST_J7-12	to ST_J7-1
From W5 P2-1 (ST J7-1)	to W5 P1-1
From ID J3-1	to ID A1P3.27
From ID A1J3.27	to ID A1J12.23
From ID A1P12.23	to ID P12-85 (S201-45)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13

Date: 04 March 2016

From ID A1P6.13 to ID P10-77 (S503-3) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 603

Description:

This step verifies the wire path from W5 P2-2 to W5 P2-8. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID AlJ6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
	,
From ID P12-21 (S201-46)	to ID A1P12.24
	to ID A1J3.26
From ID A1P3.26	to ID J3-2
From W5 P1-2	to W5 P2-2 (ST J7-2)
110 110 11 1	20 11 1 2 (81 0 / 2)
From ST_J7-2	to ST_J7-8
110111 51_07 2	00 81_07 0
From W5 P2-8 (ST J7-8)	to W5 P1-11
From ID J3-11	to ID A1P3.37
From ID A1J3.37	to ID A1J12.33
From ID A1P12.33	to ID P12-26 (S202-15)
110111 12.33	20 12 112 20 (5202 13)
From ID P12-59 (S202-1)	to ID A1P12.38
	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
110111109.29	20 10 000 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A100.20 From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 604

Description:

Date: 04 March 2016

This step verifies the wire path from W5 P2-2 to W5 P2-8. The DMM will be used to measure resistance UL= 285 ohms LL= 275 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID A1P9.19 From ID B11-177 (S509-1)	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.10 to ID A1J12.38 to ID P12-59 (S202-1)
	to ID A1P12.33 to ID A1J3.37 to ID J3-11 to W5 P2-8 (ST J7-8)
From W5 P2-2 (ST J7-2) From ID J3-22 From ID A1J3.2 From ID A1P7.10 From ID P10-132 (S301-20) From ID A1J7.28 From ID A1P4.20 From ID R101.2	to ST_J7-2 to W5 P1-22 to ID A1P3.2 to ID A1J7.10 to ID P10-3 (S301-19) to ID A1P7.28 to ID A1J4.20 to ID R101.1 to ID A1P4.21 to ID A1P4.22 to GROUND to GROUND
From ID A1J15.50	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 605

Description:

This step verifies the wire path from W5 P2-2 to W5 P2-8. The DMM will be used to measure resistance R105, UL= 32 ohms LL= 25.8 ohms.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49	
From ID	A1J15.49	to	ID	A1J8.28	
From ID	A1P8.28	to	ID	P10-203	(S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13	
From ID	A1J6.13	to	ID	BUS 1	

Date: 04 March 2016

From ID BUS 1 From ID A1P9.19 From ID P11-177 (S509-1) From ID A1J10.10 From ID A1P12.38	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.10 to ID A1J12.38 to ID P12-59 (S202-1)
From ID P12-26 (S202-15) From ID A1J12.33 From ID A1P3.37 From W5 P1-11	to ID A1P12.33 to ID A1J3.37 to ID J3-11 to W5 P2-8 (ST J7-8)
From ST_J7-8	to ST_J7-2
From W5 P2-2 (ST J7-2)	to W5 P1-19
From ID J3-19	to ID A1P3.11
From ID A1J3.11	to ID A1J2.19
From ID A1P2.19	to ID P11-23 (S101-25)
From ID P11-87 (S101-26)	to ID A1P2.9
From ID A1J2.9	to ID A1J4.3
From ID A1P4.3	to ID R105.1
From ID R105.2	to ID A1P4.10
From ID A1J4.10	to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 606

Description:

This step verifies the wire path from W5 P2-3 to W5 P2-10. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID AlJ9.15 to ID P11-77 (S508-3) to ID AlP10.6 to ID AlJ12.42 to ID P12-16 (S201-1)
From ID P12-55 (S201-47) From ID A1J12.27 From ID A1P3.31 From W5 P1-5	to ID A1P12.27 to ID A1J3.31 to ID J3-5 to W5 P2-3 (ST J7-3)

Date: 04 March 2016

From ST_J7-3	to ST_J7-10
From W5 P2-10 (ST J7-10) From ID J3-10 From ID A1J3.34 From ID A1P12.32	to W5 P1-10 to ID A1P3.34 to ID A1J12.32 to ID P12-88 (S202-14)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 607

Description:

This step verifies the wire path from W5 P2-3 to W5 P2-10. The DMM resource will be used to measure resistance UL= 10 ohms.

From P20-2 (DMM-HI) From ID A1J15.49	to ID A1P15.49
From ID A1J15.49	to ID A1J7.44
From ID A1P7.44	to ID P10-99 (S301-4)
From ID P10-226 (S301-3)	to ID A1P7.13
From ID A1J7.13	to ID A1J2.30
	to ID P11-216 (S101-32)
From ID P11-152 (S101-31)	to ID A1P2.37
From ID A1J2.37	to ID A1J3.20
From ID A1P3.20	to ID J3-28
From W5 P1-28	to W5 P2-3 (ST J7-3)
From ST_J7-3	to ST_J7-10
From W5 P2-10 (ST J7-10)	
From ID J3-10	to ID A1P3.34
From ID A1J3.34	to ID A1J12.32
From ID A1P12.32	to ID P12-88 (S202-14)
· · · · · · · · · · · · · · · · · · ·	to ID AlP12.36
From ID AlJ12.36	to ID AlJ10.12
From ID AlP10.12	to ID P11-242 (S509-2)
·	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
TD DWG 0	- TD 3176 03
From ID BUS 2	to ID AlJ6.23
	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26

Date: 04 March 2016

From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 608

Description:

This step verifies the wire path from W5 P2-4 to W5 P2-9. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40 From ID P12-86 (S201-48) From ID A1J12.28	to ID AlJ9.15 to ID P11-77 (S508-3) to ID AlP10.8 to ID AlJ12.40 to ID P12-80 (S201-2) to ID AlP12.28 to ID AlJ3.30 to ID J3-6 to W5 P2-4 (ST J7-4)
From ST_J7-4 From W5 P2-9 (ST J7-9) From ID J3-9	to ST_J7-9 to W5 P1-9 to ID A1P3.35
From ID A1J3.35 From ID A1P12.31	to ID A1J12.31 to ID P12-57 (S202-13)
	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 609

Description:

This step verifies the wire path from W5 P2-4 to W5 P2-9. The DMM resource will be used to measure resistance UL= 10 ohms.

Date: 04 March 2016

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID Alp9.19 From ID P11-177 (S509-1) From ID AlJ10.10 From ID Alp12.38	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.10 to ID A1J12.38 to ID P12-59 (S202-1)
From ID P12-57 (S202-13) From ID A1J12.31 From ID A1P3.35 From W5 P1-9	to ID A1P12.31 to ID A1J3.35 to ID J3-9 to W5 P2-9 (ST J7-9)
From ST_J7-9	to ST_J7-4
From W5 P2-4 (ST J7-4) From ID J3-27 From ID A1J3.21 From ID A1P2.36 From ID P11-88 (S101-30) From ID A1J2.22 From ID A1P1.11	to W5 P1-27 to ID A1P3.21 to ID A1J2.36 to ID P11-24 (S101-29) to ID A1P2.22 to ID A1J1.11 to ID P1-11 (DC4-LO)
- ,	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 610

Description:

This step verifies the wire path from W5 P2-4 to W5 P2-9. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-86 (S201-48)	to ID A1P12.28

Date: 04 March 2016

From ID AlJ12.28	to ID A1J3.30
From ID AlP3.30	to ID J3-6
From W5 P1-6	to W5 P2-4 (ST J7-4)
From ST_J7-4	to ST_J7-9
From W5 P2-9 (ST J7-9) From ID J3-24 From ID A1J3.4 From ID A1P7.6 From ID P10-228 (S301-14) From ID A1J7.20 From ID A1P1.4	to W5 P1-24 to ID A1P3.4 to ID A1J7.6 to ID P10-100 (S301-13) to ID A1P7.20 to ID A1J1.4 to DC10-L0
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 611

Description:

This step verifies the wire path from W5 P2-5 to W5 P2-11. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-177 (S509-1) From ID A1J10.10 From ID A1P12.38	to ID AlJ9.19 to ID P11-18 (S509-3) to ID AlP10.10 to ID AlJ12.38 to ID P12-59 (S202-1)
From ID P12-89 (S202-5) From ID A1J12.25 From ID A1P3.29 From W5 P1-3 From ST_J7-5	to ID A1P12.25 to ID A1J3.29 to ID J3-3 to W5 P2-5 (ST J7-5)
From W5 P2-11 (ST J7-11) From ID J3-8 From ID A1J3.32 From ID A1P12.30	to W5 P1-8 to ID A1P3.32 to ID A1J12.30 to ID P12-56 (S202-8)

Date: 04 March 2016

From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 612

Description:

This step verifies the wire path from W5 P2-5 to W5 P2-11. The DMM will be used to measure resistance UL= 285 ohms LL= 275 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.34 From ID P10-167 (S301-45) From ID A1J7.22 From ID A1P3.1 From ID J3-21	to ID A1P15.49 to ID A1J8.34 to ID P10-201 (S301-46) to ID A1P7.22 to ID A1J3.1 to ID J3-21 to W5 P2-11 (ST J7-11)
From ST_J7-11	to ST_J7-5
From ID J3-23 From ID A1J3.3	to W5 P1-23 to ID A1P3.3 to ID A1J7.8 to ID P10-227 (S301-21) to ID A1P7.30 to ID A1J4.20 to ID R101.1 to ID A1P4.21 to ID A1P4.22 to GROUND to GROUND
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P7.38 From ID P10-229 (S301-24) From ID A1J7.36	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 613

Description:

This step verifies the wire path from W5 P2-5 to W5 P2-11. The DMM will be used to measure resistance R104, UL= 32 ohms LL= 25.8 ohms.

Date: 04 March 2016

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.34 From ID P10-167 (S301-45) From ID A1J7.22 From ID A1P3.1 From ID J3-21	to ID A1P15.49 to ID A1J8.34 to ID P10-201 (S301-46) to ID A1P7.22 to ID A1J3.1 to ID J3-21 to W5 P2-11 (ST J7-11)
From ST_J7-11	to ST_J7-5
From W5 P2-5 (ST J7-5) From ID J3-20 From ID A1J3.23 From ID A1P2.34 From ID P11-215 (S101-28) From ID A1J2.8 From ID A1P4.4 From ID R104.2 From ID A1J4.10	to W5 P1-20 to ID A1P3.23 to ID A1J2.34 to ID P11-151 (S101-27) to ID A1P2.8 to ID A1J4.4 to ID R104.1 to ID A1P4.10 to GROUND
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P7.38 From ID P10-229 (S301-24) From ID A1J7.36	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23) to ID A1P7.36 to GROUND

Step 614

Description:

This step verifies the wire path from W5 P2-6 to W5 P2-7. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-242 (S509-2) From ID A1J10.12 From ID A1P12.36	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.12 to ID A1J12.36 to ID P12-90 (S202-2)
From ID P12-25 (S202-6) From ID A1J12.26 From ID A1P3.28 From W5 P1-4 From ST_J7-6	to ID A1P12.26 to ID A1J3.28 to ID J3-4 to W5 P2-6 (ST J7-6) to ST_J7-7

Date: 04 March 2016

From W5 P2-7 (ST J7-7)	to W5 P1-7
From ID J3-7	to ID A1P3.33
From ID A1J3.33	to ID A1J12.29
From ID A1P12.29	to ID P12-24 (S202-7)
Error ID D12 02 (G202 2)	to ID A1P14.49
From ID P13-93 (S202-3)	
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

2.10 MODULE 7 W6 GTD POWER SUPPLY SPECIFIC ID TESTS

Refer to Reference Drawings when diagnosing connection paths.

Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF
TEST PWB, A2).pdf and 13020A7601 (CABLE, W6, SCHEMATIC).pdf in section 1.4 during review of the following steps.

Step 701

Description:

This step verifies the wire path from W6 P2-1 to W6 P3-1. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID AlJ15.49 From ID AlP8.28 From ID AlP8.28 From ID P10-77 (S503-3) From ID P10-77 (S503-3) From ID AlJ6.13 From ID BUS 1 From ID BUS 1 From ID AlP9.15 From ID P11-203 (S508-1) From ID AlJ10.6 From ID AlJ10.6 From ID AlP12.42 From ID P12-79 (S201-5) From ID AlJ13.1 From W6 P1B-14A From W6 P3-A1 (ST J6-1) From ID J1A-6E From ID J1A-6E to ID AlJ14.19	From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID P10-77 (S503-3) to ID A1P6.13 From ID A1J6.13 to ID BUS 1 From ID BUS 1 to ID A1J9.15 From ID A1P9.15 to ID P11-77 (S508-3) From ID P11-203 (S508-1) to ID A1P10.6 From ID A1J10.6 to ID A1J12.42 From ID A1P12.42 to ID P12-16 (S201-1) From ID P12-79 (S201-5) to ID A1P13.1 From ID A1J13.1 to ID J1B-14A From W6 P1B-14A to W6 P2-1 (ST J8-1) From ST_J8-1 to ST_J6-A1 From W6 P3-A1 (ST J6-1) to W6 P1A-6E	From ID A1J15.49	to ID A1J8.28
From ID AlJ6.13 to ID BUS 1 From ID BUS 1 to ID AlJ9.15 From ID AlP9.15 to ID P11-77 (S508-3) From ID P11-203 (S508-1) to ID AlP10.6 From ID AlJ10.6 to ID AlJ12.42 From ID AlP12.42 to ID P12-16 (S201-1) From ID P12-79 (S201-5) to ID AlP13.1 From ID AlJ13.1 to ID J1B-14A From W6 P1B-14A to W6 P2-1 (ST J8-1) From ST_J8-1 to ST_J6-Al From W6 P3-Al (ST J6-1) to W6 P1A-6E	From ID A1P8.28	to ID P10-203 (S503-1)
From ID BUS 1 to ID AlJ9.15 From ID AlP9.15 to ID P11-77 (S508-3) From ID P11-203 (S508-1) to ID AlP10.6 From ID AlJ10.6 to ID AlJ12.42 From ID AlP12.42 to ID P12-16 (S201-1) From ID P12-79 (S201-5) to ID AlP13.1 From ID AlJ13.1 to ID J1B-14A From W6 P1B-14A to W6 P2-1 (ST J8-1) From ST_J8-1 to ST_J6-Al From W6 P3-Al (ST J6-1) to W6 P1A-6E	From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1J10.6 From ID A1P12.42 From ID P12-79 (S201-5) From ID A1J13.1 From ID A1J13.1 From W6 P1B-14A From W6 P1B-14A From W6 P3-A1 (ST J6-1) to ID P11-77 (S508-3) to ID A1P10.6 to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1) to ID A1P13.1 to ID J1B-14A to W6 P2-1 (ST J8-1)	From ID A1J6.13	to ID BUS 1
From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1J10.6 From ID A1P12.42 From ID P12-79 (S201-5) From ID A1J13.1 From ID A1J13.1 From W6 P1B-14A From W6 P1B-14A From W6 P3-A1 (ST J6-1) to ID P11-77 (S508-3) to ID A1P10.6 to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1) to ID A1P13.1 to ID J1B-14A to W6 P2-1 (ST J8-1)		
From ID P11-203 (S508-1) to ID A1P10.6 From ID A1J10.6 to ID A1J12.42 From ID A1P12.42 to ID P12-16 (S201-1) From ID P12-79 (S201-5) to ID A1P13.1 From ID A1J13.1 to ID J1B-14A From W6 P1B-14A to W6 P2-1 (ST J8-1) From ST_J8-1 to ST_J6-A1 From W6 P3-A1 (ST J6-1) to W6 P1A-6E	From ID BUS 1	to ID A1J9.15
From ID AlJ10.6 to ID AlJ12.42 From ID AlP12.42 to ID P12-16 (S201-1) From ID P12-79 (S201-5) to ID AlP13.1 From ID AlJ13.1 to ID J1B-14A From W6 P1B-14A to W6 P2-1 (ST J8-1) From ST_J8-1 to ST_J6-Al From W6 P3-Al (ST J6-1) to W6 P1A-6E	From ID A1P9.15	to ID P11-77 (S508-3)
From ID AlP12.42 to ID P12-16 (S201-1) From ID P12-79 (S201-5) to ID AlP13.1 From ID AlJ13.1 to ID J1B-14A From W6 P1B-14A to W6 P2-1 (ST J8-1) From ST_J8-1 to ST_J6-Al From W6 P3-Al (ST J6-1) to W6 P1A-6E	From ID P11-203 (S508-1)	to ID A1P10.6
From ID P12-79 (S201-5) to ID A1P13.1 From ID A1J13.1 to ID J1B-14A From W6 P1B-14A to W6 P2-1 (ST J8-1) From ST_J8-1 to ST_J6-A1 From W6 P3-A1 (ST J6-1) to W6 P1A-6E	From ID A1J10.6	to ID A1J12.42
From ID AlJ13.1 to ID J1B-14A to W6 P2-1 (ST J8-1) From ST_J8-1 to ST_J6-Al From W6 P3-Al (ST J6-1) to W6 P1A-6E	From ID A1P12.42	to ID P12-16 (S201-1)
From ID AlJ13.1 to ID J1B-14A to W6 P2-1 (ST J8-1) From ST_J8-1 to ST_J6-Al From W6 P3-Al (ST J6-1) to W6 P1A-6E		
From W6 P1B-14A to W6 P2-1 (ST J8-1) From ST_J8-1 to ST_J6-A1 From W6 P3-A1 (ST J6-1) to W6 P1A-6E	From ID P12-79 (S201-5)	to ID A1P13.1
From ST_J8-1 to ST_J6-A1 From W6 P3-A1 (ST J6-1) to W6 P1A-6E	From ID A1J13.1	to ID J1B-14A
From W6 P3-A1 (ST J6-1) to W6 P1A-6E	From W6 P1B-14A	to W6 P2-1 (ST J8-1)
From W6 P3-A1 (ST J6-1) to W6 P1A-6E		
	From ST_J8-1	to ST_J6-A1
From ID J1A-6E to ID A1J14.19	From W6 P3-A1 (ST J6-1)	to W6 P1A-6E
	From ID J1A-6E	to ID A1J14.19

Date: 04 March 2016

From ID	A1P14.19	to	ID	P13-88 (S202-19)
From ID From ID From ID	P12-59 (S202-1) A1J12.38 A1P10.10 P11-17 (S509-4) A1J9.29	to to to	ID ID ID	A1P12.38 A1J10.10 P11-177 (S509-1) A1P9.29 BUS 2
From ID From ID	BUS 2 A1P6.23 P10-139 (S503-2) A1J8.26 A1P15.50	to to to	ID ID ID	A1J6.23 P10-12 (S503-4) A1P8.26 A1J15.50 P20-3 (DMM-LO)

Step 702

Description:

This step verifies the wire path from W6 P2-2 to W6 P3-2. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-47 (S201-6) From ID A1J13.2 From W6 P1B-13A	to ID A1P13.2 to ID J1B-13A to W6 P2-2 (ST J8-2)
From ST_J8-2	to ST_J6-B1
From W6 P3-B1 (ST J6-2) From ID J1A-6F From ID A1P14.20	to W6 P1A-6F to ID A1J14.20 to ID P13-24 (S202-20)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50

Date: 04 March 2016

From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 703

Description:

This step verifies the wire path from W6 P2-3 to W6 P3-3. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-46 (S201-7) From ID A1J13.3 From W6 P1B-14B	to ID A1P13.3 to ID J1B-14B to W6 P2-3 (ST J8-3)
From ST_J8-3	to ST_J6-A2
From W6 P3-A2 (ST J6-3) From ID J1A-7F From ID A1P14.22	to W6 P1A-7F to ID A1J14.22 to ID P13-92 (S202-34)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID AlJ6.23 to ID P10-12 (S503-4) to ID AlP8.26 to ID AlJ15.50 to ID P20-3 (DMM-LO)

Step 704

Description:

This step verifies the wire path from W6 P2-3 to W6 P3-3. The DMM will be used to measure resistance R107, UL= 53 ohms LL= 47 ohms.

From ID	P20-2	(DMM-HI)	to	ID	A1P15.49
From ID	A1J15.	49	to	ID	A1J8.28

Date: 04 March 2016

From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-242 (S509-2) From ID A1J10.12 From ID A1P12.36	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.12 to ID A1J12.36 to ID P12-90 (S202-2)
From ID P13-92 (S202-34)	to ID A1P14.22
From ID A1J14.22	to ID J1A-7F
From W6 P1A-7F	to W6 P3-A2 (ST J6-3)
From ST_J6-A2	to ST_J8-3
From ID J1B-2E From ID A1P11.3 From ID P11-45 (S301-159) From ID A1J11.31 From ID A1P4.6	to W6 P1B-2E to ID A1J11.3 to ID P11-46 (S301-160) to ID A1P11.31 to ID A1J4.6 to ID R107.1 to ID A1P4.10 to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 705

Description:

This step verifies the wire path from W6 P2-4 to W6 P3-4. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.30
From ID A1P8.30	to ID P10-41 (S301-42)
From ID P10-199 (S301-41)	to ID A1P7.2
From ID A1J7.2	to ID A1J2.32
From ID A1P2.32	to ID P10-218 (S101-20)
From ID P10-154 (S101-19)	to ID A1P2.16
From ID AlJ2.16	to ID J1B-3D
From W6 P1B-3D	to W6 P2-4 (ST J8-4)
From ST_J8-4	to ST_J6-B2
From W6 P3-B2 (ST J6-4)	to W6 P1A-8E
From ID J1A-8E	to ID A1J14.23
From ID A1P14.23	to ID P13-91 (S202-35)

Date: 04 March 2016

From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 706

Description:

This step verifies the wire path from W6 P2-5 to W6 P3-5. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.30 From ID P10-199 (S301-41) From ID A1J7.2 From W6 P1B-3B	to ID A1P15.49 to ID A1J8.30 to ID P10-41 (S301-42) to ID A1P7.2 to ID J1B-3B to W6 P2-5 (ST J8-5)
From ST_J8-5	to ST_J6-A3
From W6 P3-A3 (ST J6-5)	to W6 P1B-5C
From ID J1B-5C	to ID A1J12.15
From ID A1P12.15	to ID P12-31 (S202-45)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 707

Description:

This step verifies the wire path from W6 P2-6 to W6 P3-6. The DMM resource will be used to measure resistance UL= 10 ohms.

Date: 04 March 2016

From ID From ID From ID	A1J15.49 A1P8.28	to I to I to I	D A1P15.49 D A1J8.28 D P10-203 (S503-1) D A1P6.13 D BUS 1
From ID From ID	A1P9.15 P11-139 (S508-2)	to I to I to I	D A1J9.15 D P11-77 (S508-3) D A1P10.8 D A1J12.40 D P12-80 (S201-2)
From ID	P12-13 (S201-8) A1J13.4 P1B-13B	to I	D A1P13.4 D J1B-13B 6 P2-6 (ST J8-6)
From ST_	_J8-6	to S	T_J6-B3
From ID	,	to I	76 P1B-4A D A1J12.16 D P12-63 (S202-46)
From ID From ID From ID	AlJ12.36 AlP10.12 P11-17 (S509-4)	to I to I to I	D A1P12.36 D A1J10.12 D P11-242 (S509-2) D A1P9.29 D BUS 2
From ID From ID	A1P6.23 P10-139 (S503-2)	to I to I to I	D A1J6.23 D P10-12 (S503-4) D A1P8.26 D A1J15.50 D P20-3 (DMM-LO)

Step 708

Description:

This step verifies the wire path from W6 P2-6 to W6 P3-6. The DMM will be used to measure resistance UL= 32 ohms LL= 25.8 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-242 (S509-2)	to ID A1P10.12
From ID A1J10.12	to ID A1J12.36
From ID A1P12.36	to ID P12-90 (S202-2)

Date: 04 March 2016

From ID P12-63 (S202-46)	to ID A1P12.16
From ID A1J12.16	to ID J1B-4A
From W6 P1B-4A	to W6 P3-B3 (ST J6-6)
From ST_J6-B3	to ST_J8-6
From W6 P2-6 (ST J8-6) From ID J1B-2D From ID A1P2.19 From ID P11-87 (S101-26) From ID A1J2.9 From ID A1P4.3 From ID R105.2 From ID A1J4.10	to W6 P1B-2D to ID A1J2.19 to ID P11-23 (S101-25) to ID A1P2.9 to ID A1J4.3 to ID R105.1 to ID A1P4.10 to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 709

Description:

This step verifies the wire path from W6 P3-7 to W4 P2-2. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID From ID From ID	P20-2 (DMM-HI) A1J15.49 A1P8.28 P10-77 (S503-3) A1J6.13	to to to	ID A1P15.49 ID A1J8.28 ID P10-203 (S503-1) ID A1P6.13 ID BUS 1
From ID From ID	A1P9.19 P11-177 (S509-1)	to to to	ID A1J9.19 ID P11-18 (S509-3) ID A1P10.10 ID A1J12.38 ID P12-59 (S202-1)
From ID	P12-96 (S202-47) A1J12.17 P1B-4B	to	ID A1P12.17 ID J1B-4B W6 P3-A4 (ST J6-7)
From ST	_J6-A4 P2-7 (ST J8-7)		ST_J8-7 W6 P1B-1E
From ID	J1B-1E	to	GROUND
From ID	P20-3 (DMM-LO) A1J15.50 A1P7.38	to	ID A1P15.50 ID A1J7.38 ID P10-130 (S301-23)

Date: 04 March 2016

From ID P10-229 (S301-24) to ID A1P7.36 From ID A1J7.36 to GROUND

Step 710

Description:

This step verifies the wire path from W6 P2-8 to W6 P3-8. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40 From ID P13-17 (S201-26) From ID A1J14.9 From W6 P1A-1E	to ID A1J12.40 to ID P12-80 (S201-2)
From ST_J8-8	to ST_J6-B4
From W6 P3-B4 (ST J6-8) From ID J1B-4C From ID A1P12.18	to W6 P1B-4C to ID A1J12.18 to ID P12-32 (S202-48)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1J10.12 to ID P11-242 (S509-2)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 711

Description:

This step verifies the wire path from W6 P2-9 to W6 P3-9. The DMM resource will be used to measure resistance UL= 10 ohms.

From	ID	P20-2	(DMM-HI)	to	ID	A1P15.49
From	ID	A1J15.	49	to	ID	A1J8.28

Date: 04 March 2016

From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P13-47 (S201-9)	to ID A1P14.1
From ID A1J14.1	to ID J1A-1A
From W6 P1A-1A	to W6 P2-9 (ST J8-9)
From ST_J8-9	to ST_J6-A5
From W6 P3-A5 (ST J6-9)	to W6 P1B-5B
From ID J1B-5B	to ID A1J12.14
From ID A1P12.14	to ID P12-27 (S202-24)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 712

Description:

This step verifies the wire path from W6 P2-10 to W6 P2-12. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P13-14 (S201-10) From ID A1J14.2	to ID A1P14.2 to ID J1A-2A

Date: 04 March 2016

From W6 P1A-2A	to W6 P2-10 (ST J8-10)
From ST_J8-10	to ST_J8-12
From W6 P2-12 (ST J8-12) From ID J1A-2B	to W6 P1A-2B to ID A1J14.4
From ID A1P14.4	to ID P13-48 (S201-12)
From ID P12-52 (S201-4)	to ID A1P12.44
From ID AlJ12.44	to ID A1J10.4
From ID A1P10.4	to ID P11-71 (S507-2)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID AlJ8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 713

Description:

This step verifies the wire path from W6 P2-10 to W6 P2-12. The DMM will be used to measure resistance R114, UL= 32 ohms LL= 24.9 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P13-14 (S201-10) From ID A1J14.2 From W6 P1A-2A	to ID A1P14.2 to ID J1A-2A to W6 P2-10 (ST J8-10)
From ST_J8-10	to ST_J8-12
From W6 P2-12 (ST J8-12) From ID J1B-1C From ID A1P2.13 From ID P11-89 (S101-34) From ID A1J2.7 From ID A1P4.5	to W6 P1B-1C to ID A1J2.13 to ID P11-25 (S101-33) to ID A1P2.7 to ID A1J4.5 to ID L2.1

Date: 04 March 2016

From ID L2.2	to ID R114.1
From ID R114.2	to ID A1P4.21
From ID R114.2	to ID A1P4.22
From ID A1J4.21	to GROUND
From ID A1J4.22	to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 714

Description:

This step verifies the wire path from W6 P2-11 to W6 P3-11. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID A1J15.49 From ID A1P8.28	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
,	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P13-80 (S201-11) From ID A1J14.3 From W6 P1A-1B	to ID A1P14.3 to ID J1A-1B to W6 P2-11 (ST J8-11)
From ST_J8-11	to ST_J6-A6
From W6 P3-A6 (ST J6-11) From ID J1A-7E From ID A1P14.21	to W6 P1A-7E to ID A1J14.21 to ID P13-90 (S202-26)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Date: 04 March 2016

Step 715

Description:

This step verifies the wire path from W6 P2-11 to W6 P3-11. The DMM will be used to measure resistance R106, UL= 29 ohms LL= 21 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID PIU-203 (S503-I)
	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.12 to ID A1J12.36 to ID P12-90 (S202-2)
From W6 P1A-7E	to ID J1A-7E to W6 P3-A6 (ST J6-11)
From ST_J6-A6 From W6 P2-11 (ST J8-11) From ID J1B-2F From ID A1P2.17 From ID P11-86 (S101-22) From ID A1J2.10 From ID A1P4.2 From ID L1.2 From ID R106.2 From ID A1J4.21 From ID A1J4.21 From ID A1J4.22	to ID A1J2.17 to ID P11-22 (S101-21)
From ID P20-3 (DMM-LO) From ID A1J15.50	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23)

Step 716

Description:

This step verifies the wire path from W6 P2-13 to W6 P2-20. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P	20-2 (DMM-HI)	to	ID	A1P15.49
From ID A	.1J15.49	to	ID	A1J8.28

Date: 04 March 2016

From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-14 (S201-13)	to ID A1P13.5
From ID A1J13.5	to ID J1B-14C
From W6 P1B-14C	to W6 P2-13 (ST J8-13)
From ST_J8-13	to ST_J8-20
From W6 P2-20 (ST J8-20)	to W6 P1A-2C
From ID J1A-2C	to ID A1J14.6
From ID A1P14.6	to ID P13-15 (S201-19)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID AlJ6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID AlP8.26
From ID A1J8.26	to ID AlJ15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 717

Description:

This step verifies the wire path from W6 P2-14 to W6 P2-21. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-78 (S201-14) From ID A1J13.6	to ID AlP13.6 to ID J1B-13C

Date: 04 March 2016

From W6 P1B-13C	to W6 P2-14 (ST J8-14)
From ST_J8-14	to ST_J8-21
From W6 P2-21 (ST J8-21) From ID J1A-1D From ID A1P14.7	to W6 P1A-1D to ID A1J14.7 to ID P13-79 (S201-20)
From ID P12-52 (S201-4) From ID A1J12.44 From ID A1P10.4 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.44 to ID A1J10.4 to ID P11-71 (S507-2) to ID A1P9.27 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 718

Description:

This step verifies the wire path from W6 P2-15 to W6 P2-22. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-48 (S201-15) From ID A1J12.1 From W6 P1B-9A From ST_J8-15	to ID A1P12.1 to ID J1B-9A to W6 P2-15 (ST J8-15) to ST_J8-22
	to W6 P1B-9C to ID A1J12.3 to ID P12-51 (S201-21)
From ID P12-20 (S201-3) From ID A1J12.46	to ID A1P12.46 to ID A1J10.2

Date: 04 March 2016

From ID A1P10.2
From ID P11-72 (S507-4)
From ID A1J9.27
From ID BUS 2
From ID A1P6.23
From ID P10-139 (S503-2)
From ID A1J8.26
From ID A1P15.50

to ID P11-39 (S507-1)
to ID BUS 2

to ID A1J6.23
to ID P10-12 (S503-4)
to ID A1P8.26
to ID A1J15.50
to ID P20-3 (DMM-LO)

Step 719

Description:

This step verifies the wire path from W6 P2-15 to W6 P2-22. The DMM resource will be used to measure resistance UL= 74 ohms LL= 67 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-51 (S201-21) From ID A1J12.3 From W6 P1B-9C From ST_J8-22	to ID A1P12.3 to ID J1B-9C to W6 P2-22 (ST J8-22) to ST_J8-15
From W6 P2-15 (ST J8-15) From ID J1B-2B From ID A1P11.1 From ID P11-169 (S301-155) From ID A1J11.32 From ID A1P4.8 From ID R113.2 From ID A1J4.10	to ID A1J11.1 to ID P11-11 (S301-156)
	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23)

Step 720

Description:

Date: 04 March 2016

This step verifies the wire path from W6 P2-16 to W6 P2-23. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-15 (S201-16)	to ID A1P12.2
From ID A1J12.2	to ID J1B-9B
From W6 P1B-9B	to W6 P2-16 (ST J8-16)
From ST_J8-16	to ST_J8-23
From W6 P2-23 (ST J8-23)	to W6 P1B-8A
From ID J1B-8A	to ID A1J12.4
From ID A1P12.4	to ID P12-18 (S201-22)
From ID P12-52 (S201-4) From ID A1J12.44 From ID A1P10.4 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.44 to ID A1J10.4 to ID P11-71 (S507-2) to ID A1P9.27 to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 721

Description:

This step verifies the wire path from W6 P2-18 to W6 P2-24. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID $P20-2$ (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)

Date: 04 March 2016

From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P13-49 (S201-17) From ID A1J14.5 From W6 P1A-1C	to ID A1P14.5 to ID J1A-1C to W6 P2-18 (ST J8-18)
From ST_J8-18	to ST_J8-24
From ID J1B-8B From ID A1P12.5	to W6 P1B-8B to ID A1J12.5 to ID P12-17 (S201-23)
From ID P12-20 (S201-3)	
From ID A1J12.46 From ID A1P10.2	to ID A1J10.2
From ID P11-72 (S507-4)	to ID P11-39 (S507-1) to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 722

Description:

This step verifies the wire path from W6 P2-19 to W6 P2-25. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID AlJ6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P13-16 (S201-18)	to ID A1P14.28
From ID A1J14.28	to ID J1A-10F
From W6 P1A-10F	to W6 P2-19 (ST J8-19)
	,
From ST_J8-19	to ST_J8-25
From W6 P2-25 (ST J8-25)	to W6 P1B-8C
From ID J1B-8C	to ID A1J12.6

Date: 04 March 2016

From ID A1P12.6	to ID P12-81 (S201-24)
From ID P12-52 (S201-4) From ID A1J12.44 From ID A1P10.4 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1P12.44 to ID A1J10.4 to ID P11-71 (S507-2) to ID A1P9.27 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 723

Description:

This step verifies the wire path from W6 P2-19 to W6 P2-25. The DMM resource will be used to measure resistance UL= 46 ohms LL= 40 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-81 (S201-24) From ID A1J12.6 From W6 P1B-8C	to ID A1P12.6 to ID J1B-8C to W6 P2-25 (ST J8-25)
From ST_J8-25	to ST_J8-19
From W6 P2-19 (ST J8-19) From ID J1B-2C From ID A1P11.2 From ID P11-172 (S301-157) From ID A1J11.33 From ID A1P4.7 From ID R112.2 From ID R112.2 From ID A1J4.21 From ID A1J4.22	to ID A1J11.2 to ID P11-13 (S301-158)
From ID P20-3 (DMM-LO) From ID A1J15.50 From ID A1P7.38	to ID A1P15.50 to ID A1J7.38 to ID P10-130 (S301-23)

Date: 04 March 2016

From ID P10-229 (S301-24) to ID A1P7.36 From ID A1J7.36 to GROUND

Step 724

Description:

This step verifies the wire path from W6 P2-25 to W6 P2-26. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40 From ID P12-81 (S201-24) From ID A1J12.6 From W6 P1B-8C	to ID A1J12.40 to ID P12-80 (S201-2)
From ST_J8-25	to ST_J8-26
From W6 P2-26 (ST J8-26) From ID J1A-2D From ID A1P14.9	to W6 P1A-2D to ID A1J14.8 to ID P13-50 (S201-25)
From ID P12-20 (S201-3) From ID A1J12.46 From ID A1P10.2 From ID P11-72 (S507-4) From ID A1J9.27	to ID A1J10.2 to ID P11-39 (S507-1)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 725

Description:

This step verifies the Q3 - Q5 FETs used to provide GTD PSU overload capability while testing the +/-15V Power supplies are not shorted. The DMM will be used to measure voltage LL= 14 volts across FETs.

Connection Path is as follows:

From ID P1-4 (DC2-HI) to ID A1P1.2

Date: 04 March 2016

From ID A1J1.2	to R4
From +15V	to ID R4.2
From ID R4.1	to ID A1J8.13
From ID A1P8.13	to ID P10-208 (S301-78)
From ID P10-47 (S301-77)	to ID A1P8.38
From ID A1J8.38	to ID BUS 6
FIOM ID A100.30	CO 1D BOS 0
From ID BUS 6	to ID A1J9.22
From ID A1P9.22	to ID P11-170 (S508-8)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID BUS 6	to ID A1J9.22
From ID Alp9.22	to ID P11-170 (S508-8)
	to ID AlP10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
FIOM ID AIFIZ. 42	CO 1D F12 10 (5201 1)
From ID P12-13 (S201-8)	to ID A1P13.4
From ID AlJ13.4	to ID J1B-13B
From W6 P1B-13B	to W6 P2-6 (ST J8-6)
From ID P13-49 (S201-17)	to ID A1P14.5
From ID AlJ14.5	to ID J1A-1C
From W6 P1A-1C	to W6 P2-18 (ST J8-18)
120 //0 22 20	00 110 12 20 (21 00 20)
From ID P13-16 (S201-18)	to ID A1P14.28
From ID A1J14.28	to ID J1A-10F
From W6 P1A-10F	to W6 P2-19 (ST J8-19)
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
Error ID DOO 2 /DMM IO	+
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 726

Description:

This step verifies the Q5 FET used to provide GTD PSU overload capability to test the +/-15V Power supplies is operational. The DMM resource will be used to measure voltage UL= 2 volts across the FET.

Connection Path is as follows:

From ID P1-4 (DC2-HI) to ID A1P1.2

Date: 04 March 2016

	2010		
From II	D A1J1.2	to	R4
From +1	15V	t.o	ID R4.2
From II			ID A1J8.13
	D A1P8.13		ID P10-208 (S301-78)
	D P10-47 (S301-77)		ID A1P8.38
	D A1J8.38		ID BUS 6
From II	D BUS 6	to	ID A1J9.22
From II	D A1P9.22	to	ID P11-170 (S508-8)
From II	D P11-139 (S508-2)	to	ID A1P10.8
	A1J10.8		ID A1J12.40
From II	D A1P12.40	to	ID P12-80 (S201-2)
From II	D P12-13 (S201-8)	tο	ID A1P13.4
	D A1J13.4		ID J1B-13B
	5 P1B-13B		W6 P2-6 (ST J8-6)
110111 111	3 115 135		W0 12 0 (81 00 0)
From We	5 P2-6 (ST J6-6)	to	W6 P1B-2D
	5 P2-18 (UUT J1-18)		W6 P1B-3C
From We	5 P2-19 (UUT J1-19)	to	W6 P1B-2C
From II) J1B-2D	to	ID R34.1
	D J1B-3C	to	ID R31.1
_) J1B-2C		ID R28.1
	D R34.2		ID Q5.2
	D R31.2		ID Q4.2
	D R28.2		ID Q3.2
	D P1-4 (DC2-HI)		ID A1P1.2
	D A1J1.2		+15V
From +1			ID A1J11.41
	D A1P11.41 D P11-138 (S301-150)		ID P11-9 (S301-149)
	D A1J11.43		ID R27.1
	D AlJ11.43		ID R30.1
_	D A1J11.43		ID R33.1
	D R27.2		ID Q3.1
	D R30.2		ID Q4.1
	R33.2		ID Q5.1
From II			ID R26.1
From II			ID R29.1
From II	Q5.1	to	ID R32.1
From II	D R26.2	to	GROUND
From II	R29.2	to	GROUND
From II	R32.2	to	GROUND
From II	· -	to	GROUND
From II	· -	to	GROUND
From II	Q5.3	to	GROUND
From II	D P20-2 (DMM-HI)	to	ID A1P15.49
	D A1J15.49		ID A1J8.28
	D A1P8.28		ID P10-203 (S503-1)
	P10-170 (S503-8)		ID A1P6.38
	A1J6.38		ID BUS 6

Date: 04 March 2016

From ID P20-3 (DMM-LO) to ID A1P15.50 from ID A1J15.50 to ID A1J7.38 from ID A1P7.38 to ID P10-130 (S301-23) from ID P10-229 (S301-24) to ID A1P7.36 from ID A1J7.36 to GROUND

Step 727

Description:

This step verifies the Q4 FET used to provide GTD PSU overload capability to test the +/-15V Power supplies is operational. The DMM resource will be used to measure voltage UL= 2 volts across the FET.

From ID P1-4 (DC2-HI) From ID A1J1.2	to ID A1P1.2 to R4
From +15V	to ID R4.2
From ID R4.1	to ID AlJ8.13
From ID A1P8.13	to ID P10-208 (S301-78)
From ID P10-47 (S301-77)	to ID A1P8.38
From ID A1J8.38	to ID BUS 6
	to ID A1J9.22
	to ID P11-170 (S508-8)
From ID P11-203 (S508-1)	
	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P13-49 (S201-17)	to ID A1P14.5
From ID A1J14.5	to ID J1A-1C
From W6 P1A-1C	to W6 P2-18 (ST J8-18)
From W6 P2-6 (ST J6-6)	to W6 P1B-2D
From W6 P2-18 (UUT J1-18)	to W6 P1B-3C
From W6 P2-19 (UUT J1-19)	to W6 P1B-2C
From ID J1B-2D	to ID R34.1
From ID J1B-3C	to ID R31.1
From ID J1B-2C	to ID R28.1
From ID R34.2	to ID Q5.2
From ID R31.2	to ID Q4.2
From ID R28.2	to ID Q3.2
	to ID A1P1.2
From ID A1J1.2	to +15V
From +15V	to ID A1J11.41
From ID A1P11.41	to ID P11-9 (S301-149)
From ID P11-138 (S301-150)	to ID A1P11.43
From ID Aluli.43	LO ID RZ/.I
From ID AlJ11.43	to ID R30.1
From ID A1J11.43	to ID R33.1
From ID R27.2	to ID Q3.1
From ID R30.2	to ID Q4.1
From ID R33.2	to ID Q5.1

Date: 04 March 2016

From	ID	Q3.1	to	ID R26.1
From	ID	Q4.1	to	ID R29.1
From	ID	Q5.1	to	ID R32.1
From	ID	R26.2	to	GROUND
From	ID	R29.2	to	GROUND
From	ID	R32.2	to	GROUND
From	ID	Q3.3	to	GROUND
From	ID	Q4.3	to	GROUND
From	ID	Q5.3	to	GROUND
_		-00 0 (-1)		1-15 40
		- '		ID A1P15.49
From	ID	A1J15.49	to	ID A1J8.28
From	ID	A1P8.28	to	ID P10-203 (S503-1)
From	ID	P10-170 (S503-8)	to	ID A1P6.38
From	ID	A1J6.38	to	ID BUS 6
From	TD	P20-3 (DMM-LO)	t o	ID A1P15.50
		A1J15.50		
_				ID A1J7.38
_		A1P7.38		ID P10-130 (S301-23)
		,	to	ID A1P7.36
From	ID	A1J7.36	to	GROUND

Step 728

Description:

This step verifies the Q3 FET used to provide GTD PSU overload capability to test the +/-15V Power supplies is operational. The DMM resource will be used to measure voltage UL= 2 volts across the FET.

From ID P1-4 (DC2-HI) From ID A1J1.2	to ID A1P1.2 to R4
From +15V	to ID R4.2
From ID R4.1	to ID A1J8.13
From ID A1P8.13	to ID P10-208 (S301-78)
From ID P10-47 (S301-77)	to ID A1P8.38
From ID A1J8.38	to ID BUS 6
From ID BUS 6 From ID A1P9.22 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.22 to ID P11-170 (S508-8) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P13-16 (S201-18)	to ID A1P14.28
From ID A1J14.28	to ID J1A-10F
From W6 P1A-10F	to W6 P2-19 (ST J8-19)
From W6 P2-6 (ST J6-6)	to W6 P1B-2D
From W6 P2-18 (UUT J1-18)	to W6 P1B-3C
From W6 P2-19 (UUT J1-19)	to W6 P1B-2C
From ID J1B-2D	to ID R34.1

Date: 04 March 2016

From ID J1B-3C	to ID R31.1
From ID J1B-2C	to ID R28.1
From ID R34.2	to ID Q5.2
From ID R31.2	to ID Q4.2
From ID R28.2	to ID Q3.2
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to +15V
From +15V	to ID A1J11.41
From ID A1P11.41	to ID P11-9 (S301-149)
From ID P11-138 (S301-150)	to ID A1P11.43
From ID AlJ11.43	to ID R27.1
From ID AlJ11.43	to ID R30.1
From ID AlJ11.43	to ID R33.1
From ID R27.2	to ID Q3.1
From ID R30.2	to ID Q4.1
From ID R33.2	to ID Q5.1
From ID Q3.1	to ID R26.1
From ID Q4.1	to ID R29.1
From ID Q5.1	to ID R32.1
From ID R26.2	to GROUND
From ID R29.2	to GROUND
From ID R32.2	to GROUND
From ID Q3.3	to GROUND
From ID Q4.3	to GROUND
From ID Q5.3	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	
From ID A1J6.38	to ID BUS 6
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	
From ID A1J7.36	to GROUND
110 10 1110 / . 00	30 01100112

Step 729

Description:

This step verifies the Q6 - Q8 FETs used to provide GTD PSU overload capability while testing the +/-15V Power supplies are not shorted. The DMM will be used to measure voltage UL= -14 volts across FETs.

From ID $P1-25$ (DC9-HI)	to ID AlP1.13
From ID A1J1.13	to ID A1J7.14
From ID A1P7.14	to ID P10-197 (S301-29)
From ID P10-198 (S301-30)	to ID A1P6.24
From ID A1J6.24	to ID BUS 7
From ID P1-26 (DC9-LO)	to ID A1P1.5

Date: 04 March 2016

From ID A1J1.5	to ID A1J7.16
From ID A1P7.16	to ID P10-163 (S301-12)
From ID P10-98 (S301-11)	to ID A1P7.32
From ID A1J7.32	to GROUND
From BUS 7 (ID A1J6.34)	to ID A1P6.34
From ID P10-61 (S402-4)	to ID P10-94 (S402-1)
From ID A1P6.9	to ID A1J6.9
From ID A1J6.9	to ID A1P6.9
From ID P10-94 (S402-1)	to ID P10-222 (S402-3)
From ID A1P6.44	to BUS 6 (ID A1J6.44)
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From ID R11.1	to ID A1J8.25
From ID A1P8.25	to ID P10-50 (S301-95)
From ID P10-148 (S301-96)	to ID A1P8.45
From ID A1J8.45	to ID BUS 5
From ID BUS 5 From ID A1P9.34 From ID P11-71 (S507-2) From ID A1J10.4 From ID A1P12.44	to ID A1J9.34 to ID P11-230 (S507-7) to ID A1P10.4 to ID A1J12.44 to ID P12-52 (S201-4)
From ID BUS 5 From ID A1P9.34 From ID P11-39 (S507-1) From ID A1J10.2 From ID A1P12.46	to ID A1J9.34 to ID P11-230 (S507-7) to ID A1P10.2 to ID A1J12.46 to ID P12-20 (S201-3)
From ID P12-15 (S201-16)	to ID A1P12.2
From ID A1J12.2	to ID J1B-9B
From W6 P1B-9B	to W6 P2-16 (ST J8-16)
From ID P12-48 (S201-15)	to ID A1P12.1
From ID A1J12.1	to ID J1B-9A
From W6 P1B-9A	to W6 P2-15 (ST J8-15)
From ID P12-46 (S201-7)	to ID A1P13.3
From ID A1J13.3	to ID J1B-14B
From W6 P1B-14B	to W6 P2-3 (ST J8-3)
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID P20-3 (DMM-LO)	to ID A1P15.50

Date: 04 March 2016

From ID A1J15.50 to ID A1J7.38 from ID A1P7.38 to ID P10-130 (S301-23) from ID P10-229 (S301-24) to ID A1P7.36 from ID A1J7.36 to GROUND

Step 730

Description:

This step verifies the operation of Q8 FET used to provide GTD PSU overload capability to test the +/-15v capability. The DMM resource will be used to measure voltage UL= -2 volts across FETs.

From ID P1-25 (DC9-HI) From ID A1J1.13 From ID A1P7.14 From ID P10-198 (S301-30) From ID A1J6.24	to ID A1P1.13 to ID A1J7.14 to ID P10-197 (S301-29) to ID A1P6.24 to ID BUS 7
From ID P1-26 (DC9-LO)	to ID A1P1.5
From ID A1J1.5	to ID A1J7.16
From ID A1P7.16	to ID P10-163 (S301-12)
From ID P10-98 (S301-11)	to ID A1P7.32
From ID A1J7.32	to GROUND
From BUS 7 (ID A1J6.34)	to ID A1P6.34
From ID P10-61 (S402-4)	to ID P10-94 (S402-1)
From ID A1P6.9	to ID A1J6.9
From ID A1J6.9	to ID A1P6.9
From ID P10-94 (S402-1)	to ID P10-222 (S402-3)
From ID A1P6.44	to BUS 6 (ID A1J6.44)
From ID BUS 6 From ID A1P8.46 From ID P10-179 (S301-94) From ID A1J8.23	to ID A1J8.46 to ID P10-145 (S301-93) to ID A1P8.23 to ID R11.2
From ID R11.1	to ID A1J8.25
From ID A1P8.25	to ID P10-50 (S301-95)
From ID P10-148 (S301-96)	to ID A1P8.45
From ID A1J8.45	to ID BUS 5
From ID BUS 5 From ID A1P9.34 From ID P11-71 (S507-2) From ID A1J10.4 From ID A1P12.44	to ID A1J9.34 to ID P11-230 (S507-7) to ID A1P10.4 to ID A1J12.44 to ID P12-52 (S201-4)
From ID P12-15 (S201-16)	to ID A1P12.2
From ID A1J12.2	to ID J1B-9B
From W6 P1B-9B	to W6 P2-16 (ST J8-16)

Date: 04 March 2016

From W6 P2-3 (ST J6-3)	to W6 P1B-2E
From W6 P2-15 (UUT J1-15)	to W6 P1B-2B
From W6 P2-16 (UUT J1-16)	to W6 P1B-4D
From ID J1B-2E	to ID Q7.3
From ID J1B-2B	to ID Q6.3
From ID J1B-4D	to ID Q8.3
From ID Q6.2	to ID R37.2
From ID Q7.2	to ID R40.2
From ID Q8.2	to ID R43.2
From ID R37.1	to GROUND
From ID R40.1	to GROUND
From ID R43.1	to GROUND
From ID P1-16 (DC6-HI)	to ID A1P1.15
From ID AlJ1.15	to -15V
From -15V	to ID R35.1
From -15V	to ID R38.1
From -15V	to ID R41.1
From ID R35.2	to ID Q6.1
From ID R38.2	to ID Q7.1
From ID R41.2	to ID Q8.1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to +15V
From +15V	to ID A1J11.42
From ID A1P11.42	to ID P11-106 (S301-151)
From ID P11-75 (S301-152)	to ID A1P11.44
From ID AlJ11.44	to ID R36.1
From ID A1J11.44	to ID R39.1
From ID AlJ11.44	to ID R42.1
From ID R36.2	to ID Q6.1
From ID R39.2	to ID Q7.1
From ID R42.2	to ID Q8.1
The same of the sa	t- TD 71D15 40
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 731

Description:

This step verifies the operation of Q7 FET used to provide GTD PSU overload capability to test the +/-15v capability. The DMM resource will be used to measure voltage UL= -2 volts across FETs.

Connection Path is as follows:

From ID P1-25 (DC9-HI) to ID A1P1.13

Date: 04 March 2016

From ID A1J1.13	to ID A1J7.14
From ID A1P7.14	to ID P10-197 (S301-29)
From ID P10-198 (S301-30)	to ID A1P6.24
From ID A1J6.24	to ID BUS 7
110m 15 A100.21	CO ID DOD /
From ID P1-26 (DC9-LO)	to ID A1P1.5
From ID A1J1.5	to ID A1J7.16
From ID A1P7.16	to ID P10-163 (S301-12)
From ID P10-98 (S301-11)	to ID A1P7.32
From ID A1J7.32	to GROUND
FIOR ID AIO7.32	CO GROOND
From BUS 7 (ID A1J6.34)	to ID A1P6.34
From ID P10-61 (S402-4)	to ID P10-94 (S402-1)
From ID AlP6.9	to ID A1J6.9
110111 12 11110.9	20 12 11200.5
From ID A1J6.9	to ID A1P6.9
From ID P10-94 (S402-1)	to ID P10-222 (S402-3)
From ID A1P6.44	to BUS 6 (ID A1J6.44)
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From ID R11.1	to ID A1J8.25
From ID A1P8.25	to ID P10-50 (S301-95)
From ID P10-148 (S301-96)	to ID A1P8.45
From ID A1J8.45	to ID BUS 5
Darent D. Dilla F	L- TD 71 TO 24
From ID BUS 5	to ID A1J9.34
From ID A1P9.34	to ID P11-230 (S507-7)
From ID P11-39 (S507-1)	to ID A1P10.2
From ID AlJ10.2	to ID AlJ12.46
From ID A1P12.46	to ID P12-20 (S201-3)
From ID P12-48 (S201-15)	to ID A1P12.1
From ID A1J12.1	to ID J1B-9A
From W6 P1B-9A	to W6 P2-15 (ST J8-15)
FIOM WO FID JA	CO WO FZ 13 (S1 00 13)
From W6 P2-3 (ST J6-3)	to W6 P1B-2E
From W6 P2-15 (UUT J1-15)	to W6 P1B-2B
From W6 P2-16 (UUT J1-16)	to W6 P1B-4D
From ID J1B-2E	to ID Q7.3
From ID J1B-2B	to ID Q6.3
From ID J1B-4D	to ID Q8.3
From ID Q6.2	to ID R37.2
From ID Q7.2	to ID R40.2
From ID Q8.2	to ID R43.2
From ID R37.1	to GROUND
From ID R40.1	to GROUND
From ID R43.1	to GROUND
From ID P1-16 (DC6-HI)	to ID A1P1.15
From ID AlJ1.15	to -15V
From -15V	to ID R35.1
 -	

Date: 04 March 2016

From -15V	to	ID 1	R38.1
From -15V	to	ID 1	R41.1
From ID R35.2	to	ID (Q6.1
From ID R38.2	to	ID (Q7.1
From ID R41.2	to	ID (Q8.1
From ID P1-4 (DC2-HI)	to	ID 2	A1P1.2
From ID AlJ1.2	to	+15	V
From +15V	to	ID 2	A1J11.42
From ID A1P11.42	to	ID :	P11-106 (S301-151)
From ID P11-75 (S301-152)	to	ID 2	A1P11.44
From ID AlJ11.44	to	ID 1	R36.1
From ID AlJ11.44	to	ID 1	R39.1
From ID AlJ11.44	to	ID 1	R42.1
From ID R36.2	to	ID (Q6.1
From ID R39.2	to	ID (Q7.1
From ID R42.2	to	ID (Q8.1
From ID P20-2 (DMM-HI)	to	ID 2	A1P15.49
From ID AlJ15.49	to	ID 2	A1J8.28
From ID A1P8.28	to	ID :	P10-203 (S503-1)
From ID P10-137 (S503-7)	to	ID 2	A1P6.47
From ID AlJ6.47	to	ID :	BUS 5
From ID P20-3 (DMM-LO)	to	ID 2	A1P15.50
From ID A1J15.50	to	ID 2	A1J7.38
From ID A1P7.38	to	ID :	P10-130 (S301-23)
From ID P10-229 (S301-24)	to	ID 2	A1P7.36
From ID A1J7.36	to	GRO!	UND

Step 732

Description:

This step verifies the operation of Q6 FET used to provide GTD PSU overload capability to test the +/-15v capability. The DMM resource will be used to measure voltage UL= -2 volts across FETs.

From ID P1-25 (DC9-HI) From ID A1J1.13 From ID A1P7.14 From ID P10-198 (S301-30) From ID A1J6.24	to ID A1P1.13 to ID A1J7.14 to ID P10-197 (S301-29) to ID A1P6.24 to ID BUS 7
From ID P1-26 (DC9-LO) From ID A1J1.5 From ID A1P7.16 From ID P10-98 (S301-11) From ID A1J7.32	to ID A1P1.5 to ID A1J7.16 to ID P10-163 (S301-12) to ID A1P7.32 to GROUND
From BUS 7 (ID A1J6.34) From ID P10-61 (S402-4) From ID A1P6.9 From ID A1J6.9	to ID A1P6.34 to ID P10-94 (S402-1) to ID A1J6.9

Date: 04 March 2016

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From ID P10-94 (S402-1) to ID P10-222 (S402-3)
  From ID A1P6.44
                                                                                       to BUS 6 (ID A1J6.44)
  From ID BUS 6 to ID A1J8.46 From ID A1P8.46 to ID P10-145 (S301-93) From ID P10-179 (S301-94) to ID A1P8.23 From ID A1J8.23 to ID R11.2
  From ID R11.1 to ID A1J8.25

From ID A1P8.25 to ID P10-50 (S301-95)

From ID P10-148 (S301-96) to ID A1P8.45

From ID A1J8.45
  From ID A1J8.45
                                                                                      to ID BUS 5
 From ID BUS 5 to ID A1J9.34 to ID P11-230 (S507-7) From ID P11-39 (S507-1) to ID A1P10.2 From ID A1J10.2 to ID A1J12.46 From ID A1P12.46 to ID P12-20 (S201-3)
 From ID P12-46 (S201-7) to ID A1P13.3
From ID A1J13.3 to ID J1B-14B
From W6 P1B-14B to W6 P2-3 (ST J8-3)
From W6 P2-3 (ST J6-3) to W6 P1B-2E
From W6 P2-15 (UUT J1-15) to W6 P1B-2B
From W6 P2-16 (UUT J1-16) to W6 P1B-4D
From ID J1B-2E to ID 07 3
From W6 P2-16 (UUT J1-16)

From ID J1B-2E

From ID J1B-2B

From ID J1B-4D

From ID Q6.2

From ID Q7.2

From ID Q8.2

From ID R40.2

From ID R37.1

From ID R40.1

From ID R43.1

From ID P1-16 (DC6-HI)

From ID A1J1.15
 From ID AlJ1.15
 From ID A1J1.15 to -15V
From -15V to ID R35.1
From -15V to ID R38.1
From -15V to ID R41.1
From ID R35.2 to ID Q6.1
From ID R38.2 to ID Q7.1
From ID R41.2 to ID Q8.1
                                                                                      to -15V
                                                                                       to ID R35.1
                                                                                       to ID R38.1
                                                                                      to ID R41.1
From ID R41.2 to ID Q7.1

From ID P1-4 (DC2-HI) to ID A1P1.2

From ID A1J1.2 to +15V

From HD A1P11.42 to ID P11-106 (S301-151)

From ID P11-75 (S301-152) to ID A1P11.44

From ID A1J11.44 to ID R36.1

From ID A1J11.44 to ID R39.1

From ID R36.2 to ID Q6.1

From ID R39.2 to ID Q7.1

From ID R42.2 to ID Q8.1
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Date: 04 March 2016

From ID P20-2 (DMM-HI) to ID A1P15.49
From ID A1J15.49 to ID A1J8.28
From ID A1P8.28 to ID P10-203 (S503-1)
From ID P10-137 (S503-7) to ID A1P6.47
From ID A1J6.47 to ID BUS 5

From ID P20-3 (DMM-LO) to ID A1P15.50
From ID A1J15.50 to ID A1J7.38
From ID A1P7.38 to ID P10-130 (S301-23)
From ID P10-229 (S301-24) to ID A1P7.36
From ID A1J7.36 to GROUND

2.11 MODULE 8 W7/W8 GTD PROCESSOR SPECIFIC ID TESTS

Refer to Reference Drawings when diagnosing connection paths.

Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF TEST PWB, A2).pdf, 13020A7701 (CABLE, W7, SCHEMATIC).pdf and 08014A3800 (CABLE W8, RS422).pdf in section 1.4 during review of the following steps.

Step 801

Description:

This step verifies the wire path from W7 P2-A1 to W7 P2-B3. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2(DMM-HI) From ID A1J15.49 From ID A1P7.44 From ID P10-226 (S301-3) From ID A1J7.13 From W7 P1B-1D	to ID A1P15.49 to ID A1J7.44 to ID P10-99 (S301-4) to ID A1P7.13 to ID J1B-1D to W7 P2-A1 (ST J9-A1)
From ST_J9-A1	to ST_J9-B3
From W7 P2-B3 (ST J9-B3) From ID J1A-3A From ID A1P15.1	to W7 P1A-3A to ID A1J15.1 to ID P13-39 (S701-7)
From ID P12-76 (S701-1) From ID A1J12.50 From ID A1P10.3 From ID P11-195 (S506-4) From ID A1J9.33	to ID A1P12.50 to ID A1J10.3 to ID P11-194 (S506-1) to ID A1P9.33 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Date: 04 March 2016

Step 802

Description:

This step verifies the wire path from W7 P2-B1 to W7 P2-A14. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID A1J15.49	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-48 (S201-15) From ID A1J12.1 From W7 P1B-9A	to ID A1P12.1 to ID J1B-9A to W7 P2-B1 (ST J9-B1)
From ST_J9-B1	to ST_J9-A14
From W7 P2-A14 (ST J9-A14) From ID J1B-14A From ID A1P13.1	to W7 P1B-14A to ID A1J13.1 to ID P12-79 (S201-5)
From ID A1P10.2	to ID A1P12.46 to ID A1J10.2 to ID P11-39 (S507-1) to ID A1P9.27 to ID BUS 2
	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 803

Description:

This step verifies the wire path from W7 P2-C1 to W7 P3-42. The DMM resource will be used to measure resistance UL= 10 ohms.

From II	D P20-2 (DMM-HI)	to	ID	A1P15.49	
From II	A1J15.49	to	ID	A1J8.28	
From II	A1P8.28	to	ID	P10-203	(S503-1)

Date: 04 March 2016

From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-15 (S201-16)	to ID A1P12.2
From ID A1J12.2	to ID J1B-9B
From W7 P1B-9B	to W7 P2-C1 (ST J9-C1)
From ST_J9-C1	to ST_J10-42
From W7 P3-42 (ST J10-42)	to W7 P1B-5B
From ID J1B-5B	to ID A1J12.14
From ID A1P12.14	to ID P12-27 (S202-24)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 804

Description:

This step verifies the wire path from W7 P2-C3 to W5 P2-9. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-162 (S506-2) From ID A1J10.1 From ID A1P12.48	to ID AlJ9.23 to ID P11-164 (S506-3) to ID AlP10.1 to ID AlJ12.48 to ID P12-44 (S701-2)
From ID P13-6 (S701-8) From ID A1J15.2 From W7 P1A-3B	to ID A1P15.2 to ID J1A-3B to W7 P2-C3 (ST J9-C3)

Date: 04 March 2016

From ST_J9-C3	to ST_J9-A2
From W7 P2-A2 (ST J9-A2)	to W7 P1B-1F
From ID J1B-1F	to ID A1J1.11
From ID A1P1.11	to ID P1-11 (DC4-LO)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 805

Description:

This step verifies the wire path from W7 P2-B2 to W7 P2-C2. The DMM resource will be used to verify the resolver voltage of 11.8V RMS.

From ID P10-77 (S503-3)	to ID P10-203 (S503-1)
From ID A1P9.15 From ID P11-203 (S508-1)	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
FION W/ FIB-OB	to ID A1P12.11 to ID J1B-6B to W7 P2-B2 (ST J9-B2) to ST_J9-B40
From ID A1P3.17	to W7 P1B-11C to ID A1J3.17 to ID J3-25 to W204 P1-25 (SRS Ref Hi)
From W204 P1-26 (SRS Ref Lo) From ID J3-26 From ID A1J3.16 From W7 P1B-11A	to W204 P2-26 to ID A1P3.16 to ID J1B-11A to W7 P2-C18 (ST J9-C18)
	to ST_J9-C2
From ID J1R-3A	to W7 P1B-3A to ID A1J12.19 to ID P12-54 (S201-38)

Date: 04 March 2016

From ID P12-80 (S201-2) From ID A1J12.40 From ID A1P10.8 From ID P11-12 (S508-4) From ID A1J9.25	to ID A1P12.40 to ID A1J10.8 to ID P11-139 (S508-2) to ID A1P9.25 to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 806

Description:

This step verifies the wire path from W7 P2-A3 to W7 P3-43. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P13-79 (S201-20) From ID A1J14.7 From W7 P1A-1D	to ID A1P14.7 to ID J1A-1D to W7 P2-A3 (ST J9-A3)
From ST_J9-A3	to ST_J10-43
From W7 P3-43 (ST J10-43) From ID J1A-8E From ID A1P14.23	to W7 P1A-8E to ID A1J14.23 to ID P13-91 (S202-35)
From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID AlJ6.23 to ID P10-12 (S503-4) to ID AlP8.26 to ID AlJ15.50 to ID P20-3 (DMM-LO)

Date: 04 March 2016

Step 807

Description:

This step verifies the wire path from W7 P2-B6 to W7 P2-C6. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID Alp9.23 From ID P11-194 (S506-1) From ID AlJ10.3 From ID Alp12.50	to ID AlJ9.23 to ID P11-164 (S506-3) to ID AlP10.3 to ID AlJ12.50 to ID P12-76 (S701-1)
From ID P13-7 (S701-17) From ID A1J15.7 From W7 P1A-5A	to ID A1P15.7 to ID J1A-5A to W7 P2-B6 (ST J9-B6)
From ST_J9-B6	to ST_J9-C6
From W7 P2-C6 (ST J9-C6) From ID J1A-10B From ID A1P15.22	to W7 P1A-10B to ID A1J15.22 to ID P13-71 (S701-18)
From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4) From ID A1J9.33	to ID A1J10.1 to ID P11-162 (S506-2)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID AlJ6.23 to ID P10-12 (S503-4) to ID AlP8.26 to ID AlJ15.50 to ID P20-3 (DMM-LO)

Step 808

Description:

This step verifies the wire path from W7 P2-B7 to W7 P2-C7. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49	
From ID	A1J15.49	to	ID	A1J8.28	
From ID	A1P8.28	to	ID	P10-203	(S503-1)

Date: 04 March 2016

From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P12-71 (S701-19)	to ID A1P13.15
From ID A1J13.15	to ID J1B-12F
From W7 P1B-12F	to W7 P2-B7 (ST J9-B7)
From ST_J9-B7	to ST_J9-C7
From W7 P2-C7 (ST J9-C7)	to W7 P1B-8D
From ID J1B-8D	to ID A1J13.25
From ID A1P13.25	to ID P12-39 (S701-20)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 809

Description:

This step verifies the wire path from W7 P2-A8 to W7 P2-B8. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P12-38 (S701-11)	to ID A1P13.11
From ID A1J13.11	to ID J1B-14F
From W7 P1B-14F	to W7 P2-A8 (ST J9-A8)

Date: 04 March 2016

From ST_J9-A8	to ST_J9-B8
From W7 P2-B8 (ST J9-B8) From ID J1B-13F From ID A1P13.12	to W7 P1B-13F to ID A1J13.12 to ID P12-5 (S701-12)
From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4) From ID A1J9.33	to ID A1P12.48 to ID A1J10.1 to ID P11-162 (S506-2) to ID A1P9.33 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 810

Description:

This step verifies the wire path from W7 P2-C8 to W7 P2-A9. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P12-69 (S701-13) From ID A1J13.13 From W7 P1B-12D From ST_J9-C8	to ID A1P13.13 to ID J1B-12D to W7 P2-C8 (ST J9-C8) to ST_J9-A9
From W7 P2-A9 (ST J9-A9) From ID J1B-12E From ID A1P13.14	to W7 P1B-12E to ID A1J13.14 to ID P12-37 (S701-14)
From ID A1J12.48 From ID A1P10.1	to ID A1P12.48 to ID A1J10.1 to ID P11-162 (S506-2) to ID A1P9.33 to ID BUS 2

Date: 04 March 2016

From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 811

Description:

This step verifies the wire path from W7 P2-C10 to W7 P2-A11. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
	to ID A1P15.5 to ID J1A-4B to W7 P2-C10 (ST J9-C10) to ST_J9-A11
From W7 P2-A11 (ST J9-A11) From ID J1A-8A From ID A1P15.16	to W7 P1A-8A to ID A1J15.16 to ID P13-40 (S701-16)
From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4) From ID A1J9.33	to ID A1P12.48 to ID A1J10.1 to ID P11-162 (S506-2) to ID A1P9.33 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 812

Description:

Date: 04 March 2016

This step verifies the wire path from W7 P2-B11 to W7 P2-C11. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P12-6 (S701-21)	to ID A1P13.16
From ID A1J13.16	to ID J1B-11D
From W7 P1B-11D	to W7 P2-B11 (ST J9-B11)
From ST_J9-B11	to ST_J9-C11
From W7 P2-C11 (ST J9-C11)	to W7 P1B-8F
From ID J1B-8F	to ID A1J13.27
From ID A1P13.27	to ID P12-70 (S701-22)
From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4) From ID A1J9.33	to ID A1J10.1 to ID P11-162 (S506-2)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 813

Description:

This step verifies the wire path from W7 P2-A12 to W7 P3-68. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15

Date: 04 March 2016

From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	•
From ID A1J10.8	to ID AlJ12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P13-16 (S201-18)	
From ID A1J14.28	to ID J1A-10F
From W7 P1A-10F	to W7 P2-A12 (ST J9-A12)
From ST_J9-A12	to ST_J10-68
From W7 P3-68 (ST J10-68)	to W7 P1A-7E
From ID J1A-7E	to ID A1J14.21
From ID A1P14.21	to ID P13-90 (S202-26)
110 12 1111111	00 12 110 70 (2101 10)
From ID P12-90 (S202-2)	to TD A1P12 36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 814

Description:

This step verifies the wire path from W7 P2-B12 to W7 P3-60. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID AlJ10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P13-49 (S201-17)	to ID AlP14.5
From ID AlJ14.5	to ID J1A-1C
From W7 P1A-1C	to W7 P2-B12 (ST J9-B12)
From ST_J9-B12	to ST_J10-60
From W7 P3-60 (ST J10-60)	to W7 P1A-7E

Date: 04 March 2016

From ID From ID	J1A-7E A1P14.21			A1J14.21 P13-90 (S202-26)
From ID From ID From ID	P12-90 (S202-2) A1J12.36 A1P10.12 P11-17 (S509-4) A1J9.29	to to to	ID ID ID	A1P12.36 A1J10.12 P11-242 (S509-2) A1P9.29 BUS 2
From ID	BUS 2 A1P6.23 P10-139 (S503-2) A1J8.26 A1P15.50	to to to	ID ID ID	A1J6.23 P10-12 (S503-4) A1P8.26 A1J15.50 P20-3 (DMM-LO)

Step 815

Description:

This step verifies the wire path from W7 P2-C13 to W7 P3-41. The DMM resource will be used to measure resistance UL= 10 ohms.

From From From	ID A1J ID A1P	8.28 -77 (S503-3)	to to to	ID ID ID	A1P15.49 A1J8.28 P10-203 (S503-1) A1P6.13 BUS 1
From From From	ID BUS ID A1P ID P11 ID A1J ID A1P	9.15 -139 (S508-2) 10.8	to to to	ID ID ID	A1J9.15 P11-77 (S508-3) A1P10.8 A1J12.40 P12-80 (S201-2)
From	ID P12 ID A1J W7 P1B	12.4	to	ID	A1P12.4 J1B-8A P2-C13 (ST J9-C13)
From	ST_J9-	C13	to	ST_	_J10-41
From From	_	41 (ST J10-41) -8E	to to	W7 ID	_J10-41 P1A-8E A1J14.23 P13-91 (S202-35)
From From From From From From	W7 P3- ID J1A ID A1P ID P13 ID A1J ID A1P	41 (ST J10-41) -8E -914.23 -93 (S202-3) -14.49 10.48 -147 (S510-4)	to to to to to	W7 ID ID ID ID ID	P1A-8E A1J14.23

Date: 04 March 2016

From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 816

Description:

This step verifies the wire path from W7 P2-B14 to W7 P3-66. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-47 (S201-6) From ID A1J13.2 From W7 P1B-13A	to ID A1P13.2 to ID J1B-13A to W7 P2-B14 (ST J9-B14)
From ST_J9-B14	to ST_J10-66
From W7 P3-66 (ST J10-66) From ID J1B-1B From ID A1P12.22	to W7 P1B-1B to ID A1J12.22 to ID P12-91 (S202-23)
From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 817

Description:

This step verifies the wire path from W7 P2-A15 to W7 P3-45. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) to ID A1P15.49

Date: 04 March 2016

From	ID	A1J15.49	to	ID	A1J8.28
		A1P8.28	to	ID	P10-203 (S503-1)
From	ID	P10-77 (S503-3)	to	ID	A1P6.13
From	ID	A1J6.13	to	ID	BUS 1
_		BUS 1			A1J9.15
					P11-77 (S508-3)
		P11-203 (S508-1)			
					A1J12.42
From	ID	A1P12.42	to	ID	P12-16 (S201-1)
	TD	D10 F1 (G201 01)		TD	71010 0
		P12-51 (S201-21)			
_					J1B-9C
From	W /	P1B-9C	to	W /	P2-A15 (ST J9-A15)
From	ST_	_J9-A15	to	ST_	_J10-45
${\tt From}$	W7	P3-45 (ST J10-45)	to	W7	P1A-7F
${\tt From}$	ID	J1A-7F	to	ID	A1J14.22
From	ID	A1P14.22	to	ID	P13-92 (S202-34)
From	TD	P12-90 (S202-2)	t o	TD	л1D12 36
					A1J10.12
					P11-242 (S509-2)
		P11-17 (S509-4)			
					BUS 2
PLOIII	עד	A10 9 . 2 9	LU	עד	B05 Z
From	ID	BUS 2	to	ID	A1J6.23
			to	ID	P10-12 (S503-4)
From	ID	P10-139 (S503-2)	to	ID	A1P8.26
		A1J8.26	to	ID	A1J15.50
${\tt From}$	ID	A1P15.50	to	ID	P20-3 (DMM-LO)

Step 818

Description:

This step verifies the wire path from W7 P2-B15 to W7 P3-39. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID AlJ6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P12-17 (S201-23)	to ID A1P12.5

Date: 04 March 2016

From ID	A1J12.5	to	ID	J1B-8B
From W7	P1B-8B	to	W7	P2-B15 (ST J9-B15)
From ST_	_J9-B15	to	ST_	_J10-39
				4 =
From W7	P3-39 (ST J10-39)	to	M.\	P1A-7F
From ID	J1A-8F	to	ID	A1J14.24
From ID	A1P14.24	to	ID	P13-62 (S202-42)
From ID	P12-90 (S202-2)	to	ID	A1P12.36
From ID	A1J12.36	to	ID	A1J10.12
From ID	A1P10.12	to	ID	P11-242 (S509-2)
From ID	P11-17 (S509-4)	to	ID	A1P9.29
From ID	A1J9.29	to	ID	BUS 2
From ID	BUS 2	to	ID	A1J6.23
From ID	A1P6.23	to	ID	P10-12 (S503-4)
From ID	P10-139 (S503-2)	to	ID	A1P8.26
From ID	A1J8.26	to	ID	A1J15.50
From ID	A1P15.50	to	ID	P20-3 (DMM-LO)

Step 819

Description:

This step verifies the wire path from W7 P2-C15 to W7 P2-C38. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID A1J12.6 From W7 P1B-8C	to ID A1P12.6 to ID J1B-8C to W7 P2-C15 (ST J9-C15)
From ST_J9-C15 From W7 P2-C38 (ST J9-C38) From ID J1A-8F From ID A1P14.24	to ST_J9-C38 to W7 P1A-7F to ID A1J14.24 to ID P13-62 (S202-42)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12	to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2)

Date: 04 March 2016

From ID P11-17 (S509-4) to ID A1P9.29 to ID BUS 2

From ID BUS 2 to ID A1J6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 820

Description:

This step verifies the wire path from W7 P2-A16 to W7 P2-C35. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1J8.28 to ID P10-203 (S503-1)
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P13-50 (S201-25) From ID A1J14.8 From W7 P1A-2D	to ID J1A-2D to W7 P2-A16 (ST J9-A16)
From ST_J9-A16 From W7 P2-C35 (ST J9-C35) From ID J1B-5C From ID A1P12.15	to ST_J9-C35 to W7 P1B-5C to ID A1J12.15 to ID P12-31 (S202-45)
From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 821

Description:

Date: 04 March 2016

This step verifies the wire path from W7 P2-B16 to W7 P2-A36. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P13-17 (S201-26)	to ID A1P14.9
From ID A1J14.9	to ID J1A-1E
From W7 P1A-1E	to W7 P2-B16 (ST J9-A16)
From ST_J9-B16	to ST_J9-A36
From W7 P2-A36 (ST J9-A36)	to W7 P1B-4A
From ID J1B-4A	to ID A1J12.16
From ID A1P12.16	to ID P12-63 (S202-46)
From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID AlP6.23 From ID P10-139 (S503-2) From ID AlJ8.26 From ID AlP15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 822

Description:

This step verifies the wire path from W7 P2-C16 to W7 P2-A37. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15

Date: 04 March 2016

From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P13-83 (S201-27)	
From ID AlJ14.10	to ID J1A-2E
From W7 P1A-2E	to W7 P2-C16 (ST J9-C16)
From ST_J9-C16	to ST_J9-A37
From W7 P2-A37 (ST J9-A37)	to W7 P1B-5C
From ID J1B-5C	to ID A1J12.15
From ID A1P12.15	to ID P12-31 (S202-45)
From ID P12-59 (S202-1)	
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 823

Description:

This step verifies the wire path from W7 P2-A17 to W7 P2-B37. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13
From ID	A1J6.13	to	ID	BUS 1
From ID	BUS 1	to	ID	A1J9.15
From ID	A1P9.15	to	ID	P11-77 (S508-3)
From ID	P11-139 (S508-2)	to	ID	A1P10.8
From ID	A1J10.8	to	ID	A1J12.40
From ID	A1P12.40	to	ID	P12-80 (S201-2)
From ID	P13-51 (S201-28)	to	ID	A1P14.11
From ID	A1J14.11	to	ID	J1A-1F
From W7	P1A-1F	to	W7	P2-A17 (ST J9-A17)
From ST_	_J9-A17	to	$ST_{_}$	_J9-B37
From W7	P2-B37 (ST J9-B37)	to	W7	P1B-4A

Date: 04 March 2016

From ID J1B-4A	to ID A1J12.16
From ID A1P12.16	to ID P12-63 (S202-46)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 824

Description:

This step verifies the wire path from W7 P2-A19 to W7 P3-58. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID AlJ10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P13-47 (S201-9)	to ID A1P14.1
From ID A1J14.1	to ID J1A-1A
From W7 P1A-1A	to W7 P2-A19 (ST J9-A19)
From ST_J9-A19	to ST_J10-58
From ST_J9-A19	to ST_J10-58
From ST_J9-A19 From W7 P3-58 (ST J10-58)	to ST_J10-58 to W7 P1A-12E
From W7 P3-58 (ST J10-58)	to W7 P1A-12E
From W7 P3-58 (ST J10-58) From ID J1A-12E From ID A1P14.31	to W7 P1A-12E to ID A1J14.31 to ID P13-30 (S202-41)
From W7 P3-58 (ST J10-58) From ID J1A-12E From ID A1P14.31 From ID P12-59 (S202-1)	to W7 P1A-12E to ID A1J14.31 to ID P13-30 (S202-41) to ID A1P12.38
From W7 P3-58 (ST J10-58) From ID J1A-12E From ID A1P14.31	to W7 P1A-12E to ID A1J14.31 to ID P13-30 (S202-41)
From W7 P3-58 (ST J10-58) From ID J1A-12E From ID A1P14.31 From ID P12-59 (S202-1)	to W7 P1A-12E to ID A1J14.31 to ID P13-30 (S202-41) to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1)
From W7 P3-58 (ST J10-58) From ID J1A-12E From ID A1P14.31 From ID P12-59 (S202-1) From ID A1J12.38	to W7 P1A-12E to ID A1J14.31 to ID P13-30 (S202-41) to ID A1P12.38 to ID A1J10.10
From W7 P3-58 (ST J10-58) From ID J1A-12E From ID A1P14.31 From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10	to W7 P1A-12E to ID A1J14.31 to ID P13-30 (S202-41) to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1)
From W7 P3-58 (ST J10-58) From ID J1A-12E From ID A1P14.31 From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10 From ID P11-17 (S509-4) From ID A1J9.29	to W7 P1A-12E to ID A1J14.31 to ID P13-30 (S202-41) to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2
From W7 P3-58 (ST J10-58) From ID J1A-12E From ID A1P14.31 From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10 From ID P11-17 (S509-4) From ID A1J9.29 From ID BUS 2	to W7 P1A-12E to ID A1J14.31 to ID P13-30 (S202-41) to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2 to ID A1J6.23
From W7 P3-58 (ST J10-58) From ID J1A-12E From ID A1P14.31 From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10 From ID P11-17 (S509-4) From ID A1J9.29 From ID BUS 2 From ID A1P6.23	to W7 P1A-12E to ID A1J14.31 to ID P13-30 (S202-41) to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2 to ID A1J6.23 to ID P10-12 (S503-4)
From W7 P3-58 (ST J10-58) From ID J1A-12E From ID A1P14.31 From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10 From ID P11-17 (S509-4) From ID A1J9.29 From ID BUS 2	to W7 P1A-12E to ID A1J14.31 to ID P13-30 (S202-41) to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2 to ID A1J6.23

Date: 04 March 2016

From ID A1J8.26 to ID A1J15.50 from ID A1P15.50 to ID P20-3 (DMM-LO)

Step 825

Description:

This step verifies the wire path from W7 P2-A20 to W7 P2-A23. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13
				BUS 1
From ID	BUS 1	to	ID	A1J9.23
From ID	A1P9.23	to	ID	P11-164 (S506-3)
From ID	P11-194 (S506-1)			
				A1J12.50
From ID	A1P12.50			P12-76 (S701-1)
From ID	P13-42 (S701-23)	to	ID	A1P15.8
				J1A-5B
From W7	P1A-5B	to	w7	P2-A20 (ST J9-A20)
From ST_	_J9-A20	to	ST_	_J9-A23
From W7	P2-A23 (ST J9-A23)	to	W7	P1A-11B
From ID	J1A-11B	to	ID	A1J15.24
From ID	A1P15.24	to	ID	P13-9 (S701-24)
From ID	P12-44 (S701-2)	to	ID	A1P12.48
From ID	A1J12.48	to	ID	A1J10.1
From ID	A1P10.1	to	ID	P11-162 (S506-2)
From ID	P11-195 (S506-4)	to	ID	A1P9.33
From ID	A1J9.33	to	ID	BUS 2
From ID	BUS 2	to	ID	A1J6.23
	A1P6.23	to	ID	P10-12 (S503-4)
From ID	P10-139 (S503-2)	to	ID	A1P8.26
				A1J15.50
From ID	A1P15.50	to	ID	P20-3 (DMM-LO)

Step 826

Description:

This step verifies the wire path from W7 P2-C21 to W7 P2-C22. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) to ID A1P15.49

Date: 04 March 2016

_		A1J15.49			A1J8.28
From	ID	A1P8.28	to	ID	P10-203 (S503-1)
		P10-77 (S503-3)			
From	ID	A1J6.13	to	ID	BUS 1
From	ID	BUS 1	to	ID	A1J9.23
_					P11-164 (S506-3)
		P11-194 (S506-1)	to	ID	A1P10.3
From	ID	A1J10.3	to	ID	A1J12.50
From	ID	A1P12.50	to	ID	P12-76 (S701-1)
From	ID	P12-73 (S701-37)	to	ID	A1P13.21
From	ID	A1J13.21	to	ID	J1B-10F
From	W7	P1B-10F	to	W7	P2-C21 (ST J9-C21)
From	ST_	_J9-C21	to	ST_	_J9-C22
From	w7	P2-C22 (ST J9-C22)	to	w7	P1B-11E
From	ID	J1B-11E	to	ID	A1J13.17
From	ID	A1P13.17	to	ID	P12-41 (S701-38)
From	ID	P12-44 (S701-2)	to	ID	A1P12.48
From	ID	A1J12.48	to	ID	A1J10.1
					P11-162 (S506-2)
From	ID	P11-195 (S506-4)	to	ID	A1P9.33
From	ID	A1J9.33	to	ID	BUS 2
From	ID	BUS 2	to	ID	A1J6.23
			to	ID	P10-12 (S503-4)
From	ID	P10-139 (S503-2)	to	ID	A1P8.26
From	ID	A1J8.26	to	ID	A1J15.50
From	ID	A1P15.50	to	ID	P20-3 (DMM-LO)

Step 827

Description:

This step verifies the wire path from W7 P2-A24 to W7 P2-A25. The DMM resource will be used to measure resistance UL= 10 ohms.

From	ID P20-2 (DMM-HI)	to	ID	A1P15.49
From	ID A1J15.49	to	ID	A1J8.28
From	ID A1P8.28	to	ID	P10-203 (S503-1)
From	ID P10-77 (S503-3)	to	ID	A1P6.13
From	ID A1J6.13	to	ID	BUS 1
From	ID BUS 1	to	ID	A1J9.23
From	ID A1P9.23	to	ID	P11-164 (S506-3)
From	ID P11-194 (S506-1)	to	ID	A1P10.3
From	ID A1J10.3	to	ID	A1J12.50
From	ID A1P12.50	to	ID	P12-76 (S701-1)
From	ID P13-73 (S701-25)	to	ID	A1P15.26

Date: 04 March 2016

From ID	A1J15.26	to	ID	J1A-12B
From W7	P1A-12B	to	W7	P2-A24 (ST J9-A24)
From ST_	_J9-A24	to	ST_	_J9-A25
From W7	P2-A25 (ST J9-A25)	t o	พ7	D1 A = 13R
From ID	J1A-13B	to	TD	A1J15.28
From ID	A1P15.28	to	ID	P13-41 (S701-26)
From ID	P12-44 (S701-2)	to	ID	A1P12.48
From ID	A1J12.48	to	ID	A1J10.1
From ID	A1P10.1	to	ID	P11-162 (S506-2)
From ID	P11-195 (S506-4)	to	ID	A1P9.33
From ID	A1J9.33	to	ID	BUS 2
From ID	BUS 2	to	ID	A1J6.23
From ID	A1P6.23	to	ID	P10-12 (S503-4)
From ID	P10-139 (S503-2)	to	ID	A1P8.26
From ID	A1J8.26	to	ID	A1J15.50
From ID	A1P15.50	to	ID	P20-3 (DMM-LO)

Step 828

Description:

This step verifies the wire path from W7 P2-B24 to W7 P2-B25. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P13-75 (S701-31) From ID A1J15.9 From W7 P1A-5C From ST_J9-B24	to ID A1P15.9 to ID J1A-5C to W7 P2-B24 (ST J9-B24) to ST_J9-B25
From W7 P2-B25 (ST J9-B25) From ID J1A-6A From ID A1P15.10	
From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1	to ID A1P12.48 to ID A1J10.1 to ID P11-162 (S506-2)

Date: 04 March 2016

From ID P11-195 (S506-4) to ID A1P9.33 from ID A1J9.33 to ID BUS 2

From ID BUS 2 to ID A1J6.23 from ID A1P6.23 to ID P10-12 (S503-4) from ID P10-139 (S503-2) to ID A1P8.26 from ID A1J8.26 to ID A1J15.50 from ID A1P15.50 to ID P20-3 (DMM-LO)

Step 829

Description:

This step verifies the wire path from W7 P2-C24 to W7 P2-C25. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1J8.28 to ID P10-203 (S503-1)
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P13-45 (S701-39) From ID A1J15.13 From W7 P1A-7A From ST_J9-C24	to ID A1P15.13 to ID J1A-7A to W7 P2-C24 (ST J9-C24) to ST_J9-C25
	to W7 P1A-7B to ID A1J15.14 to ID P13-12 (S701-40)
From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4) From ID A1J9.33	to ID A1J10.1 to ID P11-162 (S506-2)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 830

Description:

Date: 04 March 2016

This step verifies the wire path from W7 P2-A26 to W7 P2-A27. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID AlJ9.23 to ID P11-164 (S506-3) to ID AlP10.3 to ID AlJ12.50 to ID P12-76 (S701-1)
From ID P12-8 (S701-27)	to ID A1P13.29
From ID A1J13.29	to ID J1B-7D
From W7 P1B-7D	to W7 P2-A26 (ST J9-A26)
From ST_J9-A26	to ST_J9-A27
From W7 P2-A27 (ST J9-A27)	to W7 P1B-9E
From ID J1B-9E	to ID A1J13.23
From ID A1P13.23	to ID P12-72 (S701-28)
From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4) From ID A1J9.33	
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 831

Description:

This step verifies the wire path from W7 P2-B26 to W7 P2-B27. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23

Date: 04 March 2016

From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P13-10 (S701-33)	to ID A1P15.11
From ID A1J15.11	to ID J1A-6B
From W7 P1A-6B	to W7 P2-B26 (ST J9-B26)
From ST_J9-B26	to ST_J9-B27
From W7 P2-B27 (ST J9-B27)	to W7 P1A-6C
From ID J1A-6C	to ID A1J15.12
From ID A1P15.12	to ID P13-74 (S701-34)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 832

Description:

This step verifies the wire path from W7 P2-A28 to W7 P2-B20. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49
From ID	A1J15.49	to	ID	A1J8.28
From ID	A1P8.28	to	ID	P10-203 (S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13
From ID	A1J6.13	to	ID	BUS 1
From ID	BUS 1	to	ID	A1J9.23
From ID	A1P9.23	to	ID	P11-164 (S506-3)
From ID	P11-194 (S506-1)	to	ID	A1P10.3
From ID	A1J10.3	to	ID	A1J12.50
From ID	A1P12.50	to	ID	P12-76 (S701-1)
From ID	P12-40 (S701-29)	to	ID	A1P13.18
From ID	A1J13.18	to	ID	J1B-11F
From W7	P1B-11F	to	W7	P2-A28 (ST J9-A28)
From ST_	_J9-A28	to	ST_	_J9-B20
From W7	P2-B20 (ST J9-B20)	to	W7	P1B-7E

Date: 04 March 2016

From ID J1B-7E	to ID A1J13.31
From ID A1P13.31	to ID P12-7 (S701-30)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 833

Description:

This step verifies the wire path from W7 P2-A30 to W7 P3-61. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-46 (S201-7) From ID A1J13.3 From W7 P1B-14B	to ID A1P13.3 to ID J1B-14B to W7 P2-A30 (ST J9-A30)
From ST_J9-A30	to ST_J10-61
From ST_J9-A30 From W7 P3-61 (ST J10-61) From ID J1A-11F From ID A1P14.30	to ST_J10-61 to W7 P1A-11F to ID A1J14.30 to ID P13-59 (S202-25)
From W7 P3-61 (ST J10-61) From ID J1A-11F	to W7 P1A-11F to ID A1J14.30 to ID P13-59 (S202-25)

Date: 04 March 2016

From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 834

Description:

This step verifies the wire path from W7 P2-B30 to W7 P3-72. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P12-13 (S201-8)	to ID A1P13.4
From ID A1J13.4	to ID J1B-13B
From W7 P1B-13B	to W7 P2-B30 (ST J9-A30)
From ST_J9-B30	to ST_J10-72
From W7 P3-72 (ST J10-72)	to W7 P1A-11F
From ID J1A-11F	to ID A1J14.30
From ID A1P14.30	to ID P13-59 (S202-25)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 835

Description:

This step verifies the wire path from W7 P2-B31 to W7 P3-33. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) to ID A1P15.49

Date: 04 March 2016

From ID AlJ15.49 From ID AlP8.28 From ID P10-77 (S503-3) From ID P10-77 (S503-3) From ID D AlJ6.13 From ID BUS 1 From ID BUS 1 From ID AlP9.15 From ID P11-139 (S508-2) From ID AlJ10.8 From ID AlJ10.8 From ID AlJ10.8 From ID P13-19 (S201-34) From ID AlJ14.13 From ID J1B-4C From ID J1B-4C From ID AlP12.18 From ID AlP12.18 From ID AlP12.36 From ID AlJ12.36 From ID AlJ13.29 From ID AlJ13.29 From ID AlJ13.29 From ID AlJ13.29 From ID AlJ8.26 From ID AlJ8.26 From ID AlJ8.26 From ID AlJ8.26 From ID AlJ8.50 From ID AlP5.50 From ID AlP6.23 From ID AlJ8.26 From ID AlJ8.26 From ID AlJ8.26 From ID AlJ8.26 From ID AlJ8.50					
From ID Alp9.15 From ID P11-139 (S508-2) From ID AlJ10.8 From ID AlJ10.8 From ID AlP12.40 From ID AlP12.40 From ID P13-19 (S201-34) From ID AlJ14.13 From ID AlJ14.13 From W7 P1A-3E From W7 P2-B31 (ST J9-B31) From ID J1B-4C From ID AlP12.18 From ID AlP12.18 From ID P12-90 (S202-2) From ID AlJ12.36 From ID AlJ12.36 From ID AlJ12.36 From ID AlP10.12 From ID AlP10.12 From ID AlJ9.29 From ID BUS 2 From ID AlP6.23 From ID AlP6.23 From ID P10-139 (S503-2) From ID AlJ18.26	From I	ID A1P8.28 ID P10-77 (S503-3)	to to	ID ID	P10-203 (S503-1) A1P6.13
From ID AlJ14.13 From W7 P1A-3E From W7 P1A-3E to W7 P2-B31 (ST J9-B31) From ST_J9-B31 to ST_J10-33 From W7 P3-33 (ST J10-33) From ID J1B-4C From ID AlP12.18 From ID AlP12.18 From ID P12-90 (S202-2) From ID AlJ12.36 From ID AlJ12.36 From ID AlJ10.12 From ID AlP10.12 From ID P11-17 (S509-4) From ID AlJ9.29 From ID BUS 2 From ID BUS 2 From ID AlP6.23 From ID P10-139 (S503-2) From ID AlJ8.26 From ID AlJ8.26 From ID AlJ8.26 From ID AlJ8.26	From I	ID A1P9.15 ID P11-139 (S508-2) ID A1J10.8	to to to	ID ID ID	P11-77 (S508-3) A1P10.8 A1J12.40
From W7 P3-33 (ST J10-33) to W7 P1B-4C From ID J1B-4C to ID A1J12.18 From ID A1P12.18 to ID P12-32 (S202-48) From ID P12-90 (S202-2) to ID A1P12.36 From ID A1J12.36 to ID A1J10.12 From ID A1P10.12 to ID P11-242 (S509-2) From ID P11-17 (S509-4) to ID A1P9.29 From ID A1J9.29 to ID BUS 2 From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50	From 1	ID A1J14.13	to	ID	J1A-3E
From ID J1B-4C From ID A1P12.18 From ID A1P12.18 to ID P12-32 (S202-48) From ID P12-90 (S202-2) From ID A1J12.36 From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID P11-17 (S509-4) From ID BUS 2 From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1J8.26	From S	ST_J9-B31	to	ST_	_J10-33
From ID A1J12.36 to ID A1J10.12 From ID A1P10.12 to ID P11-242 (S509-2) From ID P11-17 (S509-4) to ID A1P9.29 From ID A1J9.29 to ID BUS 2 From ID BUS 2 to ID A1J6.23 From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50	From 1	ID J1B-4C	to	ID	A1J12.18
From ID A1P6.23 to ID P10-12 (S503-4) From ID P10-139 (S503-2) to ID A1P8.26 From ID A1J8.26 to ID A1J15.50	From I	ID A1J12.36 ID A1P10.12 ID P11-17 (S509-4)	to to to	ID ID ID	AlJ10.12 P11-242 (S509-2) AlP9.29
	From I	ID A1P6.23 ID P10-139 (S503-2) ID A1J8.26	to to to	ID ID ID	P10-12 (S503-4) A1P8.26 A1J15.50

Step 836

Description:

This step verifies the wire path from W7 P2-A32 to W7 P2-A7. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.19 From ID P11-242 (S509-2) From ID A1J10.12 From ID A1P12.36	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.12 to ID A1J12.36 to ID P12-90 (S202-2)
From ID P13-64 (S202-50)	to ID A1P14.26

Date: 04 March 2016

From ID	A1J14.26	to	ID	J1A-9F
From W7	P1A-9F	to	W7	P2-A32 (ST J9-A32)
Eron CT	TO 722	+ 0	СTT	ΤΟ 7.7
From ST_	_09-A32	LO	21_	_J9-A7
From W7	P2-A7 (ST J9-A7)	to	W7	P1B-13D
From ID	J1B-13D	to	ID	A1J13.8
From ID	A1P13.8	to	ID	P12-68 (S701-4)
From ID	P12-44 (S701-2)	to	ID	A1P12.48
	A1J12.48	to	ID	A1J10.1
From ID	A1P10.1	to	ID	P11-162 (S506-2)
From ID	P11-195 (S506-4)	to	ID	A1P9.33
From ID	A1J9.33			BUS 2
				-1-6 00
From ID				A1J6.23
From ID	A1P6.23			P10-12 (S503-4)
From ID	P10-139 (S503-2)	to	ID	A1P8.26
From ID	A1J8.26	to	ID	A1J15.50
From ID	A1P15.50	to	ID	P20-3 (DMM-LO)

Step 837

Description:

This step verifies the wire path from W7 P2-B32 to W7 P2-C20. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P12-42 (S701-35)	to ID A1P13.19
From ID A1J13.19	to ID J1B-10D
From W7 P1B-10D	to W7 P2-B32 (ST J9-B32)
From ST_J9-B32	to ST_J9-C20
From W7 P2-C20 (ST J9-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)

Date: 04 March 2016

From ID P11-195 (S506-4) to ID A1P9.33 from ID A1J9.33 to ID BUS 2

From ID BUS 2 to ID A1J6.23 from ID A1P6.23 to ID P10-12 (S503-4) from ID P10-139 (S503-2) to ID A1P8.26 from ID A1J8.26 to ID A1J15.50 from ID A1P15.50 to ID P20-3 (DMM-LO)

Step 838

Description:

This step verifies the wire path from W7 P2-B32 to W7 P2-C20. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2(DMM-HI) From ID A1J15.49 From ID A1P8.32 From ID P10-135 (S301-43) From ID A1J7.7 From W7 P1A-13F	to ID A1P15.49 to ID A1J8.32 to ID P10-136 (S301-44) to ID A1P7.7 to ID J1A-13F to W7 J2-B32 (ST J9-B32)
From ST_J9-B32	to ST_J9-C20
From W7 P2-C20 (ST J9-C20)	to W7 P1B-10E
From ID J1B-10E	to ID A1J13.20
From ID A1P13.20	to ID P12-9 (S701-36)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID AlJ6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID AlP8.26
From ID A1J8.26	to ID AlJ15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 839

Description:

This step verifies the wire path from W7 P2-C32 to W7 P2-C33. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID	P20-2 (DMM-HI)	to	ID	A1P15.49	
From ID	A1J15.49	to	ID	A1J8.28	
From ID	A1P8.28	to	ID	P10-203	(S503-1)
From ID	P10-77 (S503-3)	to	ID	A1P6.13	

Date: 04 March 2016

From ID AlJ6.13	to ID BUS 1
From ID BUS 1	to ID AlJ9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P13-80 (S201-11)	to ID A1P14.3
From ID A1J14.3	to ID J1A-1B
From W7 P1A-1B	to W7 P2-C32 (ST J9-C32)
From ST_J9-C32	to ST_J9-C33
From W7 P2-C33 (ST J9-C33)	to W7 P1A-2B
From ID J1A-2B	to ID A1J14.4
From ID A1P14.4	to ID P13-48 (S201-12)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 840

Description:

This step verifies the wire path from W7 P2-A33 to W7 P2-B38. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P13-14 (S201-10) From ID A1J14.2 From W7 P1A-2A	to ID A1P14.2 to ID J1A-2A to W7 P2-A33 (ST J9-A33

Date: 04 March 2016

From ST_J9-A33	to ST_J9-B38
From W7 P2-B38 (ST J9-B38)	to W7 P1A-12E
From ID J1A-12E	to ID A1J14.31
From ID A1P14.31	to ID P13-30 (S202-41)
From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 841

Description:

This step verifies the wire path from W7 P2-B33 to W7 P2-B4. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1J8.28 to ID P10-203 (S503-1)
From ID BUS 1 From ID A1P9.19 From ID P11-177 (S509-1) From ID A1J10.10 From ID A1P12.38	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.10 to ID A1J12.38 to ID P12-59 (S202-1)
From ID P13-63 (S202-51) From ID A1J14.48 From W7 P1A-13D From ST J9-B33	to ID A1P14.48 to ID J1A-13D to W7 P2-B33 (ST J9-B33) to ST J9-B4
From W7 P2-B4 (ST J9-B4) From ID J1B-13E From ID A1P13.10	_
From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4)	to ID A1P12.48 to ID A1J10.1 to ID P11-162 (S506-2) to ID A1P9.33

Date: 04 March 2016

From ID A1J9.33 to ID BUS 2

From ID BUS 2 to ID A1J6.23

From ID A1P6.23 to ID P10-12 (S503-4)

From ID P10-139 (S503-2) to ID A1P8.26

From ID A1J8.26 to ID A1J15.50

From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 842

Description:

This step verifies the wire path from W7 P2-A34 to W7 P2-A4. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1J8.28 to ID P10-203 (S503-1)
From ID BUS 1 From ID A1P9.19 From ID P11-242 (S509-2) From ID A1J10.12 From ID A1P12.36	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.12 to ID A1J12.36 to ID P12-90 (S202-2)
From ID P13-94 (S202-52) From ID A1J14.27 From W7 P1A-10E	to ID J1A-10E to W7 P2-A34 (ST J9-A34)
From ST_J9-A34 From W7 P2-A4 (ST J9-A4) From ID J1B-14E	to ID AlJ13.9
From ID A1P13.9 From ID P12-76 (S701-1) From ID A1J12.50 From ID A1P10.3	to ID A1J10.3 to ID P11-194 (S506-1)
From ID P11-195 (S506-4) From ID A1J9.33 From ID BUS 2 From ID A1P6.23	to ID A1P9.33 to ID BUS 2 to ID A1J6.23 to ID P10-12 (S503-4)
From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID P10-12 (\$503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 843

Description:

Date: 04 March 2016

This step verifies the wire path from W7 P2-B34 to W7 P3-34. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P13-52 (S201-33) From ID A1J14.12 From W7 P1A-2F	to ID A1P14.12 to ID J1A-2F to W7 P2-B34 (ST J9-B34)
From ST_J9-B34	to ST_J10-34
From W7 P3-34 (ST J10-34) From ID J1B-4B From ID A1P12.17	to W7 P1B-4B to ID A1J12.17 to ID P12-96 (S202-47)
From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 844

Description:

This step verifies the wire path from W7 P2-C34 to W7 P2-B36. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID AlJ15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15

Date: 04 March 2016

From ID A1P9.15 From ID P11-203 (S508-1) From ID A1J10.6 From ID A1P12.42	to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
	to ID A1P12.7
From ID A1J12.7	to ID J1B-7A
From W7 P1B-7A	to W7 P2-C34 (ST J9-C34)
From ST_J9-C34	to ST_J9-B36
From W7 P2-B36 (ST J9-B36)	to W7 P1B-4B
From ID J1B-4B	to ID A1J12.17
From ID A1P12.17	to ID P12-96 (S202-47)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
	,

Step 845

Description:

This step verifies the wire path from W7 P2-A35 to W7 P3-31. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID Alp9.15 From ID P11-203 (S508-1) From ID AlJ10.6 From ID AlP12.42	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.6 to ID A1J12.42 to ID P12-16 (S201-1)
From ID P12-19 (S201-31) From ID A1J12.9 From W7 P1B-7C From ST J9-A35	to ID A1P12.9 to ID J1B-7C to W7 P2-A35 (ST J9-A35)
From W7 P3-31 (ST J10-31)	to W7 P1B-4B

Date: 04 March 2016

From ID J1B-4B	to ID A1J12.17
From ID A1P12.17	to ID P12-96 (S202-47)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 846

Description:

This step verifies the wire path from W7 P2-B35 to W7 P3-57. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID AlJ9.15
From ID Alp9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
T T T10 02 (G001 20)	
From ID P12-83 (S201-32)	
From ID A1J12.10	to ID J1B-6A
From W7 P1B-6A	to W7 P2-B35 (ST J9-B35)
From ST J9-R35	to ST J10-57
From ST_J9-B35	to ST_J10-57
From ST_J9-B35 From W7 P3-57 (ST J10-57)	to ST_J10-57 to W7 P1B-4C
From W7 P3-57 (ST J10-57)	to W7 P1B-4C
From W7 P3-57 (ST J10-57) From ID J1B-4C	to W7 P1B-4C to ID A1J12.18
From W7 P3-57 (ST J10-57) From ID J1B-4C	to W7 P1B-4C to ID A1J12.18
From W7 P3-57 (ST J10-57) From ID J1B-4C From ID A1P12.18	to W7 P1B-4C to ID A1J12.18 to ID P12-32 (S202-48)
From W7 P3-57 (ST J10-57) From ID J1B-4C From ID A1P12.18 From ID P12-90 (S202-2)	to W7 P1B-4C to ID A1J12.18 to ID P12-32 (S202-48) to ID A1P12.36
From W7 P3-57 (ST J10-57) From ID J1B-4C From ID A1P12.18 From ID P12-90 (S202-2) From ID A1J12.36	to W7 P1B-4C to ID A1J12.18 to ID P12-32 (S202-48) to ID A1P12.36 to ID A1J10.12
From W7 P3-57 (ST J10-57) From ID J1B-4C From ID A1P12.18 From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12	to W7 P1B-4C to ID A1J12.18 to ID P12-32 (S202-48) to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2)
From W7 P3-57 (ST J10-57) From ID J1B-4C From ID A1P12.18 From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29	to W7 P1B-4C to ID A1J12.18 to ID P12-32 (S202-48) to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.29 to ID BUS 2
From W7 P3-57 (ST J10-57) From ID J1B-4C From ID A1P12.18 From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29 From ID BUS 2	to W7 P1B-4C to ID A1J12.18 to ID P12-32 (S202-48) to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.29 to ID BUS 2
From W7 P3-57 (ST J10-57) From ID J1B-4C From ID A1P12.18 From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29 From ID BUS 2 From ID A1P6.23	to W7 P1B-4C to ID A1J12.18 to ID P12-32 (S202-48) to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.29 to ID BUS 2 to ID A1J6.23 to ID P10-12 (S503-4)
From W7 P3-57 (ST J10-57) From ID J1B-4C From ID A1P12.18 From ID P12-90 (S202-2) From ID A1J12.36 From ID A1P10.12 From ID P11-17 (S509-4) From ID A1J9.29 From ID BUS 2	to W7 P1B-4C to ID A1J12.18 to ID P12-32 (S202-48) to ID A1P12.36 to ID A1J10.12 to ID P11-242 (S509-2) to ID A1P9.29 to ID BUS 2

Date: 04 March 2016

From ID A1J8.26 to ID A1J15.50 From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 847

Description:

This step verifies the wire path from W7 P2-A38 to W7 P2-A6. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-242 (S509-2)	to ID A1P10.12
From ID A1J10.12	to ID A1J12.36
From ID A1P12.36	to ID P12-90 (S202-2)
From ID P12-32 (S202-48)	to ID A1P12.18
From ID A1J12.18	to ID J1B-4C
From W7 P1B-4C	to W7 P2-A38 (ST J9-A38)
From ST_J9-A38	to ST_J9-A6
From W7 P2-A6 (ST J9-A6)	to W7 P1B-14D
From ID J1B-14D	to ID A1J13.7
From ID A1P13.7	to ID P12-4 (S701-3)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID AlJ6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID AlP8.26
From ID A1J8.26	to ID AlJ15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 848

Description:

This step verifies the wire path from W7 P2-C40 to W7 P3-64. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) to ID A1P15.49

Date: 04 March 2016

_		A1J15.49			A1J8.28
From	ID	A1P8.28	to	ID	P10-203 (S503-1)
		P10-77 (S503-3)			
From	ID	A1J6.13	to	ID	BUS 1
From	ID	BUS 1	to	ID	A1J9.15
_					P11-77 (S508-3)
		P11-203 (S508-1)	to	ID	A1P10.6
					A1J12.42
From	ID	A1P12.42	to	ID	P12-16 (S201-1)
From	ID	P13-15 (S201-19)	to	ID	A1P14.6
From	ID	A1J14.6	to	ID	J1A-2C
From	W7	P1A-2C	to	W7	P2-C40 (ST J9-C40)
From	ST_	_J9-C40	to	ST_	_J10-64
From	w7	P3-64 (ST J10-64)	to	พ7	P1A-7F
From	ID	J1A-7F	to	ID	A1J14.22
From	ID	A1P14.22	to	ID	P13-92 (S202-34)
From	ID	P12-90 (S202-2)	to	ID	A1P12.36
From	ID	A1J12.36	to	ID	A1J10.12
					P11-242 (S509-2)
From	ID	P11-17 (S509-4)	to	ID	A1P9.29
From	ID	A1J9.29	to	ID	BUS 2
From	ID	BUS 2	to	ID	A1J6.23
			to	ID	P10-12 (S503-4)
From	ID	P10-139 (S503-2)	to	ID	A1P8.26
From	ID	A1J8.26	to	ID	A1J15.50
From	ID	A1P15.50	to	ID	P20-3 (DMM-LO)

Step 849

Description:

This step verifies the wire path from W7 P3-7 to W7 P2-C37. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID AlJ15.49 From ID AlP8.28	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID AlJ6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P13-18 (S201-35)	to ID A1P14.14

Date: 04 March 2016

From ID AlJ14.14 From W7 P1A-3F	to ID J1A-3F to W7 P3-7 (ST J10-7)
From ST_J10-7	to ST_J9-C37
From W7 P2-C37 (ST J9-C37) From ID J1B-4B From ID A1P12.17	to W7 P1B-4B to ID A1J12.17 to ID P12-96 (S202-47)
From ID P12-59 (S202-1) From ID A1J12.38 From ID A1P10.10 From ID P11-17 (S509-4) From ID A1J9.29	to ID A1P12.38 to ID A1J10.10 to ID P11-177 (S509-1) to ID A1P9.29 to ID BUS 2
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 850

Description:

This step verifies the wire path from W7 P3-8 to W7 P3-12. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P13-76 (S701-41) From ID A1J15.15 From W7 P1A-7C	to ID A1P15.15 to ID J1A-7C to W7 P3-8 (ST J10-8)
From ST_J10-8	to ST_J10-12
From W7 P3-12 (ST J10-12) From ID J1A-4C From ID A1P15.6	to W7 P1A-4C to ID A1J15.6 to ID P13-44 (S701-42)
From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1	to ID A1P12.48 to ID A1J10.1 to ID P11-162 (S506-2)

Date: 04 March 2016

From ID P11-195 (S506-4) to ID A1P9.33 to ID BUS 2

From ID BUS 2 to ID A1J6.23 to ID P10-12 (S503-4) From ID A1P6.23 to ID A1P8.26 From ID A1J8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 851

Description:

This step verifies the wire path from W7 P3-9 to W7 P3-65. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID AlJ13.6 From W7 P1B-13C	to ID A1P13.6 to ID J1B-13C to W7 P3-9 (ST J10-9)
From ST_J10-9 From W7 P3-65 (ST J10-65) From ID J1B-14C From ID A1P13.5	to ST_J10-65 to W7 P1B-14C to ID A1J13.5 to ID P12-14 (S201-13)
From ID P12-16 (S201-1) From ID A1J12.42 From ID A1P10.6 From ID P11-12 (S508-4) From ID A1J9.25	to ID A1J10.6 to ID P11-203 (S508-1)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 852

Description:

Date: 04 March 2016

This step verifies the wire path from W7 P3-10 to W7 P2-C4. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID A1J1.14 From ID A1P2.2	to ID A1P1.14 to ID A1J2.2 to ID P10-24 (S101-9)
From ID P10-88 (S101-10) From ID A1J2.14	to ID AIP2.14 to ID J1A-14F
From W7 P1A-14F	to W7 P3-10 (ST J10-10)
From ST_J10-10	to ST_J9-C4
From W7 P2-C4 (ST J9-C4)	to W7 P1A-3C
From ID J1A-3C	to ID A1J15.3
From ID A1P15.3	to ID P13-70 (S701-9)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID AlJ9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 853

Description:

This step verifies the wire path from W7 P2-A5 to W7 P3-11. The DMM resource will be used to measure resistance UL= 10 ohms.

From I	D P20-2 (DMM-HI)	to	ID	A1P15.49
From I	D A1J15.49	to	ID	A1J8.28
From I	D A1P8.28	to	ID	P10-203 (S503-1)
From I	D P10-12 (S503-4)	to	ID	A1P6.23
From I	D A1J6.23	to	ID	BUS 2
From I	D BUS 2	to	ID	A1J9.33
From I	D A1P9.33	to	ID	P11-195 (S506-4)
From I	D P11-162 (S506-2)	to	ID	A1P10.1
From I	D A1J10.1	to	ID	A1J12.48
From I	D A1P12.48	to	ID	P12-44 (S701-2)
From I	D P13-38 (S701-10)	to	ID	A1P15.4
From I	D A1J15.4	to	ID	J1A-4A
From W	7 P1A-4A	to	W7	P2-A5 (ST J9-A5)

Date: 04 March 2016

From ST_J9-A5 to ST_J10-11

From W7 P3-11 (ST J10-11) to W7 P1A-12F

From ID J1A-12F to ID A1J7.12

From ID A1P7.12 to ID P10-66 (S301-6)

From ID P10-33 (S301-5) to ID A1P7.40

From ID A1J7.40 to ID A1J15.50

From ID A1P15.50 to ID P20-3 (DMM-LO)

Step 854

Description:

This step verifies the wire path from W7 P3-13 to W7 P3-14. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P13-13 (S701-49)	to ID A1P15.17
From ID A1J15.17	to ID J1A-8B
From W7 P1A-8B	to W7 P3-13 (ST J10-13)
From ST_J10-13	to ST_J10-14
From W7 P3-14 (ST J10-14)	to W7 P1A-9A
From ID J1A-9A	to ID A1J15.19
From ID A1P15.19	to ID P13-77 (S701-50)
From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4) From ID A1J9.33	to ID A1J10.1 to ID P11-162 (S506-2)
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 855

Description:

Date: 04 March 2016

This step verifies the wire path from W7 P3-15 to W7 P3-16. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
	to ID A1J9.23 to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
From ID P12-10 (S701-45) From ID A1J13.33 From W7 P1B-7F	to ID A1P13.33 to ID J1B-7F to W7 P3-15 (ST J10-15)
From ST_J10-15	to ST_J10-16
From W7 P3-16 (ST J10-16) From ID J1B-8E From ID A1P13.26	to W7 P1B-8E to ID A1J13.26 to ID P12-74 (S701-46)
From ID P12-44 (S701-2) From ID A1J12.48 From ID A1P10.1 From ID P11-195 (S506-4) From ID A1J9.33	to ID A1J10.1 to ID P11-162 (S506-2)
From ID A1P6.23	to ID A1J6.23 to ID P10-12 (S503-4) to ID A1P8.26 to ID A1J15.50 to ID P20-3 (DMM-LO)

Step 856

Description:

This step verifies the wire path from W7 P3-17 to W7 P3-37. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23

Date: 04 March 2016

From ID A1P9.23 From ID P11-194 (S506-1) From ID A1J10.3 From ID A1P12.50	to ID P11-164 (S506-3) to ID A1P10.3 to ID A1J12.50 to ID P12-76 (S701-1)
,	to ID A1P15.18
From ID AlJ15.18	to ID J1A-8C
From W7 P1A-8C	to W7 P3-17 (ST J10-17)
From ST_J10-17	to ST_J10-37
From W7 P3-37 (ST J10-37)	to W7 P1A-9B
From ID J1A-9B	to ID A1J15.20
From ID A1P15.20	to ID P13-46 (S701-48)
110111111111111111111111111111111111111	20 12 113 10 (2,01 10)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 857

Description:

This step verifies the wire path from W7 P3-20 to W7 P2-C2. The DSO will be used to verify the resolver reference voltage of 11.8V RMS.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1P8.28 From ID P10-77 (S503-3) From ID A1J6.13	to ID A1P15.49 to ID A1J8.28 to ID P10-203 (S503-1) to ID A1P6.13 to ID BUS 1
From ID BUS 1 From ID A1P9.15 From ID P11-139 (S508-2) From ID A1J10.8 From ID A1P12.40	to ID A1J9.15 to ID P11-77 (S508-3) to ID A1P10.8 to ID A1J12.40 to ID P12-80 (S201-2)
From ID P13-82 (S201-36) From ID A1J14.29 From W7 P1A-11E From ST_J10-20	to ID A1P14.29 to ID J1A-11E to W7 P3-20 (ST J10-20) to ST_J9-B18
From W7 P2-B18 (ST J9-B18)	to W7 P1B-10C

Date: 04 March 2016

From ID J1B-10C	to ID A1J3.17
From ID A1P3.17	to ID J3-25
From W204 P2-25	to W204 P1-25 (SRS Ref Hi)
From W204 P1-26 (SRS Ref Lo)	to W204 P2-26
From ID J3-26	to ID A1P3.16
From ID A1J3.16	to ID J1B-11A
From W7 P1B-11A	to W7 P2-C18 (ST J9-C18)
From ST_J9-C18	to ST_J9-C2
From W7 P2-C2 (ST J9-C2)	to W7 P1B-3A
From ID J1B-3A	to ID A1J12.19
From ID A1P12.19	to ID P12-54 (S201-38)
From ID P12-80 (S201-2) From ID A1J12.40 From ID A1P10.8 From ID P11-12 (S508-4) From ID A1J9.25	to ID A1J10.8 to ID P11-139 (S508-2)
From ID BUS 2 From ID A1P6.23 From ID P10-139 (S503-2) From ID A1J8.26 From ID A1P15.50	to ID A1J15.50

Step 858

Description:

This step verifies the wire path from W7 P3-77 to W7 P3-24. The DSO will be used to verify the resolver \sin voltage of 5.9V RMS.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
	•
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P13-21 (S201-41)	to ID A1P14.15
From ID A1J14.15	to ID J1A-4E
	** == *===
From W7 P1A-4E	to W7 P3-77 (ST J10-77)
From ST_J10-77	to ST_J9-B17
From W7 P2-B17 (ST J9-B17)	to W7 P1B-12A

Date: 04 March 2016

to ID A1J3.14 to ID J3-15
to W204 P1-15 (SRS Sin +)
CO WZO4 PI-15 (SRS SIII +)
to W204 P2-13
to ID A1P3.12
to ID J1B-12C
to W7 P2-C17 (ST J9-C17)
to ST_J10-24
to W7 P1B-6C
to ID A1J12.12
to ID P12-53 (S201-39)
to ID AlP12.46
to ID A1J10.2
to ID P11-39 (S507-1)
to ID A1P9.27
to ID BUS 2
L. TD 3176 02
to ID A1J6.23
to ID P10-12 (S503-4)
to ID A1P8.26
to ID A1J15.50
to ID P20-3 (DMM-LO)

Step 859

Description:

This step verifies the wire path for W8 RS422 Communication.

From W8 P1-1 (ICJ6.1) From ID J4-1 From ID A1J3.19 From W7 P1B-10A	to W8 P2-1 to ID A1P3.19 to ID J1B-10A to W7 P3-32 (ST J10-32)
From ST_J10-32	to ST_J10-40
From W7 P3-40 (ST J10-40) From ID J1B-9F From ID A1P3.10 From W8 P2-4	to W7 P1B-9F to ID A1J3.10 to ID J4-4 to W8 P1-4 (ICJ6.4)
From W8 P1-3 (ICJ6.3) From ID J4-3 From ID A1J3.9 From W7 P1B-11E	to W8 P2-3 to ID A1P3.9 to ID J1B-11E to W7 P3-44 (ST J10-44)
From ST_J10-44	to ST_J10-59
From W7 P3-59 (ST J10-59)	to W7 P1B-10B

Date: 04 March 2016

From ID J1B-10B to ID A1J3.18 From ID A1P3.18 to ID J4-2 From W8 P2-2 to W8 P1-2 (ICJ6.2)

Step 860

Description:

This step verifies the wire path from W7 P3-35 to W7 P3-38. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID A1J From ID A1P	8.28 -77 (S503-3)	to to	ID ID ID	A1J8.28 P10-203 (S503-1)
From ID BUS From ID A1P From ID P11 From ID A1J From ID A1P	9.19 242 (S509-2) 10.12	to to	ID ID ID	A1J9.19 P11-18 (S509-3) A1P10.12 A1J12.36 P12-90 (S202-2)
From ID P13 From ID A1J From W7 P1A		to	ID	A1P14.20 J1A-6F P3-35 (ST J10-35)
From ST_J10	-35	to	ST_	J10-38
From W7 P3- From ID J1B From ID A1P		to	ID	P1B-5A A1J12.13 P12-92 (S202-22)
From ID A1J From ID A1P	10.50 -147 (S510-4)	to to	ID ID ID	A1P14.50 A1J10.50 P11-244 (S510-2) A1P9.31 BUS 2
From ID BUS From ID A1P From ID P10 From ID A1J From ID A1P	6.23 -139 (S503-2) 8.26	to to to	ID ID ID	A1J6.23 P10-12 (S503-4) A1P8.26 A1J15.50 P20-3 (DMM-LO)

Step 861

Description:

This step verifies the wire path from W7 P3-51 to W7 P3-25. The DSO will be used to verify the resolver cos voltage of $5.9V\ RMS$.

Date: 04 March 2016

o i i i i i i i i i i i i i i i i i i i	
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID Alp6.13
From ID AlJ6.13	to ID BUS 1
FIOR ID AIOU.IS	CO 1D B05 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID AlJ12.40
From ID A1010.0	to ID P12-80 (S201-2)
FIOR ID AIF12.40	CO ID PIZ-00 (5201-2)
From ID P12-84 (S201-40)	to ID A1P12.20
From ID A1J12.20	to ID J1B-2A
From W7 P1B-2A	to W7 P3-51 (ST J10-51)
FIOR W/ FIB-ZA	CO W/ F3-31 (S1 010-31)
From ST_J10-51	to ST_J9-B19
110 51_010 31	60 B1 <u>-</u> 07 B17
From W7 P2-B19 (ST J9-B19)	to W7 P1B-12B
From ID J1B-12B	to ID A1J3.13
From ID A1P3.13	to ID J3-14
From W204 P2-14	to W204 P1-14 (SRS Cos +)
110 W201 12 11	co who is in the cost of
From W204 P1-16 (SRS Cos -)	to W204 P2-16
From ID J3-16	to ID A1P3.15
From ID AlJ3.15	to ID J1B-11B
From W7 P1B-11B	to W7 P2-C19 (ST J9-C19)
From ST_J9-C19	to ST_J10-25
_	_
From W7 P3-25 (ST J10-25)	to W7 P1A-4F
From ID J1A-4F	to ID A1J14.16
From ID A1P14.16	to ID P13-86 (S201-43)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID AlJ9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID AlJ8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)
110 12 1111 10.00	

Step 862

Description:

This step verifies the wire path from W7 P3-52 to W7 P3-76. The DMM resource will be used to measure resistance UL= 10 ohms.

Date: 04 March 2016

	to ID A1J8.28 to ID P10-203 (S503-1)
	to ID A1J9.19 to ID P11-18 (S509-3) to ID A1P10.12 to ID A1J12.36 to ID P12-90 (S202-2)
From ID P13-87 (S202-10) From ID A1J14.17 From W7 P1A-5E	to ID J1A-5E to W7 P3-52 (ST J10-52)
From ST_J10-52	to ST_J10-76
From W7 P3-76 (ST J10-76)	to W7 P1A-12C
From ID J1A-12C	to ID A1J7.6
From ID A1P7.6	to ID P10-100 (S301-13)
From ID P10-228 (S301-14)	to ID A1P7.20
From ID A1J7.20	to ID A1J1.4
From ID A1P1.4	to ID P1-29 (DC10-LO)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	
From ID A1J7.36	to GROUND

Step 863

Description:

This step verifies the wire path from W7 P3-62 to W7 P3-67. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-177 (S509-1)	to ID A1P10.10
From ID A1J10.10	to ID A1J12.38
From ID A1P12.38	to ID P12-59 (S202-1)
From ID P12-61 (S202-21)	to ID A1P12.21
From ID AlJ12.21	to ID J1B-1A
From W7 P1B-1A	to W7 P3-62 (ST J10-62)

Date: 04 March 2016

From ST_J10-62	to ST_J10-67
From W7 P3-67 (ST J10-67)	to W7 P1A-6E
From ID J1A-6E	to ID A1J14.19
From ID A1P14.19	to ID P13-88 (S202-19)
From ID P13-93 (S202-3)	to ID A1P14.49
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 864

Description:

This step verifies the wire path from W7 P3-69 to W7 P2-C36. The DMM resource will be used to measure resistance UL= 10 ohms.

From ID P20-2 (DMM-HI) From ID A1J15.49 From ID A1D8 28	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
FIOM ID AIPO.20	CO ID PIU-ZUS (8303-I)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P12-50 (S201-30)	to ID A1P12.8
From ID A1J12.8	to ID J1B-7B
From W7 P1B-7B	to W7 P3-69 (ST J10-69)
T	L GE TO G26
From ST_J10-69	to ST_J9-C36
From W7 P2-C36 (ST J9-C36)	to W7 P1B-4C
From ID J1B-4C	to ID A1J12.18
From ID A1P12.18	to ID P12-32 (S202-48)
110111111111111111111111111111111111111	00 12 112 32 (8202 10)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	` ,
From ID AlJ9.29	to ID BUS 2
	

Date: 04 March 2016

From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

(NOTE TESTs marked with "**" preceding test number are not part of the AN/USM-657-B (TETS) executable software as the functionality under test is not part of the TETS platform)

** Step 865

Description:

Test of interconnect of CAN bus based (P4 to Data-I/O) Interfaces. Connections are in place to loop CAN1 to CAN2 from the VIPER-T through the cable and test fixed loopbacks and then back to the VIPER-T on CAN2. 8 test bytes are sent (50H-57H) and are expected on CAN2.

From	W7P4-22 (BUS	I/O	CAN2HI)	to	W7P3-62
From	ST_J10-62			to	ST_J10-67
${\tt From}$	W7P3-67			to	W7P4-16 (BUS I/O CAN1HI)
${\tt From}$	W7P4-23 (BUS	I/O	CAN2LO)	to	W7P3-38
From	ST_J10-38			to	ST_J10-35
${\tt From}$	W7P3-35			to	W7P4-17 (BUS I/O CAN1LO)
${\tt From}$	W7P4-24 (BUS	I/O	CAN2SHLD)	to	W7P3-76
From	ST_J10-76			to	ST_J10-52
From	W7P3-52			to	W7P4-18 (BUS I/O CAN1SHLD)

Date: 04 March 2016

3.0 Functional Flow Chart

