English Language Test Description

MIPR # M9545012MP24797
CDRL F001

for

Unit Under Test

UUT Nomenclature: Interface Device
UUT Part Number: 13020A1000 and associated TPH
from

Light Armored Vehicle - 25A2
(LAV-25A2)

ATE SYSTEM

AN/USM-657B Third Echelon Test System (TETS)
AN/USM-717 Virtual Instrument Portable Equipment Repair/Test (VIPER/T)
Developed by

U.S. Army RDECOM
Armament Research, Development and Engineering Center
Automated Test Systems Division
RDAR-WSF-A, Building 91
Picatinny, NJ 07806

Prepared By	Signature	Date Prepared	Date Submitted
William J. Vivino Jr.		3 September 2015	

Approved By	Signature	Date Received	Date Approved
Thomas Bradford			
Rick Foyt			

ELTD REVISION SUMMARY

Revision Number	Date	Reason	Approved By - Date Approved
-	04 Mar 2016	ORIGINAL ISSUE	B. Nimmick 3/4/2016

Table of Contents

1.0 Reference Documents

1.1 Virtual Instrument Portable Equipment Repair/Test (VIPER/T)

IEEE Std 716-1989	IEEE Standard Common Abbreviated Test Language for All Systems
TM 10530A-CD	TETS IETM (Interactive Electronic Technical Manual)
System Design Description Doc # 93006A0018	Third Echelon Test System (TETS) AN/USM-657(V)

1.2 Third Echelon Test System (TETS)

TETS P/N 93006A0026	TETS CPM (Computer Programming Manual)
TM TBD-CD	VIPER/T IETM (Interactive Electronic Technical Manual)
System Design Document Doc # 7992008	VIPER/T AN/USM-717
VIPER/T P/N 7992021	VIPER/T CPM (Computer Programming Manual)

1.3 Unit Under Test

UUT P/N: 13020A1000
UUT Nomenclature: Interface Device – ASSY – LAVA2- SRU
UUT Type: Interface Device

UUT P/N: 13020A7100
UUT Nomenclature: CABLE ASSEMBLY, CDA LOGIC A, W1
UUT Type: Cable

UUT P/N: 13020A7200
UUT Nomenclature: CABLE ASSEMBLY, CDA LOGIC B,W2
UUT Type: Cable

UUT P/N: 13020A7300
UUT Nomenclature: CABLE ASSEMBLY, CDA GRENADE RELAY, W3
UUT Type: Cable

UUT P/N: 13020A7400
UUT Nomenclature: CABLE ASSEMBLY, GCU LOGIC, W4
UUT Type: Cable

UUT P/N: 13020A7500
UUT Nomenclature: CABLE ASSEMBLY, GCU POWER DRIVE, W5
UUT Type: Cable

UUT P/N: 13020A7600
UUT Nomenclature: CABLE ASSEMBLY, GTD POWER SUPPLY, W6
UUT Type: Cable

UUT P/N: 13020A7700
UUT Nomenclature: CABLE ASSEMBLY, GTD PROCESSOR, W7
UUT Type: Cable

UUT P/N: 08014A3800
UUT Nomenclature: CABLE ASSEMBLY, RS422, W8
UUT Type: Cable

UUT P/N: 13020A9015
UUT Nomenclature: FIXTURE ASSEMBLY, HOLDING
UUT Type: Holding Fixture

UUT P/N: 13020A6000
UUT Nomenclature: FIXTURE ASSEMBLY, SELF TEST
UUT Type: Test Fixture

1.4 Reference Drawings

Refer to the following schematics when diagnosing connection paths.

2.0 English Language Test Description Steps

2.1 Common Procedures

The following procedure is used to support the termination of the CAN bus Interface

CAN 2 TERMINATE

The following setup procedure properly terminates the CAN bus with a resistive termination as required.

Connection Path is as follows:

From W7 P3-62 (ST J10-62)	to W7 P1B-1A
From ID J1B-1A	to ID A1J12.21
From ID A1P12.21	to ID P12-61 (S202-21)
From W7 P3-38 (ST J10-38)	to W7 P1B-5A
From ID J1B-5A	to ID A1J12.13
From ID A1P12.13	to ID P12-92 (S202-22)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-207 (S509-7)	to ID A1P9.36
From ID A1J9.36	to ID BUS 5
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-144 (S509-8)	to ID A1P9.26
From ID A1J9.26	to ID BUS 6
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2

2.2 Interface ID

Refer to Reference Drawings when diagnosing connection paths.

Step 1 TPH (ID) Identification:

Description:
Connect R111 (324 ohms) to Bus 5. Connect R109 (698 ohms) to Bus 6. R109 and R111 are now in series between Bus 5 and 6. Connect DMM HI to Bus 5. Connect DMM LO to Bus 6. Expected Resistance: 1022 ohms +/- 5%.

Connection path as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)

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From ID P10-137 (S503-7)          to ID A1P6.47
From ID A1J6.47                   to ID BUS 5

From ID BUS 5                     to ID A1J8.47
From ID A1P8.47                   to ID P10-73 (S301-48)
From ID P10-7 (S301-47)           to ID A1P7.23
From ID A1J7.23                   to ID A1J4.15
From ID A1P4.15                   to ID R111.1
From ID R111.2                    to ID A1P4.9
From ID A1J4.9                    to +28V

From +28V                         to ID A1J4.9
From ID A1P4.9                    to ID R109.2
From ID R109.1                    to ID A1P4.16
From ID A1J4.16                   to ID A1J7.24
From ID A1P7.24                   to ID P10-42 (S301-49)
From ID P10-171 (S301-50)         to ID A1P8.48
From ID A1J8.48                   to ID BUS 6

From ID BUS 6                     to ID A1J6.38
From ID A1P6.38                   to ID P10-170 (S503-8)
From ID P10-139 (S503-2)          to ID A1P8.26
From ID A1J8.26                   to ID A1J15.50
From ID A1P15.50                  to ID P20-3 (DMM-LO)
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2.3 Safe to Turn On

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Step 2 STTO Test:

Description:
Verify that no low impedance/shorts exist on PS DC2 leads - minimum
resistance of 1E3 ohms

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J7.42
      From ID A1P7.42                 to ID P10-34 (S301-2)
      From ID P10-65 (S301-1)         to ID A1P7.3
      From ID A1J7.3                  to ID A1J2.15
      From ID A1P2.15                 to ID P10-86 (S101-2)
      From ID P10-22 (S101-1)         to ID A1P2.23
      From ID A1J2.23                 to ID A1J1.2
      From ID A1P1.2                  to ID P1-4 (DC2-HI)

      From ID P10-86 (S101-2)         to ID A1P2.15
      From ID A1J2.15                 to ID A1J10.14
      From ID A1P10.14                to ID P11-140 (S301-161)
      From ID P11-206 (S301-162)      to ID A1P10.16
      From ID A1J10.16                to ID J1A-13E

      From ID P10-86 (S101-2)         to ID A1P2.15
      From ID A1J2.15                 to ID A1J11.11
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From ID A1P11.11	to ID P11-233 (S301-153)
From ID P11-43 (S301-154)	to ID A1P11.9
From ID A1J11.9	to ID J1A-12D
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 3 STTO Test:

Description:
Verify that no low impedance/shorts exist on PS DC4 leads - minimum resistance of 1E3 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J7.44
From ID A1P7.44	to ID P10-99 (S301-4)
From ID P10-226 (S301-3)	to ID A1P7.13
From ID A1J7.13	to ID A1J2.31
From ID A1P2.31	to ID P10-87 (S101-6)
From ID P10-23 (S101-5)	to ID A1P2.4
From ID A1J2.4	to ID A1J1.3
From ID A1P1.3	to ID P1-10 (DC4-HI)
From ID P10-87 (S101-6)	to ID A1P2.31
From ID A1J2.31	to ID A1J2.30
From ID A1P2.30	to ID P11-216 (S101-32)
From ID P11-152 (S101-31)	to ID A1P2.37
From ID A1J2.37	to ID A1J3.20
From ID A1P3.20	to ID J3-28
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID A1J7.36	to GROUND

Step 4 STTO Test:

Description:
Verify that no low impedance/shorts exist on PS DC5 leads - minimum resistance of 1E3 ohms.

Connection Path is as follows:

From P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J7.46
From ID A1P7.46	to ID P10-35 (S301-8)
From ID P10-1 (S301-7)	to ID A1P7.1
From ID A1J7.1	to ID A1J2.14

From ID A1P2.14	to ID P10-88 (S101-10)
From ID P10-24 (S101-9)	to ID A1P2.2
From ID A1J2.2	to ID A1J1.14
From ID A1P1.14	to ID P1-13 (DC5-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.40
From ID A1P7.40	to ID P10-33 (S301-5)
From ID P10-66 (S301-6)	to ID A1P7.12
From ID A1J7.12	to ID A1J1.7
From ID A1P1.7	to ID P1-14 (DC5-LO)

Step 5 STTO Test:

Description:
Verify that no low impedance/shorts exist on PS DC6 leads - minimum resistance of 1E3 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J7.48
From ID A1P7.48	to ID P10-2 (S301-10)
From ID P10-97 (S301-9)	to ID A1P7.5
From ID A1J7.5	to ID A1J2.33
From ID A1P2.33	to ID P10-89 (S101-14)
From ID P10-25 (S101-13)	to ID A1P2.20
From ID A1J2.20	to ID A1J1.15
From ID A1P1.15	to ID P1-16 (DC6-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 6 STTO Test:

Description:
Verify that no low impedance/shorts exist on PS DC10 leads - minimum resistance of 1E3 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.34
From ID A1P8.34	to ID P10-201 (S301-46)
From ID P10-167 (S301-45)	to ID A1P7.22
From ID A1J7.22	to ID A1J4.14
From ID A1P4.14	to ID R102.1
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.32
From ID A1P8.32	to ID P10-136 (S301-44)

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From ID P10-135 (S301-43)        to ID A1P7.7
From ID A1J7.7                   to ID A1J4.13
From ID A1P4.13                  to ID R103.1

From ID R103.1                   to ID A1P4.13
From ID A1J4.13                  to ID A1J2.12
From ID A1P2.12                  to ID P10-150 (S101-3)
From ID P10-214 (S101-4)         to ID A1P2.35
From ID A1J2.35                  to ID A1J3.22
From ID A1P3.22                  to ID J3-18

From ID P20-2 (DMM-HI)           to ID A1P15.49
From ID A1J15.49                 to ID A1J8.30
From ID A1P8.30                  to ID P10-41 (S301-42)
From ID P10-199 (S301-41)        to ID A1P7.2
From ID A1J7.2                   to ID A1J2.29
From ID A1P2.29                  to ID P10-153 (S101-15)

From ID P1-28 (DC10-HI)          to ID A1P1.6
From ID A1J1.6                   to ID A1J7.11
From ID A1P7.11                  to ID P10-4 (S301-18)
From ID P10-67 (S301-17)         to ID A1P7.27
From ID A1J7.27                  to ID A1J4.19
From ID A1P4.19                  to ID R102.2

From ID P1-28 (DC10-HI)          to ID A1P1.6
From ID A1J1.6                   to ID A1J2.1
From ID A1P2.1                   to ID P10-90 (S101-18)
From ID P10-26 (S101-17)         to ID A1P2.11
From ID A1J2.11                  to ID A1J4.1
From ID A1P4.1                   to ID R103.2

From ID P1-28 (DC10-HI)          to ID A1P1.6
From ID A1J1.6                   to ID A1J2.21
From ID A1P2.21                  to ID P10-217 (S101-16)
From ID P10-153 (S101-15)        to ID A1P2.29
From ID A1J2.29                  to ID A1J2.32
From ID A1P2.32                  to ID P10-218 (S101-20)
From ID P10-154 (S101-19)        to ID A1P2.16
From ID A1J2.16                  to ID J1B-3D

From ID P1-29 (DC10-LO)          to ID A1P1.4
From ID A1J1.4                   to ID A1J7.20
From ID A1P7.20                  to ID P10-228 (S301-14)
From ID P10-100 (S301-13)        to ID A1P7.6
From ID A1J7.6                   to ID A1J3.4
From ID A1P3.4                   to ID J3-24

From ID J3-24                    to ID A1P3.4
From ID A1J3.4                   to ID J1A-12C

From ID J3-24                    to ID A1P3.4
From ID A1J3.4                   to ID J2A-10E
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      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J7.38
      From ID A1P7.38                   to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)         to ID A1P7.36
      From ID A1J7.36                   to GROUND

Step 7 STTO Test:

Description:
Verify that no low impedance/shorts exist on PS DC9 leads - minimum
resistance of 1E3 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-173 (S503-9)          to ID A1P6.28
      From ID A1J6.28                         to ID BUS 7

      From ID P1-25 (DC9-HI)            to ID A1P1.13
      From ID A1J1.13                         to ID A1J7.14
      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-173 (S503-9)          to ID A1P6.28
      From ID A1J6.28                   to ID BUS 7

      From ID P1-25 (DC9-HI)            to ID A1P1.13
      From ID A1J1.13                   to ID A1J7.14
      From ID A1P7.14                   to ID P10-197 (S301-29)
      From ID P10-198 (S301-30)         to ID A1P6.24
      From ID A1J6.24                   to ID BUS 7

      From ID P1-26 (DC9-LO)            to ID A1P1.5
      From ID A1J1.5                    to ID A1J7.18
      From ID A1P7.18                   to ID P10-133 (S301-27)
      From ID P10-70 (S301-28)          to ID A1P6.12
      From ID A1J6.12                   to ID BUS 8

      From ID P1-26 (DC9-LO)            to ID A1P1.5
      From ID A1J1.5                    to ID A1J7.16
      From ID A1P7.16                   to ID P10-163 (S301-12)
      From ID P10-98 (S301-11)          to ID A1P7.32
      From ID A1J7.32                   to GROUND

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J8.26
      From ID A1P8.26                   to ID P10-139 (S503-2)
      From ID P10-205 (S503-10)         to ID A1P6.16
      From ID A1J6.16                   to ID BUS 8

Step 8 STTO Test:
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Description:
Verify that no low impedance/shorts exist on PS DC1 - minimum resistance of 1E3 ohms.

Connection Path is as follows:

From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to U4.1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2 (+15V)	to R12.2
From R12.1	to U4.3
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID J2A-13A	to ID A1U4.3
From ID A1U4.2	to ID A1J5.36
From ID A1P5.36	to ID P6-17 (DTS CH23)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10

From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

2.4 MODULE 1- INTERFACE DEVICE TEST

Refer to Reference Drawings when diagnosing connection paths.

Step 101

Description:
This step verifies DCPS2 connections to the DMM. DCPS2 will be set to 15VDC and the DMM is used to measure output voltage UL= 15.5 VDC, LL= 14.5 VDC.

Connection Path is as follows:

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J2.23
From ID A1P2.23	to ID P10-22 (S101-1)
From ID P10-86 (S101-2)	to ID A1P2.15
From ID A1J2.15	to ID A1J7.3
From ID A1P7.3	to ID P10-65 (S301-1)
From ID P10-34 (S301-2)	to ID A1P7.42
From ID A1J7.42	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

From GROUND	to ID A1J1.10
From ID A1P1.10	to ID P1-5 (DC2-LO)

Step 102

Description:
This step verifies DCPS2 connections to BUS1. DCPS2 will be set to 15VDC and the DMM is used to measure output voltage UL= 15.5 VDC, LL= 14.5 VDC.

Connection Path is as follows:

From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to ID A1J8.4
From ID A1P8.4	to ID P10-174 (S301-68)
From ID P10-204 (S301-67)	to ID A1P8.29
From ID A1J8.29	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 103

Description:
This step verifies DCPS4 connections to the DMM. DCPS4 will be set to 28VDC and the DMM is used to measure output voltage UL= 28.5 VDC, LL= 27.5 VDC.

Connection Path is as follows:

From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J2.4
From ID A1P2.4	to ID P10-23 (S101-5)
From ID P10-87 (S101-6)	to ID A1P2.31
From ID A1J2.31	to ID A1J7.13
From ID A1P7.13	to ID P10-226 (S301-3)
From ID P10-99 (S301-4)	to ID A1P7.44
From ID A1J7.44	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

From GROUND	to ID A1J1.11
From ID A1P1.11	to ID P1-11 (DC4-LO)

Step 104

Description:
This step verifies DCPS4 connections to BUS2. DCPS4 will be set to 15VDC and the DMM is used to measure output voltage UL= 28.5 VDC, LL= 27.5 VDC.

Connection Path is as follows:

From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 105

Description:
This step verifies DCPS5 connections to the DMM. DCPS5 will be set to 15VDC and the DMM is used to measure output voltage UL= 15.5 VDC, LL= 14.5 VDC.

Connection Path is as follows:

From ID P1-13 (DC5-HI)	to ID A1P1.14
From ID A1J1.14	to ID A1J2.2
From ID A1P2.2	to ID P10-24 (S101-9)
From ID P10-88 (S101-10)	to ID A1P2.14
From ID A1J2.14	to ID A1J7.1
From ID A1P7.1	to ID P10-1 (S301-7)
From ID P10-35 (S301-8)	to ID A1P7.46
From ID A1J7.46	to ID A1J15.49
From ID A1P15.49	to P20-2 (DMM-HI)
From ID P1-14 (DC5-LO)	to ID A1P1.7
From ID A1J1.7	to ID A1J7.12
From ID A1P7.12	to ID P10-66 (S301-6)
From ID P10-33 (S301-5)	to ID A1P7.40
From ID A1J7.40	to ID A1J15.50

From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 106

Description:
This step verifies DCPS6 connections to the DMM. DCPS6 will be set to 15VDC and the DMM is used to measure output voltage UL= 15.5 VDC, LL= 14.5 VDC.

Connection Path is as follows:

From ID P1-16 (DC6-HI)	to ID A1P1.15
From ID A1J1.15	to ID A1J2.20
From ID A1P2.20	to ID P10-25 (S101-13)
From ID P10-89 (S101-14)	to ID A1P2.33
From ID A1J2.33	to ID A1J7.5
From ID A1P7.5	to ID P10-97 (S301-9)
From ID P10-2 (S301-10)	to ID A1P7.48
From ID A1J7.48	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID P1-17 (DC6-LO)	to ID A1P1.12
From ID A1J1.12	to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 107

Description:
This step verifies DCPS10 connections to the DMM. DCPS10 will be set to 15VDC and the DMM is used to measure output voltage UL= 15.5 VDC, LL= 14.5 VDC.

Connection Path is as follows:

From ID P1-28 (DC10-HI)	to ID A1P1.6
From ID A1J1.6	to ID A1J2.11
From ID A1P2.11	to ID P10-217 (S101-16)
From ID P10-153 (S101-15)	to ID A1P2.29
From ID A1J2.29	to ID A1J7.2
From ID A1P7.2	to ID P10-199 (S301-41)
From ID P10-41 (S301-42)	to ID A1P8.30
From ID A1J8.30	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID P1-29 (DC10-LO)	to ID A1P1.4
From ID A1J1.4	to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)

```
      From ID P10-229 (S301-24)         to ID A1P7.36
      From ID A1J7.36                   to GROUND

Step 108

Description:
This step verifies DCPS10 through resistor R103. DCPS10 will be set
to 15VDC and the DMM is used to measure output voltage UL= 15.5 VDC,
LL= 14.5 VDC.

      Connection Path is as follows:

      From ID P1-28 (DC10-HI)           to ID A1P1.6
      From ID A1J1.6                    to ID A1J2.1
      From ID A1P2.1                    to ID P10-90 (S101-18)
      From ID P10-26 (S101-17)          to ID A1P2.11
      From ID A1J2.11                   to ID A1J4.1
      From ID A1P4.1                    to ID R103.2

      From ID P1-29 (DC10-LO)           to ID A1P1.4
      From ID A1J1.4                    to GROUND

      From ID R103.1                    to ID A1P4.13
      From ID A1J4.13                   to ID A1J7.7
      From ID A1P7.7                    to ID P10-135 (S301-43)
      From ID P10-136 (S301-44)         to ID A1P8.32
      From ID A1J8.32                   to ID A1J15.49
      From ID A1P15.49                  to ID P20-2 (DMM-HI)

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J7.38
      From ID A1P7.38                   to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)         to ID A1P7.36
      From ID A1J7.36                   to GROUND

Step 109

Description:
This step verifies DCPS10 through resistor R102. DCPS10 will be set
to 15VDC and the DMM is used to measure output voltage UL= 15.5 VDC,
LL= 14.5 VDC.

      Connection Path is as follows:

      From ID P1-28 (DC10-HI)           to ID A1P1.6
      From ID A1J1.6                    to ID A1J7.11
      From ID A1P7.11                   to ID P10-4 (S301-18)
      From ID P10-67 (S301-17)          to ID A1P7.27
      From ID A1J7.27                   to ID A1J4.19
      From ID A1P4.19                   to ID R102.2

      From ID P1-29 (DC10-LO)           to ID A1P1.4
      From ID A1J1.4                    to GROUND
```

From ID R102.1	to ID A1P4.14
From ID A1J4.14	to ID A1J7.22
From ID A1P7.22	to ID P10-167 (S301-45)
From ID P10-201 (S301-46)	to ID A1P8.34
From ID A1J8.34	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 110

Description:
This step verifies DCPS9 connections to BUS7. DCPS9 will be set to 15VDC and the DMM is used to measure output voltage UL= 15.5 VDC, LL= 14.5 VDC.

Connection Path is as follows:

From ID P1-25 (DC9-HI)	to ID A1P1.13
From ID A1J1.13	to ID A1J7.14
From ID A1P7.14	to ID P10-197 (S301-29)
From ID P10-198 (S301-30)	to ID A1P6.24
From ID A1J6.24	to ID BUS 7
From ID P1-26 (DC9-LO)	to ID A1P1.5
From ID A1J1.5	to ID A1J7.16
From ID A1P7.16	to ID P10-163 (S301-12)
From ID P10-98 (S301-11)	to ID A1P7.32
From ID A1J7.32	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-173 (S503-9)	to ID A1P6.28
From ID A1J6.28	to ID BUS 7
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 111

Description:
This step verifies DCPS9 connections to BUS7 and BUS 8. DCPS9 will be set to 15VDC and the DMM is used to measure output voltage UL= 15.5 VDC, LL= 14.5 VDC.

Connection Path is as follows:

```
      From ID P1-25 (DC9-HI)           to ID A1P1.13
      From ID A1J1.13                  to ID A1J7.14
      From ID A1P7.14                  to ID P10-197 (S301-29)
      From ID P10-198 (S301-30)        to ID A1P6.24
      From ID A1J6.24                  to ID BUS 7

      From ID P1-26 (DC9-LO)           to ID A1P1.5
      From ID A1J1.5                   to ID A1J7.18
      From ID A1P7.18                  to ID P10-133 (S301-27)
      From ID P10-70 (S301-28)         to ID A1P6.12
      From ID A1J6.12                  to ID BUS 8

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-173 (S503-9)         to ID A1P6.28
      From ID A1J6.28                  to ID BUS 7

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-205 (S503-10)        to ID A1P6.16
      From ID A1J6.16                  to ID BUS 8
```

Step 112

Description:
This step verifies the value of ITA resistor R1 equals 10K Ohms +/- 5% with one end connected to ground. The DMM resource will be used to measure resistance UL= 10500 OHM, LL= 9500 OHM.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-137 (S503-7)         to ID A1P6.47
      From ID A1J6.47                  to ID BUS 5

      From ID BUS 5                    to ID A1J8.33
      From ID A1P8.33                  to ID P10-76 (S301-71)
      From ID P10-142 (S301-72)        to ID A1P8.1
      From ID A1J8.1                   to ID R1.1
      From ID R1.2                     to GROUND

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J7.38
      From ID A1P7.38                  to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)        to ID A1P7.36
      From ID A1J7.36                  to GROUND
```

Step 113

Description:

This step verifies the value of ITA resistor R2 equals 15K Ohms+/-5% with one end connected to ground. The DMM resource will be used to measure resistance UL= 15750 OHM, LL= 14250 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID BUS 6	to ID A1J8.36
From ID A1P8.36	to ID P10-14 (S301-73)
From ID P10-15 (S301-74)	to ID A1P8.3
From ID A1J8.3	to ID R2.1
From ID R2.2	to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 114

Description:
This step verifies the value of ITA resistor R3 equals 15K Ohms+/-5% with one end connected to +15VDC. The DMM resource will be used to measure resistance UL= 15750 OHM, LL= 14250 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J8.29
From ID A1P8.29	to ID P10-204 (S301-67)
From ID P10-174 (S301-68)	to ID A1P8.4
From ID A1J8.4	to ID A1J1.2
From ID A1P1.2	to ID P1-4 (DC2-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID BUS 5	to ID A1J8.35
From ID A1P8.35	to ID P10-79 (S301-75)
From ID P10-80 (S301-76)	to ID A1P8.11

From ID A1J8.11	to ID R3.1
From ID R3.2	to +15V

Step 115

Description:
This step verifies the value of ITA resistor R4 equals 909 Ohms+/-5% with one end connected to +15VDC. The DMM resource will be used to measure resistance UL= 956 OHM, LL= 864 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J8.29
From ID A1P8.29	to ID P10-204 (S301-67)
From ID P10-174 (S301-68)	to ID A1P8.4
From ID A1J8.4	to ID A1J1.2
From ID A1P1.2	to ID P1-4 (DC2-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID BUS 6	to ID A1J8.38
From ID A1P8.38	to ID P10-47 (S301-77)
From ID P10-208 (S301-78)	to ID A1P8.13
From ID A1J8.13	to ID R4.1
From ID R4.2	to +15V

Step 116

Description:
This step verifies the value of ITA resistor R5 equals 2.2K Ohms+/-5% with one end connected to +15VDC. The DMM resource will be used to measure resistance UL= 2310 OHM, LL= 2090 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J8.29
From ID A1P8.29	to ID P10-204 (S301-67)
From ID P10-174 (S301-68)	to ID A1P8.4

From ID A1J8.4	to ID A1J1.2
From ID A1P1.2	to ID P1-4 (DC2-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID BUS 5	to ID A1J8.37
From ID A1P8.37	to ID P10-143 (S301-79)
From ID P10-209 (S301-80)	to ID A1P8.15
From ID A1J8.15	to ID R5.1
From ID R5.2	to +15V

Step 117

Description:
This step verifies the value of ITA resistor R6 equals 5.6K Ohms+/-5% with one end connected to +15VDC. The DMM resource will be used to measure resistance UL= 5880 OHM, LL= 5320 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J8.29
From ID A1P8.29	to ID P10-204 (S301-67)
From ID P10-174 (S301-68)	to ID A1P8.4
From ID A1J8.4	to ID A1J1.2
From ID A1P1.2	to ID P1-4 (DC2-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID BUS 6	to ID A1J8.40
From ID A1P8.40	to ID P10-175 (S301-81)
From ID P10-48 (S301-82)	to ID A1P8.17
From ID A1J8.17	to ID R6.1
From ID R6.2	to +15V

Step 118

Description:
This step verifies the value of ITA resistor R8 equals 5.1K Ohms+/-5% with one end connected to +15VDC. The DMM resource will be used to measure resistance UL= 5355 OHM, LL= 4845 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J8.29
From ID A1P8.29	to ID P10-204 (S301-67)
From ID P10-174 (S301-68)	to ID A1P8.4
From ID A1J8.4	to ID A1J1.2
From ID A1P1.2	to ID P1-4 (DC2-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID BUS 5	to ID A1J8.39
From ID A1P8.39	to ID P10-178 (S301-85)
From ID P10-243 (S301-86)	to ID A1P8.19
From ID A1J8.19	to ID R8.1
From ID R8.2	to +15V

Step 119

Description:
This step verifies the value of ITA diode CR1 equals 5.1VDC+/-5% with one end connected to +15VDC. Power Supply DC2 will be used to apply +15VDC, and the DMM resource will be used to measure voltage across CR1. UL= 5.4 VOLTS, LL= 4.8 VOLTS

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID BUS 5	to ID A1J8.39
From ID A1P8.39	to ID P10-178 (S301-85)
From ID P10-243 (S301-86)	to ID A1P8.19
From ID A1J8.19	to ID R8.1
From ID R8.2	to +15V
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6

From ID BUS 6	to ID A1J8.44
From ID A1P8.44	to ID P10-83 (S301-88)
From ID P10-210 (S301-87)	to ID A1P8.21
From ID A1J8.21	to ID CR1.A
From ID CR1.K	to ID R8.1
From ID BUS 6	to ID A1J6.44
From ID A1P6.44	to ID P10-222 (S402-3)
From ID P10-94 (S402-1)	to ID A1P6.9
From ID A1J6.9	to INSTR-RTN
From INSTR-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND

Step 120

Description:
This step verifies the value of ITA resistor R9 equals 2.2K Ohms+/-5% with one end connected to Ground. The DMM resource will be used to measure resistance UL= 2310 OHM, LL= 2090 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID BUS 5	to ID A1J8.41
From ID A1P8.41	to ID P10-49 (S301-89)
From ID P10-82 (S301-90)	to ID A1P8.7
From ID A1J8.7	to ID R9.1
From ID R9.2	to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 121

Description:
This step verifies the value of ITA resistor R7 equals 5.6K Ohms+/-5% with one end connected to Ground. The DMM resource will be used to measure resistance UL= 5880 OHM, LL= 5320 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28

```
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-170 (S503-8)         to ID A1P6.38
      From ID A1J6.38                  to ID BUS 6

      From ID BUS 6                    to ID A1J8.42
      From ID A1P8.42                  to ID P10-78 (S301-83)
      From ID P10-81 (S301-84)         to ID A1P8.5
      From ID A1J8.5                   to ID R7.1
      From ID R7.2                     to GROUND

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J7.38
      From ID A1P7.38                  to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)        to ID A1P7.36
      From ID A1J7.36                  to GROUND
```

Step 122

Description:
This step verifies the value of ITA resistor R10 equals 11.5K Ohms+/-5% with one end connected to Ground. The DMM resource will be used to measure resistance UL= 12075 OHM, LL= 10925 OHM.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-137 (S503-7)         to ID A1P6.47
      From ID A1J6.47                  to ID BUS 5

      From ID BUS 5                    to ID A1J8.43
      From ID A1P8.43                  to ID P10-114 (S301-91)
      From ID P10-51 (S301-92)         to ID A1P8.9
      From ID A1J8.9                   to ID R10.1
      From ID R10.2                    to GROUND

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J7.38
      From ID A1P7.38                  to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)        to ID A1P7.36
      From ID A1J7.36                  to GROUND
```

Step 123

Description:
This step verifies the value of ITA resistor R111 is 324 Ohms+/-5%, with one end connected to +28VDC. The DMM resource will be used to measure resistance UL= 340 OHM, LL= 308 OHM.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
```

```
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-12 (S503-4)          to ID A1P6.23
      From ID A1J6.23                  to ID BUS 2

      From ID BUS 2                    to ID A1J8.31
      From ID A1P8.31                  to ID P10-44 (S301-69)
      From ID P10-141 (S301-70)        to ID A1P8.2
      From ID A1J8.2                   to ID A1J1.3
      From ID A1P1.3                   to ID P1-10 (DC4-HI)

      From +28V                        to ID A1J4.9
      From ID A1P4.9                   to ID R111.2
      From ID R111.1                   to ID A1P4.15
      From ID A1J4.15                  to ID A1J7.23
      From ID A1P7.23                  to ID P10-7 (S301-47)
      From ID P10-73 (S301-48)         to ID A1P8.47
      From ID A1J8.47                  to ID BUS 5

      From ID BUS 5                    to ID A1J6.47
      From ID A1P6.47                  to ID P10-137 (S503-7)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 124

Description:
This step verifies the value of ITA resistor R109 is 698 Ohms+/-5%, with one end connected to +28VDC. The DMM resource will be used to measure resistance UL= 733 OHM, LL= 663 OHM.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-12 (S503-4)          to ID A1P6.23
      From ID A1J6.23                  to ID BUS 2

      From ID BUS 2                    to ID A1J8.31
      From ID A1P8.31                  to ID P10-44 (S301-69)
      From ID P10-141 (S301-70)        to ID A1P8.2
      From ID A1J8.2                   to ID A1J1.3
      From ID A1P1.3                   to ID P1-10 (DC4-HI)

      From +28V                        to ID A1J4.9
      From ID A1P4.9                   to ID R109.2
      From ID R109.1                   to ID A1P4.16
      From ID A1J4.16                  to ID A1J7.24
      From ID A1P7.24                  to ID P10-42 (S301-49)
      From ID P10-171 (S301-50)        to ID A1P8.48
      From ID A1J8.48                  to ID BUS 6

      From ID BUS 6                    to ID A1J6.38
```

From ID A1P6.38	to ID P10-170 (S503-8)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 125

Description:
This step verifies the value of ITA resistor R110 is 80.6 Ohms+/-5%, with one end connected to Ground. The DMM resource will be used to measure resistance UL= 84.6 OHM, LL= 76.6 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID BUS 5	to ID A1J8.49
From ID A1P8.49	to ID P10-202 (S301-52)
From ID P10-234 (S301-51)	to ID A1P7.25
From ID A1J7.25	to ID A1J4.17
From ID A1P4.17	to ID R110.1
From ID R110.2	to ID A1P4.21
From ID R110.2	to ID A1P4.22
From ID A1J4.21	to GROUND
From ID A1J4.22	to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 126

Description:
This step verifies the value of ITA resistor R108 is 698 Ohms+/-5%, with one end connected to Ground. The DMM resource will be used to measure resistance UL= 733 OHM, LL= 663 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID BUS 6	to ID A1J8.50
From ID A1P8.50	to ID P10-138 (S301-54)
From ID P10-9 (S301-53)	to ID A1P7.26
From ID A1J7.26	to ID A1J4.18

From ID A1P4.18	to ID R108.1
From ID R108.2	to ID A1P4.10
From ID A1J4.10	to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 127

Description:
This step verifies the value of ITA resistor R11 is 120 Ohms+/-5%. The DMM resource will be used to measure resistance UL= 126 OHM, LL= 114 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID BUS 5	to ID A1J8.45
From ID A1P8.45	to ID P10-148 (S301-96)
From ID P10-50 (S301-95)	to ID A1P8.25
From ID A1J8.25	to ID R11.1
From ID R11.2	to ID A1J8.23
From ID A1P8.23	to ID P10-179 (S301-94)
From ID P10-145 (S301-93)	to ID A1P8.46
From ID A1J8.46	to ID BUS 6
From ID BUS 6	to ID A1J6.38
From ID A1P6.38	to ID P10-170 (S503-8)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 128

Description:
This step verifies the connection from S501-3 to BUS 1 and from S501-4 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13

```
      From ID A1J6.13                  to ID BUS 1

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-12 (S503-4)          to ID A1P6.23
      From ID A1J6.23                  to ID BUS 2

      From ID P23-12 (ARB-OUT)         to ID A1P24.1
      From ID A1J24.1                  to ID A1J6.5
      From ID A1P6.5                   to ID P10-194 (S501-1)
      From ID P10-164 (S501-3)         to ID A1P7.29
      From ID A1J7.29                  to ID BUS 1

      From ID P23-12 (ARB-OUT)         to ID A1P24.1
      From ID A1J24.1                  to ID A1J6.5
      From ID A1P6.5                   to ID P10-194 (S501-1)
      From ID P10-195 (S501-4)         to ID A1P7.33
      From ID A1J7.33                  to ID BUS 2

Step 129

Description:
This step verifies the connection from S501-5 to BUS 3 and from
S501-6 to BUS 4. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)          to ID A1P6.31
      From ID A1J6.31                  to ID BUS 3

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-74 (S503-6)          to ID A1P6.39
      From ID A1J6.39                  to ID BUS 4

      From ID P23-12 (ARB-OUT)         to ID A1P24.1
      From ID A1J24.1                  to ID A1J6.5
      From ID A1P6.5                   to ID P10-194 (S501-1)
      From ID P10-225 (S501-5)         to ID A1P7.37
      From ID A1J7.37                  to ID BUS 3

      From ID P23-12 (ARB-OUT)         to ID A1P24.1
      From ID A1J24.1                  to ID A1J6.5
      From ID A1P6.5                   to ID P10-194 (S501-1)
      From ID P10-193 (S501-6)         to ID A1P7.41
      From ID A1J7.41                  to ID BUS 4
```

Step 130

Description:
This step verifies the connection from S501-7 to BUS 5 and from S501-8 to BUS 6. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID A1J24.1	to ID A1J6.5
From ID A1P6.5	to ID P10-194 (S501-1)
From ID P10-161 (S501-7)	to ID A1P7.45
From ID A1J7.45	to ID BUS 5
From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID A1J24.1	to ID A1J6.5
From ID A1P6.5	to ID P10-194 (S501-1)
From ID P10-129 (S501-8)	to ID A1P7.47
From ID A1J7.47	to ID BUS 6

Step 131

Description:
This step verifies the connection from S501-9 to BUS 7 and from S501-10 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-173 (S503-9)	to ID A1P6.28
From ID A1J6.28	to ID BUS 7
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-205 (S503-10)	to ID A1P6.16
From ID A1J6.16	to ID BUS 8

```
      From ID P23-12 (ARB-OUT)          to ID A1P24.1
      From ID A1J24.1                   to ID A1J6.5
      From ID A1P6.5                    to ID P10-194 (S501-1)
      From ID P10-68 (S501-9)           to ID A1P7.49
      From ID A1J7.49                   to ID BUS 7

      From ID P23-12 (ARB-OUT)          to ID A1P24.1
      From ID A1J24.1                   to ID A1J6.5
      From ID A1P6.5                    to ID P10-194 (S501-1)
      From ID P10-36 (S501-10)          to ID A1P7.50
      From ID A1J7.50                   to ID BUS 8

Step 132

Description:
This step verifies the connection from S502-3 to BUS 1 and from
S502-4 to BUS 2. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J8.26
      From ID A1P8.26                   to ID P10-139 (S503-2)
      From ID P10-12 (S503-4)           to ID A1P6.23
      From ID A1J6.23                   to ID BUS 2

      From ID P19-2 (DSO-IN2)           to ID A1P18.1
      From ID A1J18.1                   to ID R25.1
      From ID R25.2                     to ID A1J6.2
      From ID A1P6.2                    to ID P10-39 (S502-1)
      From ID P10-168 (S502-3)          to ID A1P7.31
      From ID A1J7.31                   to ID BUS 1

      From ID P19-2 (DSO-IN2)           to ID A1P18.1
      From ID A1J18.1                   to ID A1J6.2
      From ID A1P6.2                    to ID P10-39 (S502-1)
      From ID P10-72 (S502-4)           to ID A1P7.35
      From ID A1J7.35                   to ID BUS 2

Step 133

Description:
This step verifies the connection from S502-5 to BUS 3 and from
S502-6 to BUS 4. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:
```

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)          to ID A1P6.31
      From ID A1J6.31                  to ID BUS 3

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-74 (S503-6)          to ID A1P6.39
      From ID A1J6.39                  to ID BUS 4

      From ID P19-2 (DSO-IN2)          to ID A1P18.1
      From ID A1J18.1                  to ID A1J6.2
      From ID A1P6.2                   to ID P10-39 (S502-1)
      From ID P10-6 (S502-5)           to ID A1P7.39
      From ID A1J7.39                  to ID BUS 3

      From ID P19-2 (DSO-IN2)          to ID A1P18.1
      From ID A1J18.1                  to ID A1J6.2
      From ID A1P6.2                   to ID P10-39 (S502-1)
      From ID P10-38 (S502-6)          to ID A1P7.43
      From ID A1J7.43                  to ID BUS 4

Step 134

Description:
This step verifies the connection from S502-7 to BUS 5 and from
S502-8 to BUS 6. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-137 (S503-7)         to ID A1P6.47
      From ID A1J6.47                  to ID BUS 5

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-170 (S503-8)         to ID A1P6.38
      From ID A1J6.38                  to ID BUS 6

      From ID P19-2 (DSO-IN2)          to ID A1P18.1
      From ID A1J18.1                  to ID A1J6.2
      From ID A1P6.2                   to ID P10-39 (S502-1)
      From ID P10-230 (S502-7)         to ID A1P6.46
      From ID A1J6.46                  to ID BUS 5
      From ID P19-2 (DSO-IN2)          to ID A1P18.1
      From ID A1J18.1                  to ID A1J6.2
      From ID A1P6.2                   to ID P10-39 (S502-1)
      From ID P10-5 (S502-8)           to ID A1P6.36
```

```
      From ID A1J6.36                   to ID BUS 6

Step 135

Description:
This step verifies the connection from S502-9 to BUS 7 and from
S502-10 to BUS 8. The DMM resource will be used to measure
resistance UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-173 (S503-9)          to ID A1P6.28
      From ID A1J6.28                   to ID BUS 7

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J8.26
      From ID A1P8.26                   to ID P10-139 (S503-2)
      From ID P10-205 (S503-10)         to ID A1P6.16
      From ID A1J6.16                   to ID BUS 8

      From ID P19-2 (DSO-IN2)           to ID A1P18.1
      From ID A1J18.1                   to ID A1J6.2
      From ID A1P6.2                    to ID P10-39 (S502-1)
      From ID P10-232 (S502-9)          to ID A1P6.26
      From ID A1J6.26                   to ID BUS 7

      From ID P19-2 (DSO-IN2)           to ID A1P18.1
      From ID A1J18.1                   to ID A1J6.2
      From ID A1P6.2                    to ID P10-39 (S502-1)
      From ID P10-105 (S502-10)         to ID A1P6.14
      From ID A1J6.14                   to ID BUS 8

Step 136

Description:
This step verifies the connection from S504-3 to BUS 1 and from
S504-4 to BUS 2. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J8.26
      From ID A1P8.26                   to ID P10-139 (S503-2)
      From ID P10-12 (S503-4)           to ID A1P6.23
```

```
      From ID A1J6.23                  to ID BUS 2

      From ID P19-1 (DSO-IN1)          to ID A1P17.1
      From ID A1J17.1                  to ID A1J6.1
      From ID A1P6.1                   to ID P10-177 (S504-1)
      From ID P10-18 (S504-3)          to ID A1P6.15
      From ID A1J6.15                  to ID BUS 1

      From ID P19-1 (DSO-IN1)          to ID A1P17.1
      From ID A1J17.1                  to ID A1J6.1
      From ID A1P6.1                   to ID P10-177 (S504-1)
      From ID P10-17 (S504-4)          to ID A1P6.25
      From ID A1J6.25                  to ID BUS 2

Step 137

Description:
This step verifies the connection from S504-5 to BUS 3 and from
S504-6 to BUS 4. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)          to ID A1P6.31
      From ID A1J6.31                  to ID BUS 3

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-74 (S503-6)          to ID A1P6.39
      From ID A1J6.39                  to ID BUS 4

      From ID P19-1 (DSO-IN1)          to ID A1P17.1
      From ID A1J17.1                  to ID A1J6.1
      From ID A1P6.1                   to ID P10-177 (S504-1)
      From ID P10-176 (S504-5)         to ID A1P6.33
      From ID A1J6.33                  to ID BUS 3

      From ID P19-1 (DSO-IN1)          to ID A1P17.1
      From ID A1J17.1                  to ID A1J6.1
      From ID A1P6.1                   to ID P10-177 (S504-1)
      From ID P10-16 (S504-6)          to ID A1P6.41
      From ID A1J6.41                  to ID BUS 4

Step 138

Description:
This step verifies the connection from S504-7 to BUS 5 and from
S504-8 to BUS 6. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.
```

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P19-1 (DSO-IN1)	to ID A1P17.1
From ID A1J17.1	to ID A1J6.1
From ID A1P6.1	to ID P10-177 (S504-1)
From ID P10-207 (S504-7)	to ID A1P6.49
From ID A1J6.49	to ID BUS 5
From ID P19-1 (DSO-IN1)	to ID A1P17.1
From ID A1J17.1	to ID A1J6.1
From ID A1P6.1	to ID P10-177 (S504-1)
From ID P10-144 (S504-8)	to ID A1P6.40
From ID A1J6.40	to ID BUS 6

Step 139

Description:
This step verifies the connection from S504-9 to BUS 7 and from S504-10 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-173 (S503-9)	to ID A1P6.28
From ID A1J6.28	to ID BUS 7
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-205 (S503-10)	to ID A1P6.16
From ID A1J6.16	to ID BUS 8
From ID P19-1 (DSO-IN1)	to ID A1P17.1
From ID A1J17.1	to ID A1J6.1
From ID A1P6.1	to ID P10-177 (S504-1)
From ID P10-211 (S504-9)	to ID A1P6.30
From ID A1J6.30	to ID BUS 7
From ID P19-1 (DSO-IN1)	to ID A1P17.1

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      From ID A1J17.1                  to ID A1J6.1
      From ID A1P6.1                   to ID P10-177 (S504-1)
      From ID P10-146 (S504-10)        to ID A1P6.18
      From ID A1J6.18                  to ID BUS 8

Step 140

Description:
This step verifies the connection from S505-3 to BUS 1 and from
S505-4 to BUS 2. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-12 (S503-4)          to ID A1P6.23
      From ID A1J6.23                  to ID BUS 2

      From ID P19-17 (CT-ARM)          to ID A1P20.1
      From ID A1P20.1                  to ID A1J6.6
      From ID A1P6.6                   to ID P10-52 (S505-1)
      From ID P10-180 (S505-3)         to ID A1P6.17
      From ID A1J6.17                  to ID BUS 1

      From ID P19-17 (CT-ARM)          to ID A1P20.1
      From ID A1P20.1                  to ID A1J6.6
      From ID A1P6.6                   to ID P10-52 (S505-1)
      From ID P10-147 (S505-4)         to ID A1P6.27
      From ID A1J6.27                  to ID BUS 2

Step 141

Description:
This step verifies the connection from S505-5 to BUS 3 and from
S505-6 to BUS 4. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)          to ID A1P6.31
      From ID A1J6.31                  to ID BUS 3

      From ID P20-3 (DMM-LO)           to ID A1P15.50
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      From ID A1J15.50                to ID A1J8.26
      From ID A1P8.26                 to ID P10-139 (S503-2)
      From ID P10-74 (S503-6)         to ID A1P6.39
      From ID A1J6.39                 to ID BUS 4

      From ID P19-17 (CT-ARM)         to ID A1P20.1
      From ID A1P20.1                 to ID A1J6.6
      From ID A1P6.6                  to ID P10-52 (S505-1)
      From ID P10-212 (S505-5)        to ID A1P6.35
      From ID A1J6.35                 to ID BUS 3

      From ID P19-17 (CT-ARM)         to ID A1P20.1
      From ID A1P20.1                 to ID A1J6.6
      From ID A1P6.6                  to ID P10-52 (S505-1)
      From ID P10-84 (S505-6)         to ID A1P6.43
      From ID A1J6.43                 to ID BUS 4

Step 142

Description:
This step verifies the connection from S505-7 to BUS 5 and from
S505-8 to BUS 6. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-137 (S503-7)        to ID A1P6.47
      From ID A1J6.47                 to ID BUS 5

      From ID P20-3 (DMM-LO)          to ID A1P15.50
      From ID A1J15.50                to ID A1J8.26
      From ID A1P8.26                 to ID P10-139 (S503-2)
      From ID P10-170 (S503-8)        to ID A1P6.38
      From ID A1J6.38                 to ID BUS 6

      From ID P19-17 (CT-ARM)         to ID A1P20.1
      From ID A1P20.1                 to ID A1J6.6
      From ID A1P6.6                  to ID P10-52 (S505-1)
      From ID P10-115 (S505-7)        to ID A1P6.50
      From ID A1J6.50                 to ID BUS 5

      From ID P19-17 (CT-ARM)         to ID A1P20.1
      From ID A1P20.1                 to ID A1J6.6
      From ID A1P6.6                  to ID P10-52 (S505-1)
      From ID P10-20 (S505-8)         to ID A1P6.42
      From ID A1J6.42                 to ID BUS 6

Step 143

Description:
This step verifies the connection from S505-9 to BUS 7 and from
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S505-10 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-173 (S503-9)	to ID A1P6.28
From ID A1J6.28	to ID BUS 7
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-205 (S503-10)	to ID A1P6.16
From ID A1J6.16	to ID BUS 8
From ID P19-17 (CT-ARM)	to ID A1P20.1
From ID A1P20.1	to ID A1J6.6
From ID A1P6.6	to ID P10-52 (S505-1)
From ID P10-116 (S505-9)	to ID A1P6.32
From ID A1J6.32	to ID BUS 7
From ID P19-17 (CT-ARM)	to ID A1P20.1
From ID A1P20.1	to ID A1J6.6
From ID A1P6.6	to ID P10-52 (S505-1)
From ID P10-19 (S505-10)	to ID A1P6.20
From ID A1J6.20	to ID BUS 8

Step 144

Description:
This step verifies the connection from S506-3 to BUS 1 and from S506-4 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-164 (S506-3)	to ID A1P9.23

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      From ID A1J9.23                  to ID BUS 1

      From ID P12-76 (S701-1)          to ID A1P12.50
      From ID A1J12.50                 to ID A1J10.3
      From ID A1P10.3                  to ID P11-194 (S506-1)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

Step 145

Description:
This step verifies the connection from S506-5 to BUS 3 and from
S506-6 to BUS 4. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)          to ID A1P6.31
      From ID A1J6.31                  to ID BUS 3

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-74 (S503-6)          to ID A1P6.39
      From ID A1J6.39                  to ID BUS 4

      From ID P12-76 (S701-1)          to ID A1P12.50
      From ID A1J12.50                 to ID A1J10.3
      From ID A1P10.3                  to ID P11-194 (S506-1)
      From ID P11-225 (S506-5)         to ID A1P9.45
      From ID A1J9.45                  to ID BUS 3

      From ID P12-76 (S701-1)          to ID A1P12.50
      From ID A1J12.50                 to ID A1J10.3
      From ID A1P10.3                  to ID P11-194 (S506-1)
      From ID P11-193 (S506-6)         to ID A1P9.50
      From ID A1J9.50                  to ID BUS 4

Step 146

Description:
This step verifies the connection from S506-7 to BUS 5 and from
S506-8 to BUS 6. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-137 (S503-7)         to ID A1P6.47
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      From ID A1J6.47                  to ID BUS 5

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-170 (S503-8)         to ID A1P6.38
      From ID A1J6.38                  to ID BUS 6

      From ID P12-76 (S701-1)          to ID A1P12.50
      From ID A1J12.50                 to ID A1J10.3
      From ID A1P10.3                  to ID P11-194 (S506-1)
      From ID P11-161 (S506-7)         to ID A1P9.40
      From ID A1J9.40                  to ID BUS 5

      From ID P12-76 (S701-1)          to ID A1P12.50
      From ID A1J12.50                 to ID A1J10.3
      From ID A1P10.3                  to ID P11-194 (S506-1)
      From ID P11-129 (S506-8)         to ID A1P9.30
      From ID A1J9.30                  to ID BUS 6

Step 147

Description:
This step verifies the connection from S506-9 to BUS 7 and from
S506-10 to BUS 8. The DMM resource will be used to measure
resistance UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-173 (S503-9)         to ID A1P6.28
      From ID A1J6.28                  to ID BUS 7

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-205 (S503-10)        to ID A1P6.16
      From ID A1J6.16                  to ID BUS 8

      From ID P12-76 (S701-1)          to ID A1P12.50
      From ID A1J12.50                 to ID A1J10.3
      From ID A1P10.3                  to ID P11-194 (S506-1)
      From ID P11-68 (S506-9)          to ID A1P9.20
      From ID A1J9.20                  to ID BUS 7

      From ID P12-76 (S701-1)          to ID A1P12.50
      From ID A1J12.50                 to ID A1J10.3
      From ID A1P10.3                  to ID P11-194 (S506-1)
      From ID P11-36 (S506-10)         to ID A1P9.10
      From ID A1J9.10                  to ID BUS 8
```

Step 148

Description:
This step verifies the connection from S507-3 to BUS 1 and from S507-4 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-168 (S507-3)	to ID A1P9.17
From ID A1J9.17	to ID BUS 1
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2

Step 149

Description:
This step verifies the connection from S507-5 to BUS 3 and from S507-6 to BUS 4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-74 (S503-6)	to ID A1P6.39
From ID A1J6.39	to ID BUS 4

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      From ID P12-20 (S201-3)          to ID A1P12.46
      From ID A1J12.46                 to ID A1J10.2
      From ID A1P10.2                  to ID P11-39 (S507-1)
      From ID P11-6 (S507-5)           to ID A1P9.39
      From ID A1J9.39                  to ID BUS 3

      From ID P12-20 (S201-3)          to ID A1P12.46
      From ID A1J12.46                 to ID A1J10.2
      From ID A1P10.2                  to ID P11-39 (S507-1)
      From ID P11-38 (S507-6)          to ID A1P9.44
      From ID A1J9.44                  to ID BUS 4

Step 150

Description:
This step verifies the connection from S507-7 to BUS 5 and from
S507-8 to BUS 6. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-137 (S503-7)         to ID A1P6.47
      From ID A1J6.47                  to ID BUS 5

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-170 (S503-8)         to ID A1P6.38
      From ID A1J6.38                  to ID BUS 6

      From ID P12-20 (S201-3)          to ID A1P12.46
      From ID A1J12.46                 to ID A1J10.2
      From ID A1P10.2                  to ID P11-39 (S507-1)
      From ID P11-230 (S507-7)         to ID A1P9.34
      From ID A1J9.34                  to ID BUS 5

      From ID P12-20 (S201-3)          to ID A1P12.46
      From ID A1J12.46                 to ID A1J10.2
      From ID A1P10.2                  to ID P11-39 (S507-1)
      From ID P11-5 (S507-8)           to ID A1P9.24
      From ID A1J9.24                  to ID BUS 6

Step 151

Description:
This step verifies the connection from S507-9 to BUS 7 and from
S507-10 to BUS 8. The DMM resource will be used to measure
resistance UL= 10 OHM, LL= 0 OHM.
      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
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      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-173 (S503-9)        to ID A1P6.28
      From ID A1J6.28                 to ID BUS 7

      From ID P20-3 (DMM-LO)          to ID A1P15.50
      From ID A1J15.50                to ID A1J8.26
      From ID A1P8.26                 to ID P10-139 (S503-2)
      From ID P10-205 (S503-10)       to ID A1P6.16
      From ID A1J6.16                 to ID BUS 8

      From ID P12-20 (S201-3)         to ID A1P12.46
      From ID A1J12.46                to ID A1J10.2
      From ID A1P10.2                 to ID P11-39 (S507-1)
      From ID P11-232 (S507-9)        to ID A1P9.14
      From ID A1J9.14                 to ID BUS 7

      From ID P12-20 (S201-3)         to ID A1P12.46
      From ID A1J12.46                to ID A1J10.2
      From ID A1P10.2                 to ID P11-39 (S507-1)
      From ID P11-105 (S507-10)       to ID A1P9.4
      From ID A1J9.4                  to ID BUS 8

Step 152

Description:
This step verifies the connection from S508-3 to BUS 1 and from
S508-4 to BUS 2. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID P20-3 (DMM-LO)          to ID A1P15.50
      From ID A1J15.50                to ID A1J8.26
      From ID A1P8.26                 to ID P10-139 (S503-2)
      From ID P10-12 (S503-4)         to ID A1P6.23
      From ID A1J6.23                 to ID BUS 2

      From ID P12-16 (S201-1)         to ID A1P12.42
      From ID A1J12.42                to ID A1J10.6
      From ID A1P10.6                 to ID P11-203 (S508-1)
      From ID P11-77 (S508-3)         to ID A1P9.15
      From ID A1J9.15                 to ID BUS 1

      From ID P12-16 (S201-1)         to ID A1P12.42
      From ID A1J12.42                to ID A1J10.6
      From ID A1P10.6                 to ID P11-203 (S508-1)
      From ID P11-12 (S508-4)         to ID A1P9.25
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      From ID A1J9.25                   to ID BUS 2

Step 153

Description:
This step verifies the connection from S508-5 to BUS 3 and from
S508-6 to BUS 4. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)           to ID A1P6.31
      From ID A1J6.31                   to ID BUS 3

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J8.26
      From ID A1P8.26                   to ID P10-139 (S503-2)
      From ID P10-74 (S503-6)           to ID A1P6.39
      From ID A1J6.39                   to ID BUS 4

      From ID P12-16 (S201-1)           to ID A1P12.42
      From ID A1J12.42                  to ID A1J10.6
      From ID A1P10.6                   to ID P11-203 (S508-1)
      From ID P11-10 (S508-5)           to ID A1P9.37
      From ID A1J9.37                   to ID BUS 3

      From ID P12-16 (S201-1)           to ID A1P12.42
      From ID A1J12.42                  to ID A1J10.6
      From ID A1P10.6                   to ID P11-203 (S508-1)
      From ID P11-74 (S508-6)           to ID A1P9.42
      From ID A1J9.42                   to ID BUS 4

Step 154

Description:
This step verifies the connection from S508-7 to BUS 5 and from
S508-8 to BUS 6. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-137 (S503-7)          to ID A1P6.47
      From ID A1J6.47                   to ID BUS 5

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J8.26
      From ID A1P8.26                   to ID P10-139 (S503-2)
      From ID P10-170 (S503-8)          to ID A1P6.38
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      From ID A1J6.38                  to ID BUS 6

      From ID P12-16 (S201-1)          to ID A1P12.42
      From ID A1J12.42                 to ID A1J10.6
      From ID A1P10.6                  to ID P11-203 (S508-1)
      From ID P11-137 (S508-7)         to ID A1P9.32
      From ID A1J9.32                  to ID BUS 5

      From ID P12-16 (S201-1)          to ID A1P12.42
      From ID A1J12.42                 to ID A1J10.6
      From ID A1P10.6                  to ID P11-203 (S508-1)
      From ID P11-170 (S508-8)         to ID A1P9.22
      From ID A1J9.22                  to ID BUS 6

Step 155

Description:
This step verifies the connection from S508-9 to BUS 7 and from
S508-10 to BUS 8. The DMM resource will be used to measure
resistance UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-173 (S503-9)         to ID A1P6.28
      From ID A1J6.28                  to ID BUS 7

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-205 (S503-10)        to ID A1P6.16
      From ID A1J6.16                  to ID BUS 8

      From ID P12-16 (S201-1)          to ID A1P12.42
      From ID A1J12.42                 to ID A1J10.6
      From ID A1P10.6                  to ID P11-203 (S508-1)
      From ID P11-173 (S508-9)         to ID A1P9.12
      From ID A1J9.12                  to ID BUS 7

      From ID P12-16 (S201-1)          to ID A1P12.42
      From ID A1J12.42                 to ID A1J10.6
      From ID A1P10.6                  to ID P11-203 (S508-1)
      From ID P11-205 (S508-10)        to ID A1P9.2
      From ID A1J9.2                   to ID BUS 8

Step 156

Description:
This step verifies the connection from S507-1 to S201-3 and from
S508-1 to S201-1. The DMM resource will be used to measure
resistance UL= 10 OHM, LL= 0 OHM.
```

```
      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J8.26
      From ID A1P8.26                   to ID P10-139 (S503-2)
      From ID P10-12 (S503-4)           to ID A1P6.23
      From ID A1J6.23                   to ID BUS 2

      From ID P12-20 (S201-3)           to ID A1P12.46
      From ID A1J12.46                  to ID A1J10.2
      From ID A1P10.2                   to ID P11-39 (S507-1)
      From ID P11-168 (S507-3)          to ID A1P9.17
      From ID A1J9.17                   to ID BUS 1

      From ID P12-16 (S201-1)           to ID A1P12.42
      From ID A1J12.42                  to ID A1J10.6
      From ID A1P10.6                   to ID P11-203 (S508-1)
      From ID P11-12 (S508-4)           to ID A1P9.25
      From ID A1J9.25                   to ID BUS 2

      From ID P12-79 (S201-5)           to ID A1P13.1
      From ID A1J13.1                   to ID J1B-14A

      From ID P12-79 (S201-5)           to ID A1P13.1
      From ID A1J13.1                   to ID J1B-14A

Step 157

Description:
This step verifies the connection from S507-2 to S201-4 and from
S508-2 to S201-2. The DMM resource will be used to measure
resistance UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J8.26
      From ID A1P8.26                   to ID P10-139 (S503-2)
      From ID P10-12 (S503-4)           to ID A1P6.23
      From ID A1J6.23                   to ID BUS 2

      From ID P12-52 (S201-4)           to ID A1P12.44
```

```
      From ID A1J12.44                to ID A1J10.4
      From ID A1P10.4                 to ID P11-71 (S507-2)
      From ID P11-168 (S507-3)        to ID A1P9.17
      From ID A1J9.17                 to ID BUS 1

      From ID P12-80 (S201-2)         to ID A1P12.40
      From ID A1J12.40                to ID A1J10.8
      From ID A1P10.8                 to ID P11-139 (S508-2)
      From ID P11-12 (S508-4)         to ID A1P9.25
      From ID A1J9.25                 to ID BUS 2

      From ID P12-47 (S201-6)         to ID A1P13.2
      From ID A1J13.2                 to ID J1B-13A

      From ID P12-47 (S201-6)         to ID A1P13.2
      From ID A1J13.2                 to ID J1B-13A

Step 158

Description:
This step verifies the connection from S509-3 to BUS 1 and from
S509-4 to BUS 2. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID P20-3 (DMM-LO)          to ID A1P15.50
      From ID A1J15.50                to ID A1J8.26
      From ID A1P8.26                 to ID P10-139 (S503-2)
      From ID P10-12 (S503-4)         to ID A1P6.23
      From ID A1J6.23                 to ID BUS 2

      From ID P12-59 (S202-1)         to ID A1P12.38
      From ID A1J12.38                to ID A1J10.10
      From ID A1P10.10                to ID P11-177 (S509-1)
      From ID P11-18 (S509-3)         to ID A1P9.19
      From ID A1J9.19                 to ID BUS 1

      From ID P12-59 (S202-1)         to ID A1P12.38
      From ID A1J12.38                to ID A1J10.10
      From ID A1P10.10                to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)         to ID A1P9.29
      From ID A1J9.29                 to ID BUS 2


Step 159

Description:
```

This step verifies the connection from S509-5 to BUS 3 and from S509-6 to BUS 4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-74 (S503-6)	to ID A1P6.39
From ID A1J6.39	to ID BUS 4
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-176 (S509-5)	to ID A1P9.41
From ID A1J9.41	to ID BUS 3
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-16 (S509-6)	to ID A1P9.46
From ID A1J9.46	to ID BUS 4

Step 160

Description:
This step verifies the connection from S509-7 to BUS 5 and from S509-8 to BUS 6. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)

```
      From ID P11-207 (S509-7)          to ID A1P9.36
      From ID A1J9.36                   to ID BUS 5

      From ID P12-59 (S202-1)           to ID A1P12.38
      From ID A1J12.38                  to ID A1J10.10
      From ID A1P10.10                  to ID P11-177 (S509-1)
      From ID P11-144 (S509-8)          to ID A1P9.26
      From ID A1J9.26                   to ID BUS 6
```

Step 161

Description:
This step verifies the connection from S509-9 to BUS 7 and from S509-10 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-173 (S503-9)          to ID A1P6.28
      From ID A1J6.28                   to ID BUS 7

      From ID A1J15.50                  to ID A1J8.26
      From ID A1P8.26                   to ID P10-139 (S503-2)
      From ID P10-205 (S503-10)         to ID A1P6.16
      From ID A1J6.16                   to ID BUS 8

      From ID P12-59 (S202-1)           to ID A1P12.38
      From ID A1J12.38                  to ID A1J10.10
      From ID A1P10.10                  to ID P11-177 (S509-1)
      From ID P11-211 (S509-9)          to ID A1P9.16
      From ID A1J9.16                   to ID BUS 7

      From ID P12-59 (S202-1)           to ID A1P12.38
      From ID A1J12.38                  to ID A1J10.10
      From ID A1P10.10                  to ID P11-177 (S509-1)
      From ID P11-146 (S509-10)         to ID A1P9.6
      From ID A1J9.6                    to ID BUS 8
```

Step 162

Description:
This step verifies the connection from S510-3 to BUS 1 and from S510-4 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
```

```
      From ID A1J6.13                  to ID BUS 1

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-12 (S503-4)          to ID A1P6.23
      From ID A1J6.23                  to ID BUS 2

      From ID P13-93 (S202-3)          to ID A1P14.49
      From ID A1J14.49                 to ID A1J10.48
      From ID A1P10.48                 to ID P11-52 (S510-1)
      From ID P11-180 (S510-3)         to ID A1P9.21
      From ID A1J9.21                  to ID BUS 1

      From ID P13-93 (S202-3)          to ID A1P14.49
      From ID A1J14.49                 to ID A1J10.48
      From ID A1P10.48                 to ID P11-52 (S510-1)
      From ID P11-147 (S510-4)         to ID A1P9.31
      From ID A1J9.31                  to ID BUS 2

Step 163

Description:
This step verifies the connection from S510-5 to BUS 3 and from
S510-6 to BUS 4. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)          to ID A1P6.31
      From ID A1J6.31                  to ID BUS 3

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-74 (S503-6)          to ID A1P6.39
      From ID A1J6.39                  to ID BUS 4

      From ID P13-93 (S202-3)          to ID A1P14.49
      From ID A1J14.49                 to ID A1J10.48
      From ID A1P10.48                 to ID P11-52 (S510-1)
      From ID P11-212 (S510-5)         to ID A1P9.43
      From ID A1J9.43                  to ID BUS 3

      From ID P13-93 (S202-3)          to ID A1P14.49
      From ID A1J14.49                 to ID A1J10.48
      From ID A1P10.48                 to ID P11-52 (S510-1)
      From ID P11-84 (S510-6)          to ID A1P9.48
      From ID A1J9.48                  to ID BUS 4
```

Step 164

Description:
This step verifies the connection from S510-7 to BUS 5 and from S510-8 to BUS 6. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6
From ID P13-93 (S202-3)	to ID A1P14.49
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-115 (S510-7)	to ID A1P9.38
From ID A1J9.38	to ID BUS 5
From ID P13-93 (S202-3)	to ID A1P14.49
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-20 (S510-8)	to ID A1P9.28
From ID A1J9.28	to ID BUS 6

Step 165

Description:
This step verifies the connection from S510-9 to BUS 7 and from S510-10 to BUS 8. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-173 (S503-9)	to ID A1P6.28
From ID A1J6.28	to ID BUS 7
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-205 (S503-10)	to ID A1P6.16
From ID A1J6.16	to ID BUS 8

```
      From ID P13-93 (S202-3)          to ID A1P14.49
      From ID A1J14.49                 to ID A1J10.48
      From ID A1P10.48                 to ID P11-52 (S510-1)
      From ID P11-116 (S510-9)         to ID A1P9.18
      From ID A1J9.18                  to ID BUS 7

      From ID P13-93 (S202-3)          to ID A1P14.49
      From ID A1J14.49                 to ID A1J10.48
      From ID A1P10.48                 to ID P11-52 (S510-1)
      From ID P11-19 (S510-10)         to ID A1P9.8
      From ID A1J9.8                   to ID BUS 8

Step 166

Description:
This step verifies the connection from S509-1 to S202-1 and from
S510-1 to S202-3. The DMM resource will be used to measure
resistance UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-12 (S503-4)          to ID A1P6.23
      From ID A1J6.23                  to ID BUS 2

      From ID P12-59 (S202-1)          to ID A1P12.38
      From ID A1J12.38                 to ID A1J10.10
      From ID A1P10.10                 to ID P11-177 (S509-1)
      From ID P11-18 (S509-3)          to ID A1P9.19
      From ID A1J9.19                  to ID BUS 1

      From ID P13-93 (S202-3)          to ID A1P14.49
      From ID A1J14.49                 to ID A1J10.48
      From ID A1P10.48                 to ID P11-52 (S510-1)
      From ID P11-147 (S510-4)         to ID A1P9.31
      From ID A1J9.31                  to ID BUS 2

      From ID P12-89 (S202-5)          to ID A1P12.25
      From ID A1J12.25                 to ID J2B-8C

      From ID P12-89 (S202-5)          to ID A1P12.25
      From ID A1J12.25                 to ID J2B-8C
```

Step 167

Description:
This step verifies the connection from S509-2 to S202-2 and from S510-2 to S202-4. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J8.26
From ID A1P8.26	to ID P10-139 (S503-2)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1
From ID P13-29 (S202-4)	to ID A1P14.50
From ID A1J14.50	to ID A1J10.50
From ID A1P10.50	to ID P11-244 (S510-2)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID P12-25 (S202-6)	to ID A1P12.26
From ID A1J12.26	to ID J2B-8B
From ID P12-25 (S202-6)	to ID A1P12.26
From ID A1J12.26	to ID J2B-8B

Step 168

Description:
This step verifies the connection from S401-2 to BUS 1 and from S401-3 to BUS 2. The DMM resource will be used to measure resistance UL= 10 OHM, LL= 0 OHM.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

```
      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J8.26
      From ID A1P8.26                   to ID P10-139 (S503-2)
      From ID P10-12 (S503-4)           to ID A1P6.23
      From ID A1J6.23                   to ID BUS 2

      From INSTR-RTN                    to ID A1J6.7
      From ID A1P6.7                    to ID P10-93 (S401-1)
      From ID P10-190 (S401-2)          to ID A1P6.19
      From ID A1J6.19                   to ID BUS 1

      From INSTR-RTN                    to ID A1J6.7
      From ID A1P6.7                    to ID P10-93 (S401-1)
      From ID P10-254 (S401-3)          to ID A1P6.29
      From ID A1J6.29                   to ID BUS 2

Step 169

Description:
This step verifies the connection from S401-4 to BUS 3 and from
S401-5 to BUS 4. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)           to ID A1P6.31
      From ID A1J6.31                   to ID BUS 3

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J8.26
      From ID A1P8.26                   to ID P10-139 (S503-2)
      From ID P10-74 (S503-6)           to ID A1P6.39

      From ID A1P6.7                    to ID P10-93 (S401-1)
      From ID P10-28 (S401-4)           to ID A1P6.37
      From ID A1J6.37                   to ID BUS 3

      From INSTR-RTN                    to ID A1J6.7
      From ID A1P6.7                    to ID P10-93 (S401-1)
      From ID P10-220 (S401-5)          to ID A1P6.45
      From ID A1J6.45                   to ID BUS 4

Step 170

Description:
This step verifies the connection from S402-2 to BUS 5 and from
S402-4 to BUS 7. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:
```

```
      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-137 (S503-7)          to ID A1P6.47
      From ID A1J6.47                   to ID BUS 5

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J8.26
      From ID A1P8.26                   to ID P10-139 (S503-2)
      From ID P10-173 (S503-9)          to ID A1P6.28
      From ID A1J6.28                   to ID BUS 7

      From INSTR-RTN                    to ID A1J6.9
      From ID A1P6.9                    to ID P10-94 (S402-1)
      From ID P10-158 (S402-2)          to ID A1P6.48
      From ID A1J6.48                   to ID BUS 5

      From INSTR-RTN                    to ID A1J6.9
      From ID A1P6.9                    to ID P10-94 (S402-1)
      From ID P10-61 (S402-4)           to ID A1P6.34
      From ID A1J6.34                   to ID BUS 7

Step 171

Description:
This step verifies the connection from S401-2 to BUS 1 and from
S402-5 to BUS 8. The DMM resource will be used to measure resistance
UL= 10 OHM, LL= 0 OHM.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J8.26
      From ID A1P8.26                   to ID P10-139 (S503-2)
      From ID P10-205 (S503-10)         to ID A1P6.16
      From ID A1J6.16                   to ID BUS 8

      From INSTR-RTN                    to ID A1J6.7
      From ID A1P6.7                    to ID P10-93 (S401-1)
      From ID P10-190 (S401-2)          to ID A1P6.19
      From ID A1J6.19                   to ID BUS 1

      From INSTR-RTN                    to ID A1J6.9
      From ID A1P6.9                    to ID P10-94 (S402-1)
      From ID P10-253 (S402-5)          to ID A1P6.22
      From ID A1J6.22                   to ID BUS 8
```

Step 172

Description:
This step verifies the connection from ARB-OUT to S501-1 and ARB-RTN to INSTR-RTN. The DMM resource will be used to measure voltage generated by the ARB-OUT UL= 1.86 Vrms, LL= 1.68Vrms.

Connection Path is as follows:

From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID A1J24.1	to ID A1J6.5
From ID A1P6.5	to ID P10-194 (S501-1)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ARB-RTN	to ID A1J6.11
From ID A1P6.11	to ID P10-166 (S301-26)
From ID P10-102 (S301-25)	to ID A1P7.34
From ID A1J7.34	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 173

Description:
This step verifies the connection from CT-IN1 to S501-2 and CT-RTN1 to ARB-RTN. The CT-IN1 resource will be used to measure frequency generated by the ARB-OUT UL= 10.5kHz, LL= 9.5kHz.

Connection Path is as follows:

From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID A1J24.1	to ID A1J6.5
From ID A1P6.5	to ID P10-194 (S501-1)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-18 (CT-IN1)	to ID A1P21.1
From ID A1J21.1	to ID A1J6.8
From ID A1P6.8	to ID P10-162 (S501-2)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1

Step 174

Description:
This step verifies the connection from CT-IN2 to S502-2 and CT-RTN2 to ARB-RTN. The CT-IN2 resource will be used to measure frequency generated by the ARB-OUT UL= 10.5kHz, LL= 9.5kHz.

Connection Path is as follows:

From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID A1J24.1	to ID A1J6.5
From ID A1P6.5	to ID P10-194 (S501-1)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-19 (CT-IN2)	to ID A1P22.1
From ID A1J22.1	to ID A1J6.10
From ID A1P6.10	to ID P10-71 (S502-2)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1

Step 175

Description:
This step verifies the connection from DSO-IN2 to S502-1 and DSO-RTN1 to ARB-RTN. The DSO-IN2 resource will be used to measure voltage generated by the ARB-OUT UL= 6 Vpp, LL= 4 Vpp.

Connection Path is as follows:

From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID A1J24.1	to ID A1J6.5
From ID A1P6.5	to ID P10-194 (S501-1)
From ID P10-164 (S501-3)	to ID A1P7.29
From ID A1J7.29	to ID BUS 1
From ID P19-2 (DSO-IN2)	to ID A1P18.1
From ID A1J18.1	to ID R25.1
From ID R25.2	to ID A1J6.2
From ID A1P6.2	to ID P10-39 (S502-1)
From ID P10-168 (S502-3)	to ID A1P7.31
From ID A1J7.31	to ID BUS 1

Step 176

Description:
This step verifies the RC filter on DSO-IN2 input. The DSO-IN2 resource will be used to measure voltage generated by the ARB-OUT UL= 1.8 Vpp, LL= 1.4 Vpp.

Connection Path is as follows:

From ID P23-12 (ARB-OUT)	to ID A1P24.1
From ID A1J24.1	to ID A1J6.5

```
      From ID A1P6.5                    to ID P10-194 (S501-1)
      From ID P10-164 (S501-3)          to ID A1P7.29
      From ID A1J7.29                   to ID BUS 1

      From ID P19-2 (DSO-IN2)           to ID A1P18.1
      From ID A1J18.1                   to ID R25.1
      From ID R25.2                     to ID A1J6.2
      From ID A1P6.2                    to ID P10-39 (S502-1)
      From ID P10-168 (S502-3)          to ID A1P7.31
      From ID A1J7.31                   to ID BUS 1

Step 177

Description:
This step verifies the connection from DSO-IN1 to S504-1 and DSO-
RTN1 to ARB-RTN. The DSO-IN1 resource will be used to measure
voltage generated by the ARB-OUT UL= 6 Vpp, LL= 4 Vpp.

      Connection Path is as follows:

      From ID P23-12 (ARB-OUT)          to ID A1P24.1
      From ID A1J24.1                   to ID A1J6.5
      From ID A1P6.5                    to ID P10-194 (S501-1)
      From ID P10-164 (S501-3)          to ID A1P7.29
      From ID A1J7.29                   to ID BUS 1

      From ID P19-1 (DSO-IN1)           to ID A1P17.1
      From ID A1J17.1                   to ID A1J6.1
      From ID A1P6.1                    to ID P10-177 (S504-1)
      From ID P10-18 (S504-3)           to ID A1P6.15
      From ID A1J6.15                   to ID BUS 1

Step 178

Description:
This step verifies the connection from DSO-TRIGIN to S504-2. The
DSO-IN1 resource will be used to measure voltage generated by the
ARB-OUT UL= 6 Vpp, LL= 4 Vpp.

      Connection Path is as follows:

      From ID P23-12 (ARB-OUT)          to ID A1P24.1
      From ID A1J24.1                   to ID A1J6.5
      From ID A1P6.5                    to ID P10-194 (S501-1)
      From ID P10-164 (S501-3)          to ID A1P7.29
      From ID A1J7.29                   to ID BUS 1

      From ID P19-1 (DSO-IN1)           to ID A1P17.1
      From ID A1J17.1                   to ID A1J6.1
      From ID A1P6.1                    to ID P10-177 (S504-1)
      From ID P10-18 (S504-3)           to ID A1P6.15
      From ID A1J6.15                   to ID BUS 1

      From ID P19-3 (DSO-TRIGIN)        to ID A1P19.1
```

```
      From ID A1J19.1                  to ID A1J6.3
      From ID A1P6.3                   to ID P10-242 (S504-2)
      From ID P10-18 (S504-3)          to ID A1P6.15
      From ID A1J6.15                  to ID BUS 1
```

Step 179

Description:
This step verifies the connection from CT-ARM to S505-1. This step uses DC2 to gate the CT (previously did not). DC2 is connected to BUS1 for 1 second. The low to high to low transition is used to gate the CT. The CT-IN1 resource will be used to measure frequency generated by the ARB-OUT UL= 10.5 kHz, LL= 9.5 kHz.

Connection Path is as follows:

```
      From ID P23-12 (ARB-OUT)         to ID A1P24.1
      From ID A1J24.1                  to ID A1J6.5
      From ID A1P6.5                   to ID P10-194 (S501-1)
      From ID P10-195 (S501-4)         to ID A1P7.33
      From ID A1J7.33                  to ID BUS 2

      From ID P19-18 (CT-IN1)          to ID A1P21.1
      From ID A1J21.1                  to ID A1J6.8
      From ID A1P6.8                   to ID P10-162 (S501-2)
      From ID P10-195 (S501-4)         to ID A1P7.33
      From ID A1J7.33                  to ID BUS 2

      From ID P19-17 (CT-ARM)          to ID A1P20.1
      From ID A1P20.1                  to ID A1J6.6
      From ID A1P6.6                   to ID P10-52 (S505-1)
      From ID P10-180 (S505-3)         to ID A1P6.17
      From ID A1J6.17                  to ID BUS 1

      From ID P1-4 (DC2-HI)            to ID A1P1.2
      From ID A1J1.2                   to ID A1J8.4
      From ID A1P8.4                   to ID P10-174 (S301-68)
      From ID P10-204 (S301-67)        to ID A1P8.29
      From ID A1J8.29                  to ID BUS 1
```

Step 180

Description:
This step verifies the connection from ARB-START-ARM to S505-2. The DSO-IN1 resource will be used to measure voltage generated by the ARB-OUT UL= 6 Vpp, LL= 4 Vpp.

Connection Path is as follows:

```
      From ID P23-12 (ARB-OUT)         to ID A1P24.1
      From ID A1J24.1                  to ID A1J6.5
      From ID A1P6.5                   to ID P10-194 (S501-1)
      From ID P10-164 (S501-3)         to ID A1P7.29
      From ID A1J7.29                  to ID BUS 1
```

```
From ID P19-1 (DSO-IN1)          to ID A1P17.1
From ID A1J17.1                  to ID A1J6.1
From ID A1P6.1                   to ID P10-177 (S504-1)
From ID P10-18 (S504-3)          to ID A1P6.15
From ID A1J6.15                  to ID BUS 1

From ID P23-10 (ARB-STARTARM)    to ID A1P23.1
From ID A1J23.1                  to ID A1J6.4
From ID A1P6.4                   to ID P10-244 (S505-2)
From ID P10-147 (S505-4)         to ID A1P6.27
From ID A1J6.27                  to ID BUS 2
```

Step 181

Description:
This step verifies the connection from APROBE to DSO-IN1 and APROBE-RTN to DSO-RTN. The DSO-IN1 resource will be used to measure voltage generated by the ARB-OUT UL= 6 Vpp, LL= 4 Vpp.

Connection Path is as follows:

```
From ID P23-12 (ARB-OUT)         to ID A1P24.1
From ID A1J24.1                  to ID A1J6.5
From ID A1P6.5                   to ID P10-194 (S501-1)
From ID P10-164 (S501-3)         to ID A1P7.29
From ID A1J7.29                  to ID BUS 1

From ID BUS 1                    to ID A1J9.15
From ID A1P9.15                  to ID P11-77 (S508-3)
From ID P11-203 (S508-1)         to ID A1P10.6
From ID A1J10.6                  to ID A1J12.42
From ID A1P12.42                 to ID P12-16 (S201-1)

From ID P12-79 (S201-5)          to ID A1P13.1
From ID A1J13.1                  to ID J1B-14A
```

2.5 MODULE 2- W1 CDA LOGIC A SPECIFIC ID TEST

Refer to Reference Drawings when diagnosing connection paths.

Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF TEST PWB, A2).pdf and 13020A7101 (CABLE, W1, SCHEMATIC).pdf in section 1.4 during review of the following steps.

Step 201

Description:
This step verifies the wire path from W1 P2-1 to W1 P2-32. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
From ID P20-2 (DMM-HI)           to ID A1P15.49
```

From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-162 (S506-2)	to ID A1P10.1
From ID A1J10.1	to ID A1J12.48
From ID A1P12.48	to ID P12-44 (S701-2)
From ID P12-68 (S701-4)	to ID A1P13.8
From ID A1J13.8	to ID J1B-13D
From W1 P1B-13D	to W1 P2-1 (ST J1-1)
From ST_J1-1	to ST_J1-32
From W1 P2-32 (ST J1-32)	to W1 P1B-6B
From ID J1B-6B	to ID A1J12.11
From ID A1P12.11	to ID P12-22 (S201-37)
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 202

Description:
This step verifies the wire path from W1 P2-2 to W1 P2-59. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From ID BUS 2	to ID A1J9.33
From ID A1P9.33	to ID P11-195 (S506-4)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P12-4 (S701-3)	to ID A1P13.7
From ID A1J13.7	to ID J1B-14D
From W1 P1B-14D	to W1 P2-2 (ST J1-2)
From ST_J1-2	to ST_J1-59
From W1 P2-59 (ST J1-59)	to W1 P1A-4C

```
      From ID J1A-4C                    to ID A1J15.6
      From ID A1P15.6                   to ID P13-44 (S701-42)

      From ID P12-44 (S701-2)           to ID A1P12.48
      From ID A1J12.48                  to ID A1J10.1
      From ID A1P10.1                   to ID P11-162 (S506-2)
      From ID P11-225 (S506-5)          to ID A1P9.45
      From ID A1J9.45                   to ID BUS 3

      From ID BUS 3                     to ID A1J6.31
      From ID A1P6.31                   to ID P10-10 (S503-5)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 203

Description:
This step verifies the wire path from W1 P2-3 to W1 P2-57. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-74 (S503-6)           to ID A1P6.39
      From ID A1J6.39                   to ID BUS 4

      From ID BUS 4                     to ID A1J9.50
      From ID A1P9.50                   to ID P11-193 (S506-6)
      From ID P11-194 (S506-1)          to ID A1P10.3
      From ID A1J10.3                   to ID A1J12.50
      From ID A1P12.50                  to ID P12-76 (S701-1)

      From ID P12-36 (S701-5)           to ID A1P13.9
      From ID A1J13.9                   to ID J1B-14E
      From W1 P1B-14E                   to W1 P2-3 (ST J1-3)

      From ST_J1-3                      to ST_J1-57

      From W1 P2-57 (ST J1-57)          to W1 P1A-7B
      From ID J1A-7B                    to ID A1J15.14
      From ID A1P15.14                  to ID P13-12 (S701-40)

      From ID P12-44 (S701-2)           to ID A1P12.48
      From ID A1J12.48                  to ID A1J10.1
      From ID A1P10.1                   to ID P11-162 (S506-2)
      From ID P11-161 (S506-7)          to ID A1P9.40
      From ID A1J9.40                   to ID BUS 5

      From ID BUS 5                     to ID A1J6.47
      From ID A1P6.47                   to ID P10-137 (S503-7)
      From ID P10-139 (S503-2)          to ID A1P8.26
```

```
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 204

Description:
This step verifies the wire path from W1 P2-4 to W1 P2-5. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P12-47 (S201-6)          to ID A1P13.2
      From ID A1J13.2                  to ID J1B-13A
      From W1 P1B-13A                  to W1 P2-4 (ST J1-4)

      From ST_J1-4                     to ST_J1-5

      From W1 P2-5 (ST J1-5)           to W1 P1B-14A
      From ID J1B-14A                  to ID A1J13.1
      From ID A1P13.1                  to ID P12-79 (S201-5)

      From ID P12-16 (S201-1)          to ID A1P12.42
      From ID A1J12.42                 to ID A1J10.6
      From ID A1P10.6                  to ID P11-203 (S508-1)
      From ID P11-12 (S508-4)          to ID A1P9.25
      From ID A1J9.25                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 205

Description:
This step verifies the wire path from W1 P2-6 to W1 P2-7. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
```

```
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-170 (S503-8)        to ID A1P6.38
      From ID A1J6.38                 to ID BUS 6

      From ID BUS 6                   to ID A1J9.30
      From ID A1P9.30                 to ID P11-129 (S506-8)
      From ID P11-162 (S506-2)        to ID A1P10.1
      From ID A1J10.1                 to ID A1J12.48
      From ID A1P12.48                to ID P12-44 (S701-2)

      From ID P12-3 (S701-6)          to ID A1P13.10
      From ID A1J13.10                to ID J1B-13E
      From W1 P1B-13E                 to W1 P2-6 (ST J1-6)

      From ST_J1-6                    to ST_J1-7

      From W1 P2-7 (ST J1-7)          to W1 P1A-8F
      From ID J1A-8F                  to ID A1J14.24
      From ID A1P14.24                to ID P13-62 (S202-42)

      From ID P12-90 (S202-2)         to ID A1P12.36
      From ID A1J12.36                to ID A1J10.12
      From ID A1P10.12                to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)         to ID A1P9.29
      From ID A1J9.29                 to ID BUS 2

      From ID BUS 2                   to ID A1J6.23
      From ID A1P6.23                 to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)        to ID A1P8.26
      From ID A1J8.26                 to ID A1J15.50
      From ID A1P15.50                to ID P20-3 (DMM-LO)

Step 206

Description:
This step verifies the wire path from W1 P2-8 to W1 P2-9. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.21
      From ID A1P9.21                 to ID P11-180 (S510-3)
      From ID P11-244 (S510-2)        to ID A1P10.50
      From ID A1J10.50                to ID A1J14.50
      From ID A1P14.50                to ID P13-29 (S202-4)

      From ID P13-87 (S202-10)        to ID A1P14.17
```

```
      From ID A1J14.17                  to ID J1A-5E
      From W1 P1A-5E                    to W1 P2-8 (ST J1-8)

      From ST_J1-8                      to ST_J1-9

      From W1 P2-9 (ST J1-9)            to W1 P1A-1A
      From ID J1A-1A                    to ID A1J14.1
      From ID A1P14.1                   to ID P13-47 (S201-9)

      From ID P12-20 (S201-3)           to ID A1P12.46
      From ID A1J12.46                  to ID A1J10.2
      From ID A1P10.2                   to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)           to ID A1P9.27
      From ID A1J9.27                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 207

Description:
This step verifies the wire path from W1 P2-10 to W1 P2-12. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J8.29
      From ID A1P8.29                   to ID P10-204 (S301-67)
      From ID P10-174 (S301-68)         to ID A1P8.4
      From ID A1J8.4                    to ID A1J1.2
      From ID A1P1.2                    to ID P1-4 (DC2-HI)

      From ID P1-4 (DC2-HI)             to ID A1P1.2
      From ID A1J1.2                    to ID A1J2.23
      From ID A1P2.23                   to ID P10-22 (S101-1)
      From ID P10-86 (S101-2)           to ID A1P2.15
      From ID A1J2.15                   to ID A1J10.14
      From ID A1P10.14                  to ID P11-140 (S301-161)
      From ID P11-206 (S301-162)        to ID A1P10.16
      From ID A1J10.16                  to ID J1A-13E
      From W1 P1A-13E                   to W1 P2-10 (ST J1-10)

      From ST_J1-10                     to ST_J1-12

      From W1 P2-12 (ST J1-12)          to W1 P1B-8A
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      From ID J1B-8A                    to ID A1J12.4
      From ID A1P12.4                   to ID P12-18 (S201-22)

      From ID P12-80 (S201-2)           to ID A1P12.40
      From ID A1J12.40                  to ID A1J10.8
      From ID A1P10.8                   to ID P11-139 (S508-2)
      From ID P11-12 (S508-4)           to ID A1P9.25
      From ID A1J9.25                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 208

Description:
This step verifies the wire path from W1 P2-11 to W1 P2-26. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-173 (S503-9)          to ID A1P6.28
      From ID A1J6.28                   to ID BUS 7

      From ID BUS 7                     to ID A1J9.20
      From ID A1P9.20                   to ID P11-68 (S506-9)
      From ID P11-162 (S506-2)          to ID A1P10.1
      From ID A1J10.1                   to ID A1J12.48
      From ID A1P12.48                  to ID P12-44 (S701-2)

      From ID P13-74 (S701-34)          to ID A1P15.12
      From ID A1J15.12                  to ID J1A-6C
      From W1 P1A-6C                    to W1 P2-26 (ST J1-26)

      From ST_J1-26                     to ST_J1-11

      From W1 P2-11 (ST J1-11)          to W1 P1B-1F
      From ID J1B-1F                    to ID A1J1.11
      From ID A1P1.11                   to ID P1-11 (DC4-LO)

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J7.38
      From ID A1P7.38                   to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)         to ID A1P7.36
      From ID A1J7.36                   to GROUND

Step 209

Description:
```

This step verifies the wire path from W1 P2-13 to W1 P2-14. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.17
From ID A1P9.17	to ID P11-168 (S507-3)
From ID P11-39 (S507-1)	to ID A1P10.2
From ID A1J10.2	to ID A1J12.46
From ID A1P12.46	to ID P12-20 (S201-3)
From ID P12-46 (S201-7)	to ID A1P13.3
From ID A1J13.3	to ID J1B-14B
From W1 P1B-14B	to W1 P2-13 (ST J1-13)
From ST_J1-13	to ST_J1-14
From W1 P2-14 (ST J1-14)	to W1 P1A-3A
From ID A1P15.1	to ID P13-39 (S701-7)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-36 (S506-10)	to ID A1P9.10
From ID A1J9.10	to ID BUS 8
From ID BUS 8	to ID A1J6.16
From ID A1P6.16	to ID P10-205 (S503-10)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 210

Description:
This step verifies the wire path from W1 P2-15 to W1 P2-16. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J7.42
From ID A1P7.42	to ID P10-34 (S301-2)
From ID P10-65 (S301-1)	to ID A1P7.3
From ID A1J7.3	to ID A1J11.11
From ID A1P11.11	to ID P11-233 (S301-153)
From ID P11-43 (S301-154)	to ID A1P11.9

From ID A1J11.9	to ID J1A-12D
From W1 P1A-12D	to W1 P2-16 (ST J1-16)
From ST_J1-16	to ST_J1-15
From W1 P2-15 (ST J1-15)	to W1 P1B-8C
From ID J1B-8C	to ID A1J12.6
From ID A1P12.6	to ID P12-81 (S201-24)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 211

Description:
This step verifies the wire path from W1 P2-17 to W1 P2-18. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.17
From ID A1P9.17	to ID P11-168 (S507-3)
From ID P11-71 (S507-2)	to ID A1P10.4
From ID A1J10.4	to ID A1J12.44
From ID A1P12.44	to ID P12-52 (S201-4)
From ID P13-48 (S201-12)	to ID A1P14.4
From ID A1J14.4	to ID J1A-2B
From W1 P1A-2B	to W1 P2-17 (ST J1-17)
From ST_J1-17	to ST_J1-18
From W1 P2-18 (ST J1-18)	to W1 P1A-3C
From ID J1A-3C	to ID A1J15.3
From ID A1P15.3	to ID P13-70 (S701-9)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)

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      From ID P11-195 (S506-4)          to ID A1P9.33
      From ID A1J9.33                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 212

Description:
This step verifies the wire path from W1 P2-19 to W1 P2-20. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)          to ID A1P10.8
      From ID A1J10.8                   to ID A1J12.40
      From ID A1P12.40                  to ID P12-80 (S201-2)

      From ID P12-78 (S201-14)          to ID A1P13.6
      From ID A1J13.6                   to ID J1B-13C
      From W1 P1B-13C                   to W1 P2-19 (ST J1-19)

      From ST_J1-19                     to ST_J1-20

      From W1 P2-20 (ST J1-20)          to W1 P1B-4A
      From ID J1B-4A                    to ID A1J12.16
      From ID A1P12.16                  to ID P12-63 (S202-46)

      From ID P12-90 (S202-2)           to ID A1P12.36
      From ID A1J12.36                  to ID A1J10.12
      From ID A1P10.12                  to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 213

Description:
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This step verifies the wire path from W1 P2-21 to W1 P2-22. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.21
From ID A1P9.21	to ID P11-180 (S510-3)
From ID P11-52 (S510-1)	to ID A1P10.48
From ID A1J10.48	to ID A1J14.49
From ID A1P14.49	to ID P13-93 (S202-3)
From ID P12-31 (S202-45)	to ID A1P12.15
From ID A1J12.15	to ID J1B-5C
From W1 P1B-5C	to W1 P2-21 (ST J1-21)
From ST_J1-21	to ST_J1-22
From W1 P2-22 (ST J1-22)	to W1 P1A-1B
From ID J1A-1B	to ID A1J14.3
From ID A1P14.3	to ID P13-80 (S201-11)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 214

Description:
This step verifies the wire path from W1 P2-23 to W1 P2-25. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23

From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-162 (S506-2)	to ID A1P10.1
From ID A1J10.1	to ID A1J12.48
From ID A1P12.48	to ID P12-44 (S701-2)
From ID P13-43 (S701-32)	to ID A1P15.10
From ID A1J15.10	to ID J1A-6A
From W1 P1A-6A	to W1 P2-23 (ST J1-23)
From ST_J1-23	to ST_J1-25
From W1 P2-25 (ST J1-25)	to W1 P1A-5C
From ID J1A-5C	to ID A1J15.9
From ID A1P15.9	to ID P13-75 (S701-31)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 215

Description:
This step verifies the wire path from W1 P2-24 to W1 P2-28. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P12-40 (S701-29)	to ID A1P13.18
From ID A1J13.18	to ID J1B-11F
From W1 P1B-11F	to W1 P2-24 (ST J1-24)
From ST_J1-24	to ST_J1-28
From W1 P2-28 (ST J1-28)	to W1 P1B-9A

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      From ID J1B-9A                    to ID A1J12.1
      From ID A1P12.1                   to ID P12-48 (S201-15)

      From ID P12-16 (S201-1)           to ID A1P12.42
      From ID A1J12.42                  to ID A1J10.6
      From ID A1P10.6                   to ID P11-203 (S508-1)
      From ID P11-12 (S508-4)           to ID A1P9.25
      From ID A1J9.25                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 216

Description:
This step verifies the wire path from W1 P2-27 to W1 P2-29. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)          to ID A1P10.6
      From ID A1J10.6                   to ID A1J12.42
      From ID A1P12.42                  to ID P12-16 (S201-1)

      From ID P12-14 (S201-13)          to ID A1P13.5
      From ID A1J13.5                   to ID J1B-14C
      From W1 P1B-14C                   to W1 P2-27 (ST J1-27)

      From ST_J1-27                     to ST_J1-29

      From W1 P2-29 (ST J1-29)          to W1 P1A-1C
      From ID J1A-1C                    to ID A1J14.5
      From ID A1P14.5                   to ID P13-49 (S201-17)

      From ID P12-20 (S201-3)           to ID A1P12.46
      From ID A1J12.46                  to ID A1J10.2
      From ID A1P10.2                   to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)           to ID A1P9.27
      From ID A1J9.27                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
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      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 217

Description:
This step verifies the wire path from W1 P2-30 to W1 P2-31. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.21
      From ID A1P9.21                  to ID P11-180 (S510-3)
      From ID P11-52 (S510-1)          to ID A1P10.48
      From ID A1J10.48                 to ID A1J14.49
      From ID A1P14.49                 to ID P13-93 (S202-3)

      From ID P12-96 (S202-47)         to ID A1P12.17
      From ID A1J12.17                 to ID J1B-4B
      From W1 P1B-4B                   to W1 P2-30 (ST J1-30)

      From ST_J1-30                    to ST_J1-31

      From W1 P2-31 (ST J1-31)         to W1 P1A-2C
      From ID J1A-2C                   to ID A1J14.6
      From ID A1P14.6                  to ID P13-15 (S201-19)

      From ID P12-16 (S201-1)          to ID A1P12.42
      From ID A1J12.42                 to ID A1J10.6
      From ID A1P10.6                  to ID P11-203 (S508-1)
      From ID P11-12 (S508-4)          to ID A1P9.25
      From ID A1J9.25                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 218

Description:
This step verifies the wire path from W1 P2-30 to W1 P2-36. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
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      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.21
      From ID A1P9.21                 to ID P11-180 (S510-3)
      From ID P11-52 (S510-1)         to ID A1P10.48
      From ID A1J10.48                to ID A1J14.49
      From ID A1P14.49                to ID P13-93 (S202-3)

      From ID P12-96 (S202-47)        to ID A1P12.17
      From ID A1J12.17                to ID J1B-4B
      From W1 P1B-4B                  to W1 P2-30 (ST J1-30)

      From ST_J1-30                   to ST_J1-36

      From W1 P2-36 (ST J1-36)        to W1 P1B-1F
      From ID J1B-1F                  to ID A1J1.11
      From ID A1P1.11                 to ID P1-11 (DC4-LO)
```

Step 219

Description:
This step verifies the wire path from W1 P2-34 to W1 P2-37. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.19
      From ID A1P9.19                 to ID P11-18 (S509-3)
      From ID P11-242 (S509-2)        to ID A1P10.12
      From ID A1J10.12                to ID A1J12.36
      From ID A1P12.36                to ID P12-90 (S202-2)

      From ID P13-89 (S202-18)        to ID A1P14.18
      From ID A1J14.18                to ID J1A-5F
      From W1 P1A-5F                  to W1 P2-34 (ST J1-34)

      From ST_J1-34                   to ST_J1-37

      From W1 P2-37 (ST J1-37)        to W1 P1B-14F
      From ID J1B-14F                 to ID A1J13.11
      From ID A1P13.11                to ID P12-38 (S701-11)

      From ID P12-76 (S701-1)         to ID A1P12.50
      From ID A1J12.50                to ID A1J10.3
      From ID A1P10.3                 to ID P11-194 (S506-1)
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      From ID P11-195 (S506-4)          to ID A1P9.33
      From ID A1J9.33                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 220

Description:
This step verifies the wire path from W1 P2-35 to W1 P2-47. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)          to ID A1P10.6
      From ID A1J10.6                   to ID A1J12.42
      From ID A1P12.42                  to ID P12-16 (S201-1)

      From ID P12-51 (S201-21)          to ID A1P12.3
      From ID A1J12.3                   to ID J1B-9C
      From W1 P1B-9C                    to W1 P2-35 (ST J1-35)

      From ST_J1-35                     to ST_J1-47

      From W1 P2-47 (ST J1-47)          to W1 P1A-3F
      From ID J1A-3F                    to ID A1J14.14
      From ID A1P14.14                  to ID P13-18 (S201-35)

      From ID P12-20 (S201-3)           to ID A1P12.46
      From ID A1J12.46                  to ID A1J10.2
      From ID A1P10.2                   to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)           to ID A1P9.27
      From ID A1J9.27                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 221

Description:
```

This step verifies the wire path from W1 P2-35 to W1 P2-68. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P12-51 (S201-21)	to ID A1P12.3
From ID A1J12.3	to ID J1B-9C
From W1 P1B-9C	to W1 P2-35 (ST J1-35)
From ST_J1-35	to ST_J1-68
From W1 P2-68 (ST J1-68)	to W1 P1B-5A
From ID J1B-5A	to ID A1J12.13
From ID A1P12.13	to ID P12-92 (S202-22)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 222

Description:
This step verifies the wire path from W1 P2-38 to W1 P2-39. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15

```
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P12-17 (S201-23)         to ID A1P12.5
      From ID A1J12.5                  to ID J1B-8B
      From W1 P1B-8B                   to W1 P2-38 (ST J1-38)

      From ST_J1-38                    to ST_J1-39

      From W1 P2-39 (ST J1-39)         to W1 P1A-1E
      From ID J1A-1E                   to ID A1J14.9
      From ID A1P14.9                  to ID P13-17 (S201-26)

      From ID P12-52 (S201-4)          to ID A1P12.44
      From ID A1J12.44                 to ID A1J10.4
      From ID A1P10.4                  to ID P11-71 (S507-2)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 223

Description:
This step verifies the wire path from W1 P2-40 to W1 P2-41. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P13-51 (S201-28)         to ID A1P14.11
      From ID A1J14.11                 to ID J1A-1F
      From W1 P1A-1F                   to W1 P2-40 (ST J1-40)

      From ST_J1-40                    to ST_J1-41

      From W1 P2-41 (ST J1-41)         to W1 P1B-10E
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      From ID J1B-10E                  to ID A1J13.20
      From ID A1P13.20                 to ID P12-9 (S701-36)

      From ID P12-44 (S701-2)          to ID A1P12.48
      From ID A1J12.48                 to ID A1J10.1
      From ID A1P10.1                  to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 224

Description:
This step verifies the wire path from W1 P2-42 to W1 P2-43. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.21
      From ID A1P9.21                  to ID P11-180 (S510-3)
      From ID P11-244 (S510-2)         to ID A1P10.50
      From ID A1J10.50                 to ID A1J14.50
      From ID A1P14.50                 to ID P13-29 (S202-4)

      From ID P13-31 (S202-44)         to ID A1P14.25
      From ID A1J14.25                 to ID J1A-9E
      From W1 P1A-9E                   to W1 P2-42 (ST J1-42)

      From ST_J1-42                    to ST_J1-43

      From W1 P2-43 (ST J1-43)         to W1 P1B-9D
      From ID J1B-9D                   to ID A1J13.17
      From ID A1P13.17                 to ID P12-41 (S701-38)

      From ID P12-44 (S701-2)          to ID A1P12.48
      From ID A1J12.48                 to ID A1J10.1
      From ID A1P10.1                  to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
```

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      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 225

Description:
This step verifies the wire path from W1 P2-46 to W1 P2-48. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P12-50 (S201-30)         to ID A1P12.8
      From ID A1J12.8                  to ID J1B-7B
      From W1 P1B-7B                   to W1 P2-46 (ST J1-46)

      From ST_J1-46                    to ST_J1-48

      From W1 P2-48 (ST J1-48)         to W1 P1B-12D
      From ID J1B-12D                  to ID A1J13.13
      From ID A1P13.13                 to ID P12-69 (S701-13)

      From ID P12-76 (S701-1)          to ID A1P12.50
      From ID A1J12.50                 to ID A1J10.3
      From ID A1P10.3                  to ID P11-194 (S506-1)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 226

Description:
This step verifies the wire path from W1 P2-49 to W1 P2-50. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
```

```
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.23
      From ID A1P9.23                 to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)        to ID A1P10.3
      From ID A1J10.3                 to ID A1J12.50
      From ID A1P12.50                to ID P12-76 (S701-1)

      From ID P13-72 (S701-15)        to ID A1P15.5
      From ID A1J15.5                 to ID J1A-4B
      From W1 P1B-4B                  to W1 P2-49 (ST J1-49)

      From ST_J1-49                   to ST_J1-50

      From W1 P2-50 (ST J1-50)        to W1 P1B-6A
      From ID J1B-6A                  to ID A1J12.10
      From ID A1P12.10                to ID P12-83 (S201-32)

      From ID P12-52 (S201-4)         to ID A1P12.44
      From ID A1J12.44                to ID A1J10.4
      From ID A1P10.4                 to ID P11-71 (S507-2)
      From ID P11-72 (S507-4)         to ID A1P9.27
      From ID A1J9.27                 to ID BUS 2

      From ID BUS 2                   to ID A1J6.23
      From ID A1P6.23                 to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)        to ID A1P8.26
      From ID A1J8.26                 to ID A1J15.50
      From ID A1P15.50                to ID P20-3 (DMM-LO)
```

Step 227

Description:
This step verifies the wire path from W1 P2-51 to W1 P2-52. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.15
      From ID A1P9.15                 to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)        to ID A1P10.8
      From ID A1J10.8                 to ID A1J12.40
      From ID A1P12.40                to ID P12-80 (S201-2)

      From ID P12-15 (S201-16)        to ID A1P12.2
```

From ID A1J12.2	to ID J1B-9B
From W1 P1B-9B	to W1 P2-51 (ST J1-51)
From ST_J1-51	to ST_J1-52
From W1 P2-52 (ST J1-52)	to W1 P1A-5A
From ID J1A-5A	to ID A1J15.7
From ID A1P15.7	to ID P13-7 (S701-17)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 228

Description:
This step verifies the wire path from W1 P2-53 to W1 P2-54. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P12-71 (S701-19)	to ID A1P13.15
From ID A1J13.15	to ID J1B-12F
From W1 P1B-12F	to W1 P2-53 (ST J1-53)
From ST_J1-53	to ST_J1-54
From W1 P2-54 (ST J1-54)	to W1 P1B-4C
From ID J1B-4C	to ID A1J12.18
From ID A1P12.18	to ID P12-32 (S202-48)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)

```
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 229

Description:
This step verifies the wire path from W1 P2-55 to W1 P2-56. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.17
      From ID A1P9.17                  to ID P11-168 (S507-3)
      From ID P11-39 (S507-1)          to ID A1P10.2
      From ID A1J10.2                  to ID A1J12.46
      From ID A1P12.46                 to ID P12-20 (S201-3)

      From ID P13-83 (S201-27)         to ID A1P14.10
      From ID A1J14.10                 to ID J1A-2E
      From W1 P1A-2E                   to W1 P2-55 (ST J1-55)

      From ST_J1-55                    to ST_J1-56

      From W1 P2-56 (ST J1-56)         to W1 P1B-11D
      From ID J1B-11D                  to ID A1J13.16
      From ID A1P13.16                 to ID P12-6 (S701-21)

      From ID P12-76 (S701-1)          to ID A1P12.50
      From ID A1J12.50                 to ID A1J10.3
      From ID A1P10.3                  to ID P11-194 (S506-1)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 230

Description:
```

This step verifies the wire path from W1 P2-58 to W1 P2-60. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P12-42 (S701-35)	to ID A1P13.19
From ID A1J13.19	to ID J1B-10D
From W1 P1B-10D	to W1 P2-58 (ST J1-58)
From ST_J1-58	to ST_J1-60
From W1 P2-60 (ST J1-60)	to W1 P1A-1D
From ID J1A-1D	to ID A1J14.7
From ID A1P14.7	to ID P13-79 (S201-20)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 231

Description:
This step verifies the wire path from W1 P2-61 to W1 P2-62. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15

```
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P12-82 (S201-29)         to ID A1P12.7
      From ID A1J12.7                  to ID J1B-7A
      From W1 P1B-7A                   to W1 P2-61 (ST J1-61)

      From ST_J1-61                    to ST_J1-62

      From W1 P2-62 (ST J1-62)         to W1 P1B-7C
      From ID J1B-7C                   to ID A1J12.9
      From ID A1P12.9                  to ID P12-19 (S201-31)

      From ID P12-20 (S201-3)          to ID A1P12.46
      From ID A1J12.46                 to ID A1J10.2
      From ID A1P10.2                  to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 232

Description:
This step verifies the wire path from W1 P2-63 to W1 P2-67. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P13-10 (S701-33)         to ID A1P15.11
      From ID A1J15.11                 to ID J1A-6B
      From W1 P1A-6B                   to W1 P2-63 (ST J1-63)

      From ST_J1-63                    to ST_J1-67

      From W1 P2-67 (ST J1-67)         to W1 P1A-6F
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      From ID J1A-6F                    to ID A1J14.20
      From ID A1P14.20                  to ID P13-24 (S202-20)

      From ID P12-90 (S202-2)           to ID A1P12.36
      From ID A1J12.36                  to ID A1J10.12
      From ID A1P10.12                  to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 233

Description:
This step verifies the wire path from W1 P2-64 to W1 P2-11. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.23
      From ID A1P9.23                   to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)          to ID A1P10.3
      From ID A1J10.3                   to ID A1J12.50
      From ID A1P12.50                  to ID P12-76 (S701-1)

      From ID P12-73 (S701-37)          to ID A1P13.21
      From ID A1J13.21                  to ID J1B-10F
      From W1 P1B-10F                   to W1 P2-64 (ST J1-64)

      From ST_J1-64                     to ST_J1-69

      From W1 P2-69 (ST J1-69)          to W1 P1B-1F
      From ID J1B-1F                    to ID A1J1.11
      From ID A1P1.11                   to ID P1-11 (DC4-LO)

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J7.38
      From ID A1P7.38                   to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)         to ID A1P7.36
      From ID A1J7.36                   to GROUND

Step 234

Description:
```

This step verifies the wire path from W1 P2-65 to W1 P2-70. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P13-45 (S701-39)	to ID A1P15.13
From ID A1J15.13	to ID J1A-7A
From W1 P1A-7A	to W1 P2-65 (ST J1-65)
From ST_J1-65	to ST_J1-70
From W1 P2-70 (ST J1-70)	to W1 P1A-4A
From ID J1A-4A	to ID A1J15.4
From ID A1P15.4	to ID P13-38 (S701-10)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 235

Description:
This step verifies the wire path from W1 P2-66 to W1 P2-68. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23

```
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P13-76 (S701-41)         to ID A1P15.15
      From ID A1J15.15                 to ID J1A-7C
      From W1 P1A-7C                   to W1 P2-66 (ST J1-66)

      From ST_J1-66                    to ST_J1-68

      From W1 P2-68 (ST J1-68)         to W1 P1B-5A
      From ID J1B-5A                   to ID A1J12.13
      From ID A1P12.13                 to ID P12-92 (S202-22)

      From ID P12-90 (S202-2)          to ID A1P12.36
      From ID A1J12.36                 to ID A1J10.12
      From ID A1P10.12                 to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 236

Description:
This step verifies the wire path from W1 P4-A1 to W1 P4-A10. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)          to ID A1P6.31
      From ID A1J6.31                  to ID BUS 3

      From ID BUS 3                    to ID A1J15.45
      From ID A1P15.45                 to ID P13-2 (S751-1)
      From ID P12-34 (S751-3)          to ID A1P13.35
      From ID A1J13.35                 to ID J1B-6E
      From W1 P1B-6E                   to W1 P4-A1 (ST J3-1)

      From ST_J3-A1                    to ST_J3-A10

      From W1 P4-A10 (ST J3-19)        to W1 P1B-6F
      From ID J1B-6F                   to ID A1J13.37
      From ID A1P13.37                 to ID P12-1 (S751-4)
      From ID P13-66 (S751-2)          to ID A1P15.47
      From ID A1J15.47                 to ID BUS 4
```

```
      From ID BUS 4                    to ID A1J6.39
      From ID A1P6.39                  to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 237

Description:
This step verifies the wire path from W1P4-A2 to W1P4-B5. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)          to ID A1P6.31
      From ID A1J6.31                  to ID BUS 3

      From ID BUS 3                    to ID A1J15.45
      From ID A1P15.45                 to ID P13-2 (S751-1)
      From ID P12-65 (S751-5)          to ID A1P13.39
      From ID A1J13.39                 to ID J1B-5E
      From W1 P1B-5E                   to W1 P4-A2 (ST J3-3)

      From ST_J3-A2                    to ST_J3-B5

      From W1 P4-B5 (ST J3-10)         to W1 P1B-5F
      From ID J1B-5F                   to ID A1J13.41
      From ID A1P13.41                 to ID P12-33 (S751-6)
      From ID P13-66 (S751-2)          to ID A1P15.47
      From ID A1J15.47                 to ID BUS 4

      From ID BUS 4                    to ID A1J6.39
      From ID A1P6.39                  to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 238

Description:
This step verifies the wire path from W1P4-B2 to W1P4-A5. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)          to ID A1P6.31
      From ID A1J6.31                  to ID BUS 3
```

```
      From ID BUS 3                    to ID A1J15.45
      From ID A1P15.45                 to ID P13-2 (S751-1)
      From ID P13-68 (S751-7)          to ID A1P15.21
      From ID A1J15.21                 to ID J1A-10A
      From W1 P1A-10A                  to W1 P4-B2 (ST J3-4)

      From ST_J3-B2                    to ST_J3-A5

      From W1 P4-A5 (ST J3-9)          to W1 P1A-11A
      From ID J1A-11A                  to ID A1J15.23
      From ID A1P15.23                 to ID P13-36 (S751-8)
      From ID P13-66 (S751-2)          to ID A1P15.47
      From ID A1J15.47                 to ID BUS 4

      From ID BUS 4                    to ID A1J6.39
      From ID A1P6.39                  to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 239

Description:
This step verifies the wire path from W1P4-A3 to W1P4-B7. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)          to ID A1P6.31
      From ID A1J6.31                  to ID BUS 3

      From ID BUS 3                    to ID A1J15.45
      From ID A1P15.45                 to ID P13-2 (S751-1)
      From ID P13-67 (S751-9)          to ID A1P15.25
      From ID A1J15.25                 to ID J1A-12A
      From W1 P1A-12A                  to W1 P4-A3 (ST J3-5)

      From ST_J3-A3                    to ST_J3-B7

      From W1 P4-B7 (ST J3-14)         to W1 P1A-13A
      From ID J1A-13A                  to ID A1J15.27
      From ID A1P15.27                 to ID P13-35 (S751-10)
      From ID P13-66 (S751-2)          to ID A1P15.47
      From ID A1J15.47                 to ID BUS 4

      From ID BUS 4                    to ID A1J6.39
      From ID A1P6.39                  to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 240

Description:
This step verifies the wire path from W1P4-B3 to W1P4-A6. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J15.45
From ID A1P15.45	to ID P13-2 (S751-1)
From ID P12-67 (S751-11)	to ID A1P13.43
From ID A1J13.43	to ID J1B-4E
From W1 P1B-4E	to W1 P4-B3 (ST J3-6)
From ST_J3-B3	to ST_J3-A6
From W1 P4-A6 (ST J3-11)	to W1 P1B-4F
From ID J1B-4F	to ID A1J13.45
From ID A1P13.45	to ID P12-35 (S751-12)
From ID P13-66 (S751-2)	to ID A1P15.47
From ID A1J15.47	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 241

Description:
This step verifies the wire path from W1P4-A4 to W1P4-A8. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J15.45
From ID A1P15.45	to ID P13-2 (S751-1)
From ID P12-2 (S751-13)	to ID A1P13.47
From ID A1J13.47	to ID J1B-3E
From W1 P1B-3E	to W1 P4-A4 (ST J3-7)

From ST_J3-A4	to ST_J3-A8
From W1 P4-A8 (ST J3-15)	to W1 P1B-3F
From ID J1B-3F	to ID A1J13.49
From ID A1P13.49	to ID P12-66 (S751-14)
From ID P13-66 (S751-2)	to ID A1P15.47
From ID A1J15.47	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 242

Description:
This step verifies the wire path from W1P4-B4 to W1P4-B8. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J15.45
From ID A1P15.45	to ID P13-2 (S751-1)
From ID P13-5 (S751-15)	to ID A1P15.30
From ID A1J15.30	to ID J1A-13C
From W1 P1A-13C	to W1 P4-B4 (ST J3-8)
From ST_J3-B4	to ST_J3-B8
From W1 P4-B8 (ST J3-16)	to W1 P1A-14A
From ID J1A-14A	to ID A1J15.32
From ID A1P15.32	to ID P13-69 (S751-16)
From ID P13-66 (S751-2)	to ID A1P15.47
From ID A1J15.47	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 243

Description:
This step verifies the wire path from W1P4-B6 to W1P4-B9. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J15.45
From ID A1P15.45	to ID P13-2 (S751-1)
From ID P13-37 (S751-17)	to ID A1P15.34
From ID A1J15.34	to ID J1A-14B
From W1 P1A-14B	to W1 P4-B6 (ST J3-12)
From ST_J3-B6	to ST_J3-B9
From W1 P4-B9 (ST J3-18)	to W1 P1A-14C
From ID J1A-14C	to ID A1J15.36
From ID A1P15.36	to ID P13-4 (S751-18)
From ID P13-66 (S751-2)	to ID A1P15.47
From ID A1J15.47	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 244

Description:
This step verifies the wire path from W1 P3-1 to W1 P3-2. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P12-75 (S701-43)	to ID A1P13.22
From ID A1J13.22	to ID J1B-11E
From W1 P1B-11E	to W1 P3-1 (ST J2-1)
From ST_J2-1	to ST_J2-2

```
      From W1 P3-2 (ST J2-2)          to W1 P1A-2F
      From ID J1A-2F                  to ID A1J14.12
      From ID A1P14.12                to ID P13-52 (S201-33)

      From ID P12-16 (S201-1)         to ID A1P12.42
      From ID A1J12.42                to ID A1J10.6
      From ID A1P10.6                 to ID P11-203 (S508-1)
      From ID P11-12 (S508-4)         to ID A1P9.25
      From ID A1J9.25                 to ID BUS 2

      From ID BUS 2                   to ID A1J6.23
      From ID A1P6.23                 to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)        to ID A1P8.26
      From ID A1J8.26                 to ID A1J15.50
      From ID A1P15.50                to ID P20-3 (DMM-LO)

Step 245

Description:
This step verifies the wire path from W1 P3-4 to W1 P3-3. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.23
      From ID A1P9.23                 to ID P11-164 (S506-3)
      From ID P11-162 (S506-2)        to ID A1P10.1
      From ID A1J10.1                 to ID A1J12.48
      From ID A1P12.48                to ID P12-44 (S701-2)

      From ID P13-6 (S701-8)          to ID A1P15.2
      From ID A1J15.2                 to ID J1A-3B
      From W1 P1A-3B                  to W1 P3-4 (ST J2-4)

      From ST_J2-4                    to ST_J2-3

      From W1 P3-3 (ST J2-3)          to W1 P1A-5B
      From ID J1A-5B                  to ID A1J15.8
      From ID A1P15.8                 to ID P13-42 (S701-23)

      From ID P12-76 (S701-1)         to ID A1P12.50
      From ID A1J12.50                to ID A1J10.3
      From ID A1P10.3                 to ID P11-194 (S506-1)
      From ID P11-195 (S506-4)        to ID A1P9.33
      From ID A1J9.33                 to ID BUS 2

      From ID BUS 2                   to ID A1J6.23
      From ID A1P6.23                 to ID P10-12 (S503-4)
```

```
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 246

Description:
This step verifies the wire path from W1 P3-6 to W1 P3-7. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.19
      From ID A1P9.19                   to ID P11-18 (S509-3)
      From ID P11-242 (S509-2)          to ID A1P10.12
      From ID A1J10.12                  to ID A1J12.36
      From ID A1P12.36                  to ID P12-90 (S202-2)

      From ID P12-27 (S202-24)          to ID A1P12.14
      From ID A1J12.14                  to ID J1B-5B
      From W1 P1B-5B                    to W1 P3-6 (ST J2-6)

      From ST_J2-6                      to ST_J2-7

      From W1 P3-7 (ST J2-7)            to W1 P1A-9F
      From ID J1A-9F                    to ID A1J14.26
      From ID A1P14.26                  to ID P13-64 (S202-50)

      From ID P13-29 (S202-4)           to ID A1P14.50
      From ID A1J14.50                  to ID A1J10.50
      From ID A1P10.50                  to ID P11-244 (S510-2)
      From ID P11-147 (S510-4)          to ID A1P9.31
      From ID A1J9.31                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 247

Description:
This step verifies the wire path from W1 P3-8 to W1 P3-11. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:
```

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P13-50 (S201-25)         to ID A1P14.9
      From ID A1J14.8                  to ID J1A-2D
      From W1 P1A-2D                   to W1 P3-8 (ST J2-8)

      From ST_J2-8                     to ST_J2-11

      From W1 P3-11 (ST J2-11)         to W1 P1A-7E
      From ID J1A-7E                   to ID A1J14.21
      From ID A1P14.21                 to ID P13-90 (S202-26)

      From ID P12-90 (S202-2)          to ID A1P12.36
      From ID A1J12.36                 to ID A1J10.12
      From ID A1P10.12                 to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 248

Description:
This step verifies the wire path from W1 P3-18 to W1 P3-10. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-162 (S506-2)         to ID A1P10.1
      From ID A1J10.1                  to ID A1J12.48
      From ID A1P12.48                 to ID P12-44 (S701-2)
```

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      From ID P12-37 (S701-14)          to ID A1P13.14
      From ID A1J13.14                  to ID J1B-12E
      From W1 P1B-12E                   to W1 P3-18 (ST J2-18)

      From ST_J2-18                     to ST_J2-10

      From W1 P3-10 (ST J2-10)          to W1 P1B-1E
      From ID J1B-1E                    to ID A1J1.11
      From ID A1P1.11                   to ID P1-11 (DC4-LO)

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J7.38
      From ID A1P7.38                   to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)         to ID A1P7.36
      From ID A1J7.36                   to GROUND

Step 249

Description:
This step verifies the wire path from W1 P3-14 to W1 P3-16. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.19
      From ID A1P9.19                   to ID P11-18 (S509-3)
      From ID P11-242 (S509-2)          to ID A1P10.12
      From ID A1J10.12                  to ID A1J12.36
      From ID A1P12.36                  to ID P12-90 (S202-2)

      From ID P13-94 (S202-52)          to ID A1P14.27
      From ID A1J14.27                  to ID J1A-10E
      From W1 P1A-10E                   to W1 P3-14 (ST J2-14)

      From ST_J2-14                     to ST_J2-16

      From W1 P3-16 (ST J2-16)          to W1 P1B-6C
      From ID J1B-6C                    to ID A1J12.12
      From ID A1P12.12                  to ID P12-53 (S201-39)

      From ID P12-16 (S201-1)           to ID A1P12.42
      From ID A1J12.42                  to ID A1J10.6
      From ID A1P10.6                   to ID P11-203 (S508-1)
      From ID P11-12 (S508-4)           to ID A1P9.25
      From ID A1J9.25                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
```

```
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 250

Description:
This step verifies the wire path from W1 P3-17 to W1 P3-20. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.23
      From ID A1P9.23                   to ID P11-164 (S506-3)
      From ID P11-162 (S506-2)          to ID A1P10.1
      From ID A1J10.1                   to ID A1J12.48
      From ID A1P12.48                  to ID P12-44 (S701-2)

      From ID P12-5 (S701-12)           to ID A1P13.12
      From ID A1J13.12                  to ID J1B-13F
      From W1 P1B-13F                   to W1 P3-17 (ST J2-17)

      From ST_J2-17                     to ST_J2-20

      From W1 P3-20 (ST J2-20)          to W1 P1A-4E
      From ID J1A-4E                    to ID A1J14.15
      From ID A1P14.15                  to ID P13-21 (S201-41)

      From ID P12-16 (S201-1)           to ID A1P12.42
      From ID A1J12.42                  to ID A1J10.6
      From ID A1P10.6                   to ID P11-203 (S508-1)
      From ID P11-12 (S508-4)           to ID A1P9.25
      From ID A1J9.25                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 251

Description:
This step verifies the wire path from W1 P3-19 to W1 P3-21. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:
```

```
      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.23
      From ID A1P9.23                 to ID P11-164 (S506-3)
      From ID P11-162 (S506-2)        to ID A1P10.1
      From ID A1J10.1                 to ID A1J12.48
      From ID A1P12.48                to ID P12-44 (S701-2)

      From ID P12-43 (S701-44)        to ID A1P13.24
      From ID A1J13.24                to ID J1B-9F
      From W1 P1B-9F                  to W1 P3-19 (ST J2-19)

      From ST_J2-19                   to ST_J2-21

      From W1 P3-21 (ST J2-21)        to W1 P1A-4F
      From ID J1A-4F                  to ID A1J14.16
      From ID A1P14.16                to ID P13-86 (S201-43)

      From ID P12-16 (S201-1)         to ID A1P12.42
      From ID A1J12.42                to ID A1J10.6
      From ID A1P10.6                 to ID P11-203 (S508-1)
      From ID P11-12 (S508-4)         to ID A1P9.25
      From ID A1J9.25                 to ID BUS 2

      From ID BUS 2                   to ID A1J6.23
      From ID A1P6.23                 to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)        to ID A1P8.26
      From ID A1J8.26                 to ID A1J15.50
      From ID A1P15.50                to ID P20-3 (DMM-LO)

Step 252

Description:
This step verifies the wire path from W1 P3-22 to W1 P3-24. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.15
      From ID A1P9.15                 to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)        to ID A1P10.8
      From ID A1J10.8                 to ID A1J12.40
      From ID A1P12.40                to ID P12-80 (S201-2)
```

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      From ID P13-19 (S201-34)          to ID A1P14.13
      From ID A1J14.13                  to ID J1A-3E
      From W1 P1A-3E                    to W1 P3-22 (ST J2-22)

      From ST_J2-22                     to ST_J2-24

      From W1 P3-24 (ST J2-24)          to W1 P1A-7F
      From ID J1A-7F                    to ID A1J14.22
      From ID A1P14.22                  to ID P13-92 (S202-34)

      From ID P12-90 (S202-2)           to ID A1P12.36
      From ID A1J12.36                  to ID A1J10.12
      From ID A1P10.12                  to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 253

Description:
This step verifies the wire path from W1 P3-29 to W1 P3-25. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J7.48
      From ID A1P7.48                   to ID P10-2 (S301-10)
      From ID P10-97 (S301-9)           to ID A1P7.5
      From ID A1J7.5                    to ID J1A-14D
      From W1 P1A-14D                   to W1 P3-29 (ST J2-29)

      From ST_J2-29                     to ST_J2-25

      From W1 P3-25 (ST J2-25)          to W1 P1A-8E
      From ID J1A-8E                    to ID A1J14.23
      From ID A1P14.23                  to ID P13-91 (S202-35)

      From ID P12-59 (S202-1)           to ID A1P12.38
      From ID A1J12.38                  to ID A1J10.10
      From ID A1P10.10                  to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)
```

Step 254

Description:
This step verifies the wire path from W1 P3-29 to W1 P3-30. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J7.42
From ID A1P7.42	to ID P10-34 (S301-2)
From ID P10-65 (S301-1)	to ID A1P7.3
From ID A1J7.3	to ID J1A-14E
From W1 P1A-14E	to W1 P3-30 (ST J2-30)
From ST_J2-30	to ST_J2-40
From W1 P3-40 (ST J2-40)	to W1 P1A-9A
From ID J1A-9A	to ID A1J15.19
From ID A1P15.19	to ID P13-77 (S701-50)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

2.6 MODULE 3 W2 CDA LOGIC B SPECIFIC ID TESTS

Refer to Reference Drawings when diagnosing connection paths.

Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF TEST PWB, A2).pdf and 13020A7201 (CABLE, W2, SCHEMATIC).pdf in section 1.4 during review of the following steps.

Step 301

Description:
This step verifies the wire path from W2 P2-1 to W2 P2-32. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)

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      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-162 (S506-2)         to ID A1P10.1
      From ID A1J10.1                  to ID A1J12.48
      From ID A1P12.48                 to ID P12-44 (S701-2)

      From ID P13-40 (S701-16)         to ID A1P15.16
      From ID A1J15.16                 to ID J2A-7C
      From W2 P1A-7C                   to W2 P2-1 (ST J1-1)

      From ST_J1-1                     to ST_J1-32

      From W2 P2-32 (ST J1-32)         to W2 P1B-7A
      From ID J2B-7A                   to ID A1J12.24
      From ID A1P12.24                 to ID P12-21 (S201-46)

      From ID P12-80 (S201-2)          to ID A1P12.40
      From ID A1J12.40                 to ID A1J10.8
      From ID A1P10.8                  to ID P11-139 (S508-2)
      From ID P11-12 (S508-4)          to ID A1P9.25
      From ID A1J9.25                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 302

Description:
This step verifies the wire path from W2 P2-2 to W2 P2-59. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P13-73 (S701-25)         to ID A1P15.26
      From ID A1J15.26                 to ID J2A-6B
      From W2 P1A-6B                   to W2 P2-2 (ST J1-2)
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      From ST_J1-2                      to ST_J1-59

      From W2 P2-59 (ST J1-59)          to W2 P1B-13D
      From ID J2B-13D                   to ID A1J12.30
      From ID A1P12.30                  to ID P12-56 (S202-8)

      From ID P12-90 (S202-2)           to ID A1P12.36
      From ID A1J12.36                  to ID A1J10.12
      From ID A1P10.12                  to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 303

Description:
This step verifies the wire path from W2 P2-3 to W2 P2-57. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.19
      From ID A1P9.19                   to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)          to ID A1P10.10
      From ID A1J10.10                  to ID A1J12.38
      From ID A1P12.38                  to ID P12-59 (S202-1)

      From ID P12-64 (S202-37)          to ID A1P12.43
      From ID A1J12.43                  to ID J2B-8F
      From W2 P1B-8F                    to W2 P2-3 (ST J1-3)

      From ST_J1-3                      to ST_J1-57

      From W2 P2-57 (ST J1-57)          to W2 P1B-10A
      From ID J2B-10A                   to ID A1J13.31
      From ID A1P13.31                  to ID P12-7 (S701-30)

      From ID P12-44 (S701-2)           to ID A1P12.48
      From ID A1J12.48                  to ID A1J10.1
      From ID A1P10.1                   to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)          to ID A1P9.33
      From ID A1J9.33                   to ID BUS 2
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      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 304

Description:
This step verifies the wire path from W2 P2-4 to W2 P2-5. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P12-8 (S701-27)          to ID A1P13.29
      From ID A1J13.29                 to ID J2B-9B
      From W2 P1B-9B                   to W2 P2-4 (ST J1-4)

      From ST_J1-4                     to ST_J1-5

      From W2 P2-5 (ST J1-5)           to W2 P1B-6F
      From ID J2B-6F                   to ID A1J12.47
      From ID A1P12.47                 to ID P12-94 (S202-39)

      From ID P12-59 (S202-1)          to ID A1P12.38
      From ID A1J12.38                 to ID A1J10.10
      From ID A1P10.10                 to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 305

Description:
This step verifies the wire path from W2 P2-6 to W2 P2-7. The DMM
resource will be used to measure resistance UL= 10 ohms.
```

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P13-16 (S201-18)	to ID A1P14.28
From ID A1J14.28	to ID J2A-1A
From W2 P1A-1A	to W2 P2-6 (ST J1-6)
From ST_J1-6	to ST_J1-7
From W2 P2-7 (ST J1-7)	to W2 P1B-8A
From ID J2B-8A	to ID A1J13.25
From ID A1P13.25	to ID P12-39 (S701-20)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 306

Description:
This step verifies the wire path from W2 P2-8 to W2 P2-9. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-177 (S509-1)	to ID A1P10.10
From ID A1J10.10	to ID A1J12.38

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      From ID A1P12.38                  to ID P12-59 (S202-1)

      From ID P13-30 (S202-41)          to ID A1P14.31
      From ID A1J14.31                  to ID J2A-2B
      From W2 P1A-2B                    to W2 P2-8 (ST J1-8)

      From ST_J1-8                      to ST_J1-9

      From W2 P2-9 (ST J1-9)            to W2 P1B-10B
      From ID J2B-10B                   to ID A1J13.33
      From ID A1P13.33                  to ID P12-10 (S701-45)

      From ID P12-76 (S701-1)           to ID A1P12.50
      From ID A1J12.50                  to ID A1J10.3
      From ID A1P10.3                   to ID P11-194 (S506-1)
      From ID P11-195 (S506-4)          to ID A1P9.33
      From ID A1J9.33                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 307

Description:
This step verifies the wire path from W2 P2-10 to W2 P2-12. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.23
      From ID A1P9.23                   to ID P11-164 (S506-3)
      From ID P11-162 (S506-2)          to ID A1P10.1
      From ID A1J10.1                   to ID A1J12.48
      From ID A1P12.48                  to ID P12-44 (S701-2)

      From ID P12-70 (S701-22)          to ID A1P13.27
      From ID A1J13.27                  to ID J2B-9A
      From W2 P1B-9A                    to W2 P2-10 (ST J1-10)

      From ST_J1-10                     to ST_J1-12

      From W2 P2-12 (ST J1-12)          to W2 P1B-9F
      From ID J2B-9F                    to ID A1J12.41
      From ID A1P12.41                  to ID P12-29 (S202-32)
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      From ID P12-90 (S202-2)          to ID A1P12.36
      From ID A1J12.36                 to ID A1J10.12
      From ID A1P10.12                 to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 308

Description:
This step verifies the wire path from W2 P2-13 to W2 P2-14. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P13-82 (S201-36)         to ID A1P14.29
      From ID A1J14.29                 to ID J2A-2A
      From W2 P1A-2A                   to W2 P2-13 (ST J1-13)

      From ST_J1-13                    to ST_J1-14

      From W2 P2-14 (ST J1-14)         to W2 P1B-6C
      From ID J2B-6C                   to ID A1J12.19
      From ID A1P12.19                 to ID P12-54 (S201-38)

      From ID P12-52 (S201-4)          to ID A1P12.44
      From ID A1J12.44                 to ID A1J10.4
      From ID A1P10.4                  to ID P11-71 (S507-2)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 309

Description:
This step verifies the wire path from W2 P2-15 to W2 P2-16. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P12-85 (S201-45)	to ID A1P12.23
From ID A1J12.23	to ID J2B-7B
From W2 P1B-7B	to W2 P2-15 (ST J1-15)
From ST_J1-15	to ST_J1-16
From W2 P2-16 (ST J1-16)	to W2 P1B-11F
From ID J2B-11F	to ID A1J12.37
From ID A1P12.37	to ID P12-60 (S202-30)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 310

Description:
This step verifies the wire path from W2 P2-17 to W2 P2-18. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13

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      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P13-78 (S701-47)         to ID A1P15.18
      From ID A1J15.18                 to ID J2A-8C
      From W2 P1A-8C                   to W2 P2-17 (ST J1-17)

      From ST_J1-17                    to ST_J1-18

      From W2 P2-18 (ST J1-18)         to W2 P1A-3C
      From ID J2A-3C                   to ID A1J14.32
      From ID A1P14.32                 to ID P13-53 (S201-42)

      From ID P12-52 (S201-4)          to ID A1P12.44
      From ID A1J12.44                 to ID A1J10.4
      From ID A1P10.4                  to ID P11-71 (S507-2)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 311

Description:
This step verifies the wire path from W2 P2-19 to W2 P2-20. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P13-22 (S201-44)         to ID A1P14.33
      From ID A1J14.33                 to ID J2A-2C
      From W2 P1A-2C                   to W2 P2-19 (ST J1-19)
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      From ST_J1-19                    to ST_J1-20

      From W2 P2-20 (ST J1-20)         to W2 P1B-14D
      From ID J2B-14D                  to ID A1J12.27
      From ID A1P12.27                 to ID P12-55 (S201-47)

      From ID P12-20 (S201-3)          to ID A1P12.46
      From ID A1J12.46                 to ID A1J10.2
      From ID A1P10.2                  to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 312

Description:
This step verifies the wire path from W2 P2-21 to W2 P2-22. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P13-23 (S201-49)         to ID A1P14.35
      From ID A1J14.35                 to ID J2A-3D
      From W2 P1A-3D                   to W2 P2-21 (ST J1-21)

      From ST_J1-21                    to ST_J1-22

      From W2 P2-22 (ST J1-22)         to W2 P1A-4F
      From ID J2A-4F                   to ID A1J14.46
      From ID A1P14.46                 to ID P13-95 (S202-43)

      From ID P12-59 (S202-1)          to ID A1P12.38
      From ID A1J12.38                 to ID A1J10.10
      From ID A1P10.10                 to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2
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From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 313

Description:
This step verifies the wire path from W2 P2-23 to W2 P2-25. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-162 (S506-2)	to ID A1P10.1
From ID A1J10.1	to ID A1J12.48
From ID A1P12.48	to ID P12-44 (S701-2)
From ID P13-9 (S701-24)	to ID A1P15.24
From ID A1J15.24	to ID J2A-7B
From W2 P1A-7B	to W2 P2-23 (ST J1-23)
From ST_J1-23	to ST_J1-25
From W2 P2-25 (ST J1-25)	to W2 P1A-5D
From ID J2A-5D	to ID A1J14.47
From ID A1P14.47	to ID P13-32 (S202-49)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 314

Description:
This step verifies the wire path from W2 P2-24 to W2 P2-28. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-177 (S509-1)	to ID A1P10.10
From ID A1J10.10	to ID A1J12.38
From ID A1P12.38	to ID P12-59 (S202-1)
From ID P12-89 (S202-5)	to ID A1P12.25
From ID A1J12.25	to ID J2B-8C
From W2 P1B-8C	to W2 P2-24 (ST J1-24)
From ST_J1-24	to ST_J1-28
From W2 P2-28 (ST J1-28)	to W2 P1B-14F
From ID J2B-14F	to ID A1J12.29
From ID A1P12.29	to ID P12-24 (S202-7)
From ID P13-93 (S202-3)	to ID A1P14.49
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 315

Description:
This step verifies the wire path from W2 P2-33 to W2 P2-36. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40

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      From ID A1P12.40                  to ID P12-80 (S201-2)

      From ID P12-86 (S201-48)          to ID A1P12.28
      From ID A1J12.28                  to ID J2B-14E
      From W2 P1B-14E                   to W2 P2-33 (ST J1-33)

      From ST_J1-33                     to ST_J1-36

      From W2 P2-36 (ST J1-36)          to W2 P1A-2E
      From ID J2A-2E                    to ID A1J14.39
      From ID A1P14.39                  to ID P13-56 (S202-9)

      From ID P12-59 (S202-1)           to ID A1P12.38
      From ID A1J12.38                  to ID A1J10.10
      From ID A1P10.10                  to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 316

Description:
This step verifies the wire path from W2 P2-34 to W2 P2-37. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.19
      From ID A1P9.19                   to ID P11-18 (S509-3)
      From ID P11-242 (S509-2)          to ID A1P10.12
      From ID A1J10.12                  to ID A1J12.36
      From ID A1P12.36                  to ID P12-90 (S202-2)

      From ID P13-27 (S202-36)          to ID A1P14.45
      From ID A1J14.45                  to ID J2A-4E
      From W2 P1A-4E                    to W2 P2-34 (ST J1-34)

      From ST_J1-34                     to ST_J1-37

      From W2 P2-37 (ST J1-37)          to W2 P1A-8A
      From ID J2A-8A                    to ID A1J15.22
      From ID A1P15.22                  to ID P13-71 (S701-18)
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      From ID P12-44 (S701-2)          to ID A1P12.48
      From ID A1J12.48                 to ID A1J10.1
      From ID A1P10.1                  to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 317

Description:
This step verifies the wire path from W2 P2-38 to W2 P2-39. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.19
      From ID A1P9.19                  to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)         to ID A1P10.10
      From ID A1J10.10                 to ID A1J12.38
      From ID A1P12.38                 to ID P12-59 (S202-1)

      From ID P13-25 (S202-11)         to ID A1P14.40
      From ID A1J14.40                 to ID J2A-1E
      From W2 P1A-1E                   to W2 P2-38 (ST J1-38)

      From ST_J1-38                    to ST_J1-39

      From W2 P2-39 (ST J1-39)         to W2 P1A-5E
      From ID J2A-5E                   to ID A1J14.48
      From ID A1P14.48                 to ID P13-63 (S202-51)

      From ID P13-93 (S202-3)          to ID A1P14.49
      From ID A1J14.49                 to ID A1J10.48
      From ID A1P10.48                 to ID P11-52 (S510-1)
      From ID P11-147 (S510-4)         to ID A1P9.31
      From ID A1J9.31                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 318

Description:
This step verifies the wire path from W2 P2-40 to W2 P2-41. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-177 (S509-1)	to ID A1P10.10
From ID A1J10.10	to ID A1J12.38
From ID A1P12.38	to ID P12-59 (S202-1)
From ID P12-57 (S202-13)	to ID A1P12.31
From ID A1J12.31	to ID J2B-13E
From W2 P1B-13E	to W2 P2-40 (ST J1-40)
From ST_J1-40	to ST_J1-41
From W2 P2-41 (ST J1-41)	to W2 P1B-12D
From ID J2B-12D	to ID A1J12.33
From ID A1P12.33	to ID P12-26 (S202-15)
From ID P13-93 (S202-3)	to ID A1P14.49
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 319

Description:
This step verifies the wire path from W2 P2-42 to W2 P2-43. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13

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      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.19
      From ID A1P9.19                  to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)         to ID A1P10.10
      From ID A1J10.10                 to ID A1J12.38
      From ID A1P12.38                 to ID P12-59 (S202-1)

      From ID P13-58 (S202-17)         to ID A1P14.34
      From ID A1J14.34                 to ID J2A-1C
      From W2 P1A-1C                   to W2 P2-42 (ST J1-42)

      From ST_J1-42                    to ST_J1-43

      From W2 P2-43 (ST J1-43)         to W2 P1A-2D
      From ID J2A-2D                   to ID A1J14.36
      From ID A1P14.36                 to ID P13-55 (S201-50)

      From ID P12-80 (S201-2)          to ID A1P12.40
      From ID A1J12.40                 to ID A1J10.8
      From ID A1P10.8                  to ID P11-139 (S508-2)
      From ID P11-12 (S508-4)          to ID A1P9.25
      From ID A1J9.25                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 320

Description:
This step verifies the wire path from W2 P2-44 to W2 P2-47. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-162 (S506-2)         to ID A1P10.1
      From ID A1J10.1                  to ID A1J12.48
      From ID A1P12.48                 to ID P12-44 (S701-2)

      From ID P13-41 (S701-26)         to ID A1P15.28
      From ID A1J15.28                 to ID J2A-5B
      From W2 P1A-5B                   to W2 P2-44 (ST J1-44)
```

From ST_J1-44	to ST_J1-47
From W2 P2-47 (ST J1-47)	to W2 P1B-7F
From ID J2B-7F	to ID A1J12.45
From ID A1P12.45	to ID P12-95 (S202-38)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 321

Description:
This step verifies the wire path from W2 P2-46 to W2 P2-48. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-162 (S506-2)	to ID A1P10.1
From ID A1J10.1	to ID A1J12.48
From ID A1P12.48	to ID P12-44 (S701-2)
From ID P13-46 (S701-48)	to ID A1P15.20
From ID A1J15.20	to ID J2A-8B
From W2 P1A-8B	to W2 P2-46 (ST J1-46)
From ST_J1-46	to ST_J1-48
From W2 P2-48 (ST J1-48)	to W2 P1A-1D
From ID J2A-1D	to ID A1J14.37
From ID A1P14.37	to ID P13-54 (S201-51)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2

From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 322

Description:
This step verifies the wire path from W2 P2-49 to W2 P2-50. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-177 (S509-1)	to ID A1P10.10
From ID A1J10.10	to ID A1J12.38
From ID A1P12.38	to ID P12-59 (S202-1)
From ID P12-61 (S202-21)	to ID A1P12.21
From ID A1J12.21	to ID J2B-6A
From W2 P1B-6A	to W2 P2-49 (ST J1-49)
From ST_J1-49	to ST_J1-50
From W2 P2-50 (ST J1-50)	to W2 P1B-8B
From ID J2B-8B	to ID A1J12.26
From ID A1P12.26	to ID P12-25 (S202-6)
From ID P13-29 (S202-4)	to ID A1P14.50
From ID A1J14.50	to ID A1J10.50
From ID A1P10.50	to ID P11-244 (S510-2)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 323

Description:
This step verifies the wire path from W2 P2-52 to W1 P2-69. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-177 (S509-1)	to ID A1P10.10
From ID A1J10.10	to ID A1J12.38
From ID A1P12.38	to ID P12-59 (S202-1)
From ID P12-91 (S202-23)	to ID A1P12.22
From ID A1J12.22	to ID J2B-7C
From W2 P1B-7C	to W2 P2-52 (ST J1-52)
From ST_J1-52	to ST_J1-51
From W2 P2-51 (ST J1-51)	to W2 P1B-4F
From ID J2B-4F	to ID A1J1.11
From ID A1P1.11	to ID P1-11 (DC4-LO)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 324

Description:
This step verifies the wire path from W2 P2-53 to W2 P2-54. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-177 (S509-1)	to ID A1P10.10
From ID A1J10.10	to ID A1J12.38
From ID A1P12.38	to ID P12-59 (S202-1)
From ID P13-59 (S202-25)	to ID A1P14.30
From ID A1J14.30	to ID J2A-1B
From W2 P1A-1B	to W2 P2-53 (ST J1-53)

```
      From ST_J1-53                    to ST_J1-54

      From W2 P2-54 (ST J1-54)         to W2 P1A-2F
      From ID J2A-2F                   to ID A1J14.42
      From ID A1P14.42                 to ID P13-28 (S202-27)

      From ID P13-93 (S202-3)          to ID A1P14.49
      From ID A1J14.49                 to ID A1J10.48
      From ID A1P10.48                 to ID P11-52 (S510-1)
      From ID P11-147 (S510-4)         to ID A1P9.31
      From ID A1J9.31                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 325

Description:
This step verifies the wire path from W2 P2-55 to W2 P2-56. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.19
      From ID A1P9.19                  to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)         to ID A1P10.10
      From ID A1J10.10                 to ID A1J12.38
      From ID A1P12.38                 to ID P12-59 (S202-1)

      From ID P12-28 (S202-29)         to ID A1P12.35
      From ID A1J12.35                 to ID J2B-12F
      From W2 P1B-12F                  to W2 P2-55 (ST J1-55)

      From ST_J1-55                    to ST_J1-56

      From W2 P2-56 (ST J1-56)         to W2 P1A-3E
      From ID J2A-3E                   to ID A1J14.38
      From ID A1P14.38                 to ID P13-85 (S201-52)

      From ID P12-80 (S201-2)          to ID A1P12.40
      From ID A1J12.40                 to ID A1J10.8
      From ID A1P10.8                  to ID P11-139 (S508-2)
      From ID P11-12 (S508-4)          to ID A1P9.25
      From ID A1J9.25                  to ID BUS 2
```

```
      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 326

Description:
This step verifies the wire path from W2 P2-58 to W2 P2-60. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P13.13 (S701-49)         to ID A1P15.17
      From ID A1J15.17                 to ID J1A-11E
      From W2 P1A-11E                  to W2 P2-58 (ST J1-58)

      From ST_J1-58                    to ST_J1-60

      From W2 P2-60 (ST J1-60)         to W2 P1A-1F
      From ID J2A-1F                   to ID A1J14.43
      From ID A1P14.43                 to ID P13-60 (S202-28)

      From ID P12-90 (S202-2)          to ID A1P12.36
      From ID A1J12.36                 to ID A1J10.12
      From ID A1P10.12                 to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 327

Description:
This step verifies the wire path from W2 P2-61 to W2 P2-62. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-242 (S509-2)	to ID A1P10.12
From ID A1J10.12	to ID A1J12.36
From ID A1P12.36	to ID P12-90 (S202-2)
From ID P13-57 (S202-12)	to ID A1P14.41
From ID A1J14.41	to ID J2A-3F
From W2 P1A-3F	to W2 P2-61 (ST J1-61)
From ST_J1-61	to ST_J1-62
From W2 P2-62 (ST J1-62)	to W2 P1B-13F
From ID J2B-13F	to ID A1J12.32
From ID A1P12.32	to ID P12-88 (S202-14)
From ID P13-29 (S202-4)	to ID A1P14.50
From ID A1J14.50	to ID A1J10.50
From ID A1P10.50	to ID P11-244 (S510-2)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 328

Description:
This step verifies the wire path from W2 P2-63 to W2 P2-67. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-242 (S509-2)	to ID A1P10.12
From ID A1J10.12	to ID A1J12.36

```
      From ID A1P12.36                  to ID P12-90 (S202-2)

      From ID P12-30 (S202-40)          to ID A1P12.49
      From ID A1J12.49                  to ID J2B-5F
      From W2 P1B-5F                    to W2 P2-63 (ST J1-63)

      From ST_J1-63                     to ST_J1-67

      From W2 P2-67 (ST J1-67)          to W2 P1B-12E
      From ID J2B-12E                   to ID A1J12.34
      From ID A1P12.34                  to ID P12-58 (S202-16)

      From ID P13-29 (S202-4)           to ID A1P14.50
      From ID A1J14.50                  to ID A1J10.50
      From ID A1P10.50                  to ID P11-244 (S510-2)
      From ID P11-147 (S510-4)          to ID A1P9.31
      From ID A1J9.31                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)
```

Step 329

Description:
This step verifies the wire path from W2 P2-64 to W2 P2-69. The DMM resource will be used to measure resistance UL= 10 ohms.

```
      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.19
      From ID A1P9.19                   to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)          to ID A1P10.10
      From ID A1J10.10                  to ID A1J12.38
      From ID A1P12.38                  to ID P12-59 (S202-1)

      From ID P12-93 (S202-31)          to ID A1P12.39
      From ID A1J12.39                  to ID J2B-10F
      From W2 P1B-10F                   to W2 P2-64 (ST J1-64)

      From ST_J1-64                     to ST_J1-69

      From W2 P2-69 (ST J1-69)          to W2 P1A-4D
      From ID J2A-4D                    to ID A1J14.44
      From ID A1P14.44                  to ID P13-61 (S202-33)
```

```
      From ID P13-93 (S202-3)          to ID A1P14.49
      From ID A1J14.49                 to ID A1J10.48
      From ID A1P10.48                 to ID P11-52 (S510-1)
      From ID P11-147 (S510-4)         to ID A1P9.31
      From ID A1J9.31                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 330

Description:
This step verifies the wire path from W2 P2-65 to W2 P2-70. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J8.26
      From ID A1P8.26                  to ID P10-139 (S503-2)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-162 (S506-2)         to ID A1P10.1
      From ID A1J10.1                  to ID A1J12.48
      From ID A1P12.48                 to ID P12-44 (S701-2)

      From ID P12-72 (S701-28)         to ID A1P13.23
      From ID A1J13.23                 to ID J2B-9C
      From W2 P1B-9C                   to W2 P2-65 (ST J1-65)

      From ST_J1-65                    to ST_J1-70

      From W2 P2-70 (ST J1-70)         to W2 P1B-14C
      From ID J2B-14C                  to ID A1J2.31
      From ID A1P2.31                  to ID P10-87 (S101-6)
      From ID P10-23 (S101-5)          to ID A1P2.4
      From ID A1J2.4                   to ID A1J1.3
      From ID A1P1.3                   to ID P1-10 (DC4-HI)

      From ID P1-10 (DC4-HI)           to ID A1P1.3
      From ID A1J1.3                   to ID A1J8.2
      From ID A1P8.2                   to ID P10-141 (S301-70)
      From ID P10-44 (S301-69)         to ID A1P8.31
      From ID A1J8.31                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-203 (S503-1)         to ID A1P8.28
```

From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)

Step 331

Description:
This step verifies the wire path from W2P3-A1 to W2P3-A10. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From ID BUS 2	to ID A1J9.35
From ID A1P9.35	to ID P11-93 (S404-1)
From ID P11-28 (S404-4)	to ID A1P10.9
From ID A1J10.9	to ID J2B-3D
From W2 P1B-3D	to W2 P3-A1 (ST J3-1)
From ST_J3-A1	to ST_J3-A10
From W2 P3-A10 (ST J3-19)	to W2 P1A-5F
From ID J2A-5F	to ID A1J9.7
From ID A1P9.7	to ID P11-221 (S406-5)
From ID P11-92 (S406-1)	to ID A1P9.49
From ID A1J9.49	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 332

Description:
This step verifies the wire path from W2 P3-A2 to W2 P3-B5. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From ID BUS 2	to ID A1J9.35
From ID A1P9.35	to ID P11-93 (S404-1)
From ID P11-220 (S404-5)	to ID A1P10.11

```
      From ID A1J10.11                  to ID J2B-2E
      From W2 P1B-2E                    to W2 P3-A2 (ST J3-3)

      From ST_J3-A2                     to ST_J3-B5

      From W2 P3-B5 (ST J3-10)          to W2 P1A-14B
      From ID J2A-14B                   to ID A1J10.17
      From ID A1P10.17                  to ID P11-61 (S405-4)
      From ID P11-94 (S405-1)           to ID A1P9.47
      From ID A1J9.47                   to ID BUS 3

      From ID BUS 3                     to ID A1J6.31
      From ID A1P6.31                   to ID P10-10 (S503-5)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 333

Description:
This step verifies the wire path from W2P3-A3 to W2P3-B7. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)           to ID A1P6.31
      From ID A1J6.31                   to ID BUS 3

      From ID BUS 3                     to ID A1J9.47
      From ID A1P9.47                   to ID P11-94 (S405-1)
      From ID P11-158 (S405-2)          to ID A1P10.13
      From ID A1J10.13                  to ID J2B-2D
      From W2 P1B-2D                    to W2 P3-A3 (ST J3-5)

      From ST_J3-A3                     to ST_J3-B7

      From W2 P3-B7 (ST J3-14)          to W2 P1A-8F
      From ID J2A-8F                    to ID A1J9.1
      From ID A1P9.1                    to ID P11-191 (S406-2)
      From ID P11-92 (S406-1)           to ID A1P9.49
      From ID A1J9.49                   to ID BUS 4

      From ID BUS 4                     to ID A1J6.39
      From ID A1P6.39                   to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 334

Description:
```

This step verifies the wire path from W2P3-B4 to W2P3-B8. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J9.47
From ID A1P9.47	to ID P11-94 (S405-1)
From ID P11-222 (S405-3)	to ID A1P10.15
From ID A1J10.15	to ID J2B-14A
From W2 P1B-14A	to W2 P3-B4 (ST J3-8)
From ST_J3-B4	to ST_J3-B8
From W2 P3-B8 (ST J3-16)	to W2 P1A-7F
From ID J2A-7F	to ID A1J9.3
From ID A1P9.3	to ID P11-95 (S406-3)
From ID P11-92 (S406-1)	to ID A1P9.49
From ID A1J9.49	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 335

Description:
This step verifies the wire path from W2P3-B6 to W2P3-B9. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J9.47
From ID A1P9.47	to ID P11-94 (S405-1)
From ID P11-253 (S405-5)	to ID A1P10.19
From ID A1J10.19	to ID J2A-14C
From W2 P1A-14C	to W2 P3-B6 (ST J3-12)
From ST_J3-B6	to ST_J3-B9
From W2 P3-B9 (ST J3-18)	to W2 P1A-6F

```
      From ID J2A-6F                    to ID A1J9.5
      From ID A1P9.5                    to ID P11-60 (S406-4)
      From ID P11-92 (S406-1)           to ID A1P9.49
      From ID A1J9.49                   to ID BUS 4

      From ID BUS 4                     to ID A1J6.39
      From ID A1P6.39                   to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)
```

Step 336

Description:
This step verifies the wire path from W2P4-A1 to W2P4-A10. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)           to ID A1P6.31
      From ID A1J6.31                   to ID BUS 3

      From ID BUS 3                     to ID A1J15.45
      From ID A1P15.45                  to ID P13-2 (S751-1)
      From ID P12-34 (S751-3)           to ID A1P13.35
      From ID A1J13.35                  to ID J2B-11A
      From W2 P1B-11D                   to W2 P4-A1 (ST J4-1)

      From ST_J4-A1                     to ST_J4-A10

      From W2 P4-A10 (ST J4-19)         to W2 P1B-11B
      From ID J2B-11B                   to ID A1J13.37
      From ID A1P13.37                  to ID P12-1 (S751-4)
      From ID P13-66 (S751-2)           to ID A1P15.47
      From ID A1J15.47                  to ID BUS 4

      From ID BUS 4                     to ID A1J6.39
      From ID A1P6.39                   to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)
```

Step 337

Description:
This step verifies the wire path from W2P4-B1 to W2P4-A11. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)            to ID A1P15.49
```

```
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J6.21
      From ID A1P6.21                   to ID P10-92 (S403-1)
      From ID P10-191 (S403-2)          to ID A1P7.15
      From ID A1J7.15                   to ID J2A-12F
      From W2 P1A-12F                   to W2 P4-B1 (ST J4-2)

      From ST_J4-B1                     to ST_J4-A11

      From W2 P4-A11 (ST J4-21)         to W2 P1B-3D
      From ID J2B-3D                    to ID A1J10.9
      From ID A1P10.9                   to ID P11-28 (S404-4)
      From ID P11-93 (S404-1)           to ID A1P9.35
      From ID A1J9.35                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 338

Description:
This step verifies the wire path from W2P4-A2 to W2P4-B5. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)           to ID A1P6.31
      From ID A1J6.31                   to ID BUS 3

      From ID BUS 3                     to ID A1J15.45
      From ID A1P15.45                  to ID P13-2 (S751-1)
      From ID P12-65 (S751-5)           to ID A1P13.39
      From ID A1J13.39                  to ID J2B-12A
      From W2 P1B-12A                   to W2 P4-A2 (ST J4-3)

      From ST_J4-A2                     to ST_J4-B5

      From W2 P4-B5 (ST J4-10)          to W2 P1B-12B
      From ID J2B-12B                   to ID A1J13.41
      From ID A1P13.41                  to ID P12-33 (S751-6)
      From ID P13-66 (S751-2)           to ID A1P15.47
      From ID A1J15.47                  to ID BUS 4

      From ID BUS 4                     to ID A1J6.39
```

```
      From ID A1P6.39                  to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 339

Description:
This step verifies the wire path from W2P4-B2 to W2P4-A5. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)          to ID A1P6.31
      From ID A1J6.31                  to ID BUS 3

      From ID BUS 3                    to ID A1J15.45
      From ID A1P15.45                 to ID P13-2 (S751-1)
      From ID P13-68 (S751-7)          to ID A1P15.21
      From ID A1J15.21                 to ID J2A-9A
      From W2 P1A-9A                   to W2 P4-B2 (ST J4-4)

      From ST_J4-B2                    to ST_J4-A5

      From W2 P4-A5 (ST J4-9)          to W2 P1A-7A
      From ID J2A-7A                   to ID A1J15.23
      From ID A1P15.23                 to ID P13-36 (S751-8)
      From ID P13-66 (S751-2)          to ID A1P15.47
      From ID A1J15.47                 to ID BUS 4

      From ID BUS 4                    to ID A1J6.39
      From ID A1P6.39                  to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 340

Description:
This step verifies the wire path from W2P4-A3 to W2P4-B6. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)          to ID A1P6.31
      From ID A1J6.31                  to ID BUS 3

      From ID BUS 3                    to ID A1J15.45
```

```
      From ID A1P15.45                to ID P13-2 (S751-1)
      From ID P13-67 (S751-9)         to ID A1P15.25
      From ID A1J15.25                to ID J2A-6A
      From W2 P1A-6A                  to W2 P4-A3 (ST J4-5)

      From ST_J4-A3                   to ST_J4-B6

      From W2 P4-B6 (ST J4-12)        to W2 P1A-5A
      From ID J2A-5A                  to ID A1J15.27
      From ID A1P15.27                to ID P13-35 (S751-10)
      From ID P13-66 (S751-2)         to ID A1P15.47
      From ID A1J15.47                to ID BUS 4

      From ID BUS 4                   to ID A1J6.39
      From ID A1P6.39                 to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)        to ID A1P8.26
      From ID A1J8.26                 to ID A1J15.50
      From ID A1P15.50                to ID P20-3 (DMM-LO)

Step 341

Description:
This step verifies the wire path from W2P4-B3 to W2P4-A6. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)         to ID A1P6.31
      From ID A1J6.31                 to ID BUS 3

      From ID BUS 3                   to ID A1J15.45
      From ID A1P15.45                to ID P13-2 (S751-1)
      From ID P12-67 (S751-11)        to ID A1P13.43
      From ID A1J13.43                to ID J2B-13A
      From W2 P1B-13A                 to W2 P4-B3 (ST J4-6)

      From ST_J4-B3                   to ST_J4-A6

      From W2 P4-A6 (ST J4-11)        to W2 P1B-13B
      From ID J2B-13B                 to ID A1J13.45
      From ID A1P13.45                to ID P12-35 (S751-12)
      From ID P13-66 (S751-2)         to ID A1P15.47
      From ID A1J15.47                to ID BUS 4

      From ID BUS 4                   to ID A1J6.39
      From ID A1P6.39                 to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)        to ID A1P8.26
      From ID A1J8.26                 to ID A1J15.50
      From ID A1P15.50                to ID P20-3 (DMM-LO)
```

Step 342

Description:
This step verifies the wire path from W2P4-A4 to W2P4-A8. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J15.45
From ID A1P15.45	to ID P13-2 (S751-1)
From ID P12-2 (S751-13)	to ID A1P13.47
From ID A1J13.47	to ID J2B-14A
From W2 P1B-14A	to W2 P4-A4 (ST J4-7)
From ST_J4-A4	to ST_J4-A8
From W2 P4-A8 (ST J4-15)	to W2 P1B-14B
From ID J2B-14B	to ID A1J13.49
From ID A1P13.49	to ID P12-66 (S751-14)
From ID P13-66 (S751-2)	to ID A1P15.47
From ID A1J15.47	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 343

Description:
This step verifies the wire path from W2P4-B4 to W2P4-B8. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J15.45
From ID A1P15.45	to ID P13-2 (S751-1)
From ID P13-5 (S751-15)	to ID A1P15.30
From ID A1J15.30	to ID J2A-4A
From W2 P1A-4A	to W2 P4-B4 (ST J4-8)

```
      From ST_J4-B4                    to ST_J4-B8

      From W2 P4-B8 (ST J4-16)         to W2 P1A-4B
      From ID J2A-4B                   to ID A1J15.32
      From ID A1P15.32                 to ID P13-69 (S751-16)
      From ID P13-66 (S751-2)          to ID A1P15.47
      From ID A1J15.47                 to ID BUS 4

      From ID BUS 4                    to ID A1J6.39
      From ID A1P6.39                  to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 344

Description:
This step verifies the wire path from W2P4-A7 to W2P4-B11. The DMM resource will be used to measure resistance UL= 10 ohms.

```
      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J6.21
      From ID A1P6.21                  to ID P10-92 (S403-1)
      From ID P10-95 (S403-3)          to ID A1P7.17
      From ID A1J7.17                  to ID J2A-11F
      From W2 P1A-11F                  to W2 P4-A7 (ST J4-13)

      From ST_J4-A7                    to ST_J4-B11

      From W2 P4-B11 (ST J4-22)        to W2 P1B-3F
      From ID J2B-3F                   to ID A1J10.5
      From ID A1P10.5                  to ID P11-190 (S404-2)
      From ID P11-93 (S404-1)          to ID A1P9.35
      From ID A1J9.35                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 345

Description:
This step verifies the wire path from W2P4-B7 to W2P4-B9. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-10 (S503-5)	to ID A1P6.31
From ID A1J6.31	to ID BUS 3
From ID BUS 3	to ID A1J15.45
From ID A1P15.45	to ID P13-2 (S751-1)
From ID P13-37 (S751-17)	to ID A1P15.34
From ID A1J15.34	to ID J2A-3A
From W2 P1A-3A	to W2 P4-B7 (ST J4-14)
From ST_J4-B7	to ST_J4-B9
From W2 P4-B9 (ST J4-18)	to W2 P1A-3B
From ID J2A-3B	to ID A1J15.36
From ID A1P15.36	to ID P13-4 (S751-18)
From ID P13-66 (S751-2)	to ID A1P15.47
From ID A1J15.47	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 346

Description:
This step verifies the wire path from W2P4-A9 to W2P4-A12. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J6.21
From ID A1P6.21	to ID P10-92 (S403-1)
From ID P10-60 (S403-4)	to ID A1P7.19
From ID A1J7.19	to ID J2A-10F
From W2 P1A-10F	to W2 P4-A9 (ST J4-17)
From ST_J4-A9	to ST_J4-A12
From W2 P4-A12 (ST J4-23)	to W2 P1B-2E
From ID J2B-2E	to ID A1J10.11
From ID A1P10.11	to ID P11-220 (S404-5)
From ID P11-93 (S404-1)	to ID A1P9.35

```
      From ID A1J9.35                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)
```

Step 347

Description:
This step verifies the wire path from W2P4-B10 to W2P4-B14. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J6.21
      From ID A1P6.21                   to ID P10-92 (S403-1)
      From ID P10-221 (S403-5)          to ID A1P7.21
      From ID A1J7.21                   to ID J2A-9F
      From W2 P1A-9F                    to W2 P4-B10 (ST J4-20)

      From ST_J4-B10                    to ST_J4-B14

      From W2 P4-B14 (ST J4-28)         to W2 P1B-3E
      From ID J2B-3E                    to ID A1J10.7
      From ID A1P10.7                   to ID P11-254 (S404-3)
      From ID P11-93 (S404-1)           to ID A1P9.35
      From ID A1J9.35                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)
```

Step 348

Description:
This step verifies the wire path from W2P4-A13 to W2P4-A17. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)           to ID A1P6.31
```

```
      From ID A1J6.31                  to ID BUS 3

      From ID BUS 3                    to ID A1J9.47
      From ID A1P9.47                  to ID P11-94 (S405-1)
      From ID P11-158 (S405-2)         to ID A1P10.13
      From ID A1J10.13                 to ID J2B-2D
      From W2 P1B-2D                   to W2 P4-A13 (ST J4-25)

      From ST_J4-A13                   to ST_J4-A17

      From W2 P4-A17 (ST J4-33)        to W2 P1A-8F
      From ID J2A-8F                   to ID A1J9.1
      From ID A1P9.1                   to ID P11-191 (S406-2)
      From ID P11-92 (S406-1)          to ID A1P9.49
      From ID A1J9.49                  to ID BUS 4

      From ID BUS 4                    to ID A1J6.39
      From ID A1P6.39                  to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 349

Description:
This step verifies the wire path from W2P4-A14 to W2P4-A18. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)          to ID A1P6.31
      From ID A1J6.31                  to ID BUS 3

      From ID BUS 3                    to ID A1J9.47
      From ID A1P9.47                  to ID P11-94 (S405-1)
      From ID P11-222 (S405-3)         to ID A1P10.15
      From ID A1J10.15                 to ID J2A-14A
      From W2 P1A-14A                  to W2 P4-A14 (ST J4-27)

      From ST_J4-A14                   to ST_J4-A18

      From W2 P4-A18 (ST J4-35)        to W2 P1A-7F
      From ID J2A-7F                   to ID A1J9.3
      From ID A1P9.3                   to ID P11-95 (S406-3)
      From ID P11-92 (S406-1)          to ID A1P9.49
      From ID A1J9.49                  to ID BUS 4

      From ID BUS 4                    to ID A1J6.39
      From ID A1P6.39                  to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
```

```
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 350

Description:
This step verifies the wire path from W2P4-A15 to W2P4-A19. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)           to ID A1P6.31
      From ID A1J6.31                   to ID BUS 3

      From ID BUS 3                     to ID A1J9.47
      From ID A1P9.47                   to ID P11-94 (S405-1)
      From ID P11-61 (S405-4)           to ID A1P10.17
      From ID A1J10.17                  to ID J2A-14B
      From W2 P1A-14B                   to W2 P4-A15 (ST J4-29)

      From ST_J4-A15                    to ST_J4-A19

      From W2 P4-A19 (ST J4-37)         to W2 P1A-6F
      From ID J2A-6F                    to ID A1J9.5
      From ID A1P9.5                    to ID P11-60 (S406-4)
      From ID P11-92 (S406-1)           to ID A1P9.49
      From ID A1J9.49                   to ID BUS 4

      From ID BUS 4                     to ID A1J6.39
      From ID A1P6.39                   to ID P10-74 (S503-6)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 351

Description:
This step verifies the wire path from W2P4-A16 to W2P4-A20. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-10 (S503-5)           to ID A1P6.31
      From ID A1J6.31                   to ID BUS 3

      From ID BUS 3                     to ID A1J9.47
      From ID A1P9.47                   to ID P11-94 (S405-1)
      From ID P11-253 (S405-5)          to ID A1P10.19
      From ID A1J10.19                  to ID J2A-14C
```

From W2 P1A-14C	to W2 P4-A16 (ST J4-31)
From ST_J4-A16	to ST_J4-A20
From W2 P4-A20 (ST J4-39)	to W2 P1A-5F
From ID J2A-5F	to ID A1J9.7
From ID A1P9.7	to ID P11-221 (S406-5)
From ID P11-92 (S406-1)	to ID A1P9.49
From ID A1J9.49	to ID BUS 4
From ID BUS 4	to ID A1J6.39
From ID A1P6.39	to ID P10-74 (S503-6)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

2.7 MODULE 4 W3 CDA GRENADE RELAY SPECIFIC ID TESTS

Refer to Reference Drawings when diagnosing connection paths.

Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF TEST PWB, A2).pdf and 13020A7301 (CABLE, W3, SCHEMATIC).pdf in section 1.4 during review of the following steps.

Step 401

Description:
This step verifies the wire path from W3 P2-1 to W3 P2-2. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P12-14 (S201-13)	to ID A1P13.5
From ID A1J13.5	to ID J1B-14C
From W3 P1B-14C	to W3 P2-1 (ST J2-1)
From ST_J2-1	to ST_J2-2
From W3 P2-2 (ST J2-2)	to W3 P1B-13C
From ID J1B-13C	to ID A1J13.6
From ID A1P13.6	to ID P12-78 (S201-14)

```
      From ID P12-52 (S201-4)          to ID A1P12.44
      From ID A1J12.44                 to ID A1J10.4
      From ID A1P10.4                  to ID P11-71 (S507-2)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 402

Description:
This step verifies the wire path from W3 P2-3 to W3 P2-4. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P12-79 (S201-5)          to ID A1P13.1
      From ID A1J13.1                  to ID J1B-14A
      From W3 P1B-14A                  to W3 P2-3 (ST J2-3)

      From ST_J2-3                     to ST_J2-4

      From W3 P2-4 (ST J2-4)           to W3 P1A-2E
      From ID J1A-2E                   to ID A1J14.10
      From ID A1P14.10                 to ID P13-83 (S201-27)

      From ID P12-20 (S201-3)          to ID A1P12.46
      From ID A1J12.46                 to ID A1J10.2
      From ID A1P10.2                  to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 403

Description:
This step verifies the wire path from W3 P2-5 to W3 P2-9. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-242 (S509-2)	to ID A1P10.12
From ID A1J10.12	to ID A1J12.36
From ID A1P12.36	to ID P12-90 (S202-2)
From ID P12-63 (S202-46)	to ID A1P12.16
From ID A1J12.16	to ID J1B-4A
From W3 P1B-4A	to W3 P2-5 (ST J2-5)
From ST_J2-5	to ST_J2-9
From W3 P2-9 (ST J2-9)	to W3 P1B-13A
From ID J1B-13A	to ID A1J13.2
From ID A1P13.2	to ID P12-47 (S201-6)
From ID P12-80 (S201-2)	to ID A1P12.40
From ID A1J12.40	to ID A1J10.8
From ID A1P10.8	to ID P11-139 (S508-2)
From ID P11-12 (S508-4)	to ID A1P9.25
From ID A1J9.25	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 404

Description:
This step verifies the wire path from W3 P2-6 to W3 P2-7. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13

```
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P12-82 (S201-29)         to ID A1P12.7
      From ID A1J12.7                  to ID J1B-7A
      From W3 P1B-7A                   to W3 P2-6 (ST J2-6)

      From ST_J2-6                     to ST_J2-7

      From W3 P2-7 (ST J2-7)           to W3 P1B-13B
      From ID J1B-13B                  to ID A1J13.4
      From ID A1P13.4                  to ID P12-13 (S201-8)

      From ID P12-52 (S201-4)          to ID A1P12.44
      From ID A1J12.44                 to ID A1J10.4
      From ID A1P10.4                  to ID P11-71 (S507-2)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 405

Description:
This step verifies the wire path from W3 P2-8 to W3 P2-11. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P12-46 (S201-7)          to ID A1P13.3
      From ID A1J13.3                  to ID J1B-14B
      From W3 P1B-14B                  to W3 P2-8 (ST J2-8)
```

```
      From ST_J2-8                      to ST_J2-11

      From W3 P2-11 (ST J2-11)          to W3 P1B-5C
      From ID J1B-5C                    to ID A1J12.15
      From ID A1P12.15                  to ID P12-31 (S202-45)

      From ID P12-59 (S202-1)           to ID A1P12.38
      From ID A1J12.38                  to ID A1J10.10
      From ID A1P10.10                  to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)
```

Step 406

Description:
This step verifies the wire path from W3 P2-10 to W3 P2-12. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.19
      From ID A1P9.19                   to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)          to ID A1P10.10
      From ID A1J10.10                  to ID A1J12.38
      From ID A1P12.38                  to ID P12-59 (S202-1)

      From ID P13-88 (S202-19)          to ID A1P14.19
      From ID A1J14.19                  to ID J1A-6E
      From W3 P1A-6E                    to W3 P2-10 (ST J2-10)

      From ST_J2-10                     to ST_J2-12

      From W3 P2-12 (ST J2-12)          to W3 P1A-1C
      From ID J1A-1C                    to ID A1J14.5
      From ID A1P14.5                   to ID P13-49 (S201-17)

      From ID P12-16 (S201-1)           to ID A1P12.42
      From ID A1J12.42                  to ID A1J10.6
      From ID A1P10.6                   to ID P11-203 (S508-1)
      From ID P11-12 (S508-4)           to ID A1P9.25
      From ID A1J9.25                   to ID BUS 2
```

```
      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 407

Description:
This step verifies the wire path from W3 P2-13 to W3 P2-15. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P13-15 (S201-19)         to ID A1P14.6
      From ID A1J14.6                  to ID J1A-2C
      From W3 P1A-2C                   to W3 P2-13 (ST J2-13)

      From ST_J2-13                    to ST_J2-15

      From W3 P2-15 (ST J2-15)         to W3 P1A-2A
      From ID J1A-2A                   to ID A1J14.2
      From ID A1P14.2                  to ID P13-14 (S201-10)

      From ID P12-52 (S201-4)          to ID A1P12.44
      From ID A1J12.44                 to ID A1J10.4
      From ID A1P10.4                  to ID P11-71 (S507-2)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 408

Description:
This step verifies the wire path from W3 P2-14 to W3 P2-16. The DMM
resource will be used to measure resistance UL= 10 ohms.
```

```
      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)          to ID A1P10.8
      From ID A1J10.8                   to ID A1J12.40
      From ID A1P12.40                  to ID P12-80 (S201-2)

      From ID P12-50 (S201-30)          to ID A1P12.8
      From ID A1J12.8                   to ID J1B-7B
      From W3 P1B-7B                    to W3 P2-14 (ST J2-14)

      From ST_J2-14                     to ST_J2-16

      From W3 P2-16 (ST J2-16)          to W3 P1A-5F
      From ID J1A-5F                    to ID A1J14.18
      From ID A1P14.18                  to ID P13-89 (S202-18)

      From ID P12-90 (S202-2)           to ID A1P12.36
      From ID A1J12.36                  to ID A1J10.12
      From ID A1P10.12                  to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 409

Description:
This step verifies the wire path from W3 P2-17 to W3 P2-20. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.19
      From ID A1P9.19                   to ID P11-18 (S509-3)
      From ID P11-242 (S509-2)          to ID A1P10.12
      From ID A1J10.12                  to ID A1J12.36
```

```
      From ID A1P12.36                  to ID P12-90 (S202-2)

      From ID P13-24 (S202-20)          to ID A1P14.20
      From ID A1J14.20                  to ID J1A-6F
      From W3 P1A-6F                    to W3 P2-17 (ST J2-17)

      From ST_J2-17                     to ST_J2-20

      From W3 P2-20 (ST J2-20)          to W3 P1A-1D
      From ID J1A-1D                    to ID A1J14.7
      From ID A1P14.7                   to ID P13-79 (S201-20)

      From ID P12-80 (S201-2)           to ID A1P12.40
      From ID A1J12.40                  to ID A1J10.8
      From ID A1P10.8                   to ID P11-139 (S508-2)
      From ID P11-12 (S508-4)           to ID A1P9.25
      From ID A1J9.25                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 410

Description:
This step verifies the wire path from W3 P2-19 to W3 P2-21. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)          to ID A1P10.8
      From ID A1J10.8                   to ID A1J12.40
      From ID A1P12.40                  to ID P12-80 (S201-2)

      From ID P13-16 (S201-18)          to ID A1P14.28
      From ID A1J14.28                  to ID J1A-10F
      From W3 P1A-10F                   to W3 P2-19 (ST J2-19)

      From ST_J2-19                     to ST_J2-21

      From W3 P2-21 (ST J2-21)          to W3 P1B-9A
      From ID J1B-9A                    to ID A1J12.1
      From ID A1P12.1                   to ID P12-48 (S201-15)
```

```
      From ID P12-20 (S201-3)          to ID A1P12.46
      From ID A1J12.46                 to ID A1J10.2
      From ID A1P10.2                  to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 411

Description:
This step verifies the wire path from W3 P2-22 to W3 P2-24. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P12-15 (S201-16)         to ID A1P12.2
      From ID A1J12.2                  to ID J1B-9B
      From W3 P1B-9B                   to W3 P2-22 (ST J2-22)

      From ST_J2-22                    to ST_J2-24

      From W3 P2-24 (ST J2-24)         to W3 P1B-14D
      From ID J1B-14D                  to ID A1J13.7
      From ID A1P13.7                  to ID P12-4 (S701-3)

      From ID P12-76 (S701-1)          to ID A1P12.50
      From ID A1J12.50                 to ID A1J10.3
      From ID A1P10.3                  to ID P11-194 (S506-1)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 412

Description:
This step verifies the wire path from W3 P2-23 to W3 P2-26. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P13-51 (S201-28)	to ID A1P14.11
From ID A1J14.11	to ID J1A-1F
From W3 P1A-1F	to W3 P2-23 (ST J2-23)
From ST_J2-23	to ST_J2-26
From W3 P2-26 (ST J2-26)	to W3 P1B-7C
From ID J1B-7C	to ID A1J12.9
From ID A1P12.9	to ID P12-19 (S201-31)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 413

Description:
This step verifies the wire path from W3 P2-25 to W3 P2-29. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13

```
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P13-52 (S201-33)         to ID A1P14.12
      From ID A1J14.12                 to ID J1A-2F
      From W3 P1A-2F                   to W3 P2-25 (ST J2-25)

      From ST_J2-25                    to ST_J2-29

      From W3 P2-29 (ST J2-29)         to W3 P1B-13D
      From ID J1B-13D                  to ID A1J13.8
      From ID A1P13.8                  to ID P12-68 (S701-4)

      From ID P12-44 (S701-2)          to ID A1P12.48
      From ID A1J12.48                 to ID A1J10.1
      From ID A1P10.1                  to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 414

Description:
This step verifies the wire path from W3 P2-28 to W3 P2-31. The DMM resource will be used to measure resistance UL= 10 ohms.

```
      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P13-47 (S201-9)          to ID A1P14.1
      From ID A1J14.1                  to ID J1A-1A
      From W3 P1A-1A                   to W3 P2-28 (ST J2-28)
```

```
      From ST_J2-28                    to ST_J2-31

      From W3 P2-31 (ST J2-31)         to W3 P1B-9C
      From ID J1B-9C                   to ID A1J12.3
      From ID A1P12.3                  to ID P12-51 (S201-21)

      From ID P12-20 (S201-3)          to ID A1P12.46
      From ID A1J12.46                 to ID A1J10.2
      From ID A1P10.2                  to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 415

Description:
This step verifies the wire path from W3 P2-30 to W3 P2-40. The DMM resource will be used to measure resistance UL= 10 ohms.

```
      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P12-83 (S201-32)         to ID A1P12.10
      From ID A1J12.10                 to ID J1B-6A
      From W3 P1B-6A                   to W3 P2-30 (ST J2-30)

      From ST_J2-30                    to ST_J2-40

      From W3 P2-40 (ST J2-40)         to W3 P1A-1E
      From ID J1A-1E                   to ID A1J14.9
      From ID A1P14.9                  to ID P13-17 (S201-26)

      From ID P12-52 (S201-4)          to ID A1P12.44
      From ID A1J12.44                 to ID A1J10.4
      From ID A1P10.4                  to ID P11-71 (S507-2)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2
```

```
      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 416

Description:
This step verifies the wire path from W3 P2-32 to W3 P2-33. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P12-17 (S201-23)         to ID A1P12.5
      From ID A1J12.5                  to ID J1B-8B
      From W3 P1B-8B                   to W3 P2-32 (ST J2-32)

      From ST_J2-32                    to ST_J2-33

      From W3 P2-33 (ST J2-33)         to W3 P1A-2D
      From ID J1A-2D                   to ID A1J14.8
      From ID A1P14.9                  to ID P13-50 (S201-25)

      From ID P12-20 (S201-3)          to ID A1P12.46
      From ID A1J12.46                 to ID A1J10.2
      From ID A1P10.2                  to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 417

Description:
This step verifies the wire path from W3 P2-34 to W3 P2-35. The DMM
resource will be used to measure resistance UL= 10 ohms.
```

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P13-19 (S201-34)	to ID A1P14.13
From ID A1J14.13	to ID J1A-3E
From W3 P1A-3E	to W3 P2-34 (ST J2-34)
From ST_J2-34	to ST_J2-35
From W3 P2-35 (ST J2-35)	to W3 P1A-1B
From ID J1A-1B	to ID A1J14.3
From ID A1P14.3	to ID P13-80 (S201-11)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 418

Description:
This step verifies the wire path from W3 P2-36 to W3 P2-38. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40

```
      From ID A1P12.40                  to ID P12-80 (S201-2)

      From ID P13-48 (S201-12)          to ID A1P14.4
      From ID A1J14.4                   to ID J1A-2B
      From W3 P1A-2B                    to W3 P2-36 (ST J2-36)

      From ST_J2-36                     to ST_J2-38

      From W3 P2-38 (ST J2-38)          to W3 P1B-8A
      From ID J1B-8A                    to ID A1J12.4
      From ID A1P12.4                   to ID P12-18 (S201-22)

      From ID P12-52 (S201-4)           to ID A1P12.44
      From ID A1J12.44                  to ID A1J10.4
      From ID A1P10.4                   to ID P11-71 (S507-2)
      From ID P11-72 (S507-4)           to ID A1P9.27
      From ID A1J9.27                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)
```

Step 419

Description:
This step verifies the wire path from W3 P2-39 to W3 P2-40. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.19
      From ID A1P9.19                   to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)          to ID A1P10.10
      From ID A1J10.10                  to ID A1J12.38
      From ID A1P12.38                  to ID P12-59 (S202-1)

      From ID P13-91 (S202-35)          to ID A1P14.23
      From ID A1J14.23                  to ID J1A-8E
      From W3 P1A-8E                    to W3 P2-39 (ST J2-39)

      From ST_J2-39                     to ST_J2-40

      From W3 P2-40 (ST J2-40)          to W3 P1A-1E
      From ID J1A-1E                    to ID A1J14.9
      From ID A1P14.9                   to ID P13-17 (S201-26)
```

From ID P12-52 (S201-4)	to ID A1P12.44
From ID A1J12.44	to ID A1J10.4
From ID A1P10.4	to ID P11-71 (S507-2)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

2.8 MODULE 5 W4 GCU LOGIC SPECIFIC ID TESTS

Refer to Reference Drawings when diagnosing connection paths.

Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF TEST PWB, A2).pdf and 13020A7401 (CABLE, W4, SCHEMATIC).pdf in section 1.4 during review of the following steps.

Step 501

Description:
This step verifies proper receive path components associated with DTS ch 23 while connecting W4 with loopback.

Connection Path is as follows:

From ID P12-29 (S202-32)	to ID A1P12.41
From ID A1J12.41	to ID J2B-9F
From W4 P1B-9F	to W4 P3-B4 (ST J6-8)
From ST_J6-B4	to ST_J5-1
From W4 P2-1 (ST J5-1)	to W4 P1A-13A
From ID J2A-13A	to ID A1U4.3
From ID J2A-13A	to ID A1R12.1
From ID A1R12.2	to ID A1J1.2 (+15V)
From ID A1U4.2	to ID A1J5.36
From ID A1P5.36	to ID P6-17 (DTS CH23)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1

```
      From ID A1J1.1 (+5V)              to ID A1C1.1
      From ID A1J1.1 (+5V)              to ID A1C3.1
      From ID A1J1.1 (+5V)              to ID A1C5.1
      From ID A1J1.1 (+5V)              to ID A1C7.1
      From ID A1J1.1 (+5V)              to ID A1C8.1
      From ID A1J1.1 (+5V)              to ID A1C9.1
      From ID A1J1.1 (+5V)              to ID A1C10.1
      From ID A1J1.1 (+5V)              to ID A1C11.1
      From ID P1-2 (DC1-LO)             to ID A1P1.9
      From ID A1J1.9                    to GROUND
      From ID P1-4 (DC2-HI)             to ID A1P2.2
      From ID A1J2.2 (+15V)             to ID A1U1.16
      From ID A1J2.2 (+15V)             to ID A1U2.16
      From ID A1J2.2 (+15V)             to ID A1U3.16
      From ID A1J2.2 (+15V)             to ID A1C2.1
      From ID A1J2.2 (+15V)             to ID A1C4.1
      From ID A1J2.2 (+15V)             to ID A1C6.1
      From ID P1-5 (DC2-LO)             to ID A1P1.10
      From ID A1J1.10                   to GROUND
      From ID A1C1.2                    to GROUND
      From ID A1C2.2                    to GROUND
      From ID A1C3.2                    to GROUND
      From ID A1C4.2                    to GROUND
      From ID A1C5.2                    to GROUND
      From ID A1C6.2                    to GROUND
      From ID A1C7.2                    to GROUND
      From ID A1C8.2                    to GROUND
      From ID A1C9.2                    to GROUND
      From ID A1C10.2                   to GROUND
      From ID A1C11.2                   to GROUND
      From ID A1U1.8                    to GROUND
      From ID A1U2.8                    to GROUND
      From ID A1U3.8                    to GROUND
      From ID A1U4.8                    to GROUND
      From ID A1U5.8                    to GROUND
      From ID A1U6.8                    to GROUND
      From ID A1U7.8                    to GROUND
      From ID A1U8.8                    to GROUND
      From ID P7-24 (DTS GCH 40)        to ID A1P5.3
      From ID A1J5.3                    to GROUND
      From ID P6-64 (DTS GCH 7)         to ID A1P5.1
      From ID A1J5.1                    to GROUND

Step 502

Description:
This step verifies connectivity from S202-32 out through the proper
receive path components associated with DTS ch 23 while connecting
W4 with loopback and applying 0 volts out on ID 2B-9F.

      Connection Path is as follows:

      From ID P1-10 (DC4-HI)            to ID A1P1.3
      From ID A1J1.3                    to ID A1J8.2
```

From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2
From ID BUS 2	to ID A1J9.29
From ID A1P9.29	to ID P11-17 (S509-4)
From ID P11-242 (S509-2)	to ID A1P10.12
From ID A1J10.12	to ID A1J12.36
From ID A1P12.36	to ID P12-90 (S202-2)
From ID P12-29 (S202-32)	to ID A1P12.41
From ID A1J12.41	to ID J2B-9F
From W4 P1B-9F	to W4 P3-B4 (ST J6-8)
From ST_J6-B4	to ST_J5-1
From W4 P2-1 (ST J5-1)	to W4 P1A-13A
From ID J2A-13A	to ID A1U4.3
From ID J2A-13A	to ID A1R12.1
From ID A1R12.2	to ID A1J1.2 (+15V)
From ID A1U4.2	to ID A1J5.36
From ID A1P5.36	to ID P6-17 (DTS CH23)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND

From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 503

Description:
This step verifies the wire path from W4 P2-1 to W4 P3-B4. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P12-86 (S201-48)	to ID A1P12.28
From ID A1J12.28	to ID J2B-14E
From W4 P1B-14E	to W4 P2-1 (ST J5-1)
From ST_J5-1	to ST_J6-B4
From W4 P3-B4 (ST J6-8)	to W4 P1B-9F
From ID J2B-9F	to ID A1J12.41
From ID A1P12.41	to ID P12-29 (S202-32)

From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 504

Description:
This step verifies the wire path from DC4-LO through W4P2-2 to W4P2-19 via ST board and back to S201-52. The DMM will be used to measure resistance UL= 10 ohms as DC4-LO will tie the path to GND.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P13-85 (S201-52)	to ID A1P14.38
From ID A1J14.38	to ID J2A-3E
From W4 P1A-3E	to W4 P2-19 (ST J5-19)
From ST_J5-19	to ST_J5-2
From W4 P2-2 (ST J5-2)	to W4 P1B-4F
From ID J2B-4F	to ID A1J1.11
From ID A1P1.11	to ID P1-11 (DC4-LO)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 505

Description:

This step verifies the wire path from W4 P2-3 to W4 P3-A5. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-177 (S509-1)	to ID A1P10.10
From ID A1J10.10	to ID A1J12.38
From ID A1P12.38	to ID P12-59 (S202-1)
From ID P13-25 (S202-11)	to ID A1P14.40
From ID A1J14.40	to ID J2A-1E
From W4 P1A-1E	to W4 P2-3 (ST J5-3)
From ST_J5-3	to ST_J6-A5
From W4 P3-B3 (ST J6-9)	to W4 P1A-2E
From ID J2A-2E	to ID A1J14.39
From ID A1P14.39	to ID P13-56 (S202-9)
From ID P13-93 (S202-3)	to ID A1P14.49
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 506

Description:
This step verifies connectivity from DC4-HI out through W4 and to the proper receive path associated with DTS ch 41 while connecting W4 with loopback and applying 15 volts out on ID 2B-14C.

Connection Path is as follows:

From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J2.4
From ID A1P2.4	to ID P10-23 (S101-5)
From ID P10-87 (S101-6)	to ID A1P2.31
From ID A1J2.31	to ID J2B-14C
From W4 P1B-14C	to W4 P2-4 (ST J5-4)

From ST_J5-4	to ST_J6-B1
From W4 P3-B1 (ST J6-2)	to W4 P1B-1C
From ID J2B-1C	to ID A1U6.5
From ID A1U6.4	to ID A1J5.26
From ID A1P5.26	to ID P7-55 (DTS CH41)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND

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      From ID A1U6.8                    to GROUND
      From ID A1U7.8                    to GROUND
      From ID A1U8.8                    to GROUND
      From ID P7-24 (DTS GCH 40)        to ID A1P5.3
      From ID A1J5.3                    to GROUND
      From ID P6-64 (DTS GCH 7)         to ID A1P5.1
      From ID A1J5.1                    to GROUND

Step 507

Description:
This step verifies the wire path from W4P2-5 to W4P3-A10. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.19
      From ID A1P9.19                   to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)          to ID A1P10.10
      From ID A1J10.10                  to ID A1J12.38
      From ID A1P12.38                  to ID P12-59 (S202-1)

      From ID P12-24 (S202-7)           to ID A1P12.29
      From ID A1J12.29                  to ID J2B-14F
      From W4 P1B-14F                   to W4 P2-5 (ST J5-5)

      From ST_J5-5                      to ST_J6-A10

      From W4 P3-A10 (ST J6-19)         to W4 P1A-3F
      From ID J2A-3F                    to ID A1J14.41
      From ID A1P14.41                  to ID P13-57 (S202-12)

      From ID P13-29 (S202-4)           to ID A1P14.50
      From ID A1J14.50                  to ID A1J10.50
      From ID A1P10.50                  to ID P11-244 (S510-2)
      From ID P11-147 (S510-4)          to ID A1P9.31
      From ID A1J9.31                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 508

Description:
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This step verifies the wire path from W4 P2-6 to W4 P2-8. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-242 (S509-2)	to ID A1P10.12
From ID A1J10.12	to ID A1J12.36
From ID A1P12.36	to ID P12-90 (S202-2)
From ID P12-56 (S202-8)	to ID A1P12.30
From ID A1J12.30	to ID J2B-13D
From W4 P1B-13D	to W4 P2-6 (ST J5-6)
From ST_J5-6	to ST_J5-8
From W4 P2-8 (ST J5-8)	to W4 P1B-11F
From ID J2B-11F	to ID A1J12.37
From ID A1P12.37	to ID P12-60 (S202-30)
From ID P13-29 (S202-4)	to ID A1P14.50
From ID A1J14.50	to ID A1J10.50
From ID A1P10.50	to ID P11-244 (S510-2)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 509

Description:
This step will verify the connections from the S701 switch out through W4 to FET#1 and FET#2 Gate inputs. Functionality of the FETs will be verified.

Connection Path is as follows:

From ID P13-73 (S701-25)	to ID A1P15.26
From ID A1J15.26	to ID J2A-6B
From W4 P1A-6B	to W4 P2-28 (ST J5-28)
From W4 P2-28 (ST J5-28)	to W4 P1B-4E
From ID J2B-4E	to ID A1D1.A

From ID A1D1.K	to ID A1Q1.1
From ID A1D1.K	to ID A1CR2.K
From ID A1D1.K	to ID A1R20.1
From ID P13-41 (S701-26)	to ID A1P15.28
From ID A1J15.28	to ID J2A-5B
From W4 P1A-5B	to W4 P2-9 (ST J5-9)
From W4 P2-9 (ST J5-9)	to W4 P1B-5E
From ID J2B-5E	to ID A1Q2.1
From ID J2B-5E	to ID A1CR3.K
From ID J2B-5E	to ID A1R21.1
From W4 P2-8 (UUT J1-8)	to W4 P1B-6E
From ID J2B-6E	to IDA1Q2.3
From ID J2B-6E	to ID A1R22.1
From ID J2B-6E	to ID A1CR3.A
From ID J2B-6E	to ID A1R21.2
From ID J2B-6E	to ID A1R23.1
From ID A1R23.2	to ID J2B-8E
From ID A1R23.2	to ID A1R24.1
From ID A1R23.2	to ID A1C12.1
From ID A1C12.2	to GROUND
From ID A1R24.2	to GROUND
From ID A1R22.2	to GROUND
From ID A1J1.3	to ID A1Q1.2
From ID A1Q1.3	to ID A1Q2.2
From ID A1Q1.3	to ID A1CR2.A
From ID A1Q1.3	to ID A1 R20.2
From W4 P1B-8E	to W4 P1A-14D
From ID J1A-14D	to ID A1U5.3
From ID A1U5.2	to ID A1J5.32
From ID A1P5.32	to ID P6-13 (DTS CH27)

Step 510

Description:
This step verifies the wire path from W4 P2-9 to W4 P2-24. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-162 (S506-2)	to ID A1P10.1
From ID A1J10.1	to ID A1J12.48
From ID A1P12.48	to ID P12-44 (S701-2)

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      From ID P13-41 (S701-26)          to ID A1P15.28
      From ID A1J15.28                  to ID J2A-5B
      From W4 P1A-5B                    to W4 P2-9 (ST J5-9)

      From ST_J5-9                      to ST_J5-24

      From W4 P2-24 (ST J5-24)          to W4 P1B-13F
      From ID J2B-13F                   to ID A1J12.32
      From ID A1P12.32                  to ID P12-88 (S202-14)

      From ID P13-29 (S202-4)           to ID A1P14.50
      From ID A1J14.50                  to ID A1J10.50
      From ID A1P10.50                  to ID P11-244 (S510-2)
      From ID P11-147 (S510-4)          to ID A1P9.31
      From ID A1J9.31                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 511

Description:
This step verifies connectivity from DC4-HI out through W4 and to
the proper receive path associated with DTS ch 28 while connecting
W4 with loopback and applying 0 volts out on W4P2-23 then driving 15
volts with DC4 to detect that CH28 receives High level and reads
"1".

      Connection Path is as follows:

      From ID P1-10 (DC4-HI)            to ID A1P1.3
      From ID A1J1.3                    to ID A1J8.2
      From ID A1P8.2                    to ID P10-141 (S301-70)
      From ID P10-44 (S301-69)          to ID A1P8.31
      From ID A1J8.31                   to ID BUS 2

      From ID BUS 2                     to ID A1J9.29
      From ID A1P9.29                   to ID P11-17 (S509-4)
      From ID P11-177 (S509-1)          to ID A1P10.10
      From ID A1J10.10                  to ID A1J12.38
      From ID A1P12.38                  to ID P12-59 (S202-1)

      From ID P12-57 (S202-13)          to ID A1P12.31
      From ID A1J12.31                  to ID J2B-13E
      From W4 P1B-13E                   to W4 P2-23 (ST J5-23)

      From ST_J5-23                     to ST_J5-13

      From W4 P2-13 (ST J5-13)          to W4 P1A-14E
      From ID J2A-14E                   to ID A1U5.5
      From ID A1U5.4                    to ID A1J5.29
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From ID A1P5.29	to ID P6-12 (DTS CH28)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND

```
      From ID P6-64 (DTS GCH 7)         to ID A1P5.1
      From ID A1J5.1                    to GROUND

Step 512

Description:
This step verifies connectivity from CH6 (ID J2A=11B) out through W4
and to the proper receive path associated with DTS ch 42 while
connecting W4 with loopback and applying 0 volts out on W4P2-23 then
driving 15 volts with DC4 to detect that CH42 receives High level
and reads "1".

      Connection Path is as follows:

      From ID P6-31 (DTS CH6)           to ID A1P5.43
      From ID A1J5.43                   to ID A1U2.3
      From ID A1U2.2                    to ID A1J10.34
      From ID A1P10.34                  to ID P11-143 (S301-175)
      From ID P11-209 (S301-176)        to ID A1P10.37
      From ID A1J10.37                  to ID J2A-11B
      From W4 P1A-11B                   to W4 P2-10 (ST J5-10)

      From ST_J5-10                     to ST_J6-A2

      From W4 P3-A2 (ST J6-3)           to W4 P1B-1B
      From ID J2B-1B                    to ID A1U6.7
      From ID A1U6.6                    to ID A1J5.23
      From ID A1P5.23                   to ID P7-54 (DTS CH42)

      From ID P1-1 (DC1-HI)             to ID A1P1.1
      From ID A1J1.1 (+5V)              to ID A1U1.1
      From ID A1J1.1 (+5V)              to ID A1U1.13
      From ID A1J1.1 (+5V)              to ID A1U2.1
      From ID A1J1.1 (+5V)              to ID A1U2.13
      From ID A1J1.1 (+5V)              to ID A1U3.1
      From ID A1J1.1 (+5V)              to ID A1U3.13
      From ID A1J1.1 (+5V)              to ID A1U4.1
      From ID A1J1.1 (+5V)              to ID A1U5.1
      From ID A1J1.1 (+5V)              to ID A1U6.1
      From ID A1J1.1 (+5V)              to ID A1U7.1
      From ID A1J1.1 (+5V)              to ID A1U8.1
      From ID A1J1.1 (+5V)              to ID A1C1.1
      From ID A1J1.1 (+5V)              to ID A1C3.1
      From ID A1J1.1 (+5V)              to ID A1C5.1
      From ID A1J1.1 (+5V)              to ID A1C7.1
      From ID A1J1.1 (+5V)              to ID A1C8.1
      From ID A1J1.1 (+5V)              to ID A1C9.1
      From ID A1J1.1 (+5V)              to ID A1C10.1
      From ID A1J1.1 (+5V)              to ID A1C11.1
      From ID P1-2 (DC1-LO)             to ID A1P1.9
      From ID A1J1.9                    to GROUND
      From ID P1-4 (DC2-HI)             to ID A1P2.2
      From ID A1J2.2 (+15V)             to ID A1U1.16
      From ID A1J2.2 (+15V)             to ID A1U2.16
```

From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 513

Description:
This step verifies connectivity from CH7 (ID J2A=11B) out through W4 and to the proper receive path associated with DTS ch 43 while connecting W4 with loopback and applying 0 volts out on W4P2-11 then driving 15 volts with DC4 to detect that CH43 receives High level and reads "1".

Connection Path is as follows:

From ID P6-32 (DTS CH7)	to ID A1P5.44
From ID A1J5.44	to ID A1U2.5
From ID A1U2.4	to ID A1J10.32
From ID A1P10.32	to ID P11-175 (S301-177)
From ID P11-48 (S301-178)	to ID A1P10.35
From ID A1J10.35	to ID J2A-11A
From W4 P1A-11A	to W4 P2-11 (ST J5-11)
From ST_J5-11	to ST_J6-B2
From W4 P3-B2 (ST J6-4)	to W4 P1B-1A
From ID J2B-1A	to ID A1U6.9
From ID A1U6.10	to ID A1J5.24

From ID A1P5.24	to ID P7-53 (DTS CH43)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND

```
      From ID P6-64 (DTS GCH 7)          to ID A1P5.1
      From ID A1J5.1                     to GROUND

Step 514

Description:
This step verifies connectivity from CH016 (ID J2A=12E) out through
W4 and to the proper receive path associated with DTS ch 44 while
connecting W4 with loopback and applying "0" out on W4P2-14 then
driving "1" and detecting CH44 receives High level and reads "1".

      Connection Path is as follows:

      From ID P6-24 (DTS CH16)           to ID A1P5.41
      From ID A1J5.41                    to ID A1U2.7
      From ID A1U2.6                     to ID A1J10.30
      From ID A1P10.30                   to ID P11-78 (S301-179)
      From ID P11-81 (S301-180)          to ID A1P10.33
      From ID A1J10.33                   to ID J2A-12E
      From W4 P1A-12E                    to W4 P2-14 (ST J5-14)

      From ST_J5-14                      to ST_J6-A3

      From W4 P3-A3 (ST J6-5)            to W4 P1B-2F
      From ID J2B-2F                     to ID A1U6.11
      From ID A1U6.12                    to ID A1J5.21
      From ID A1P5.21                    to ID P7-52 (DTS CH44)

      From ID P1-1 (DC1-HI)              to ID A1P1.1
      From ID A1J1.1 (+5V)               to ID A1U1.1
      From ID A1J1.1 (+5V)               to ID A1U1.13
      From ID A1J1.1 (+5V)               to ID A1U2.1
      From ID A1J1.1 (+5V)               to ID A1U2.13
      From ID A1J1.1 (+5V)               to ID A1U3.1
      From ID A1J1.1 (+5V)               to ID A1U3.13
      From ID A1J1.1 (+5V)               to ID A1U4.1
      From ID A1J1.1 (+5V)               to ID A1U5.1
      From ID A1J1.1 (+5V)               to ID A1U6.1
      From ID A1J1.1 (+5V)               to ID A1U7.1
      From ID A1J1.1 (+5V)               to ID A1U8.1
      From ID A1J1.1 (+5V)               to ID A1C1.1
      From ID A1J1.1 (+5V)               to ID A1C3.1
      From ID A1J1.1 (+5V)               to ID A1C5.1
      From ID A1J1.1 (+5V)               to ID A1C7.1
      From ID A1J1.1 (+5V)               to ID A1C8.1
      From ID A1J1.1 (+5V)               to ID A1C9.1
      From ID A1J1.1 (+5V)               to ID A1C10.1
      From ID A1J1.1 (+5V)               to ID A1C11.1
      From ID P1-2 (DC1-LO)              to ID A1P1.9
      From ID A1J1.9                     to GROUND
      From ID P1-4 (DC2-HI)              to ID A1P2.2
      From ID A1J2.2 (+15V)              to ID A1U1.16
      From ID A1J2.2 (+15V)              to ID A1U2.16
      From ID A1J2.2 (+15V)              to ID A1U3.16
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From ID A1J2.2 (+15V)            to ID A1C2.1
From ID A1J2.2 (+15V)            to ID A1C4.1
From ID A1J2.2 (+15V)            to ID A1C6.1
From ID P1-5 (DC2-LO)            to ID A1P1.10
From ID A1J1.10                  to GROUND
From ID A1C1.2                   to GROUND
From ID A1C2.2                   to GROUND
From ID A1C3.2                   to GROUND
From ID A1C4.2                   to GROUND
From ID A1C5.2                   to GROUND
From ID A1C6.2                   to GROUND
From ID A1C7.2                   to GROUND
From ID A1C8.2                   to GROUND
From ID A1C9.2                   to GROUND
From ID A1C10.2                  to GROUND
From ID A1C11.2                  to GROUND
From ID A1U1.8                   to GROUND
From ID A1U2.8                   to GROUND
From ID A1U3.8                   to GROUND
From ID A1U4.8                   to GROUND
From ID A1U5.8                   to GROUND
From ID A1U6.8                   to GROUND
From ID A1U7.8                   to GROUND
From ID A1U8.8                   to GROUND
From ID P7-24 (DTS GCH 40)       to ID A1P5.3
From ID A1J5.3                   to GROUND
From ID P6-64 (DTS GCH 7)        to ID A1P5.1
From ID A1J5.1                   to GROUND
```

Step 515

Description:
This step verifies connectivity from CH017 (ID J2A-12D) out through W4 and to the proper receive path associated with DTS ch 45 while connecting W4 with loopback and applying "0" out on W4P2-15 then driving "1" and detecting CH45 receives High level and reads "1".

Connection Path is as follows:

```
From ID P6-23 (DTS CH17)         to ID A1P5.42
From ID A1J5.42                  to ID A1U2.9
From ID A1U2.10                  to ID A1J10.28
From ID A1P10.28                 to ID P11-178 (S301-181)
From ID P11-243 (S301-182)       to ID A1P10.31
From ID A1J10.31                 to ID J2A-12D
From W4 P1A-12D                  to W4 P2-15 (ST J5-15)

From ST_J5-15                    to ST_J6-B3

From W4 P3-B3 (ST J6-6)          to W4 P1B-2C
From ID J2B-2C                   to ID A1U6.14
From ID A1U6.15                  to ID A1J5.22
From ID A1P5.22                  to ID P7-51 (DTS CH45)
```

```
From ID P1-1 (DC1-HI)           to ID A1P1.1
From ID A1J1.1 (+5V)            to ID A1U1.1
From ID A1J1.1 (+5V)            to ID A1U1.13
From ID A1J1.1 (+5V)            to ID A1U2.1
From ID A1J1.1 (+5V)            to ID A1U2.13
From ID A1J1.1 (+5V)            to ID A1U3.1
From ID A1J1.1 (+5V)            to ID A1U3.13
From ID A1J1.1 (+5V)            to ID A1U4.1
From ID A1J1.1 (+5V)            to ID A1U5.1
From ID A1J1.1 (+5V)            to ID A1U6.1
From ID A1J1.1 (+5V)            to ID A1U7.1
From ID A1J1.1 (+5V)            to ID A1U8.1
From ID A1J1.1 (+5V)            to ID A1C1.1
From ID A1J1.1 (+5V)            to ID A1C3.1
From ID A1J1.1 (+5V)            to ID A1C5.1
From ID A1J1.1 (+5V)            to ID A1C7.1
From ID A1J1.1 (+5V)            to ID A1C8.1
From ID A1J1.1 (+5V)            to ID A1C9.1
From ID A1J1.1 (+5V)            to ID A1C10.1
From ID A1J1.1 (+5V)            to ID A1C11.1
From ID P1-2 (DC1-LO)           to ID A1P1.9
From ID A1J1.9                  to GROUND
From ID P1-4 (DC2-HI)           to ID A1P2.2
From ID A1J2.2 (+15V)           to ID A1U1.16
From ID A1J2.2 (+15V)           to ID A1U2.16
From ID A1J2.2 (+15V)           to ID A1U3.16
From ID A1J2.2 (+15V)           to ID A1C2.1
From ID A1J2.2 (+15V)           to ID A1C4.1
From ID A1J2.2 (+15V)           to ID A1C6.1
From ID P1-5 (DC2-LO)           to ID A1P1.10
From ID A1J1.10                 to GROUND
From ID A1C1.2                  to GROUND
From ID A1C2.2                  to GROUND
From ID A1C3.2                  to GROUND
From ID A1C4.2                  to GROUND
From ID A1C5.2                  to GROUND
From ID A1C6.2                  to GROUND
From ID A1C7.2                  to GROUND
From ID A1C8.2                  to GROUND
From ID A1C9.2                  to GROUND
From ID A1C10.2                 to GROUND
From ID A1C11.2                 to GROUND
From ID A1U1.8                  to GROUND
From ID A1U2.8                  to GROUND
From ID A1U3.8                  to GROUND
From ID A1U4.8                  to GROUND
From ID A1U5.8                  to GROUND
From ID A1U6.8                  to GROUND
From ID A1U7.8                  to GROUND
From ID A1U8.8                  to GROUND
From ID P7-24 (DTS GCH 40)      to ID A1P5.3
From ID A1J5.3                  to GROUND
From ID P6-64 (DTS GCH 7)       to ID A1P5.1
From ID A1J5.1                  to GROUND
```

Step 516

Description:
This step verifies connectivity from CH18 (ID J2A-12C) out through W4 and to the proper receive path associated with DTS ch 46 while connecting W4 with loopback and applying "0" out on W4P2-16 then driving "1" and detecting CH46 receives High level and reads "1".

Connection Path is as follows:

From ID P6-22 (DTS CH18)	to ID A1P5.39
From ID A1J5.39	to ID A1U2.11
From ID A1U2.12	to ID A1J10.26
From ID A1P10.26	to ID P11-210 (S301-183)
From ID P11-83 (S301-184)	to ID A1P10.29
From ID A1J10.29	to ID J2A-12C
From W4 P1A-12C	to W4 P2-16 (ST J5-16)
From ST_J5-16	to ST_J6-A4
From W4 P3-A4 (ST J6-7)	to W4 P1B-2B
From ID J2B-2B	to ID A1U7.3
From ID A1U7.2	to ID A1J5.19
From ID A1P5.19	to ID P7-50 (DTS CH46)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1

```
      From ID A1J2.2 (+15V)             to ID A1C6.1
      From ID P1-5 (DC2-LO)             to ID A1P1.10
      From ID A1J1.10                   to GROUND
      From ID A1C1.2                    to GROUND
      From ID A1C2.2                    to GROUND
      From ID A1C3.2                    to GROUND
      From ID A1C4.2                    to GROUND
      From ID A1C5.2                    to GROUND
      From ID A1C6.2                    to GROUND
      From ID A1C7.2                    to GROUND
      From ID A1C8.2                    to GROUND
      From ID A1C9.2                    to GROUND
      From ID A1C10.2                   to GROUND
      From ID A1C11.2                   to GROUND
      From ID A1U1.8                    to GROUND
      From ID A1U2.8                    to GROUND
      From ID A1U3.8                    to GROUND
      From ID A1U4.8                    to GROUND
      From ID A1U5.8                    to GROUND
      From ID A1U6.8                    to GROUND
      From ID A1U7.8                    to GROUND
      From ID A1U8.8                    to GROUND
      From ID P7-24 (DTS GCH 40)        to ID A1P5.3
      From ID A1J5.3                    to GROUND
      From ID P6-64 (DTS GCH 7)         to ID A1P5.1
      From ID A1J5.1                    to GROUND

Step 517

Description:
This step verifies proper receive path components associated with
DTS ch 26 while connecting W4 with loopback.

      Connection Path is as follows:

      From ID P13-60 (S202-28)          to ID A1P14.43
      From ID A1J14.43                  to ID J2A-1F
      From W4 P1A-1F                    to W4 P2-17 (ST J5-17)

      From ST_J5-17                     to ST_J5-27

      From W4 P2-27 (ST J5-27)          to W4 P1A-13D
      From ID J2A-13D                   to ID A1U4.9
      From ID J2A-13D                   to ID A1R15.1
      From ID A1R15.2                   to ID A1J1.2 (+15V)
      From ID A1U4.10                   to ID A1J5.31
      From ID A1P5.31                   to ID P6-14 (DTS CH26)

      From ID P1-1 (DC1-HI)             to ID A1P1.1
      From ID A1J1.1 (+5V)              to ID A1U1.1
      From ID A1J1.1 (+5V)              to ID A1U1.13
      From ID A1J1.1 (+5V)              to ID A1U2.1
      From ID A1J1.1 (+5V)              to ID A1U2.13
      From ID A1J1.1 (+5V)              to ID A1U3.1
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From ID A1J1.1 (+5V)              to ID A1U3.13
From ID A1J1.1 (+5V)              to ID A1U4.1
From ID A1J1.1 (+5V)              to ID A1U5.1
From ID A1J1.1 (+5V)              to ID A1U6.1
From ID A1J1.1 (+5V)              to ID A1U7.1
From ID A1J1.1 (+5V)              to ID A1U8.1
From ID A1J1.1 (+5V)              to ID A1C1.1
From ID A1J1.1 (+5V)              to ID A1C3.1
From ID A1J1.1 (+5V)              to ID A1C5.1
From ID A1J1.1 (+5V)              to ID A1C7.1
From ID A1J1.1 (+5V)              to ID A1C8.1
From ID A1J1.1 (+5V)              to ID A1C9.1
From ID A1J1.1 (+5V)              to ID A1C10.1
From ID A1J1.1 (+5V)              to ID A1C11.1
From ID P1-2 (DC1-LO)             to ID A1P1.9
From ID A1J1.9                    to GROUND
From ID P1-4 (DC2-HI)             to ID A1P2.2
From ID A1J2.2 (+15V)             to ID A1U1.16
From ID A1J2.2 (+15V)             to ID A1U2.16
From ID A1J2.2 (+15V)             to ID A1U3.16
From ID A1J2.2 (+15V)             to ID A1C2.1
From ID A1J2.2 (+15V)             to ID A1C4.1
From ID A1J2.2 (+15V)             to ID A1C6.1
From ID P1-5 (DC2-LO)             to ID A1P1.10
From ID A1J1.10                   to GROUND
From ID A1C1.2                    to GROUND
From ID A1C2.2                    to GROUND
From ID A1C3.2                    to GROUND
From ID A1C4.2                    to GROUND
From ID A1C5.2                    to GROUND
From ID A1C6.2                    to GROUND
From ID A1C7.2                    to GROUND
From ID A1C8.2                    to GROUND
From ID A1C9.2                    to GROUND
From ID A1C10.2                   to GROUND
From ID A1C11.2                   to GROUND
From ID A1U1.8                    to GROUND
From ID A1U2.8                    to GROUND
From ID A1U3.8                    to GROUND
From ID A1U4.8                    to GROUND
From ID A1U5.8                    to GROUND
From ID A1U6.8                    to GROUND
From ID A1U7.8                    to GROUND
From ID A1U8.8                    to GROUND
From ID P7-24 (DTS GCH 40)        to ID A1P5.3
From ID A1J5.3                    to GROUND
From ID P6-64 (DTS GCH 7)         to ID A1P5.1
From ID A1J5.1                    to GROUND

Step 518

Description:
```

This step verifies connectivity from S202-28 out through the proper receive path components associated with DTS ch 26 while connecting W4 with loopback and applying 0 volts out on ID J2A-1F.

Connection Path is as follows:

From ID P13-60 (S202-28)	to ID A1P14.43
From ID A1J14.43	to ID J2A-1F
From W4 P1A-1F	to W4 P2-17 (ST J5-17)
From ST_J5-17	to ST_J5-27
From W4 P2-27 (ST J5-27)	to W4 P1A-13D
From ID J2A-13D	to ID A1U4.9
From ID J2A-13D	to ID A1R15.1
From ID A1R15.2	to ID A1J1.2 (+15V)
From ID A1U4.10	to ID A1J5.31
From ID A1P5.31	to ID P6-14 (DTS CH26)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND

From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 519

Description:
This step verifies the wire path from S201-2/W4P2-17 to S201-1/W4P2-27. The DMM will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-242 (S509-2)	to ID A1P10.12
From ID A1J10.12	to ID A1J12.36
From ID A1P12.36	to ID P12-90 (S202-2)
From ID P13-60 (S202-28)	to ID A1P14.43
From ID A1J14.43	to ID J2A-1F
From W4 P1A-1F	to W4 P2-17 (ST J5-17)
From ST_J5-17	to ST_J5-27
From W4 P2-27 (ST J5-27)	to W4 P1B-14D
From ID J2B-14D	to ID A1J12.27
From ID A1P12.27	to ID P12-55 (S201-47)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)

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      From ID P11-12 (S508-4)          to ID A1P9.25
      From ID A1J9.25                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 520

Description:
This step verifies connectivity from CH19 (ID J2A-12B) out through
W4 and to the proper receive path associated with DTS ch 47 while
connecting W4 with loopback and applying "0" out on W4P2-18 then
driving "1" and detecting CH 47 receives High level and reads "1".

      Connection Path is as follows:

      From ID P6-21 (DTS CH19)         to ID A1P5.40
      From ID A1J5.40                  to ID A1U2.14
      From ID A1U2.15                  to ID A1J10.24
      From ID A1P10.24                 to ID P11-49 (S301-185)
      From ID P11-82 (S301-186)        to ID A1P10.27
      From ID A1J10.27                 to ID J2A-12B
      From W4 P1A-12B                  to W4 P2-18 (ST J5-18)

      From ST_J5-18                    to ST_J6-B5

      From W4 P3-B5 (ST J6-10)         to W4 P1B-2A
      From ID J2B-2A                   to ID A1U7.5
      From ID A1U7.4                   to ID A1J5.20
      From ID A1P5.20                  to ID P7-49 (DTS CH47)

      From ID P1-1 (DC1-HI)            to ID A1P1.1
      From ID A1J1.1 (+5V)             to ID A1U1.1
      From ID A1J1.1 (+5V)             to ID A1U1.13
      From ID A1J1.1 (+5V)             to ID A1U2.1
      From ID A1J1.1 (+5V)             to ID A1U2.13
      From ID A1J1.1 (+5V)             to ID A1U3.1
      From ID A1J1.1 (+5V)             to ID A1U3.13
      From ID A1J1.1 (+5V)             to ID A1U4.1
      From ID A1J1.1 (+5V)             to ID A1U5.1
      From ID A1J1.1 (+5V)             to ID A1U6.1
      From ID A1J1.1 (+5V)             to ID A1U7.1
      From ID A1J1.1 (+5V)             to ID A1U8.1
      From ID A1J1.1 (+5V)             to ID A1C1.1
      From ID A1J1.1 (+5V)             to ID A1C3.1
      From ID A1J1.1 (+5V)             to ID A1C5.1
      From ID A1J1.1 (+5V)             to ID A1C7.1
      From ID A1J1.1 (+5V)             to ID A1C8.1
      From ID A1J1.1 (+5V)             to ID A1C9.1
      From ID A1J1.1 (+5V)             to ID A1C10.1
      From ID A1J1.1 (+5V)             to ID A1C11.1
```

From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 521

Description:
This step verifies connectivity from CH000 (ID J2A-9C) out through W4 and to the proper receive path associated with DTS ch 48 while connecting W4 with loopback and applying "0" out on W4P2-20 then driving "1" and detecting CH 48 receives High level and reads "1".

Connection Path is as follows:

From ID P6-25 (DTS CH0)	to ID A1P5.49
From ID A1J5.49	to ID A1U1.3
From ID A1U1.2	to ID A1J10.46
From ID A1P10.46	to ID P11-204 (S301-163)
From ID P11-174 (S301-164)	to ID A1P10.49
From ID A1J10.49	to ID J2A-9C
From W4 P1A-9C	to W4 P2-20 (ST J5-20)
From ST_J5-20	to ST_J6-A6

From W4 P3-A6 (ST J6-11)	to W4 P1B-3C
From ID J2B-3C	to ID A1U7.7
From ID A1U7.6	to ID A1J5.17
From ID A1P5.17	to ID P7-48 (DTS CH48)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND

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      From ID A1U7.8                    to GROUND
      From ID A1U8.8                    to GROUND
      From ID P7-24 (DTS GCH 40)        to ID A1P5.3
      From ID A1J5.3                    to GROUND
      From ID P6-64 (DTS GCH 7)         to ID A1P5.1
      From ID A1J5.1                    to GROUND

Step 522

Description:
This step verifies proper receive path components associated with
DTS ch 24 while connecting W4 with loopback.

      Connection Path is as follows:

      From ID P13-54 (S201-51)          to ID A1P14.37
      From ID A1J14.37                  to ID J2A-1D
      From W4 P1A-1D                    to W4 P2-22 9ST J5-22)

      From ST_J5-22                     to ST_J5-21

      From W4 P2-21 (ST J5-21)          to W4 P1A-13B
      From ID J2A-13B                   to ID A1U4.5
      From ID J2A-13B                   to ID A1R13.1
      From ID A1R13.2                   to ID A1J1.2 (+15V)
      From ID A1U4.4                    to ID A1J5.33
      From ID A1P5.33                   to ID P6-16 (DTS CH24)

      From ID P1-1 (DC1-HI)             to ID A1P1.1
      From ID A1J1.1 (+5V)              to ID A1U1.1
      From ID A1J1.1 (+5V)              to ID A1U1.13
      From ID A1J1.1 (+5V)              to ID A1U2.1
      From ID A1J1.1 (+5V)              to ID A1U2.13
      From ID A1J1.1 (+5V)              to ID A1U3.1
      From ID A1J1.1 (+5V)              to ID A1U3.13
      From ID A1J1.1 (+5V)              to ID A1U4.1
      From ID A1J1.1 (+5V)              to ID A1U5.1
      From ID A1J1.1 (+5V)              to ID A1U6.1
      From ID A1J1.1 (+5V)              to ID A1U7.1
      From ID A1J1.1 (+5V)              to ID A1U8.1
      From ID A1J1.1 (+5V)              to ID A1C1.1
      From ID A1J1.1 (+5V)              to ID A1C3.1
      From ID A1J1.1 (+5V)              to ID A1C5.1
      From ID A1J1.1 (+5V)              to ID A1C7.1
      From ID A1J1.1 (+5V)              to ID A1C8.1
      From ID A1J1.1 (+5V)              to ID A1C9.1
      From ID A1J1.1 (+5V)              to ID A1C10.1
      From ID A1J1.1 (+5V)              to ID A1C11.1
      From ID P1-2 (DC1-LO)             to ID A1P1.9
      From ID A1J1.9                    to GROUND
      From ID P1-4 (DC2-HI)             to ID A1P2.2
      From ID A1J2.2 (+15V)             to ID A1U1.16
      From ID A1J2.2 (+15V)             to ID A1U2.16
      From ID A1J2.2 (+15V)             to ID A1U3.16
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From ID A1J2.2 (+15V)            to ID A1C2.1
From ID A1J2.2 (+15V)            to ID A1C4.1
From ID A1J2.2 (+15V)            to ID A1C6.1
From ID P1-5 (DC2-LO)            to ID A1P1.10
From ID A1J1.10                  to GROUND
From ID A1C1.2                   to GROUND
From ID A1C2.2                   to GROUND
From ID A1C3.2                   to GROUND
From ID A1C4.2                   to GROUND
From ID A1C5.2                   to GROUND
From ID A1C6.2                   to GROUND
From ID A1C7.2                   to GROUND
From ID A1C8.2                   to GROUND
From ID A1C9.2                   to GROUND
From ID A1C10.2                  to GROUND
From ID A1C11.2                  to GROUND
From ID A1U1.8                   to GROUND
From ID A1U2.8                   to GROUND
From ID A1U3.8                   to GROUND
From ID A1U4.8                   to GROUND
From ID A1U5.8                   to GROUND
From ID A1U6.8                   to GROUND
From ID A1U7.8                   to GROUND
From ID A1U8.8                   to GROUND
From ID P7-24 (DTS GCH 40)       to ID A1P5.3
From ID A1J5.3                   to GROUND
From ID P6-64 (DTS GCH 7)        to ID A1P5.1
From ID A1J5.1                   to GROUND
```

Step 523

Description:
This step verifies connectivity from S201-51 out through the proper receive path components associated with DTS ch 24 while connecting W4 with loopback and applying 0 volts out on ID J2A-1D.

Connection Path is as follows:

```
From ID P13-54 (S201-51)         to ID A1P14.37
From ID A1J14.37                 to ID J2A-1D
From W4 P1A-1D                   to W4 P2-22 9ST J5-22)

From ST_J5-22                    to ST_J5-21

From W4 P2-21 (ST J5-21)         to W4 P1A-13B
From ID J2A-13B                  to ID A1U4.5
From ID J2A-13B                  to ID A1R13.1
From ID A1R13.2                  to ID A1J1.2 (+15V)
From ID A1U4.4                   to ID A1J5.33
From ID A1P5.33                  to ID P6-16 (DTS CH24)

From ID P1-1 (DC1-HI)            to ID A1P1.1
From ID A1J1.1 (+5V)             to ID A1U1.1
From ID A1J1.1 (+5V)             to ID A1U1.13
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From ID A1J1.1 (+5V)            to ID A1U2.1
From ID A1J1.1 (+5V)            to ID A1U2.13
From ID A1J1.1 (+5V)            to ID A1U3.1
From ID A1J1.1 (+5V)            to ID A1U3.13
From ID A1J1.1 (+5V)            to ID A1U4.1
From ID A1J1.1 (+5V)            to ID A1U5.1
From ID A1J1.1 (+5V)            to ID A1U6.1
From ID A1J1.1 (+5V)            to ID A1U7.1
From ID A1J1.1 (+5V)            to ID A1U8.1
From ID A1J1.1 (+5V)            to ID A1C1.1
From ID A1J1.1 (+5V)            to ID A1C3.1
From ID A1J1.1 (+5V)            to ID A1C5.1
From ID A1J1.1 (+5V)            to ID A1C7.1
From ID A1J1.1 (+5V)            to ID A1C8.1
From ID A1J1.1 (+5V)            to ID A1C9.1
From ID A1J1.1 (+5V)            to ID A1C10.1
From ID A1J1.1 (+5V)            to ID A1C11.1
From ID P1-2 (DC1-LO)           to ID A1P1.9
From ID A1J1.9                  to GROUND
From ID P1-4 (DC2-HI)           to ID A1P2.2
From ID A1J2.2 (+15V)           to ID A1U1.16
From ID A1J2.2 (+15V)           to ID A1U2.16
From ID A1J2.2 (+15V)           to ID A1U3.16
From ID A1J2.2 (+15V)           to ID A1C2.1
From ID A1J2.2 (+15V)           to ID A1C4.1
From ID A1J2.2 (+15V)           to ID A1C6.1
From ID P1-5 (DC2-LO)           to ID A1P1.10
From ID A1J1.10                 to GROUND
From ID A1C1.2                  to GROUND
From ID A1C2.2                  to GROUND
From ID A1C3.2                  to GROUND
From ID A1C4.2                  to GROUND
From ID A1C5.2                  to GROUND
From ID A1C6.2                  to GROUND
From ID A1C7.2                  to GROUND
From ID A1C8.2                  to GROUND
From ID A1C9.2                  to GROUND
From ID A1C10.2                 to GROUND
From ID A1C11.2                 to GROUND
From ID A1U1.8                  to GROUND
From ID A1U2.8                  to GROUND
From ID A1U3.8                  to GROUND
From ID A1U4.8                  to GROUND
From ID A1U5.8                  to GROUND
From ID A1U6.8                  to GROUND
From ID A1U7.8                  to GROUND
From ID A1U8.8                  to GROUND
From ID P7-24 (DTS GCH 40)      to ID A1P5.3
From ID A1J5.3                  to GROUND
From ID P6-64 (DTS GCH 7)       to ID A1P5.1
From ID A1J5.1                  to GROUND

Step 524
```

Description:
This step verifies proper receive path components associated with DTS ch 25 while connecting W4 with loopback.

Connection Path is as follows:

From ID P13-23 (S201-49)	to ID A1P14.35
From ID A1J14.35	to ID J2A-3D
From W4 P1A-3D	to W4 P2-21 (ST J5-21)
From ST_J5-21	to ST_J5-22
From W4 P2-22 (ST J5-22)	to W4 P1A-13C
From ID J2A-13C	to ID A1U4.7
From ID J2A-13C	to ID A1R14.1
From ID A1R14.2	to ID A1J1.2 (+15V)
From ID A1U4.6	to ID A1J5.34
From ID A1P5.34	to ID P6-15 (DTS CH25)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND

From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 525

Description:
This step verifies connectivity from S201-49 out through the proper receive path components associated with DTS ch 25 while connecting W4 with loopback and applying 0 volts out on ID J2A-3D.

Connection Path is as follows:

From ID P13-23 (S201-49)	to ID A1P14.35
From ID A1J14.35	to ID J2A-3D
From W4 P1A-3D	to W4 P2-21 (ST J5-21)
From ST_J5-21	to ST_J5-22
From W4 P2-22 (ST J5-22)	to W4 P1A-13C
From ID J2A-13C	to ID A1U4.7
From ID J2A-13C	to ID A1R14.1
From ID A1R14.2	to ID A1J1.2 (+15V)
From ID A1U4.6	to ID A1J5.34
From ID A1P5.34	to ID P6-15 (DTS CH25)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1

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      From ID A1J1.1 (+5V)              to ID A1U8.1
      From ID A1J1.1 (+5V)              to ID A1C1.1
      From ID A1J1.1 (+5V)              to ID A1C3.1
      From ID A1J1.1 (+5V)              to ID A1C5.1
      From ID A1J1.1 (+5V)              to ID A1C7.1
      From ID A1J1.1 (+5V)              to ID A1C8.1
      From ID A1J1.1 (+5V)              to ID A1C9.1
      From ID A1J1.1 (+5V)              to ID A1C10.1
      From ID A1J1.1 (+5V)              to ID A1C11.1
      From ID P1-2 (DC1-LO)             to ID A1P1.9
      From ID A1J1.9                    to GROUND
      From ID P1-4 (DC2-HI)             to ID A1P2.2
      From ID A1J2.2 (+15V)             to ID A1U1.16
      From ID A1J2.2 (+15V)             to ID A1U2.16
      From ID A1J2.2 (+15V)             to ID A1U3.16
      From ID A1J2.2 (+15V)             to ID A1C2.1
      From ID A1J2.2 (+15V)             to ID A1C4.1
      From ID A1J2.2 (+15V)             to ID A1C6.1
      From ID P1-5 (DC2-LO)             to ID A1P1.10
      From ID A1J1.10                   to GROUND
      From ID A1C1.2                    to GROUND
      From ID A1C2.2                    to GROUND
      From ID A1C3.2                    to GROUND
      From ID A1C4.2                    to GROUND
      From ID A1C5.2                    to GROUND
      From ID A1C6.2                    to GROUND
      From ID A1C7.2                    to GROUND
      From ID A1C8.2                    to GROUND
      From ID A1C9.2                    to GROUND
      From ID A1C10.2                   to GROUND
      From ID A1C11.2                   to GROUND
      From ID A1U1.8                    to GROUND
      From ID A1U2.8                    to GROUND
      From ID A1U3.8                    to GROUND
      From ID A1U4.8                    to GROUND
      From ID A1U5.8                    to GROUND
      From ID A1U6.8                    to GROUND
      From ID A1U7.8                    to GROUND
      From ID A1U8.8                    to GROUND
      From ID P7-24 (DTS GCH 40)        to ID A1P5.3
      From ID A1J5.3                    to GROUND
      From ID P6-64 (DTS GCH 7)         to ID A1P5.1
      From ID A1J5.1                    to GROUND

Step 526

Description:
This step verifies the wire path from W4P2-23 to W4P2-13. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
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      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.19
      From ID A1P9.19                 to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)        to ID A1P10.10
      From ID A1J10.10                to ID A1J12.38
      From ID A1P12.38                to ID P12-59 (S202-1)

      From ID P12-57 (S202-13)        to ID A1P12.31
      From ID A1J12.31                to ID J2B-13E
      From W4 P1B-13E                 to W4 P2-23 (ST J5-23)

      From ST_J5-23                   to ST_J5-13

      From W4 P2-13 (ST J5-13)        to W4 P1B-12E
      From ID J2B-12E                 to ID A1J12.34
      From ID A1P12.34                to ID P12-58 (S202-16)

      From ID P13-29 (S202-4)         to ID A1P14.50
      From ID A1J14.50                to ID A1J10.50
      From ID A1P10.50                to ID P11-244 (S510-2)
      From ID P11-147 (S510-4)        to ID A1P9.31
      From ID A1J9.31                 to ID BUS 2

      From ID BUS 2                   to ID A1J6.23
      From ID A1P6.23                 to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)        to ID A1P8.26
      From ID A1J8.26                 to ID A1J15.50
      From ID A1P15.50                to ID P20-3 (DMM-LO)

Step 527

Description:
This step verifies connectivity from DC4-HI out through W4 and to
the proper receive path associated with DTS ch 28 while connecting
W4 with loopback and applying 0 volts out on W4P2-23 then driving 15
volts with DC4 to detect that CH28 receives High level and reads
"1".

      Connection Path is as follows:

      From ID P1-10 (DC4-HI)          to ID A1P1.3
      From ID A1J1.3                  to ID A1J8.2
      From ID A1P8.2                  to ID P10-141 (S301-70)
      From ID P10-44 (S301-69)        to ID A1P8.31
      From ID A1J8.31                 to ID BUS 2

      From ID BUS 2                   to ID A1J9.29
      From ID A1P9.29                 to ID P11-17 (S509-4)
      From ID P11-177 (S509-1)        to ID A1P10.10
      From ID A1J10.10                to ID A1J12.38
      From ID A1P12.38                to ID P12-59 (S202-1)
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From ID P12-28 (S202-29)          to ID A1P12.35
From ID A1J12.35                  to ID J2B-12F
From W4 P1B-12F                   to W4 P2-25 (ST J5-25)

From ST_J5-25                     to ST_J6-A1

From W4 P3-A1 (ST J6-1)           to W4 P1B-1D
From ID J2B-1D                    to ID A1U6.3
From ID A1U6.2                    to ID A1J5.25
From ID A1P5.25                   to ID P7-56 (DTS CH40)

From ID P1-1 (DC1-HI)             to ID A1P1.1
From ID A1J1.1 (+5V)              to ID A1U1.1
From ID A1J1.1 (+5V)              to ID A1U1.13
From ID A1J1.1 (+5V)              to ID A1U2.1
From ID A1J1.1 (+5V)              to ID A1U2.13
From ID A1J1.1 (+5V)              to ID A1U3.1
From ID A1J1.1 (+5V)              to ID A1U3.13
From ID A1J1.1 (+5V)              to ID A1U4.1
From ID A1J1.1 (+5V)              to ID A1U5.1
From ID A1J1.1 (+5V)              to ID A1U6.1
From ID A1J1.1 (+5V)              to ID A1U7.1
From ID A1J1.1 (+5V)              to ID A1U8.1
From ID A1J1.1 (+5V)              to ID A1C1.1
From ID A1J1.1 (+5V)              to ID A1C3.1
From ID A1J1.1 (+5V)              to ID A1C5.1
From ID A1J1.1 (+5V)              to ID A1C7.1
From ID A1J1.1 (+5V)              to ID A1C8.1
From ID A1J1.1 (+5V)              to ID A1C9.1
From ID A1J1.1 (+5V)              to ID A1C10.1
From ID A1J1.1 (+5V)              to ID A1C11.1
From ID P1-2 (DC1-LO)             to ID A1P1.9
From ID A1J1.9                    to GROUND
From ID P1-4 (DC2-HI)             to ID A1P2.2
From ID A1J2.2 (+15V)             to ID A1U1.16
From ID A1J2.2 (+15V)             to ID A1U2.16
From ID A1J2.2 (+15V)             to ID A1U3.16
From ID A1J2.2 (+15V)             to ID A1C2.1
From ID A1J2.2 (+15V)             to ID A1C4.1
From ID A1J2.2 (+15V)             to ID A1C6.1
From ID P1-5 (DC2-LO)             to ID A1P1.10
From ID A1J1.10                   to GROUND
From ID A1C1.2                    to GROUND
From ID A1C2.2                    to GROUND
From ID A1C3.2                    to GROUND
From ID A1C4.2                    to GROUND
From ID A1C5.2                    to GROUND
From ID A1C6.2                    to GROUND
From ID A1C7.2                    to GROUND
From ID A1C8.2                    to GROUND
From ID A1C9.2                    to GROUND
From ID A1C10.2                   to GROUND
From ID A1C11.2                   to GROUND
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From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 528

Description:
This step verifies connectivity from CH22 (ID J2A-13E) out through W4 and to the proper receive path associated with DTS ch 49 while connecting W4 with loopback and applying 0 volts out on W4P2-26 then driving 15 volts with DC4 to detect that CH49 receives High level and reads "1".

Connection Path is as follows:

From ID P6-18 (DTS CH22)	to ID A1P5.35
From ID A1J5.35	to ID A1U3.7
From ID A1U3.6	to ID A1J10.18
From ID A1P10.18	to ID P11-50 (S301-191)
From ID P11-148 (S301-192)	to ID A1P10.21
From ID A1J10.21	to ID J2A-13E
From W4 P1A-13E	to W4 P2-26 (ST J5-26)
From ST_J5-26	to ST_J6-B6
From W4 P3-B6 (ST J6-12)	to W4 P1B-3B
From ID J2B-3B	to ID A1U7.9
From ID A1U7.10	to ID A1J5.18
From ID A1P5.18	to ID P7-47 (DTS CH49)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1

From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 529

Description:
This step verifies the wire path from W4 P2-28 to W4 P2-40. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23

From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P13-73 (S701-25)	to ID A1P15.26
From ID A1J15.26	to ID J2A-6B
From W4 P1A-6B	to W4 P2-28 (ST J5-28)
From ST_J5-28	to ST_J5-40
From W4 P2-40 (ST J5-40)	to W4 P1A-2F
From ID J2A-2F	to ID A1J14.42
From ID A1P14.42	to ID P13-28 (S202-27)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 530

Description:
This step verifies connectivity from DC4-HI out through W4 and to the proper receive path associated with DTS CH30 while connecting W4 with loopback and applying 0 volts out on W4P2-35 then driving 15 volts with DC4 to detect that CH30 receives High level and reads "1".

Connection Path is as follows:

From ID P1-10 (DC4-HI)	to ID A1P1.3
From ID A1J1.3	to ID A1J8.2
From ID A1P8.2	to ID P10-141 (S301-70)
From ID P10-44 (S301-69)	to ID A1P8.31
From ID A1J8.31	to ID BUS 2
From ID BUS 2	to ID A1J9.25
From ID A1P9.25	to ID P11-12 (S508-4)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P13-55 (S201-50)	to ID A1P14.36
From ID A1J14.36	to ID J2A-2D
From W4 P1A-2D	to W4 P2-35 (ST J5-35)

From ST_J5-35	to ST_J5-29
From W4 P2-29 (ST J5-29)	to W4 P1B-1F
From ID J2B-1F	to ID A1U5.9
From ID A1U5.10	to ID A1J5.27
From ID A1P5.27	to ID P6-10 (DTS CH30)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND

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      From ID A1U6.8                    to GROUND
      From ID A1U7.8                    to GROUND
      From ID A1U8.8                    to GROUND
      From ID P7-24 (DTS GCH 40)        to ID A1P5.3
      From ID A1J5.3                    to GROUND
      From ID P6-64 (DTS GCH 7)         to ID A1P5.1
      From ID A1J5.1                    to GROUND

Step 531

Description:
This step verifies connectivity from CH001 (ID J2A-9B) out through
W4 and to the proper receive path associated with DTS ch 50 while
connecting W4 with loopback and applying "0" out on W4P2-30 then
driving "1" and detecting CH050 receives High level and reads "1".

      Connection Path is as follows:

      From ID P6-26 (DTS CH1)           to ID A1P5.50
      From ID A1J5.50                   to ID A1U1.5
      From ID A1U1.4                    to ID A1J10.44
      From ID A1P10.44                  to ID P11-44 (S301-165)
      From ID P11-141 (S301-166)        to ID A1P10.47
      From ID A1J10.47                  to ID J2A-9B
      From W4 P1A-9B                    to W4 P2-30 (ST J5-30)

      From ST_J5-30                     to ST_J6-A7

      From W4 P3-A7 (ST J6-13)          to W4 P1B-3A
      From ID J2B-3A                    to ID A1U7.11
      From ID A1U7.12                   to ID A1J5.15
      From ID A1P5.15                   to ID P7-46 (DTS CH50)

      From ID P1-1 (DC1-HI)             to ID A1P1.1
      From ID A1J1.1 (+5V)              to ID A1U1.1
      From ID A1J1.1 (+5V)              to ID A1U1.13
      From ID A1J1.1 (+5V)              to ID A1U2.1
      From ID A1J1.1 (+5V)              to ID A1U2.13
      From ID A1J1.1 (+5V)              to ID A1U3.1
      From ID A1J1.1 (+5V)              to ID A1U3.13
      From ID A1J1.1 (+5V)              to ID A1U4.1
      From ID A1J1.1 (+5V)              to ID A1U5.1
      From ID A1J1.1 (+5V)              to ID A1U6.1
      From ID A1J1.1 (+5V)              to ID A1U7.1
      From ID A1J1.1 (+5V)              to ID A1U8.1
      From ID A1J1.1 (+5V)              to ID A1C1.1
      From ID A1J1.1 (+5V)              to ID A1C3.1
      From ID A1J1.1 (+5V)              to ID A1C5.1
      From ID A1J1.1 (+5V)              to ID A1C7.1
      From ID A1J1.1 (+5V)              to ID A1C8.1
      From ID A1J1.1 (+5V)              to ID A1C9.1
      From ID A1J1.1 (+5V)              to ID A1C10.1
      From ID A1J1.1 (+5V)              to ID A1C11.1
      From ID P1-2 (DC1-LO)             to ID A1P1.9
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      From ID A1J1.9                  to GROUND
      From ID P1-4 (DC2-HI)           to ID A1P2.2
      From ID A1J2.2 (+15V)           to ID A1U1.16
      From ID A1J2.2 (+15V)           to ID A1U2.16
      From ID A1J2.2 (+15V)           to ID A1U3.16
      From ID A1J2.2 (+15V)           to ID A1C2.1
      From ID A1J2.2 (+15V)           to ID A1C4.1
      From ID A1J2.2 (+15V)           to ID A1C6.1
      From ID P1-5 (DC2-LO)           to ID A1P1.10
      From ID A1J1.10                 to GROUND
      From ID A1C1.2                  to GROUND
      From ID A1C2.2                  to GROUND
      From ID A1C3.2                  to GROUND
      From ID A1C4.2                  to GROUND
      From ID A1C5.2                  to GROUND
      From ID A1C6.2                  to GROUND
      From ID A1C7.2                  to GROUND
      From ID A1C8.2                  to GROUND
      From ID A1C9.2                  to GROUND
      From ID A1C10.2                 to GROUND
      From ID A1C11.2                 to GROUND
      From ID A1U1.8                  to GROUND
      From ID A1U2.8                  to GROUND
      From ID A1U3.8                  to GROUND
      From ID A1U4.8                  to GROUND
      From ID A1U5.8                  to GROUND
      From ID A1U6.8                  to GROUND
      From ID A1U7.8                  to GROUND
      From ID A1U8.8                  to GROUND
      From ID P7-24 (DTS GCH 40)      to ID A1P5.3
      From ID A1J5.3                  to GROUND
      From ID P6-64 (DTS GCH 7)       to ID A1P5.1
      From ID A1J5.1                  to GROUND

Step 532

Description:
This step verifies connectivity from CH002 (ID J2A-10C) out through
W4 and to the proper receive path associated with DTS ch 51 while
connecting W4 with loopback and applying "0" out on W4P2-31 then
driving "1" and detecting CH051 receives High level and reads "1".

      Connection Path is as follows:

      From ID P6-27 (DTS CH2)         to ID A1P5.47
      From ID A1J5.47                 to ID A1U1.7
      From ID A1U1.6                  to ID A1J10.42
      From ID A1P10.42                to ID P11-76 (S301-167)
      From ID P11-142 (S301-168)      to ID A1P10.45
      From ID A1J10.45                to ID J2A-10C
      From W4 P1A-10C                 to W4 P2-31 (ST J5-31)

      From ST_J5-31                   to ST_J6-B7
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From W4 P3-B7 (ST J6-14)	to W4 P1B-4C
From ID J2B-4C	to ID A1U7.14
From ID A1U7.15	to ID A1J5.16
From ID A1P5.16	to ID P7-45 (DTS CH51)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND

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      From ID A1U8.8                    to GROUND
      From ID P7-24 (DTS GCH 40)        to ID A1P5.3
      From ID A1J5.3                    to GROUND
      From ID P6-64 (DTS GCH 7)         to ID A1P5.1
      From ID A1J5.1                    to GROUND

Step 533

Description:
This step verifies connectivity from DC4-HI out through W4 and to
the proper receive path associated with DTS ch 31 while connecting
W4 with loopback and applying 0 volts out on W4P2-35 then driving 15
volts with DC4 to detect that CH31 receives High level and reads
"1".

      Connection Path is as follows:

      From ID P13-55 (S201-50)          to ID A1P14.36
      From ID A1J14.36                  to ID J2A-2D
      From W4 P1A-2D                    to W4 P2-35 (ST J5-35)

      From ST_J5-35                     to ST_J5-32

      From W4 P2-32 (ST J5-32)          to W4 P1B-1E
      From ID J2B-1E                    to ID A1U5.11
      From ID A1U5.12                   to ID A1J5.28
      From ID A1P5.28                   to ID P6-9 (DTS CH31)

      From ID P1-1 (DC1-HI)             to ID A1P1.1
      From ID A1J1.1 (+5V)              to ID A1U1.1
      From ID A1J1.1 (+5V)              to ID A1U1.13
      From ID A1J1.1 (+5V)              to ID A1U2.1
      From ID A1J1.1 (+5V)              to ID A1U2.13
      From ID A1J1.1 (+5V)              to ID A1U3.1
      From ID A1J1.1 (+5V)              to ID A1U3.13
      From ID A1J1.1 (+5V)              to ID A1U4.1
      From ID A1J1.1 (+5V)              to ID A1U5.1
      From ID A1J1.1 (+5V)              to ID A1U6.1
      From ID A1J1.1 (+5V)              to ID A1U7.1
      From ID A1J1.1 (+5V)              to ID A1U8.1
      From ID A1J1.1 (+5V)              to ID A1C1.1
      From ID A1J1.1 (+5V)              to ID A1C3.1
      From ID A1J1.1 (+5V)              to ID A1C5.1
      From ID A1J1.1 (+5V)              to ID A1C7.1
      From ID A1J1.1 (+5V)              to ID A1C8.1
      From ID A1J1.1 (+5V)              to ID A1C9.1
      From ID A1J1.1 (+5V)              to ID A1C10.1
      From ID A1J1.1 (+5V)              to ID A1C11.1
      From ID P1-2 (DC1-LO)             to ID A1P1.9
      From ID A1J1.9                    to GROUND
      From ID P1-4 (DC2-HI)             to ID A1P2.2
      From ID A1J2.2 (+15V)             to ID A1U1.16
      From ID A1J2.2 (+15V)             to ID A1U2.16
      From ID A1J2.2 (+15V)             to ID A1U3.16
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From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 534

Description:
This step verifies the wire path from W4 P2-32 to W4 P2-35. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P12-26 (S202-15)	to ID A1P12.33
From ID A1J12.33	to ID J2B-12D
From W4 P1B-12D	to W4 P2-32 (ST J5-32)
From ST_J5-32	to ST_J5-35
From W4 P2-35 (ST J5-35)	to W4 P1A-2D
From ID J2A-2D	to ID A1J14.36
From ID A1P14.36	to ID P13-55 (S201-50)

Step 535

Description:
This step verifies connectivity from CH003 (ID J2A-10B) out through W4 and to the proper receive path associated with DTS ch 52 while connecting W4 with loopback and applying "0" out on W4P2-34 then driving "1" and detecting CH052 receives High level and reads "1".

Connection Path is as follows:

From ID P6-28 (DTS CH3)	to ID A1P5.48
From ID A1J5.48	to ID A1U1.9
From ID A1U1.10	to ID A1J10.40
From ID A1P10.40	to ID P11-14 (S301-169)
From ID P11-15 (S301-170)	to ID A1P10.43
From ID A1J10.43	to ID J2A-10B
From W4 P1A-10B	to W4 P2-34 (ST J5-34)
From ST_J5-34	to ST_J6-A8
From W4 P3-A8 (ST J6-15)	to W4 P1B-4B
From ID J2B-4B	to ID A1U8.3
From ID A1U8.2	to ID A1J5.13
From ID A1P5.13	to ID P7-44 (DTS CH52)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND

From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 536

Description:
This step verifies the wire path from W4 P2-35 to W4 P2-29. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P13-55 (S201-50)	to ID A1P14.36
From ID A1J14.36	to ID J2A-2D
From W4 P1A-2D	to W4 P2-35 (ST J5-35)
From ST_J5-35	to ST_J5-29
From W4 P2-29 (ST J5-29)	to W4 P1B-10F
From ID J2B-10F	to ID A1J12.39
From ID A1P12.39	to ID P12-93 (S202-31)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)

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      From ID P11-12 (S508-4)          to ID A1P9.25
      From ID A1J9.25                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 537

Description:
This step verifies connectivity from CH004 (ID J2A-10A) out through
W4 and to the proper receive path associated with DTS ch 53 while
connecting W4 with loopback and applying "0" out on W4P2-36 then
driving "1" and detecting CH053 receives High level and reads "1".

      Connection Path is as follows:

      From ID P6-29 (DTS CH4)          to ID A1P5.45
      From ID A1J5.45                  to ID A1U1.11
      From ID A1U1.12                  to ID A1J10.38
      From ID A1P10.38                 to ID P11-79 (S301-171)
      From ID P11-80 (S301-172)        to ID A1P10.41
      From ID A1J10.41                 to ID J2A-10A
      From W4 P1A-10A                  to W4 P2-36 (ST J5-36)

      From ST_J5-36                    to ST_J6-B8

      From W4 P3-B8 (ST J6-16)         to W4 P1B-4A
      From ID J2B-4A                   to ID A1U8.5
      From ID A1U8.4                   to ID A1J5.11
      From ID A1P5.11                  to ID P7-43 (DTS CH53)

      From ID P1-1 (DC1-HI)            to ID A1P1.1
      From ID A1J1.1 (+5V)             to ID A1U1.1
      From ID A1J1.1 (+5V)             to ID A1U1.13
      From ID A1J1.1 (+5V)             to ID A1U2.1
      From ID A1J1.1 (+5V)             to ID A1U2.13
      From ID A1J1.1 (+5V)             to ID A1U3.1
      From ID A1J1.1 (+5V)             to ID A1U3.13
      From ID A1J1.1 (+5V)             to ID A1U4.1
      From ID A1J1.1 (+5V)             to ID A1U5.1
      From ID A1J1.1 (+5V)             to ID A1U6.1
      From ID A1J1.1 (+5V)             to ID A1U7.1
      From ID A1J1.1 (+5V)             to ID A1U8.1
      From ID A1J1.1 (+5V)             to ID A1C1.1
      From ID A1J1.1 (+5V)             to ID A1C3.1
      From ID A1J1.1 (+5V)             to ID A1C5.1
      From ID A1J1.1 (+5V)             to ID A1C7.1
      From ID A1J1.1 (+5V)             to ID A1C8.1
      From ID A1J1.1 (+5V)             to ID A1C9.1
      From ID A1J1.1 (+5V)             to ID A1C10.1
      From ID A1J1.1 (+5V)             to ID A1C11.1
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```
From ID P1-2 (DC1-LO)            to ID A1P1.9
From ID A1J1.9                   to GROUND
From ID P1-4 (DC2-HI)            to ID A1P2.2
From ID A1J2.2 (+15V)            to ID A1U1.16
From ID A1J2.2 (+15V)            to ID A1U2.16
From ID A1J2.2 (+15V)            to ID A1U3.16
From ID A1J2.2 (+15V)            to ID A1C2.1
From ID A1J2.2 (+15V)            to ID A1C4.1
From ID A1J2.2 (+15V)            to ID A1C6.1
From ID P1-5 (DC2-LO)            to ID A1P1.10
From ID A1J1.10                  to GROUND
From ID A1C1.2                   to GROUND
From ID A1C2.2                   to GROUND
From ID A1C3.2                   to GROUND
From ID A1C4.2                   to GROUND
From ID A1C5.2                   to GROUND
From ID A1C6.2                   to GROUND
From ID A1C7.2                   to GROUND
From ID A1C8.2                   to GROUND
From ID A1C9.2                   to GROUND
From ID A1C10.2                  to GROUND
From ID A1C11.2                  to GROUND
From ID A1U1.8                   to GROUND
From ID A1U2.8                   to GROUND
From ID A1U3.8                   to GROUND
From ID A1U4.8                   to GROUND
From ID A1U5.8                   to GROUND
From ID A1U6.8                   to GROUND
From ID A1U7.8                   to GROUND
From ID A1U8.8                   to GROUND
From ID P7-24 (DTS GCH 40)       to ID A1P5.3
From ID A1J5.3                   to GROUND
From ID P6-64 (DTS GCH 7)        to ID A1P5.1
From ID A1J5.1                   to GROUND
```

Step 538

Description:
This step verifies connectivity from CH005 (ID J2A-11C) out through W4 and to the proper receive path associated with DTS ch 54 while connecting W4 with loopback and applying "0" out on W4P2-37 then driving "1" and detecting CH054 receives High level and reads "1".

Connection Path is as follows:

```
From ID P6-30 (DTS CH5)          to ID A1P5.46
From ID A1J5.46                  to ID A1U1.14
From ID A1U1.15                  to ID A1J10.36
From ID A1P10.36                 to ID P11-47 (S301-173)
From ID P11-208 (S301-174)       to ID A1P10.39
From ID A1J10.39                 to ID J2A-11C
From W4 P1A-11C                  to W4 P2-37 (ST J5-37)

From ST_J5-37                    to ST_J6-A9
```

From W4 P3-A9 (ST J6-17)	to W4 P1B-5C
From ID J2B-5C	to ID A1U8.7
From ID A1U8.6	to ID A1J5.9
From ID A1P5.9	to ID P7-42 (DTS CH54)
From ID P1-1 (DC1-HI)	to ID A1P1.1
From ID A1J1.1 (+5V)	to ID A1U1.1
From ID A1J1.1 (+5V)	to ID A1U1.13
From ID A1J1.1 (+5V)	to ID A1U2.1
From ID A1J1.1 (+5V)	to ID A1U2.13
From ID A1J1.1 (+5V)	to ID A1U3.1
From ID A1J1.1 (+5V)	to ID A1U3.13
From ID A1J1.1 (+5V)	to ID A1U4.1
From ID A1J1.1 (+5V)	to ID A1U5.1
From ID A1J1.1 (+5V)	to ID A1U6.1
From ID A1J1.1 (+5V)	to ID A1U7.1
From ID A1J1.1 (+5V)	to ID A1U8.1
From ID A1J1.1 (+5V)	to ID A1C1.1
From ID A1J1.1 (+5V)	to ID A1C3.1
From ID A1J1.1 (+5V)	to ID A1C5.1
From ID A1J1.1 (+5V)	to ID A1C7.1
From ID A1J1.1 (+5V)	to ID A1C8.1
From ID A1J1.1 (+5V)	to ID A1C9.1
From ID A1J1.1 (+5V)	to ID A1C10.1
From ID A1J1.1 (+5V)	to ID A1C11.1
From ID P1-2 (DC1-LO)	to ID A1P1.9
From ID A1J1.9	to GROUND
From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND

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      From ID A1U7.8                    to GROUND
      From ID A1U8.8                    to GROUND
      From ID P7-24 (DTS GCH 40)        to ID A1P5.3
      From ID A1J5.3                    to GROUND
      From ID P6-64 (DTS GCH 7)         to ID A1P5.1
      From ID A1J5.1                    to GROUND

Step 539

Description:
This step verifies connectivity from CH020 (ID J2A-12A) out through
W4 and to the proper receive path associated with DTS ch 55 while
connecting W4 with loopback and applying "0" out on W4P2-38 then
driving "1" and detecting CH055 receives High level and reads "1".

      Connection Path is as follows:

      From ID P6-20 (DTS CH20)          to ID A1P5.37
      From ID A1J5.37                   to ID A1U3.3
      From ID A1U3.2                    to ID A1J10.22
      From ID A1P10.22                  to ID P11-114 (S301-187)
      From ID P11-51 (S301-188)         to ID A1P10.25
      From ID A1J10.25                  to ID J2A-12A
      From W4 P1A-12A                   to W4 P2-38 (ST J5-38)

      From ST_J5-38                     to ST_J6-B9

      From W4 P3-B9 (ST J6-18)          to W4 P1B-5B
      From ID J2B-5B                    to ID A1U8.9
      From ID A1U8.10                   to ID A1J5.7
      From ID A1P5.7                    to ID P7-41 (DTS CH55)

      From ID P1-1 (DC1-HI)             to ID A1P1.1
      From ID A1J1.1 (+5V)              to ID A1U1.1
      From ID A1J1.1 (+5V)              to ID A1U1.13
      From ID A1J1.1 (+5V)              to ID A1U2.1
      From ID A1J1.1 (+5V)              to ID A1U2.13
      From ID A1J1.1 (+5V)              to ID A1U3.1
      From ID A1J1.1 (+5V)              to ID A1U3.13
      From ID A1J1.1 (+5V)              to ID A1U4.1
      From ID A1J1.1 (+5V)              to ID A1U5.1
      From ID A1J1.1 (+5V)              to ID A1U6.1
      From ID A1J1.1 (+5V)              to ID A1U7.1
      From ID A1J1.1 (+5V)              to ID A1U8.1
      From ID A1J1.1 (+5V)              to ID A1C1.1
      From ID A1J1.1 (+5V)              to ID A1C3.1
      From ID A1J1.1 (+5V)              to ID A1C5.1
      From ID A1J1.1 (+5V)              to ID A1C7.1
      From ID A1J1.1 (+5V)              to ID A1C8.1
      From ID A1J1.1 (+5V)              to ID A1C9.1
      From ID A1J1.1 (+5V)              to ID A1C10.1
      From ID A1J1.1 (+5V)              to ID A1C11.1
      From ID P1-2 (DC1-LO)             to ID A1P1.9
      From ID A1J1.9                    to GROUND
```

From ID P1-4 (DC2-HI)	to ID A1P2.2
From ID A1J2.2 (+15V)	to ID A1U1.16
From ID A1J2.2 (+15V)	to ID A1U2.16
From ID A1J2.2 (+15V)	to ID A1U3.16
From ID A1J2.2 (+15V)	to ID A1C2.1
From ID A1J2.2 (+15V)	to ID A1C4.1
From ID A1J2.2 (+15V)	to ID A1C6.1
From ID P1-5 (DC2-LO)	to ID A1P1.10
From ID A1J1.10	to GROUND
From ID A1C1.2	to GROUND
From ID A1C2.2	to GROUND
From ID A1C3.2	to GROUND
From ID A1C4.2	to GROUND
From ID A1C5.2	to GROUND
From ID A1C6.2	to GROUND
From ID A1C7.2	to GROUND
From ID A1C8.2	to GROUND
From ID A1C9.2	to GROUND
From ID A1C10.2	to GROUND
From ID A1C11.2	to GROUND
From ID A1U1.8	to GROUND
From ID A1U2.8	to GROUND
From ID A1U3.8	to GROUND
From ID A1U4.8	to GROUND
From ID A1U5.8	to GROUND
From ID A1U6.8	to GROUND
From ID A1U7.8	to GROUND
From ID A1U8.8	to GROUND
From ID P7-24 (DTS GCH 40)	to ID A1P5.3
From ID A1J5.3	to GROUND
From ID P6-64 (DTS GCH 7)	to ID A1P5.1
From ID A1J5.1	to GROUND

Step 540

Description:
This step verifies connectivity from CH021 (ID J2A-13F) out through W4 and to the proper receive path associated with DTS ch 56 while connecting W4 with loopback and applying "0" out on W4P2-39 then driving "1" and detecting CH056 receives High level and reads "1".

Connection Path is as follows:

From ID P6-19 (DTS CH21)	to ID A1P5.38
From ID A1J5.38	to ID A1U3.5
From ID A1U3.4	to ID A1J10.20
From ID A1P10.20	to ID P11-145 (S301-189)
From ID P11-179 (S301-190)	to ID A1P10.23
From ID A1J10.23	to ID J2A-13F
From W4 P1A-13F	to W4 P2-39 (ST J5-39)
From ST_J5-39	to ST_J6-B10
From W4 P3-B10 (ST J6-20)	to W4 P1B-5A

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From ID J2B-5A                  to ID A1U8.11
From ID A1U8.12                 to ID A1J5.5
From ID A1P5.5                  to ID P7-40 (DTS CH56)

From ID P1-1 (DC1-HI)           to ID A1P1.1
From ID A1J1.1 (+5V)            to ID A1U1.1
From ID A1J1.1 (+5V)            to ID A1U1.13
From ID A1J1.1 (+5V)            to ID A1U2.1
From ID A1J1.1 (+5V)            to ID A1U2.13
From ID A1J1.1 (+5V)            to ID A1U3.1
From ID A1J1.1 (+5V)            to ID A1U3.13
From ID A1J1.1 (+5V)            to ID A1U4.1
From ID A1J1.1 (+5V)            to ID A1U5.1
From ID A1J1.1 (+5V)            to ID A1U6.1
From ID A1J1.1 (+5V)            to ID A1U7.1
From ID A1J1.1 (+5V)            to ID A1U8.1
From ID A1J1.1 (+5V)            to ID A1C1.1
From ID A1J1.1 (+5V)            to ID A1C3.1
From ID A1J1.1 (+5V)            to ID A1C5.1
From ID A1J1.1 (+5V)            to ID A1C7.1
From ID A1J1.1 (+5V)            to ID A1C8.1
From ID A1J1.1 (+5V)            to ID A1C9.1
From ID A1J1.1 (+5V)            to ID A1C10.1
From ID A1J1.1 (+5V)            to ID A1C11.1
From ID P1-2 (DC1-LO)           to ID A1P1.9
From ID A1J1.9                  to GROUND
From ID P1-4 (DC2-HI)           to ID A1P2.2
From ID A1J2.2 (+15V)           to ID A1U1.16
From ID A1J2.2 (+15V)           to ID A1U2.16
From ID A1J2.2 (+15V)           to ID A1U3.16
From ID A1J2.2 (+15V)           to ID A1C2.1
From ID A1J2.2 (+15V)           to ID A1C4.1
From ID A1J2.2 (+15V)           to ID A1C6.1
From ID P1-5 (DC2-LO)           to ID A1P1.10
From ID A1J1.10                 to GROUND
From ID A1C1.2                  to GROUND
From ID A1C2.2                  to GROUND
From ID A1C3.2                  to GROUND
From ID A1C4.2                  to GROUND
From ID A1C5.2                  to GROUND
From ID A1C6.2                  to GROUND
From ID A1C7.2                  to GROUND
From ID A1C8.2                  to GROUND
From ID A1C9.2                  to GROUND
From ID A1C10.2                 to GROUND
From ID A1C11.2                 to GROUND
From ID A1U1.8                  to GROUND
From ID A1U2.8                  to GROUND
From ID A1U3.8                  to GROUND
From ID A1U4.8                  to GROUND
From ID A1U5.8                  to GROUND
From ID A1U6.8                  to GROUND
From ID A1U7.8                  to GROUND
From ID A1U8.8                  to GROUND
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      From ID P7-24 (DTS GCH 40)        to ID A1P5.3
      From ID A1J5.3                    to GROUND
      From ID P6-64 (DTS GCH 7)         to ID A1P5.1
      From ID A1J5.1                    to GROUND

Step 541

Description:
This step will verify the connections from the S701 switch out
through W4 to FET#1 and FET#2 Gate inputs. This step will validate
that the Q2 source loopback path is connected and received at the
input of S202-30.

      Connection Path is as follows:

      From ID P1-10 (DC4-HI)            to ID A1P1.3
      From ID A1J1.3                    to ID A1J8.2
      From ID A1P8.2                    to ID P10-141 (S301-70)
      From ID P10-44 (S301-69)          to ID A1P8.31
      From ID A1J8.31                   to ID BUS 2

      From ID BUS 2                     to ID A1J9.33
      From ID A1P9.33                   to ID P11-195 (S506-4)
      From ID P11-194 (S506-1)          to ID A1P10.3
      From ID A1J10.3                   to ID A1J12.50
      From ID A1P12.50                  to ID P12-76 (S701-1)

      From ID BUS 2                     to ID A1J9.33
      From ID A1P9.33                   to ID P11-195 (S506-4)
      From ID P11-162 (S506-2)          to ID A1P10.1
      From ID A1J10.1                   to ID A1J12.48
      From ID A1P12.48                  to ID P12-44 (S701-2)

      From ID P13-73 (S701-25)          to ID A1P15.26
      From ID A1J15.26                  to ID J2A-6B
      From W4 P1A-6B                    to W4 P2-28 (ST J5-28)

      From W4 P2-28 (ST J5-28)          to W4 P1B-4E
      From ID J2B-4E                    to ID A1D1.A
      From ID A1D1.K                    to ID A1Q1.1
      From ID A1D1.K                    to ID A1CR2.K
      From ID A1D1.K                    to ID A1R20.1

      From ID P13-41 (S701-26)          to ID A1P15.28
      From ID A1J15.28                  to ID J2A-5B
      From W4 P1A-5B                    to W4 P2-9 (ST J5-9)

      From W4 P2-9 (ST J5-9)            to W4 P1B-5E
      From ID J2B-5E                    to ID A1Q2.1
      From ID A1Q2.3                    to ID A1R23.1
      From ID_J2B-6E                    to W4P2-8

      From W4 P2-8 (ST J5-8)            to W4 P1B-11F
      From ID J2B-11F                   to ID A1J12.37
```

From ID A1P12.37	to ID P12-60 (S202-30)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-18 (S509-3)	to ID A1P9.19
From ID A1J9.19	to ID BUS 1
From ID BUS 1	to ID A1J6.13
From ID A1P6.13	to ID P10-77 (S503-3)
From ID P10-203 (S503-1)	to ID A1P8.28
From ID A1J8.28	to ID A1J15.49
From ID A1P15.49	to ID P20-2 (DMM-HI)
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

2.9 MODULE 6 W5 GCU POWER DRIVE SPECIFIC ID TESTS

Refer to Reference Drawings when diagnosing connection paths.

Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF TEST PWB, A2).pdf and 13020A7501 (CABLE, W5, SCHEMATIC).pdf in section 1.4 during review of the following steps.

Step 601

Description:
This step verifies the wire path from W5 P2-1 to W5 P2-12. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P12-85 (S201-45)	to ID A1P12.23
From ID A1J12.23	to ID A1J3.27
From ID A1P3.27	to ID J3-1
From W5 P1-1	to W5 P2-1 (ST J7-1)

From ST_J7-1	to ST_J7-12
From W5 P2-12 (ST J7-12)	to W5 P1-12
From ID J3-12	to ID A1P3.36
From ID A1J3.36	to ID A1J12.34
From ID A1P12.34	to ID P12-58 (S202-16)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 602

Description:
This step verifies the wire path from W5 P2-1 to W5 P2-12. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.32
From ID A1P8.32	to ID P10-136 (S301-44)
From ID P10-135 (S301-43)	to ID A1P7.7
From ID A1J7.7	to ID A1J2.12
From ID A1P2.12	to ID P10-150 (S101-3)
From ID P10-214 (S101-4)	to ID A1P2.35
From ID A1J2.35	to ID A1J3.22
From ID A1P3.22	to ID J3-18
From W5 P1-12	to W5 P2-12 (ST J7-12)
From ST_J7-12	to ST_J7-1
From W5 P2-1 (ST J7-1)	to W5 P1-1
From ID J3-1	to ID A1P3.27
From ID A1J3.27	to ID A1J12.23
From ID A1P12.23	to ID P12-85 (S201-45)
From ID P12-16 (S201-1)	to ID A1P12.42
From ID A1J12.42	to ID A1J10.6
From ID A1P10.6	to ID P11-203 (S508-1)
From ID P11-77 (S508-3)	to ID A1P9.15
From ID A1J9.15	to ID BUS 1
From ID BUS 1	to ID A1J6.13

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      From ID A1P6.13                  to ID P10-77 (S503-3)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 603

Description:
This step verifies the wire path from W5 P2-2 to W5 P2-8. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P12-21 (S201-46)         to ID A1P12.24
      From ID A1J12.24                 to ID A1J3.26
      From ID A1P3.26                  to ID J3-2
      From W5 P1-2                     to W5 P2-2 (ST J7-2)

      From ST_J7-2                     to ST_J7-8

      From W5 P2-8 (ST J7-8)           to W5 P1-11
      From ID J3-11                    to ID A1P3.37
      From ID A1J3.37                  to ID A1J12.33
      From ID A1P12.33                 to ID P12-26 (S202-15)

      From ID P12-59 (S202-1)          to ID A1P12.38
      From ID A1J12.38                 to ID A1J10.10
      From ID A1P10.10                 to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 604

Description:
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This step verifies the wire path from W5 P2-2 to W5 P2-8. The DMM will be used to measure resistance UL= 285 ohms LL= 275 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-177 (S509-1)	to ID A1P10.10
From ID A1J10.10	to ID A1J12.38
From ID A1P12.38	to ID P12-59 (S202-1)
From ID P12-26 (S202-15)	to ID A1P12.33
From ID A1J12.33	to ID A1J3.37
From ID A1P3.37	to ID J3-11
From W5 P1-11	to W5 P2-8 (ST J7-8)
From ST_J7-8	to ST_J7-2
From W5 P2-2 (ST J7-2)	to W5 P1-22
From ID J3-22	to ID A1P3.2
From ID A1J3.2	to ID A1J7.10
From ID A1P7.10	to ID P10-3 (S301-19)
From ID P10-132 (S301-20)	to ID A1P7.28
From ID A1J7.28	to ID A1J4.20
From ID A1P4.20	to ID R101.1
From ID R101.2	to ID A1P4.21
From ID R101.2	to ID A1P4.22
From ID A1J4.21	to GROUND
From ID A1J4.22	to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 605

Description:
This step verifies the wire path from W5 P2-2 to W5 P2-8. The DMM will be used to measure resistance R105, UL= 32 ohms LL= 25.8 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1

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      From ID BUS 1                    to ID A1J9.19
      From ID A1P9.19                  to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)         to ID A1P10.10
      From ID A1J10.10                 to ID A1J12.38
      From ID A1P12.38                 to ID P12-59 (S202-1)

      From ID P12-26 (S202-15)         to ID A1P12.33
      From ID A1J12.33                 to ID A1J3.37
      From ID A1P3.37                  to ID J3-11
      From W5 P1-11                    to W5 P2-8 (ST J7-8)

      From ST_J7-8                     to ST_J7-2

      From W5 P2-2 (ST J7-2)           to W5 P1-19
      From ID J3-19                    to ID A1P3.11
      From ID A1J3.11                  to ID A1J2.19
      From ID A1P2.19                  to ID P11-23 (S101-25)
      From ID P11-87 (S101-26)         to ID A1P2.9
      From ID A1J2.9                   to ID A1J4.3
      From ID A1P4.3                   to ID R105.1
      From ID R105.2                   to ID A1P4.10
      From ID A1J4.10                  to GROUND
      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J7.38
      From ID A1P7.38                  to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)        to ID A1P7.36
      From ID A1J7.36                  to GROUND

Step 606

Description:
This step verifies the wire path from W5 P2-3 to W5 P2-10. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P12-55 (S201-47)         to ID A1P12.27
      From ID A1J12.27                 to ID A1J3.31
      From ID A1P3.31                  to ID J3-5
      From W5 P1-5                     to W5 P2-3 (ST J7-3)
```

From ST_J7-3	to ST_J7-10
From W5 P2-10 (ST J7-10)	to W5 P1-10
From ID J3-10	to ID A1P3.34
From ID A1J3.34	to ID A1J12.32
From ID A1P12.32	to ID P12-88 (S202-14)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 607

Description:
This step verifies the wire path from W5 P2-3 to W5 P2-10. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J7.44
From ID A1P7.44	to ID P10-99 (S301-4)
From ID P10-226 (S301-3)	to ID A1P7.13
From ID A1J7.13	to ID A1J2.30
From ID A1P2.30	to ID P11-216 (S101-32)
From ID P11-152 (S101-31)	to ID A1P2.37
From ID A1J2.37	to ID A1J3.20
From ID A1P3.20	to ID J3-28
From W5 P1-28	to W5 P2-3 (ST J7-3)
From ST_J7-3	to ST_J7-10
From W5 P2-10 (ST J7-10)	to W5 P1-10
From ID J3-10	to ID A1P3.34
From ID A1J3.34	to ID A1J12.32
From ID A1P12.32	to ID P12-88 (S202-14)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26

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      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 608

Description:
This step verifies the wire path from W5 P2-4 to W5 P2-9. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)
      From ID P12-86 (S201-48)         to ID A1P12.28
      From ID A1J12.28                 to ID A1J3.30
      From ID A1P3.30                  to ID J3-6
      From W5 P1-6                     to W5 P2-4 (ST J7-4)

      From ST_J7-4                     to ST_J7-9

      From W5 P2-9 (ST J7-9)           to W5 P1-9
      From ID J3-9                     to ID A1P3.35
      From ID A1J3.35                  to ID A1J12.31
      From ID A1P12.31                 to ID P12-57 (S202-13)

      From ID P12-59 (S202-1)          to ID A1P12.38
      From ID A1J12.38                 to ID A1J10.10
      From ID A1P10.10                 to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 609

Description:
This step verifies the wire path from W5 P2-4 to W5 P2-9. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:
```

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.19
      From ID A1P9.19                  to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)         to ID A1P10.10
      From ID A1J10.10                 to ID A1J12.38
      From ID A1P12.38                 to ID P12-59 (S202-1)

      From ID P12-57 (S202-13)         to ID A1P12.31
      From ID A1J12.31                 to ID A1J3.35
      From ID A1P3.35                  to ID J3-9
      From W5 P1-9                     to W5 P2-9 (ST J7-9)

      From ST_J7-9                     to ST_J7-4

      From W5 P2-4 (ST J7-4)           to W5 P1-27
      From ID J3-27                    to ID A1P3.21
      From ID A1J3.21                  to ID A1J2.36
      From ID A1P2.36                  to ID P11-24 (S101-29)
      From ID P11-88 (S101-30)         to ID A1P2.22
      From ID A1J2.22                  to ID A1J1.11
      From ID A1P1.11                  to ID P1-11 (DC4-LO)

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J7.38
      From ID A1P7.38                  to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)        to ID A1P7.36
      From ID A1J7.36                  to GROUND

Step 610

Description:
This step verifies the wire path from W5 P2-4 to W5 P2-9. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P12-86 (S201-48)         to ID A1P12.28
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      From ID A1J12.28                  to ID A1J3.30
      From ID A1P3.30                   to ID J3-6
      From W5 P1-6                      to W5 P2-4 (ST J7-4)

      From ST_J7-4                      to ST_J7-9

      From W5 P2-9 (ST J7-9)            to W5 P1-24
      From ID J3-24                     to ID A1P3.4
      From ID A1J3.4                    to ID A1J7.6
      From ID A1P7.6                    to ID P10-100 (S301-13)
      From ID P10-228 (S301-14)         to ID A1P7.20
      From ID A1J7.20                   to ID A1J1.4
      From ID A1P1.4                    to DC10-LO

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J7.38
      From ID A1P7.38                   to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)         to ID A1P7.36
      From ID A1J7.36                   to GROUND



Step 611

Description:
This step verifies the wire path from W5 P2-5 to W5 P2-11. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.19
      From ID A1P9.19                   to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)          to ID A1P10.10
      From ID A1J10.10                  to ID A1J12.38
      From ID A1P12.38                  to ID P12-59 (S202-1)

      From ID P12-89 (S202-5)           to ID A1P12.25
      From ID A1J12.25                  to ID A1J3.29
      From ID A1P3.29                   to ID J3-3
      From W5 P1-3                      to W5 P2-5 (ST J7-5)

      From ST_J7-5                      to ST_J7-11

      From W5 P2-11 (ST J7-11)          to W5 P1-8
      From ID J3-8                      to ID A1P3.32
      From ID A1J3.32                   to ID A1J12.30
      From ID A1P12.30                  to ID P12-56 (S202-8)
```

From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 612

Description:
This step verifies the wire path from W5 P2-5 to W5 P2-11. The DMM will be used to measure resistance UL= 285 ohms LL= 275 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.34
From ID A1P8.34	to ID P10-201 (S301-46)
From ID P10-167 (S301-45)	to ID A1P7.22
From ID A1J7.22	to ID A1J3.1
From ID A1P3.1	to ID J3-21
From ID J3-21	to W5 P2-11 (ST J7-11)
From ST_J7-11	to ST_J7-5
From W5 P2-5 (ST J7-5)	to W5 P1-23
From ID J3-23	to ID A1P3.3
From ID A1J3.3	to ID A1J7.8
From ID A1P7.8	to ID P10-227 (S301-21)
From ID P10-37 (S301-22)	to ID A1P7.30
From ID A1J7.30	to ID A1J4.20
From ID A1P4.20	to ID R101.1
From ID R101.2	to ID A1P4.21
From ID R101.2	to ID A1P4.22
From ID A1J4.21	to GROUND
From ID A1J4.22	to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 613

Description:
This step verifies the wire path from W5 P2-5 to W5 P2-11. The DMM will be used to measure resistance R104, UL= 32 ohms LL= 25.8 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.34
From ID A1P8.34	to ID P10-201 (S301-46)
From ID P10-167 (S301-45)	to ID A1P7.22
From ID A1J7.22	to ID A1J3.1
From ID A1P3.1	to ID J3-21
From ID J3-21	to W5 P2-11 (ST J7-11)
From ST_J7-11	to ST_J7-5
From W5 P2-5 (ST J7-5)	to W5 P1-20
From ID J3-20	to ID A1P3.23
From ID A1J3.23	to ID A1J2.34
From ID A1P2.34	to ID P11-151 (S101-27)
From ID P11-215 (S101-28)	to ID A1P2.8
From ID A1J2.8	to ID A1J4.4
From ID A1P4.4	to ID R104.1
From ID R104.2	to ID A1P4.10
From ID A1J4.10	to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 614

Description:
This step verifies the wire path from W5 P2-6 to W5 P2-7. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-242 (S509-2)	to ID A1P10.12
From ID A1J10.12	to ID A1J12.36
From ID A1P12.36	to ID P12-90 (S202-2)
From ID P12-25 (S202-6)	to ID A1P12.26
From ID A1J12.26	to ID A1J3.28
From ID A1P3.28	to ID J3-4
From W5 P1-4	to W5 P2-6 (ST J7-6)
From ST_J7-6	to ST_J7-7

From W5 P2-7 (ST J7-7)	to W5 P1-7
From ID J3-7	to ID A1P3.33
From ID A1J3.33	to ID A1J12.29
From ID A1P12.29	to ID P12-24 (S202-7)
From ID P13-93 (S202-3)	to ID A1P14.49
From ID A1J14.49	to ID A1J10.48
From ID A1P10.48	to ID P11-52 (S510-1)
From ID P11-147 (S510-4)	to ID A1P9.31
From ID A1J9.31	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

2.10 MODULE 7 W6 GTD POWER SUPPLY SPECIFIC ID TESTS

Refer to Reference Drawings when diagnosing connection paths.
Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF TEST PWB, A2).pdf and 13020A7601 (CABLE, W6, SCHEMATIC).pdf in section 1.4 during review of the following steps.

Step 701

Description:
This step verifies the wire path from W6 P2-1 to W6 P3-1. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P12-79 (S201-5)	to ID A1P13.1
From ID A1J13.1	to ID J1B-14A
From W6 P1B-14A	to W6 P2-1 (ST J8-1)
From ST_J8-1	to ST_J6-A1
From W6 P3-A1 (ST J6-1)	to W6 P1A-6E
From ID J1A-6E	to ID A1J14.19

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      From ID A1P14.19                  to ID P13-88 (S202-19)

      From ID P12-59 (S202-1)           to ID A1P12.38
      From ID A1J12.38                  to ID A1J10.10
      From ID A1P10.10                  to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 702

Description:
This step verifies the wire path from W6 P2-2 to W6 P3-2. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)          to ID A1P10.8
      From ID A1J10.8                   to ID A1J12.40
      From ID A1P12.40                  to ID P12-80 (S201-2)

      From ID P12-47 (S201-6)           to ID A1P13.2
      From ID A1J13.2                   to ID J1B-13A
      From W6 P1B-13A                   to W6 P2-2 (ST J8-2)

      From ST_J8-2                      to ST_J6-B1

      From W6 P3-B1 (ST J6-2)           to W6 P1A-6F
      From ID J1A-6F                    to ID A1J14.20
      From ID A1P14.20                  to ID P13-24 (S202-20)

      From ID P12-90 (S202-2)           to ID A1P12.36
      From ID A1J12.36                  to ID A1J10.12
      From ID A1P10.12                  to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
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      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 703

Description:
This step verifies the wire path from W6 P2-3 to W6 P3-3. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)          to ID A1P10.6
      From ID A1J10.6                   to ID A1J12.42
      From ID A1P12.42                  to ID P12-16 (S201-1)

      From ID P12-46 (S201-7)           to ID A1P13.3
      From ID A1J13.3                   to ID J1B-14B
      From W6 P1B-14B                   to W6 P2-3 (ST J8-3)

      From ST_J8-3                      to ST_J6-A2

      From W6 P3-A2 (ST J6-3)           to W6 P1A-7F
      From ID J1A-7F                    to ID A1J14.22
      From ID A1P14.22                  to ID P13-92 (S202-34)

      From ID P12-90 (S202-2)           to ID A1P12.36
      From ID A1J12.36                  to ID A1J10.12
      From ID A1P10.12                  to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 704

Description:
This step verifies the wire path from W6 P2-3 to W6 P3-3. The DMM
will be used to measure resistance R107, UL= 53 ohms LL= 47 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
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      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.19
      From ID A1P9.19                  to ID P11-18 (S509-3)
      From ID P11-242 (S509-2)         to ID A1P10.12
      From ID A1J10.12                 to ID A1J12.36
      From ID A1P12.36                 to ID P12-90 (S202-2)

      From ID P13-92 (S202-34)         to ID A1P14.22
      From ID A1J14.22                 to ID J1A-7F
      From W6 P1A-7F                   to W6 P3-A2 (ST J6-3)

      From ST_J6-A2                    to ST_J8-3

      From W6 P-3 (ST J8-3)            to W6 P1B-2E
      From ID J1B-2E                   to ID A1J11.3
      From ID A1P11.3                  to ID P11-46 (S301-160)
      From ID P11-45 (S301-159)        to ID A1P11.31
      From ID A1J11.31                 to ID A1J4.6
      From ID A1P4.6                   to ID R107.1
      From ID R107.2                   to ID A1P4.10
      From ID A1J4.10                  to GROUND

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J7.38
      From ID A1P7.38                  to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)        to ID A1P7.36
      From ID A1J7.36                  to GROUND

Step 705

Description:
This step verifies the wire path from W6 P2-4 to W6 P3-4. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.30
      From ID A1P8.30                  to ID P10-41 (S301-42)
      From ID P10-199 (S301-41)        to ID A1P7.2
      From ID A1J7.2                   to ID A1J2.32
      From ID A1P2.32                  to ID P10-218 (S101-20)
      From ID P10-154 (S101-19)        to ID A1P2.16
      From ID A1J2.16                  to ID J1B-3D
      From W6 P1B-3D                   to W6 P2-4 (ST J8-4)

      From ST_J8-4                     to ST_J6-B2

      From W6 P3-B2 (ST J6-4)          to W6 P1A-8E
      From ID J1A-8E                   to ID A1J14.23
      From ID A1P14.23                 to ID P13-91 (S202-35)
```

```
      From ID P12-59 (S202-1)          to ID A1P12.38
      From ID A1J12.38                 to ID A1J10.10
      From ID A1P10.10                 to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 706

Description:
This step verifies the wire path from W6 P2-5 to W6 P3-5. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.30
      From ID A1P8.30                  to ID P10-41 (S301-42)
      From ID P10-199 (S301-41)        to ID A1P7.2
      From ID A1J7.2                   to ID J1B-3B
      From W6 P1B-3B                   to W6 P2-5 (ST J8-5)

      From ST_J8-5                     to ST_J6-A3

      From W6 P3-A3 (ST J6-5)          to W6 P1B-5C
      From ID J1B-5C                   to ID A1J12.15
      From ID A1P12.15                 to ID P12-31 (S202-45)

      From ID P12-59 (S202-1)          to ID A1P12.38
      From ID A1J12.38                 to ID A1J10.10
      From ID A1P10.10                 to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 707

Description:
This step verifies the wire path from W6 P2-6 to W6 P3-6. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:
```

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P12-13 (S201-8)	to ID A1P13.4
From ID A1J13.4	to ID J1B-13B
From W6 P1B-13B	to W6 P2-6 (ST J8-6)
From ST_J8-6	to ST_J6-B3
From W6 P3-B3 (ST J6-6)	to W6 P1B-4A
From ID J1B-4A	to ID A1J12.16
From ID A1P12.16	to ID P12-63 (S202-46)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 708

Description:
This step verifies the wire path from W6 P2-6 to W6 P3-6. The DMM will be used to measure resistance UL= 32 ohms LL= 25.8 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-242 (S509-2)	to ID A1P10.12
From ID A1J10.12	to ID A1J12.36
From ID A1P12.36	to ID P12-90 (S202-2)

```
      From ID P12-63 (S202-46)          to ID A1P12.16
      From ID A1J12.16                  to ID J1B-4A
      From W6 P1B-4A                    to W6 P3-B3 (ST J6-6)

      From ST_J6-B3                     to ST_J8-6

      From W6 P2-6 (ST J8-6)            to W6 P1B-2D
      From ID J1B-2D                    to ID A1J2.19
      From ID A1P2.19                   to ID P11-23 (S101-25)
      From ID P11-87 (S101-26)          to ID A1P2.9
      From ID A1J2.9                    to ID A1J4.3
      From ID A1P4.3                    to ID R105.1
      From ID R105.2                    to ID A1P4.10
      From ID A1J4.10                   to GROUND

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J7.38
      From ID A1P7.38                   to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)         to ID A1P7.36
      From ID A1J7.36                   to GROUND


Step 709

Description:
This step verifies the wire path from W6 P3-7 to W4 P2-2. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.19
      From ID A1P9.19                   to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)          to ID A1P10.10
      From ID A1J10.10                  to ID A1J12.38
      From ID A1P12.38                  to ID P12-59 (S202-1)

      From ID P12-96 (S202-47)          to ID A1P12.17
      From ID A1J12.17                  to ID J1B-4B
      From W6 P1B-4B                    to W6 P3-A4 (ST J6-7)

      From ST_J6-A4                     to ST_J8-7

      From W6 P2-7 (ST J8-7)            to W6 P1B-1E
      From ID J1B-1E                    to GROUND

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J7.38
      From ID A1P7.38                   to ID P10-130 (S301-23)
```

```
      From ID P10-229 (S301-24)          to ID A1P7.36
      From ID A1J7.36                    to GROUND

Step 710

Description:
This step verifies the wire path from W6 P2-8 to W6 P3-8. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)             to ID A1P15.49
      From ID A1J15.49                   to ID A1J8.28
      From ID A1P8.28                    to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)            to ID A1P6.13
      From ID A1J6.13                    to ID BUS 1

      From ID BUS 1                      to ID A1J9.15
      From ID A1P9.15                    to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)           to ID A1P10.8
      From ID A1J10.8                    to ID A1J12.40
      From ID A1P12.40                   to ID P12-80 (S201-2)
      From ID P13-17 (S201-26)           to ID A1P14.9
      From ID A1J14.9                    to ID J1A-1E
      From W6 P1A-1E                     to W6 P2-8 (ST J8-8)

      From ST_J8-8                       to ST_J6-B4

      From W6 P3-B4 (ST J6-8)            to W6 P1B-4C
      From ID J1B-4C                     to ID A1J12.18
      From ID A1P12.18                   to ID P12-32 (S202-48)

      From ID P12-90 (S202-2)            to ID A1P12.36
      From ID A1J12.36                   to ID A1J10.12
      From ID A1P10.12                   to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)            to ID A1P9.29
      From ID A1J9.29                    to ID BUS 2

      From ID BUS 2                      to ID A1J6.23
      From ID A1P6.23                    to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)           to ID A1P8.26
      From ID A1J8.26                    to ID A1J15.50
      From ID A1P15.50                   to ID P20-3 (DMM-LO)

Step 711

Description:
This step verifies the wire path from W6 P2-9 to W6 P3-9. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)             to ID A1P15.49
      From ID A1J15.49                   to ID A1J8.28
```

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      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.15
      From ID A1P9.15                 to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)        to ID A1P10.6
      From ID A1J10.6                 to ID A1J12.42
      From ID A1P12.42                to ID P12-16 (S201-1)

      From ID P13-47 (S201-9)         to ID A1P14.1
      From ID A1J14.1                 to ID J1A-1A
      From W6 P1A-1A                  to W6 P2-9 (ST J8-9)

      From ST_J8-9                    to ST_J6-A5

      From W6 P3-A5 (ST J6-9)         to W6 P1B-5B
      From ID J1B-5B                  to ID A1J12.14
      From ID A1P12.14                to ID P12-27 (S202-24)

      From ID P12-90 (S202-2)         to ID A1P12.36
      From ID A1J12.36                to ID A1J10.12
      From ID A1P10.12                to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)         to ID A1P9.29
      From ID A1J9.29                 to ID BUS 2

      From ID BUS 2                   to ID A1J6.23
      From ID A1P6.23                 to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)        to ID A1P8.26
      From ID A1J8.26                 to ID A1J15.50
      From ID A1P15.50                to ID P20-3 (DMM-LO)

Step 712

Description:
This step verifies the wire path from W6 P2-10 to W6 P2-12. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.15
      From ID A1P9.15                 to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)        to ID A1P10.8
      From ID A1J10.8                 to ID A1J12.40
      From ID A1P12.40                to ID P12-80 (S201-2)

      From ID P13-14 (S201-10)        to ID A1P14.2
      From ID A1J14.2                 to ID J1A-2A
```

```
From W6 P1A-2A                  to W6 P2-10 (ST J8-10)

From ST_J8-10                   to ST_J8-12

From W6 P2-12 (ST J8-12)        to W6 P1A-2B
From ID J1A-2B                  to ID A1J14.4
From ID A1P14.4                 to ID P13-48 (S201-12)

From ID P12-52 (S201-4)         to ID A1P12.44
From ID A1J12.44                to ID A1J10.4
From ID A1P10.4                 to ID P11-71 (S507-2)
From ID P11-72 (S507-4)         to ID A1P9.27
From ID A1J9.27                 to ID BUS 2

From ID BUS 2                   to ID A1J6.23
From ID A1P6.23                 to ID P10-12 (S503-4)
From ID P10-139 (S503-2)        to ID A1P8.26
From ID A1J8.26                 to ID A1J15.50
From ID A1P15.50                to ID P20-3 (DMM-LO)
```

Step 713

Description:
This step verifies the wire path from W6 P2-10 to W6 P2-12. The DMM will be used to measure resistance R114, UL= 32 ohms LL= 24.9 ohms.

Connection Path is as follows:

```
From ID P20-2 (DMM-HI)          to ID A1P15.49
From ID A1J15.49                to ID A1J8.28
From ID A1P8.28                 to ID P10-203 (S503-1)
From ID P10-77 (S503-3)         to ID A1P6.13
From ID A1J6.13                 to ID BUS 1

From ID BUS 1                   to ID A1J9.15
From ID A1P9.15                 to ID P11-77 (S508-3)
From ID P11-139 (S508-2)        to ID A1P10.8
From ID A1J10.8                 to ID A1J12.40
From ID A1P12.40                to ID P12-80 (S201-2)

From ID P13-14 (S201-10)        to ID A1P14.2
From ID A1J14.2                 to ID J1A-2A
From W6 P1A-2A                  to W6 P2-10 (ST J8-10)

From ST_J8-10                   to ST_J8-12

From W6 P2-12 (ST J8-12)        to W6 P1B-1C
From ID J1B-1C                  to ID A1J2.13
From ID A1P2.13                 to ID P11-25 (S101-33)
From ID P11-89 (S101-34)        to ID A1P2.7
From ID A1J2.7                  to ID A1J4.5
From ID A1P4.5                  to ID L2.1
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      From ID L2.2                      to ID R114.1
      From ID R114.2                    to ID A1P4.21
      From ID R114.2                    to ID A1P4.22
      From ID A1J4.21                   to GROUND
      From ID A1J4.22                   to GROUND

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J7.38
      From ID A1P7.38                   to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)         to ID A1P7.36
      From ID A1J7.36                   to GROUND

Step 714

Description:
This step verifies the wire path from W6 P2-11 to W6 P3-11. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)          to ID A1P10.6
      From ID A1J10.6                   to ID A1J12.42
      From ID A1P12.42                  to ID P12-16 (S201-1)

      From ID P13-80 (S201-11)          to ID A1P14.3
      From ID A1J14.3                   to ID J1A-1B
      From W6 P1A-1B                    to W6 P2-11 (ST J8-11)

      From ST_J8-11                     to ST_J6-A6

      From W6 P3-A6 (ST J6-11)          to W6 P1A-7E
      From ID J1A-7E                    to ID A1J14.21
      From ID A1P14.21                  to ID P13-90 (S202-26)

      From ID P12-90 (S202-2)           to ID A1P12.36
      From ID A1J12.36                  to ID A1J10.12
      From ID A1P10.12                  to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)
```

Step 715

Description:
This step verifies the wire path from W6 P2-11 to W6 P3-11. The DMM will be used to measure resistance R106, UL= 29 ohms LL= 21 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.19
From ID A1P9.19	to ID P11-18 (S509-3)
From ID P11-242 (S509-2)	to ID A1P10.12
From ID A1J10.12	to ID A1J12.36
From ID A1P12.36	to ID P12-90 (S202-2)
From ID P13-90 (S202-26)	to ID A1P14.21
From ID A1J14.21	to ID J1A-7E
From W6 P1A-7E	to W6 P3-A6 (ST J6-11)
From ST_J6-A6	to ST_J8-11
From W6 P2-11 (ST J8-11)	to W6 P1B-2F
From ID J1B-2F	to ID A1J2.17
From ID A1P2.17	to ID P11-22 (S101-21)
From ID P11-86 (S101-22)	to ID A1P2.10
From ID A1J2.10	to ID A1J4.2
From ID A1P4.2	to ID L1.1
From ID L1.2	to ID R106.1
From ID R106.2	to ID A1P4.21
From ID R106.2	to ID A1P4.22
From ID A1J4.21	to GROUND
From ID A1J4.22	to GROUND
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 716

Description:
This step verifies the wire path from W6 P2-13 to W6 P2-20. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28

```
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.15
      From ID A1P9.15                 to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)        to ID A1P10.6
      From ID A1J10.6                 to ID A1J12.42
      From ID A1P12.42                to ID P12-16 (S201-1)

      From ID P12-14 (S201-13)        to ID A1P13.5
      From ID A1J13.5                 to ID J1B-14C
      From W6 P1B-14C                 to W6 P2-13 (ST J8-13)

      From ST_J8-13                   to ST_J8-20

      From W6 P2-20 (ST J8-20)        to W6 P1A-2C
      From ID J1A-2C                  to ID A1J14.6
      From ID A1P14.6                 to ID P13-15 (S201-19)

      From ID P12-20 (S201-3)         to ID A1P12.46
      From ID A1J12.46                to ID A1J10.2
      From ID A1P10.2                 to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)         to ID A1P9.27
      From ID A1J9.27                 to ID BUS 2

      From ID BUS 2                   to ID A1J6.23
      From ID A1P6.23                 to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)        to ID A1P8.26
      From ID A1J8.26                 to ID A1J15.50
      From ID A1P15.50                to ID P20-3 (DMM-LO)

Step 717

Description:
This step verifies the wire path from W6 P2-14 to W6 P2-21. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.15
      From ID A1P9.15                 to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)        to ID A1P10.8
      From ID A1J10.8                 to ID A1J12.40
      From ID A1P12.40                to ID P12-80 (S201-2)

      From ID P12-78 (S201-14)        to ID A1P13.6
      From ID A1J13.6                 to ID J1B-13C
```

```
      From W6 P1B-13C                  to W6 P2-14 (ST J8-14)

      From ST_J8-14                    to ST_J8-21

      From W6 P2-21 (ST J8-21)         to W6 P1A-1D
      From ID J1A-1D                   to ID A1J14.7
      From ID A1P14.7                  to ID P13-79 (S201-20)

      From ID P12-52 (S201-4)          to ID A1P12.44
      From ID A1J12.44                 to ID A1J10.4
      From ID A1P10.4                  to ID P11-71 (S507-2)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 718

Description:
This step verifies the wire path from W6 P2-15 to W6 P2-22. The DMM resource will be used to measure resistance UL= 10 ohms.

```
      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P12-48 (S201-15)         to ID A1P12.1
      From ID A1J12.1                  to ID J1B-9A
      From W6 P1B-9A                   to W6 P2-15 (ST J8-15)

      From ST_J8-15                    to ST_J8-22

      From W6 P2-22 (ST J8-22)         to W6 P1B-9C
      From ID J1B-9C                   to ID A1J12.3
      From ID A1P12.3                  to ID P12-51 (S201-21)

      From ID P12-20 (S201-3)          to ID A1P12.46
      From ID A1J12.46                 to ID A1J10.2
```

```
      From ID A1P10.2                  to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 719

Description:
This step verifies the wire path from W6 P2-15 to W6 P2-22. The DMM
resource will be used to measure resistance UL= 74 ohms LL= 67 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P12-51 (S201-21)         to ID A1P12.3
      From ID A1J12.3                  to ID J1B-9C
      From W6 P1B-9C                   to W6 P2-22 (ST J8-22)

      From ST_J8-22                    to ST_J8-15

      From W6 P2-15 (ST J8-15)         to W6 P1B-2B
      From ID J1B-2B                   to ID A1J11.1
      From ID A1P11.1                  to ID P11-11 (S301-156)
      From ID P11-169 (S301-155)       to ID A1P11.32
      From ID A1J11.32                 to ID A1J4.8
      From ID A1P4.8                   to ID R113.1
      From ID R113.2                   to ID A1P4.10
      From ID A1J4.10                  to GROUND

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J7.38
      From ID A1P7.38                  to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)        to ID A1P7.36
      From ID A1J7.36                  to GROUND

Step 720

Description:
```

This step verifies the wire path from W6 P2-16 to W6 P2-23. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P12-15 (S201-16)	to ID A1P12.2
From ID A1J12.2	to ID J1B-9B
From W6 P1B-9B	to W6 P2-16 (ST J8-16)
From ST_J8-16	to ST_J8-23
From W6 P2-23 (ST J8-23)	to W6 P1B-8A
From ID J1B-8A	to ID A1J12.4
From ID A1P12.4	to ID P12-18 (S201-22)
From ID P12-52 (S201-4)	to ID A1P12.44
From ID A1J12.44	to ID A1J10.4
From ID A1P10.4	to ID P11-71 (S507-2)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 721

Description:
This step verifies the wire path from W6 P2-18 to W6 P2-24. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)

```
      From ID P11-203 (S508-1)          to ID A1P10.6
      From ID A1J10.6                   to ID A1J12.42
      From ID A1P12.42                  to ID P12-16 (S201-1)

      From ID P13-49 (S201-17)          to ID A1P14.5
      From ID A1J14.5                   to ID J1A-1C
      From W6 P1A-1C                    to W6 P2-18 (ST J8-18)

      From ST_J8-18                     to ST_J8-24

      From W6 P2-24 9ST J8-24)          to W6 P1B-8B
      From ID J1B-8B                    to ID A1J12.5
      From ID A1P12.5                   to ID P12-17 (S201-23)

      From ID P12-20 (S201-3)           to ID A1P12.46
      From ID A1J12.46                  to ID A1J10.2
      From ID A1P10.2                   to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)           to ID A1P9.27
      From ID A1J9.27                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 722

Description:
This step verifies the wire path from W6 P2-19 to W6 P2-25. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)          to ID A1P10.8
      From ID A1J10.8                   to ID A1J12.40
      From ID A1P12.40                  to ID P12-80 (S201-2)

      From ID P13-16 (S201-18)          to ID A1P14.28
      From ID A1J14.28                  to ID J1A-10F
      From W6 P1A-10F                   to W6 P2-19 (ST J8-19)

      From ST_J8-19                     to ST_J8-25

      From W6 P2-25 (ST J8-25)          to W6 P1B-8C
      From ID J1B-8C                    to ID A1J12.6
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      From ID A1P12.6                  to ID P12-81 (S201-24)

      From ID P12-52 (S201-4)          to ID A1P12.44
      From ID A1J12.44                 to ID A1J10.4
      From ID A1P10.4                  to ID P11-71 (S507-2)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 723

Description:
This step verifies the wire path from W6 P2-19 to W6 P2-25. The DMM resource will be used to measure resistance UL= 46 ohms LL= 40 ohms.

Connection Path is as follows:

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P12-81 (S201-24)         to ID A1P12.6
      From ID A1J12.6                  to ID J1B-8C
      From W6 P1B-8C                   to W6 P2-25 (ST J8-25)

      From ST_J8-25                    to ST_J8-19

      From W6 P2-19 (ST J8-19)         to W6 P1B-2C
      From ID J1B-2C                   to ID A1J11.2
      From ID A1P11.2                  to ID P11-13 (S301-158)
      From ID P11-172 (S301-157)       to ID A1P11.33
      From ID A1J11.33                 to ID A1J4.7
      From ID A1P4.7                   to ID R112.1
      From ID R112.2                   to ID A1P4.21
      From ID R112.2                   to ID A1P4.22
      From ID A1J4.21                  to GROUND
      From ID A1J4.22                  to GROUND

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J7.38
      From ID A1P7.38                  to ID P10-130 (S301-23)
```

```
      From ID P10-229 (S301-24)          to ID A1P7.36
      From ID A1J7.36                    to GROUND

Step 724

Description:
This step verifies the wire path from W6 P2-25 to W6 P2-26. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)             to ID A1P15.49
      From ID A1J15.49                   to ID A1J8.28
      From ID A1P8.28                    to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)            to ID A1P6.13
      From ID A1J6.13                    to ID BUS 1

      From ID BUS 1                      to ID A1J9.15
      From ID A1P9.15                    to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)           to ID A1P10.8
      From ID A1J10.8                    to ID A1J12.40
      From ID A1P12.40                   to ID P12-80 (S201-2)
      From ID P12-81 (S201-24)           to ID A1P12.6
      From ID A1J12.6                    to ID J1B-8C
      From W6 P1B-8C                     to W6 P2-25 (ST J8-25)

      From ST_J8-25                      to ST_J8-26

      From W6 P2-26 (ST J8-26)           to W6 P1A-2D
      From ID J1A-2D                     to ID A1J14.8
      From ID A1P14.9                    to ID P13-50 (S201-25)

      From ID P12-20 (S201-3)            to ID A1P12.46
      From ID A1J12.46                   to ID A1J10.2
      From ID A1P10.2                    to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)            to ID A1P9.27
      From ID A1J9.27                    to ID BUS 2

      From ID BUS 2                      to ID A1J6.23
      From ID A1P6.23                    to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)           to ID A1P8.26
      From ID A1J8.26                    to ID A1J15.50
      From ID A1P15.50                   to ID P20-3 (DMM-LO)

Step 725

Description:
This step verifies the Q3 - Q5 FETs used to provide GTD PSU overload
capability while testing the +/-15V Power supplies are not shorted.
The DMM will be used to measure voltage LL= 14 volts across FETs.

      Connection Path is as follows:

      From ID P1-4 (DC2-HI)              to ID A1P1.2
```

```
      From ID A1J1.2                    to R4

      From +15V                         to ID R4.2
      From ID R4.1                      to ID A1J8.13
      From ID A1P8.13                   to ID P10-208 (S301-78)
      From ID P10-47 (S301-77)          to ID A1P8.38
      From ID A1J8.38                   to ID BUS 6

      From ID BUS 6                     to ID A1J9.22
      From ID A1P9.22                   to ID P11-170 (S508-8)
      From ID P11-139 (S508-2)          to ID A1P10.8
      From ID A1J10.8                   to ID A1J12.40
      From ID A1P12.40                  to ID P12-80 (S201-2)

      From ID BUS 6                     to ID A1J9.22
      From ID A1P9.22                   to ID P11-170 (S508-8)
      From ID P11-203 (S508-1)          to ID A1P10.6
      From ID A1J10.6                   to ID A1J12.42
      From ID A1P12.42                  to ID P12-16 (S201-1)

      From ID P12-13 (S201-8)           to ID A1P13.4
      From ID A1J13.4                   to ID J1B-13B
      From W6 P1B-13B                   to W6 P2-6 (ST J8-6)

      From ID P13-49 (S201-17)          to ID A1P14.5
      From ID A1J14.5                   to ID J1A-1C
      From W6 P1A-1C                    to W6 P2-18 (ST J8-18)

      From ID P13-16 (S201-18)          to ID A1P14.28
      From ID A1J14.28                  to ID J1A-10F
      From W6 P1A-10F                   to W6 P2-19 (ST J8-19)

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-170 (S503-8)          to ID A1P6.38
      From ID A1J6.38                   to ID BUS 6

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J7.38
      From ID A1P7.38                   to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)         to ID A1P7.36
      From ID A1J7.36                   to GROUND

Step 726

Description:
This step verifies the Q5 FET used to provide GTD PSU overload
capability to test the +/-15V Power supplies is operational. The DMM
resource will be used to measure voltage UL= 2 volts across the FET.

      Connection Path is as follows:

      From ID P1-4 (DC2-HI)             to ID A1P1.2
```

From ID A1J1.2	to R4
From +15V	to ID R4.2
From ID R4.1	to ID A1J8.13
From ID A1P8.13	to ID P10-208 (S301-78)
From ID P10-47 (S301-77)	to ID A1P8.38
From ID A1J8.38	to ID BUS 6
From ID BUS 6	to ID A1J9.22
From ID A1P9.22	to ID P11-170 (S508-8)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P12-13 (S201-8)	to ID A1P13.4
From ID A1J13.4	to ID J1B-13B
From W6 P1B-13B	to W6 P2-6 (ST J8-6)
From W6 P2-6 (ST J6-6)	to W6 P1B-2D
From W6 P2-18 (UUT J1-18)	to W6 P1B-3C
From W6 P2-19 (UUT J1-19)	to W6 P1B-2C
From ID J1B-2D	to ID R34.1
From ID J1B-3C	to ID R31.1
From ID J1B-2C	to ID R28.1
From ID R34.2	to ID Q5.2
From ID R31.2	to ID Q4.2
From ID R28.2	to ID Q3.2
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to +15V
From +15V	to ID A1J11.41
From ID A1P11.41	to ID P11-9 (S301-149)
From ID P11-138 (S301-150)	to ID A1P11.43
From ID A1J11.43	to ID R27.1
From ID A1J11.43	to ID R30.1
From ID A1J11.43	to ID R33.1
From ID R27.2	to ID Q3.1
From ID R30.2	to ID Q4.1
From ID R33.2	to ID Q5.1
From ID Q3.1	to ID R26.1
From ID Q4.1	to ID R29.1
From ID Q5.1	to ID R32.1
From ID R26.2	to GROUND
From ID R29.2	to GROUND
From ID R32.2	to GROUND
From ID Q3.3	to GROUND
From ID Q4.3	to GROUND
From ID Q5.3	to GROUND
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-170 (S503-8)	to ID A1P6.38
From ID A1J6.38	to ID BUS 6

```
      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J7.38
      From ID A1P7.38                   to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)         to ID A1P7.36
      From ID A1J7.36                   to GROUND

Step 727

Description:
This step verifies the Q4 FET used to provide GTD PSU overload
capability to test the +/-15V Power supplies is operational. The DMM
resource will be used to measure voltage UL= 2 volts across the FET.

      Connection Path is as follows:

      From ID P1-4 (DC2-HI)             to ID A1P1.2
      From ID A1J1.2                    to R4

      From +15V                         to ID R4.2
      From ID R4.1                      to ID A1J8.13
      From ID A1P8.13                   to ID P10-208 (S301-78)
      From ID P10-47 (S301-77)          to ID A1P8.38
      From ID A1J8.38                   to ID BUS 6

      From ID BUS 6                     to ID A1J9.22
      From ID A1P9.22                   to ID P11-170 (S508-8)
      From ID P11-203 (S508-1)          to ID A1P10.6
      From ID A1J10.6                   to ID A1J12.42
      From ID A1P12.42                  to ID P12-16 (S201-1)

      From ID P13-49 (S201-17)          to ID A1P14.5
      From ID A1J14.5                   to ID J1A-1C
      From W6 P1A-1C                    to W6 P2-18 (ST J8-18)

      From W6 P2-6 (ST J6-6)            to W6 P1B-2D
      From W6 P2-18 (UUT J1-18)         to W6 P1B-3C
      From W6 P2-19 (UUT J1-19)         to W6 P1B-2C
      From ID J1B-2D                    to ID R34.1
      From ID J1B-3C                    to ID R31.1
      From ID J1B-2C                    to ID R28.1
      From ID R34.2                     to ID Q5.2
      From ID R31.2                     to ID Q4.2
      From ID R28.2                     to ID Q3.2
      From ID P1-4 (DC2-HI)             to ID A1P1.2
      From ID A1J1.2                    to +15V
      From +15V                         to ID A1J11.41
      From ID A1P11.41                  to ID P11-9 (S301-149)
      From ID P11-138 (S301-150)        to ID A1P11.43
      From ID A1J11.43                  to ID R27.1
      From ID A1J11.43                  to ID R30.1
      From ID A1J11.43                  to ID R33.1
      From ID R27.2                     to ID Q3.1
      From ID R30.2                     to ID Q4.1
      From ID R33.2                     to ID Q5.1
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      From ID Q3.1                      to ID R26.1
      From ID Q4.1                      to ID R29.1
      From ID Q5.1                      to ID R32.1
      From ID R26.2                     to GROUND
      From ID R29.2                     to GROUND
      From ID R32.2                     to GROUND
      From ID Q3.3                      to GROUND
      From ID Q4.3                      to GROUND
      From ID Q5.3                      to GROUND

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-170 (S503-8)          to ID A1P6.38
      From ID A1J6.38                   to ID BUS 6

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J7.38
      From ID A1P7.38                   to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)         to ID A1P7.36
      From ID A1J7.36                   to GROUND

Step 728

Description:
This step verifies the Q3 FET used to provide GTD PSU overload
capability to test the +/-15V Power supplies is operational. The DMM
resource will be used to measure voltage UL= 2 volts across the FET.
.
      Connection Path is as follows:

      From ID P1-4 (DC2-HI)             to ID A1P1.2
      From ID A1J1.2                    to R4

      From +15V                         to ID R4.2
      From ID R4.1                      to ID A1J8.13
      From ID A1P8.13                   to ID P10-208 (S301-78)
      From ID P10-47 (S301-77)          to ID A1P8.38
      From ID A1J8.38                   to ID BUS 6

      From ID BUS 6                     to ID A1J9.22
      From ID A1P9.22                   to ID P11-170 (S508-8)
      From ID P11-139 (S508-2)          to ID A1P10.8
      From ID A1J10.8                   to ID A1J12.40
      From ID A1P12.40                  to ID P12-80 (S201-2)

      From ID P13-16 (S201-18)          to ID A1P14.28
      From ID A1J14.28                  to ID J1A-10F
      From W6 P1A-10F                   to W6 P2-19 (ST J8-19)

      From W6 P2-6 (ST J6-6)            to W6 P1B-2D
      From W6 P2-18 (UUT J1-18)         to W6 P1B-3C
      From W6 P2-19 (UUT J1-19)         to W6 P1B-2C
      From ID J1B-2D                    to ID R34.1
```

```
      From ID J1B-3C                  to ID R31.1
      From ID J1B-2C                  to ID R28.1
      From ID R34.2                   to ID Q5.2
      From ID R31.2                   to ID Q4.2
      From ID R28.2                   to ID Q3.2
      From ID P1-4 (DC2-HI)           to ID A1P1.2
      From ID A1J1.2                  to +15V
      From +15V                       to ID A1J11.41
      From ID A1P11.41                to ID P11-9 (S301-149)
      From ID P11-138 (S301-150)      to ID A1P11.43
      From ID A1J11.43                to ID R27.1
      From ID A1J11.43                to ID R30.1
      From ID A1J11.43                to ID R33.1
      From ID R27.2                   to ID Q3.1
      From ID R30.2                   to ID Q4.1
      From ID R33.2                   to ID Q5.1
      From ID Q3.1                    to ID R26.1
      From ID Q4.1                    to ID R29.1
      From ID Q5.1                    to ID R32.1
      From ID R26.2                   to GROUND
      From ID R29.2                   to GROUND
      From ID R32.2                   to GROUND
      From ID Q3.3                    to GROUND
      From ID Q4.3                    to GROUND
      From ID Q5.3                    to GROUND

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-170 (S503-8)        to ID A1P6.38
      From ID A1J6.38                 to ID BUS 6

      From ID P20-3 (DMM-LO)          to ID A1P15.50
      From ID A1J15.50                to ID A1J7.38
      From ID A1P7.38                 to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)       to ID A1P7.36
      From ID A1J7.36                 to GROUND

Step 729

Description:
This step verifies the Q6 - Q8 FETs used to provide GTD PSU overload
capability while testing the +/-15V Power supplies are not shorted.
The DMM will be used to measure voltage UL= -14 volts across FETs.

      Connection Path is as follows:

      From ID P1-25 (DC9-HI)          to ID A1P1.13
      From ID A1J1.13                 to ID A1J7.14
      From ID A1P7.14                 to ID P10-197 (S301-29)
      From ID P10-198 (S301-30)       to ID A1P6.24
      From ID A1J6.24                 to ID BUS 7

      From ID P1-26 (DC9-LO)          to ID A1P1.5
```

From ID A1J1.5	to ID A1J7.16
From ID A1P7.16	to ID P10-163 (S301-12)
From ID P10-98 (S301-11)	to ID A1P7.32
From ID A1J7.32	to GROUND
From BUS 7 (ID A1J6.34)	to ID A1P6.34
From ID P10-61 (S402-4)	to ID P10-94 (S402-1)
From ID A1P6.9	to ID A1J6.9
From ID A1J6.9	to ID A1P6.9
From ID P10-94 (S402-1)	to ID P10-222 (S402-3)
From ID A1P6.44	to BUS 6 (ID A1J6.44)
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From ID R11.1	to ID A1J8.25
From ID A1P8.25	to ID P10-50 (S301-95)
From ID P10-148 (S301-96)	to ID A1P8.45
From ID A1J8.45	to ID BUS 5
From ID BUS 5	to ID A1J9.34
From ID A1P9.34	to ID P11-230 (S507-7)
From ID P11-71 (S507-2)	to ID A1P10.4
From ID A1J10.4	to ID A1J12.44
From ID A1P12.44	to ID P12-52 (S201-4)
From ID BUS 5	to ID A1J9.34
From ID A1P9.34	to ID P11-230 (S507-7)
From ID P11-39 (S507-1)	to ID A1P10.2
From ID A1J10.2	to ID A1J12.46
From ID A1P12.46	to ID P12-20 (S201-3)
From ID P12-15 (S201-16)	to ID A1P12.2
From ID A1J12.2	to ID J1B-9B
From W6 P1B-9B	to W6 P2-16 (ST J8-16)
From ID P12-48 (S201-15)	to ID A1P12.1
From ID A1J12.1	to ID J1B-9A
From W6 P1B-9A	to W6 P2-15 (ST J8-15)
From ID P12-46 (S201-7)	to ID A1P13.3
From ID A1J13.3	to ID J1B-14B
From W6 P1B-14B	to W6 P2-3 (ST J8-3)
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID P20-3 (DMM-LO)	to ID A1P15.50

```
      From ID A1J15.50                to ID A1J7.38
      From ID A1P7.38                 to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)       to ID A1P7.36
      From ID A1J7.36                 to GROUND

Step 730

Description:
This step verifies the operation of Q8 FET used to provide GTD PSU
overload capability to test the +/-15v capability. The DMM resource
will be used to measure voltage UL= -2 volts across FETs.

      Connection Path is as follows:

      From ID P1-25 (DC9-HI)          to ID A1P1.13
      From ID A1J1.13                 to ID A1J7.14
      From ID A1P7.14                 to ID P10-197 (S301-29)
      From ID P10-198 (S301-30)       to ID A1P6.24
      From ID A1J6.24                 to ID BUS 7

      From ID P1-26 (DC9-LO)          to ID A1P1.5
      From ID A1J1.5                  to ID A1J7.16
      From ID A1P7.16                 to ID P10-163 (S301-12)
      From ID P10-98 (S301-11)        to ID A1P7.32
      From ID A1J7.32                 to GROUND

      From BUS 7 (ID A1J6.34)         to ID A1P6.34
      From ID P10-61 (S402-4)         to ID P10-94 (S402-1)
      From ID A1P6.9                  to ID A1J6.9

      From ID A1J6.9                  to ID A1P6.9
      From ID P10-94 (S402-1)         to ID P10-222 (S402-3)
      From ID A1P6.44                 to BUS 6 (ID A1J6.44)

      From ID BUS 6                   to ID A1J8.46
      From ID A1P8.46                 to ID P10-145 (S301-93)
      From ID P10-179 (S301-94)       to ID A1P8.23
      From ID A1J8.23                 to ID R11.2

      From ID R11.1                   to ID A1J8.25
      From ID A1P8.25                 to ID P10-50 (S301-95)
      From ID P10-148 (S301-96)       to ID A1P8.45
      From ID A1J8.45                 to ID BUS 5

      From ID BUS 5                   to ID A1J9.34
      From ID A1P9.34                 to ID P11-230 (S507-7)
      From ID P11-71 (S507-2)         to ID A1P10.4
      From ID A1J10.4                 to ID A1J12.44
      From ID A1P12.44                to ID P12-52 (S201-4)

      From ID P12-15 (S201-16)        to ID A1P12.2
      From ID A1J12.2                 to ID J1B-9B
      From W6 P1B-9B                  to W6 P2-16 (ST J8-16)
```

```
      From W6 P2-3 (ST J6-3)            to W6 P1B-2E
      From W6 P2-15 (UUT J1-15)         to W6 P1B-2B
      From W6 P2-16 (UUT J1-16)         to W6 P1B-4D
      From ID J1B-2E                    to ID Q7.3
      From ID J1B-2B                    to ID Q6.3
      From ID J1B-4D                    to ID Q8.3
      From ID Q6.2                      to ID R37.2
      From ID Q7.2                      to ID R40.2
      From ID Q8.2                      to ID R43.2
      From ID R37.1                     to GROUND
      From ID R40.1                     to GROUND
      From ID R43.1                     to GROUND
      From ID P1-16 (DC6-HI)            to ID A1P1.15
      From ID A1J1.15                   to -15V
      From -15V                         to ID R35.1
      From -15V                         to ID R38.1
      From -15V                         to ID R41.1
      From ID R35.2                     to ID Q6.1
      From ID R38.2                     to ID Q7.1
      From ID R41.2                     to ID Q8.1
      From ID P1-4 (DC2-HI)             to ID A1P1.2
      From ID A1J1.2                    to +15V
      From +15V                         to ID A1J11.42
      From ID A1P11.42                  to ID P11-106 (S301-151)
      From ID P11-75 (S301-152)         to ID A1P11.44
      From ID A1J11.44                  to ID R36.1
      From ID A1J11.44                  to ID R39.1
      From ID A1J11.44                  to ID R42.1
      From ID R36.2                     to ID Q6.1
      From ID R39.2                     to ID Q7.1
      From ID R42.2                     to ID Q8.1

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-137 (S503-7)          to ID A1P6.47
      From ID A1J6.47                   to ID BUS 5

      From ID P20-3 (DMM-LO)            to ID A1P15.50
      From ID A1J15.50                  to ID A1J7.38
      From ID A1P7.38                   to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)         to ID A1P7.36
      From ID A1J7.36                   to GROUND

Step 731

Description:
This step verifies the operation of Q7 FET used to provide GTD PSU
overload capability to test the +/-15v capability. The DMM resource
will be used to measure voltage UL= -2 volts across FETs.

      Connection Path is as follows:

      From ID P1-25 (DC9-HI)            to ID A1P1.13
```

From ID A1J1.13	to ID A1J7.14
From ID A1P7.14	to ID P10-197 (S301-29)
From ID P10-198 (S301-30)	to ID A1P6.24
From ID A1J6.24	to ID BUS 7
From ID P1-26 (DC9-LO)	to ID A1P1.5
From ID A1J1.5	to ID A1J7.16
From ID A1P7.16	to ID P10-163 (S301-12)
From ID P10-98 (S301-11)	to ID A1P7.32
From ID A1J7.32	to GROUND
From BUS 7 (ID A1J6.34)	to ID A1P6.34
From ID P10-61 (S402-4)	to ID P10-94 (S402-1)
From ID A1P6.9	to ID A1J6.9
From ID A1J6.9	to ID A1P6.9
From ID P10-94 (S402-1)	to ID P10-222 (S402-3)
From ID A1P6.44	to BUS 6 (ID A1J6.44)
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From ID R11.1	to ID A1J8.25
From ID A1P8.25	to ID P10-50 (S301-95)
From ID P10-148 (S301-96)	to ID A1P8.45
From ID A1J8.45	to ID BUS 5
From ID BUS 5	to ID A1J9.34
From ID A1P9.34	to ID P11-230 (S507-7)
From ID P11-39 (S507-1)	to ID A1P10.2
From ID A1J10.2	to ID A1J12.46
From ID A1P12.46	to ID P12-20 (S201-3)
From ID P12-48 (S201-15)	to ID A1P12.1
From ID A1J12.1	to ID J1B-9A
From W6 P1B-9A	to W6 P2-15 (ST J8-15)
From W6 P2-3 (ST J6-3)	to W6 P1B-2E
From W6 P2-15 (UUT J1-15)	to W6 P1B-2B
From W6 P2-16 (UUT J1-16)	to W6 P1B-4D
From ID J1B-2E	to ID Q7.3
From ID J1B-2B	to ID Q6.3
From ID J1B-4D	to ID Q8.3
From ID Q6.2	to ID R37.2
From ID Q7.2	to ID R40.2
From ID Q8.2	to ID R43.2
From ID R37.1	to GROUND
From ID R40.1	to GROUND
From ID R43.1	to GROUND
From ID P1-16 (DC6-HI)	to ID A1P1.15
From ID A1J1.15	to -15V
From -15V	to ID R35.1

From -15V	to ID R38.1
From -15V	to ID R41.1
From ID R35.2	to ID Q6.1
From ID R38.2	to ID Q7.1
From ID R41.2	to ID Q8.1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to +15V
From +15V	to ID A1J11.42
From ID A1P11.42	to ID P11-106 (S301-151)
From ID P11-75 (S301-152)	to ID A1P11.44
From ID A1J11.44	to ID R36.1
From ID A1J11.44	to ID R39.1
From ID A1J11.44	to ID R42.1
From ID R36.2	to ID Q6.1
From ID R39.2	to ID Q7.1
From ID R42.2	to ID Q8.1
From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-137 (S503-7)	to ID A1P6.47
From ID A1J6.47	to ID BUS 5
From ID P20-3 (DMM-LO)	to ID A1P15.50
From ID A1J15.50	to ID A1J7.38
From ID A1P7.38	to ID P10-130 (S301-23)
From ID P10-229 (S301-24)	to ID A1P7.36
From ID A1J7.36	to GROUND

Step 732

Description:
This step verifies the operation of Q6 FET used to provide GTD PSU overload capability to test the +/-15v capability. The DMM resource will be used to measure voltage UL= -2 volts across FETs.

Connection Path is as follows:

From ID P1-25 (DC9-HI)	to ID A1P1.13
From ID A1J1.13	to ID A1J7.14
From ID A1P7.14	to ID P10-197 (S301-29)
From ID P10-198 (S301-30)	to ID A1P6.24
From ID A1J6.24	to ID BUS 7
From ID P1-26 (DC9-LO)	to ID A1P1.5
From ID A1J1.5	to ID A1J7.16
From ID A1P7.16	to ID P10-163 (S301-12)
From ID P10-98 (S301-11)	to ID A1P7.32
From ID A1J7.32	to GROUND
From BUS 7 (ID A1J6.34)	to ID A1P6.34
From ID P10-61 (S402-4)	to ID P10-94 (S402-1)
From ID A1P6.9	to ID A1J6.9
From ID A1J6.9	to ID A1P6.9

From ID P10-94 (S402-1)	to ID P10-222 (S402-3)
From ID A1P6.44	to BUS 6 (ID A1J6.44)
From ID BUS 6	to ID A1J8.46
From ID A1P8.46	to ID P10-145 (S301-93)
From ID P10-179 (S301-94)	to ID A1P8.23
From ID A1J8.23	to ID R11.2
From ID R11.1	to ID A1J8.25
From ID A1P8.25	to ID P10-50 (S301-95)
From ID P10-148 (S301-96)	to ID A1P8.45
From ID A1J8.45	to ID BUS 5
From ID BUS 5	to ID A1J9.34
From ID A1P9.34	to ID P11-230 (S507-7)
From ID P11-39 (S507-1)	to ID A1P10.2
From ID A1J10.2	to ID A1J12.46
From ID A1P12.46	to ID P12-20 (S201-3)
From ID P12-46 (S201-7)	to ID A1P13.3
From ID A1J13.3	to ID J1B-14B
From W6 P1B-14B	to W6 P2-3 (ST J8-3)
From W6 P2-3 (ST J6-3)	to W6 P1B-2E
From W6 P2-15 (UUT J1-15)	to W6 P1B-2B
From W6 P2-16 (UUT J1-16)	to W6 P1B-4D
From ID J1B-2E	to ID Q7.3
From ID J1B-2B	to ID Q6.3
From ID J1B-4D	to ID Q8.3
From ID Q6.2	to ID R37.2
From ID Q7.2	to ID R40.2
From ID Q8.2	to ID R43.2
From ID R37.1	to GROUND
From ID R40.1	to GROUND
From ID R43.1	to GROUND
From ID P1-16 (DC6-HI)	to ID A1P1.15
From ID A1J1.15	to -15V
From -15V	to ID R35.1
From -15V	to ID R38.1
From -15V	to ID R41.1
From ID R35.2	to ID Q6.1
From ID R38.2	to ID Q7.1
From ID R41.2	to ID Q8.1
From ID P1-4 (DC2-HI)	to ID A1P1.2
From ID A1J1.2	to +15V
From +15V	to ID A1J11.42
From ID A1P11.42	to ID P11-106 (S301-151)
From ID P11-75 (S301-152)	to ID A1P11.44
From ID A1J11.44	to ID R36.1
From ID A1J11.44	to ID R39.1
From ID A1J11.44	to ID R42.1
From ID R36.2	to ID Q6.1
From ID R39.2	to ID Q7.1
From ID R42.2	to ID Q8.1

```
From ID P20-2 (DMM-HI)          to ID A1P15.49
From ID A1J15.49                to ID A1J8.28
From ID A1P8.28                 to ID P10-203 (S503-1)
From ID P10-137 (S503-7)        to ID A1P6.47
From ID A1J6.47                 to ID BUS 5

From ID P20-3 (DMM-LO)          to ID A1P15.50
From ID A1J15.50                to ID A1J7.38
From ID A1P7.38                 to ID P10-130 (S301-23)
From ID P10-229 (S301-24)       to ID A1P7.36
From ID A1J7.36                 to GROUND
```

2.11 MODULE 8 W7/W8 GTD PROCESSOR SPECIFIC ID TESTS

Refer to Reference Drawings when diagnosing connection paths.

Open 13020A0001 (SYSTEM INTERCONNECT).pdf, 13020A6004 (SELF TEST PWB, A2).pdf, 13020A7701 (CABLE, W7, SCHEMATIC).pdf and 08014A3800 (CABLE W8, RS422).pdf in section 1.4 during review of the following steps.

Step 801

Description:
This step verifies the wire path from W7 P2-A1 to W7 P2-B3. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

```
From ID P20-2(DMM-HI)           to ID A1P15.49
From ID A1J15.49                to ID A1J7.44
From ID A1P7.44                 to ID P10-99 (S301-4)
From ID P10-226 (S301-3)        to ID A1P7.13
From ID A1J7.13                 to ID J1B-1D
From W7 P1B-1D                  to W7 P2-A1 (ST J9-A1)

From ST_J9-A1                   to ST_J9-B3

From W7 P2-B3 (ST J9-B3)        to W7 P1A-3A
From ID J1A-3A                  to ID A1J15.1
From ID A1P15.1                 to ID P13-39 (S701-7)

From ID P12-76 (S701-1)         to ID A1P12.50
From ID A1J12.50                to ID A1J10.3
From ID A1P10.3                 to ID P11-194 (S506-1)
From ID P11-195 (S506-4)        to ID A1P9.33
From ID A1J9.33                 to ID BUS 2

From ID BUS 2                   to ID A1J6.23
From ID A1P6.23                 to ID P10-12 (S503-4)
From ID P10-139 (S503-2)        to ID A1P8.26
From ID A1J8.26                 to ID A1J15.50
From ID A1P15.50                to ID P20-3 (DMM-LO)
```

Step 802

Description:
This step verifies the wire path from W7 P2-B1 to W7 P2-A14. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P12-48 (S201-15)	to ID A1P12.1
From ID A1J12.1	to ID J1B-9A
From W7 P1B-9A	to W7 P2-B1 (ST J9-B1)
From ST_J9-B1	to ST_J9-A14
From W7 P2-A14 (ST J9-A14)	to W7 P1B-14A
From ID J1B-14A	to ID A1J13.1
From ID A1P13.1	to ID P12-79 (S201-5)
From ID P12-20 (S201-3)	to ID A1P12.46
From ID A1J12.46	to ID A1J10.2
From ID A1P10.2	to ID P11-39 (S507-1)
From ID P11-72 (S507-4)	to ID A1P9.27
From ID A1J9.27	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 803

Description:
This step verifies the wire path from W7 P2-C1 to W7 P3-42. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)

```
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P12-15 (S201-16)         to ID A1P12.2
      From ID A1J12.2                  to ID J1B-9B
      From W7 P1B-9B                   to W7 P2-C1 (ST J9-C1)

      From ST_J9-C1                    to ST_J10-42

      From W7 P3-42 (ST J10-42)        to W7 P1B-5B
      From ID J1B-5B                   to ID A1J12.14
      From ID A1P12.14                 to ID P12-27 (S202-24)

      From ID P12-90 (S202-2)          to ID A1P12.36
      From ID A1J12.36                 to ID A1J10.12
      From ID A1P10.12                 to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 804

Description:
This step verifies the wire path from W7 P2-C3 to W5 P2-9. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-162 (S506-2)         to ID A1P10.1
      From ID A1J10.1                  to ID A1J12.48
      From ID A1P12.48                 to ID P12-44 (S701-2)

      From ID P13-6 (S701-8)           to ID A1P15.2
      From ID A1J15.2                  to ID J1A-3B
      From W7 P1A-3B                   to W7 P2-C3 (ST J9-C3)
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      From ST_J9-C3                    to ST_J9-A2

      From W7 P2-A2 (ST J9-A2)         to W7 P1B-1F
      From ID J1B-1F                   to ID A1J1.11
      From ID A1P1.11                  to ID P1-11 (DC4-LO)

      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J7.38
      From ID A1P7.38                  to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)        to ID A1P7.36
      From ID A1J7.36                  to GROUND

Step 805

Description:
This step verifies the wire path from W7 P2-B2 to W7 P2-C2. The DMM
resource will be used to verify the resolver voltage of 11.8V RMS.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P12-22 (S201-37)         to ID A1P12.11
      From ID A1J12.11                 to ID J1B-6B
      From W7 P1B-6B                   to W7 P2-B2 (ST J9-B2)

      From ST_J9-B2                    to ST_J9-B40

      From W7 P2-B40 (ST J9-B40)       to W7 P1B-11C
      From ID J1B-11C                  to ID A1J3.17
      From ID A1P3.17                  to ID J3-25
      From W204 P2-25                  to W204 P1-25 (SRS Ref Hi)

      From W204 P1-26 (SRS Ref Lo)     to W204 P2-26
      From ID J3-26                    to ID A1P3.16
      From ID A1J3.16                  to ID J1B-11A
      From W7 P1B-11A                  to W7 P2-C18 (ST J9-C18)

      From ST_J9-C18                   to ST_J9-C2

      From W7 P2-C2 (ST J9-C2)         to W7 P1B-3A
      From ID J1B-3A                   to ID A1J12.19
      From ID A1P12.19                 to ID P12-54 (S201-38)
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      From ID P12-80 (S201-2)          to ID A1P12.40
      From ID A1J12.40                 to ID A1J10.8
      From ID A1P10.8                  to ID P11-139 (S508-2)
      From ID P11-12 (S508-4)          to ID A1P9.25
      From ID A1J9.25                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 806

Description:
This step verifies the wire path from W7 P2-A3 to W7 P3-43. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P13-79 (S201-20)         to ID A1P14.7
      From ID A1J14.7                  to ID J1A-1D
      From W7 P1A-1D                   to W7 P2-A3 (ST J9-A3)

      From ST_J9-A3                    to ST_J10-43

      From W7 P3-43 (ST J10-43)        to W7 P1A-8E
      From ID J1A-8E                   to ID A1J14.23
      From ID A1P14.23                 to ID P13-91 (S202-35)

      From ID P12-59 (S202-1)          to ID A1P12.38
      From ID A1J12.38                 to ID A1J10.10
      From ID A1P10.10                 to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
```

Step 807

Description:
This step verifies the wire path from W7 P2-B6 to W7 P2-C6. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P13-7 (S701-17)	to ID A1P15.7
From ID A1J15.7	to ID J1A-5A
From W7 P1A-5A	to W7 P2-B6 (ST J9-B6)
From ST_J9-B6	to ST_J9-C6
From W7 P2-C6 (ST J9-C6)	to W7 P1A-10B
From ID J1A-10B	to ID A1J15.22
From ID A1P15.22	to ID P13-71 (S701-18)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 808

Description:
This step verifies the wire path from W7 P2-B7 to W7 P2-C7. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)

```
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P12-71 (S701-19)         to ID A1P13.15
      From ID A1J13.15                 to ID J1B-12F
      From W7 P1B-12F                  to W7 P2-B7 (ST J9-B7)

      From ST_J9-B7                    to ST_J9-C7

      From W7 P2-C7 (ST J9-C7)         to W7 P1B-8D
      From ID J1B-8D                   to ID A1J13.25
      From ID A1P13.25                 to ID P12-39 (S701-20)

      From ID P12-44 (S701-2)          to ID A1P12.48
      From ID A1J12.48                 to ID A1J10.1
      From ID A1P10.1                  to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 809

Description:
This step verifies the wire path from W7 P2-A8 to W7 P2-B8. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P12-38 (S701-11)         to ID A1P13.11
      From ID A1J13.11                 to ID J1B-14F
      From W7 P1B-14F                  to W7 P2-A8 (ST J9-A8)
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      From ST_J9-A8                    to ST_J9-B8

      From W7 P2-B8 (ST J9-B8)         to W7 P1B-13F
      From ID J1B-13F                  to ID A1J13.12
      From ID A1P13.12                 to ID P12-5 (S701-12)

      From ID P12-44 (S701-2)          to ID A1P12.48
      From ID A1J12.48                 to ID A1J10.1
      From ID A1P10.1                  to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)
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Step 810

Description:
This step verifies the wire path from W7 P2-C8 to W7 P2-A9. The DMM resource will be used to measure resistance UL= 10 ohms.

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      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P12-69 (S701-13)         to ID A1P13.13
      From ID A1J13.13                 to ID J1B-12D
      From W7 P1B-12D                  to W7 P2-C8 (ST J9-C8)

      From ST_J9-C8                    to ST_J9-A9

      From W7 P2-A9 (ST J9-A9)         to W7 P1B-12E
      From ID J1B-12E                  to ID A1J13.14
      From ID A1P13.14                 to ID P12-37 (S701-14)

      From ID P12-44 (S701-2)          to ID A1P12.48
      From ID A1J12.48                 to ID A1J10.1
      From ID A1P10.1                  to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2
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      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 811

Description:
This step verifies the wire path from W7 P2-C10 to W7 P2-A11. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P13-72 (S701-15)         to ID A1P15.5
      From ID A1J15.5                  to ID J1A-4B
      From W7 P1A-4B                   to W7 P2-C10 (ST J9-C10)

      From ST_J9-C10                   to ST_J9-A11

      From W7 P2-A11 (ST J9-A11)       to W7 P1A-8A
      From ID J1A-8A                   to ID A1J15.16
      From ID A1P15.16                 to ID P13-40 (S701-16)

      From ID P12-44 (S701-2)          to ID A1P12.48
      From ID A1J12.48                 to ID A1J10.1
      From ID A1P10.1                  to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 812

Description:
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This step verifies the wire path from W7 P2-B11 to W7 P2-C11. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P12-6 (S701-21)	to ID A1P13.16
From ID A1J13.16	to ID J1B-11D
From W7 P1B-11D	to W7 P2-B11 (ST J9-B11)
From ST_J9-B11	to ST_J9-C11
From W7 P2-C11 (ST J9-C11)	to W7 P1B-8F
From ID J1B-8F	to ID A1J13.27
From ID A1P13.27	to ID P12-70 (S701-22)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 813

Description:
This step verifies the wire path from W7 P2-A12 to W7 P3-68. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15

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      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P13-16 (S201-18)         to ID A1P14.28
      From ID A1J14.28                 to ID J1A-10F
      From W7 P1A-10F                  to W7 P2-A12 (ST J9-A12)

      From ST_J9-A12                   to ST_J10-68

      From W7 P3-68 (ST J10-68)        to W7 P1A-7E
      From ID J1A-7E                   to ID A1J14.21
      From ID A1P14.21                 to ID P13-90 (S202-26)

      From ID P12-90 (S202-2)          to ID A1P12.36
      From ID A1J12.36                 to ID A1J10.12
      From ID A1P10.12                 to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 814

Description:
This step verifies the wire path from W7 P2-B12 to W7 P3-60. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P13-49 (S201-17)         to ID A1P14.5
      From ID A1J14.5                  to ID J1A-1C
      From W7 P1A-1C                   to W7 P2-B12 (ST J9-B12)

      From ST_J9-B12                   to ST_J10-60

      From W7 P3-60 (ST J10-60)        to W7 P1A-7E
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      From ID J1A-7E                    to ID A1J14.21
      From ID A1P14.21                  to ID P13-90 (S202-26)

      From ID P12-90 (S202-2)           to ID A1P12.36
      From ID A1J12.36                  to ID A1J10.12
      From ID A1P10.12                  to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 815

Description:
This step verifies the wire path from W7 P2-C13 to W7 P3-41. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)          to ID A1P10.8
      From ID A1J10.8                   to ID A1J12.40
      From ID A1P12.40                  to ID P12-80 (S201-2)

      From ID P12-18 (S201-22)          to ID A1P12.4
      From ID A1J12.4                   to ID J1B-8A
      From W7 P1B-8A                    to W7 P2-C13 (ST J9-C13)

      From ST_J9-C13                    to ST_J10-41

      From W7 P3-41 (ST J10-41)         to W7 P1A-8E
      From ID J1A-8E                    to ID A1J14.23
      From ID A1P14.23                  to ID P13-91 (S202-35)

      From ID P13-93 (S202-3)           to ID A1P14.49
      From ID A1J14.49                  to ID A1J10.48
      From ID A1P10.48                  to ID P11-52 (S510-1)
      From ID P11-147 (S510-4)          to ID A1P9.31
      From ID A1J9.31                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
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      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 816

Description:
This step verifies the wire path from W7 P2-B14 to W7 P3-66. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P12-47 (S201-6)          to ID A1P13.2
      From ID A1J13.2                  to ID J1B-13A
      From W7 P1B-13A                  to W7 P2-B14 (ST J9-B14)

      From ST_J9-B14                   to ST_J10-66

      From W7 P3-66 (ST J10-66)        to W7 P1B-1B
      From ID J1B-1B                   to ID A1J12.22
      From ID A1P12.22                 to ID P12-91 (S202-23)

      From ID P12-59 (S202-1)          to ID A1P12.38
      From ID A1J12.38                 to ID A1J10.10
      From ID A1P10.10                 to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 817

Description:
This step verifies the wire path from W7 P2-A15 to W7 P3-45. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
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      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.15
      From ID A1P9.15                 to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)        to ID A1P10.6
      From ID A1J10.6                 to ID A1J12.42
      From ID A1P12.42                to ID P12-16 (S201-1)

      From ID P12-51 (S201-21)        to ID A1P12.3
      From ID A1J12.3                 to ID J1B-9C
      From W7 P1B-9C                  to W7 P2-A15 (ST J9-A15)

      From ST_J9-A15                  to ST_J10-45

      From W7 P3-45 (ST J10-45)       to W7 P1A-7F
      From ID J1A-7F                  to ID A1J14.22
      From ID A1P14.22                to ID P13-92 (S202-34)

      From ID P12-90 (S202-2)         to ID A1P12.36
      From ID A1J12.36                to ID A1J10.12
      From ID A1P10.12                to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)         to ID A1P9.29
      From ID A1J9.29                 to ID BUS 2

      From ID BUS 2                   to ID A1J6.23
      From ID A1P6.23                 to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)        to ID A1P8.26
      From ID A1J8.26                 to ID A1J15.50
      From ID A1P15.50                to ID P20-3 (DMM-LO)

Step 818

Description:
This step verifies the wire path from W7 P2-B15 to W7 P3-39. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.15
      From ID A1P9.15                 to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)        to ID A1P10.6
      From ID A1J10.6                 to ID A1J12.42
      From ID A1P12.42                to ID P12-16 (S201-1)

      From ID P12-17 (S201-23)        to ID A1P12.5
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      From ID A1J12.5                  to ID J1B-8B
      From W7 P1B-8B                   to W7 P2-B15 (ST J9-B15)

      From ST_J9-B15                   to ST_J10-39

      From W7 P3-39 (ST J10-39)        to W7 P1A-7F
      From ID J1A-8F                   to ID A1J14.24
      From ID A1P14.24                 to ID P13-62 (S202-42)

      From ID P12-90 (S202-2)          to ID A1P12.36
      From ID A1J12.36                 to ID A1J10.12
      From ID A1P10.12                 to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 819

Description:
This step verifies the wire path from W7 P2-C15 to W7 P2-C38. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P12-81 (S201-24)         to ID A1P12.6
      From ID A1J12.6                  to ID J1B-8C
      From W7 P1B-8C                   to W7 P2-C15 (ST J9-C15)

      From ST_J9-C15                   to ST_J9-C38

      From W7 P2-C38 (ST J9-C38)       to W7 P1A-7F
      From ID J1A-8F                   to ID A1J14.24
      From ID A1P14.24                 to ID P13-62 (S202-42)

      From ID P12-90 (S202-2)          to ID A1P12.36
      From ID A1J12.36                 to ID A1J10.12
      From ID A1P10.12                 to ID P11-242 (S509-2)
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      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 820

Description:
This step verifies the wire path from W7 P2-A16 to W7 P2-C35. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P13-50 (S201-25)         to ID A1P14.9
      From ID A1J14.8                  to ID J1A-2D
      From W7 P1A-2D                   to W7 P2-A16 (ST J9-A16)

      From ST_J9-A16                   to ST_J9-C35

      From W7 P2-C35 (ST J9-C35)       to W7 P1B-5C
      From ID J1B-5C                   to ID A1J12.15
      From ID A1P12.15                 to ID P12-31 (S202-45)

      From ID P12-59 (S202-1)          to ID A1P12.38
      From ID A1J12.38                 to ID A1J10.10
      From ID A1P10.10                 to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 821

Description:
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This step verifies the wire path from W7 P2-B16 to W7 P2-A36. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-139 (S508-2)	to ID A1P10.8
From ID A1J10.8	to ID A1J12.40
From ID A1P12.40	to ID P12-80 (S201-2)
From ID P13-17 (S201-26)	to ID A1P14.9
From ID A1J14.9	to ID J1A-1E
From W7 P1A-1E	to W7 P2-B16 (ST J9-A16)
From ST_J9-B16	to ST_J9-A36
From W7 P2-A36 (ST J9-A36)	to W7 P1B-4A
From ID J1B-4A	to ID A1J12.16
From ID A1P12.16	to ID P12-63 (S202-46)
From ID P12-90 (S202-2)	to ID A1P12.36
From ID A1J12.36	to ID A1J10.12
From ID A1P10.12	to ID P11-242 (S509-2)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 822

Description:
This step verifies the wire path from W7 P2-C16 to W7 P2-A37. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15

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      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P13-83 (S201-27)         to ID A1P14.10
      From ID A1J14.10                 to ID J1A-2E
      From W7 P1A-2E                   to W7 P2-C16 (ST J9-C16)

      From ST_J9-C16                   to ST_J9-A37

      From W7 P2-A37 (ST J9-A37)       to W7 P1B-5C
      From ID J1B-5C                   to ID A1J12.15
      From ID A1P12.15                 to ID P12-31 (S202-45)

      From ID P12-59 (S202-1)          to ID A1P12.38
      From ID A1J12.38                 to ID A1J10.10
      From ID A1P10.10                 to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 823

Description:
This step verifies the wire path from W7 P2-A17 to W7 P2-B37. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P13-51 (S201-28)         to ID A1P14.11
      From ID A1J14.11                 to ID J1A-1F
      From W7 P1A-1F                   to W7 P2-A17 (ST J9-A17)

      From ST_J9-A17                   to ST_J9-B37

      From W7 P2-B37 (ST J9-B37)       to W7 P1B-4A
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      From ID J1B-4A                    to ID A1J12.16
      From ID A1P12.16                  to ID P12-63 (S202-46)

      From ID P12-90 (S202-2)           to ID A1P12.36
      From ID A1J12.36                  to ID A1J10.12
      From ID A1P10.12                  to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 824

Description:
This step verifies the wire path from W7 P2-A19 to W7 P3-58. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)          to ID A1P10.6
      From ID A1J10.6                   to ID A1J12.42
      From ID A1P12.42                  to ID P12-16 (S201-1)

      From ID P13-47 (S201-9)           to ID A1P14.1
      From ID A1J14.1                   to ID J1A-1A
      From W7 P1A-1A                    to W7 P2-A19 (ST J9-A19)

      From ST_J9-A19                    to ST_J10-58

      From W7 P3-58 (ST J10-58)         to W7 P1A-12E
      From ID J1A-12E                   to ID A1J14.31
      From ID A1P14.31                  to ID P13-30 (S202-41)

      From ID P12-59 (S202-1)           to ID A1P12.38
      From ID A1J12.38                  to ID A1J10.10
      From ID A1P10.10                  to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
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      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 825

Description:
This step verifies the wire path from W7 P2-A20 to W7 P2-A23. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P13-42 (S701-23)         to ID A1P15.8
      From ID A1J15.8                  to ID J1A-5B
      From W7 P1A-5B                   to W7 P2-A20 (ST J9-A20)

      From ST_J9-A20                   to ST_J9-A23

      From W7 P2-A23 (ST J9-A23)       to W7 P1A-11B
      From ID J1A-11B                  to ID A1J15.24
      From ID A1P15.24                 to ID P13-9 (S701-24)

      From ID P12-44 (S701-2)          to ID A1P12.48
      From ID A1J12.48                 to ID A1J10.1
      From ID A1P10.1                  to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 826

Description:
This step verifies the wire path from W7 P2-C21 to W7 P2-C22. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
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      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.23
      From ID A1P9.23                 to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)        to ID A1P10.3
      From ID A1J10.3                 to ID A1J12.50
      From ID A1P12.50                to ID P12-76 (S701-1)

      From ID P12-73 (S701-37)        to ID A1P13.21
      From ID A1J13.21                to ID J1B-10F
      From W7 P1B-10F                 to W7 P2-C21 (ST J9-C21)

      From ST_J9-C21                  to ST_J9-C22

      From W7 P2-C22 (ST J9-C22)      to W7 P1B-11E
      From ID J1B-11E                 to ID A1J13.17
      From ID A1P13.17                to ID P12-41 (S701-38)

      From ID P12-44 (S701-2)         to ID A1P12.48
      From ID A1J12.48                to ID A1J10.1
      From ID A1P10.1                 to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)        to ID A1P9.33
      From ID A1J9.33                 to ID BUS 2

      From ID BUS 2                   to ID A1J6.23
      From ID A1P6.23                 to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)        to ID A1P8.26
      From ID A1J8.26                 to ID A1J15.50
      From ID A1P15.50                to ID P20-3 (DMM-LO)

Step 827

Description:
This step verifies the wire path from W7 P2-A24 to W7 P2-A25. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.23
      From ID A1P9.23                 to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)        to ID A1P10.3
      From ID A1J10.3                 to ID A1J12.50
      From ID A1P12.50                to ID P12-76 (S701-1)

      From ID P13-73 (S701-25)        to ID A1P15.26
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      From ID A1J15.26                  to ID J1A-12B
      From W7 P1A-12B                   to W7 P2-A24 (ST J9-A24)

      From ST_J9-A24                    to ST_J9-A25

      From W7 P2-A25 (ST J9-A25)        to W7 P1A-13B
      From ID J1A-13B                   to ID A1J15.28
      From ID A1P15.28                  to ID P13-41 (S701-26)

      From ID P12-44 (S701-2)           to ID A1P12.48
      From ID A1J12.48                  to ID A1J10.1
      From ID A1P10.1                   to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)          to ID A1P9.33
      From ID A1J9.33                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 828

Description:
This step verifies the wire path from W7 P2-B24 to W7 P2-B25. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.23
      From ID A1P9.23                   to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)          to ID A1P10.3
      From ID A1J10.3                   to ID A1J12.50
      From ID A1P12.50                  to ID P12-76 (S701-1)

      From ID P13-75 (S701-31)          to ID A1P15.9
      From ID A1J15.9                   to ID J1A-5C
      From W7 P1A-5C                    to W7 P2-B24 (ST J9-B24)

      From ST_J9-B24                    to ST_J9-B25

      From W7 P2-B25 (ST J9-B25)        to W7 P1A-6A
      From ID J1A-6A                    to ID A1J15.10
      From ID A1P15.10                  to ID P13-43 (S701-32)

      From ID P12-44 (S701-2)           to ID A1P12.48
      From ID A1J12.48                  to ID A1J10.1
      From ID A1P10.1                   to ID P11-162 (S506-2)
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      From ID P11-195 (S506-4)          to ID A1P9.33
      From ID A1J9.33                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 829

Description:
This step verifies the wire path from W7 P2-C24 to W7 P2-C25. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.23
      From ID A1P9.23                   to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)          to ID A1P10.3
      From ID A1J10.3                   to ID A1J12.50
      From ID A1P12.50                  to ID P12-76 (S701-1)

      From ID P13-45 (S701-39)          to ID A1P15.13
      From ID A1J15.13                  to ID J1A-7A
      From W7 P1A-7A                    to W7 P2-C24 (ST J9-C24)

      From ST_J9-C24                    to ST_J9-C25

      From W7 P2-C25 (ST J9-C25)        to W7 P1A-7B
      From ID J1A-7B                    to ID A1J15.14
      From ID A1P15.14                  to ID P13-12 (S701-40)

      From ID P12-44 (S701-2)           to ID A1P12.48
      From ID A1J12.48                  to ID A1J10.1
      From ID A1P10.1                   to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)          to ID A1P9.33
      From ID A1J9.33                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 830

Description:
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This step verifies the wire path from W7 P2-A26 to W7 P2-A27. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P12-8 (S701-27)	to ID A1P13.29
From ID A1J13.29	to ID J1B-7D
From W7 P1B-7D	to W7 P2-A26 (ST J9-A26)
From ST_J9-A26	to ST_J9-A27
From W7 P2-A27 (ST J9-A27)	to W7 P1B-9E
From ID J1B-9E	to ID A1J13.23
From ID A1P13.23	to ID P12-72 (S701-28)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 831

Description:
This step verifies the wire path from W7 P2-B26 to W7 P2-B27. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23

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      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P13-10 (S701-33)         to ID A1P15.11
      From ID A1J15.11                 to ID J1A-6B
      From W7 P1A-6B                   to W7 P2-B26 (ST J9-B26)

      From ST_J9-B26                   to ST_J9-B27

      From W7 P2-B27 (ST J9-B27)       to W7 P1A-6C
      From ID J1A-6C                   to ID A1J15.12
      From ID A1P15.12                 to ID P13-74 (S701-34)

      From ID P12-44 (S701-2)          to ID A1P12.48
      From ID A1J12.48                 to ID A1J10.1
      From ID A1P10.1                  to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 832

Description:
This step verifies the wire path from W7 P2-A28 to W7 P2-B20. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P12-40 (S701-29)         to ID A1P13.18
      From ID A1J13.18                 to ID J1B-11F
      From W7 P1B-11F                  to W7 P2-A28 (ST J9-A28)

      From ST_J9-A28                   to ST_J9-B20

      From W7 P2-B20 (ST J9-B20)       to W7 P1B-7E
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      From ID J1B-7E                    to ID A1J13.31
      From ID A1P13.31                  to ID P12-7 (S701-30)

      From ID P12-44 (S701-2)           to ID A1P12.48
      From ID A1J12.48                  to ID A1J10.1
      From ID A1P10.1                   to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)          to ID A1P9.33
      From ID A1J9.33                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 833

Description:
This step verifies the wire path from W7 P2-A30 to W7 P3-61. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)          to ID A1P10.6
      From ID A1J10.6                   to ID A1J12.42
      From ID A1P12.42                  to ID P12-16 (S201-1)

      From ID P12-46 (S201-7)           to ID A1P13.3
      From ID A1J13.3                   to ID J1B-14B
      From W7 P1B-14B                   to W7 P2-A30 (ST J9-A30)

      From ST_J9-A30                    to ST_J10-61

      From W7 P3-61 (ST J10-61)         to W7 P1A-11F
      From ID J1A-11F                   to ID A1J14.30
      From ID A1P14.30                  to ID P13-59 (S202-25)

      From ID P12-59 (S202-1)           to ID A1P12.38
      From ID A1J12.38                  to ID A1J10.10
      From ID A1P10.10                  to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
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      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 834

Description:
This step verifies the wire path from W7 P2-B30 to W7 P3-72. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P12-13 (S201-8)          to ID A1P13.4
      From ID A1J13.4                  to ID J1B-13B
      From W7 P1B-13B                  to W7 P2-B30 (ST J9-A30)

      From ST_J9-B30                   to ST_J10-72

      From W7 P3-72 (ST J10-72)        to W7 P1A-11F
      From ID J1A-11F                  to ID A1J14.30
      From ID A1P14.30                 to ID P13-59 (S202-25)

      From ID P12-59 (S202-1)          to ID A1P12.38
      From ID A1J12.38                 to ID A1J10.10
      From ID A1P10.10                 to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 835

Description:
This step verifies the wire path from W7 P2-B31 to W7 P3-33. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
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      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.15
      From ID A1P9.15                 to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)        to ID A1P10.8
      From ID A1J10.8                 to ID A1J12.40
      From ID A1P12.40                to ID P12-80 (S201-2)

      From ID P13-19 (S201-34)        to ID A1P14.13
      From ID A1J14.13                to ID J1A-3E
      From W7 P1A-3E                  to W7 P2-B31 (ST J9-B31)

      From ST_J9-B31                  to ST_J10-33

      From W7 P3-33 (ST J10-33)       to W7 P1B-4C
      From ID J1B-4C                  to ID A1J12.18
      From ID A1P12.18                to ID P12-32 (S202-48)

      From ID P12-90 (S202-2)         to ID A1P12.36
      From ID A1J12.36                to ID A1J10.12
      From ID A1P10.12                to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)         to ID A1P9.29
      From ID A1J9.29                 to ID BUS 2

      From ID BUS 2                   to ID A1J6.23
      From ID A1P6.23                 to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)        to ID A1P8.26
      From ID A1J8.26                 to ID A1J15.50
      From ID A1P15.50                to ID P20-3 (DMM-LO)

Step 836

Description:
This step verifies the wire path from W7 P2-A32 to W7 P2-A7. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.19
      From ID A1P9.19                 to ID P11-18 (S509-3)
      From ID P11-242 (S509-2)        to ID A1P10.12
      From ID A1J10.12                to ID A1J12.36
      From ID A1P12.36                to ID P12-90 (S202-2)

      From ID P13-64 (S202-50)        to ID A1P14.26
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      From ID A1J14.26                  to ID J1A-9F
      From W7 P1A-9F                    to W7 P2-A32 (ST J9-A32)

      From ST_J9-A32                    to ST_J9-A7

      From W7 P2-A7 (ST J9-A7)          to W7 P1B-13D
      From ID J1B-13D                   to ID A1J13.8
      From ID A1P13.8                   to ID P12-68 (S701-4)

      From ID P12-44 (S701-2)           to ID A1P12.48
      From ID A1J12.48                  to ID A1J10.1
      From ID A1P10.1                   to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)          to ID A1P9.33
      From ID A1J9.33                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 837

Description:
This step verifies the wire path from W7 P2-B32 to W7 P2-C20. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.23
      From ID A1P9.23                   to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)          to ID A1P10.3
      From ID A1J10.3                   to ID A1J12.50
      From ID A1P12.50                  to ID P12-76 (S701-1)

      From ID P12-42 (S701-35)          to ID A1P13.19
      From ID A1J13.19                  to ID J1B-10D
      From W7 P1B-10D                   to W7 P2-B32 (ST J9-B32)

      From ST_J9-B32                    to ST_J9-C20

      From W7 P2-C20 (ST J9-C20)        to W7 P1B-10E
      From ID J1B-10E                   to ID A1J13.20
      From ID A1P13.20                  to ID P12-9 (S701-36)

      From ID P12-44 (S701-2)           to ID A1P12.48
      From ID A1J12.48                  to ID A1J10.1
      From ID A1P10.1                   to ID P11-162 (S506-2)
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      From ID P11-195 (S506-4)          to ID A1P9.33
      From ID A1J9.33                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 838

Description:
This step verifies the wire path from W7 P2-B32 to W7 P2-C20. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2(DMM-HI)             to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.32
      From ID A1P8.32                   to ID P10-136 (S301-44)
      From ID P10-135 (S301-43)         to ID A1P7.7
      From ID A1J7.7                    to ID J1A-13F
      From W7 P1A-13F                   to W7 J2-B32 (ST J9-B32)

      From ST_J9-B32                    to ST_J9-C20

      From W7 P2-C20 (ST J9-C20)        to W7 P1B-10E
      From ID J1B-10E                   to ID A1J13.20
      From ID A1P13.20                  to ID P12-9 (S701-36)

      From ID P12-44 (S701-2)           to ID A1P12.48
      From ID A1J12.48                  to ID A1J10.1
      From ID A1P10.1                   to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)          to ID A1P9.33
      From ID A1J9.33                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 839

Description:
This step verifies the wire path from W7 P2-C32 to W7 P2-C33. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
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      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P13-80 (S201-11)         to ID A1P14.3
      From ID A1J14.3                  to ID J1A-1B
      From W7 P1A-1B                   to W7 P2-C32 (ST J9-C32)

      From ST_J9-C32                   to ST_J9-C33

      From W7 P2-C33 (ST J9-C33)       to W7 P1A-2B
      From ID J1A-2B                   to ID A1J14.4
      From ID A1P14.4                  to ID P13-48 (S201-12)

      From ID P12-80 (S201-2)          to ID A1P12.40
      From ID A1J12.40                 to ID A1J10.8
      From ID A1P10.8                  to ID P11-139 (S508-2)
      From ID P11-12 (S508-4)          to ID A1P9.25
      From ID A1J9.25                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 840

Description:
This step verifies the wire path from W7 P2-A33 to W7 P2-B38. The
DMM resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P13-14 (S201-10)         to ID A1P14.2
      From ID A1J14.2                  to ID J1A-2A
      From W7 P1A-2A                   to W7 P2-A33 (ST J9-A33)
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      From ST_J9-A33                    to ST_J9-B38

      From W7 P2-B38 (ST J9-B38)        to W7 P1A-12E
      From ID J1A-12E                   to ID A1J14.31
      From ID A1P14.31                  to ID P13-30 (S202-41)

      From ID P12-59 (S202-1)           to ID A1P12.38
      From ID A1J12.38                  to ID A1J10.10
      From ID A1P10.10                  to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 841

Description:
This step verifies the wire path from W7 P2-B33 to W7 P2-B4. The DMM
resource will be used to measure resistance UL= 10 ohms.


      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.19
      From ID A1P9.19                   to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)          to ID A1P10.10
      From ID A1J10.10                  to ID A1J12.38
      From ID A1P12.38                  to ID P12-59 (S202-1)

      From ID P13-63 (S202-51)          to ID A1P14.48
      From ID A1J14.48                  to ID J1A-13D
      From W7 P1A-13D                   to W7 P2-B33 (ST J9-B33)

      From ST_J9-B33                    to ST_J9-B4

      From W7 P2-B4 (ST J9-B4)          to W7 P1B-13E
      From ID J1B-13E                   to ID A1J13.10
      From ID A1P13.10                  to ID P12-3 (S701-6)

      From ID P12-44 (S701-2)           to ID A1P12.48
      From ID A1J12.48                  to ID A1J10.1
      From ID A1P10.1                   to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)          to ID A1P9.33
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      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 842

Description:
This step verifies the wire path from W7 P2-A34 to W7 P2-A4. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.19
      From ID A1P9.19                  to ID P11-18 (S509-3)
      From ID P11-242 (S509-2)         to ID A1P10.12
      From ID A1J10.12                 to ID A1J12.36
      From ID A1P12.36                 to ID P12-90 (S202-2)

      From ID P13-94 (S202-52)         to ID A1P14.27
      From ID A1J14.27                 to ID J1A-10E
      From W7 P1A-10E                  to W7 P2-A34 (ST J9-A34)

      From ST_J9-A34                   to ST_J9-A4

      From W7 P2-A4 (ST J9-A4)         to W7 P1B-14E
      From ID J1B-14E                  to ID A1J13.9
      From ID A1P13.9                  to ID P12-36 (S701-5)

      From ID P12-76 (S701-1)          to ID A1P12.50
      From ID A1J12.50                 to ID A1J10.3
      From ID A1P10.3                  to ID P11-194 (S506-1)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 843

Description:
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This step verifies the wire path from W7 P2-B34 to W7 P3-34. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15
From ID A1P9.15	to ID P11-77 (S508-3)
From ID P11-203 (S508-1)	to ID A1P10.6
From ID A1J10.6	to ID A1J12.42
From ID A1P12.42	to ID P12-16 (S201-1)
From ID P13-52 (S201-33)	to ID A1P14.12
From ID A1J14.12	to ID J1A-2F
From W7 P1A-2F	to W7 P2-B34 (ST J9-B34)
From ST_J9-B34	to ST_J10-34
From W7 P3-34 (ST J10-34)	to W7 P1B-4B
From ID J1B-4B	to ID A1J12.17
From ID A1P12.17	to ID P12-96 (S202-47)
From ID P12-59 (S202-1)	to ID A1P12.38
From ID A1J12.38	to ID A1J10.10
From ID A1P10.10	to ID P11-177 (S509-1)
From ID P11-17 (S509-4)	to ID A1P9.29
From ID A1J9.29	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 844

Description:
This step verifies the wire path from W7 P2-C34 to W7 P2-B36. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.15

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      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P12-82 (S201-29)         to ID A1P12.7
      From ID A1J12.7                  to ID J1B-7A
      From W7 P1B-7A                   to W7 P2-C34 (ST J9-C34)

      From ST_J9-C34                   to ST_J9-B36

      From W7 P2-B36 (ST J9-B36)       to W7 P1B-4B
      From ID J1B-4B                   to ID A1J12.17
      From ID A1P12.17                 to ID P12-96 (S202-47)

      From ID P12-59 (S202-1)          to ID A1P12.38
      From ID A1J12.38                 to ID A1J10.10
      From ID A1P10.10                 to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)          to ID A1P9.29
      From ID A1J9.29                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 845

Description:
This step verifies the wire path from W7 P2-A35 to W7 P3-31. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P12-19 (S201-31)         to ID A1P12.9
      From ID A1J12.9                  to ID J1B-7C
      From W7 P1B-7C                   to W7 P2-A35 (ST J9-A35)

      From ST_J9-A35                   to ST_J10-31

      From W7 P3-31 (ST J10-31)        to W7 P1B-4B
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      From ID J1B-4B                    to ID A1J12.17
      From ID A1P12.17                  to ID P12-96 (S202-47)

      From ID P12-59 (S202-1)           to ID A1P12.38
      From ID A1J12.38                  to ID A1J10.10
      From ID A1P10.10                  to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 846

Description:
This step verifies the wire path from W7 P2-B35 to W7 P3-57. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)          to ID A1P10.8
      From ID A1J10.8                   to ID A1J12.40
      From ID A1P12.40                  to ID P12-80 (S201-2)

      From ID P12-83 (S201-32)          to ID A1P12.10
      From ID A1J12.10                  to ID J1B-6A
      From W7 P1B-6A                    to W7 P2-B35 (ST J9-B35)

      From ST_J9-B35                    to ST_J10-57

      From W7 P3-57 (ST J10-57)         to W7 P1B-4C
      From ID J1B-4C                    to ID A1J12.18
      From ID A1P12.18                  to ID P12-32 (S202-48)

      From ID P12-90 (S202-2)           to ID A1P12.36
      From ID A1J12.36                  to ID A1J10.12
      From ID A1P10.12                  to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
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      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 847

Description:
This step verifies the wire path from W7 P2-A38 to W7 P2-A6. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.19
      From ID A1P9.19                  to ID P11-18 (S509-3)
      From ID P11-242 (S509-2)         to ID A1P10.12
      From ID A1J10.12                 to ID A1J12.36
      From ID A1P12.36                 to ID P12-90 (S202-2)

      From ID P12-32 (S202-48)         to ID A1P12.18
      From ID A1J12.18                 to ID J1B-4C
      From W7 P1B-4C                   to W7 P2-A38 (ST J9-A38)

      From ST_J9-A38                   to ST_J9-A6

      From W7 P2-A6 (ST J9-A6)         to W7 P1B-14D
      From ID J1B-14D                  to ID A1J13.7
      From ID A1P13.7                  to ID P12-4 (S701-3)

      From ID P12-76 (S701-1)          to ID A1P12.50
      From ID A1J12.50                 to ID A1J10.3
      From ID A1P10.3                  to ID P11-194 (S506-1)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 848

Description:
This step verifies the wire path from W7 P2-C40 to W7 P3-64. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
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      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.15
      From ID A1P9.15                 to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)        to ID A1P10.6
      From ID A1J10.6                 to ID A1J12.42
      From ID A1P12.42                to ID P12-16 (S201-1)

      From ID P13-15 (S201-19)        to ID A1P14.6
      From ID A1J14.6                 to ID J1A-2C
      From W7 P1A-2C                  to W7 P2-C40 (ST J9-C40)

      From ST_J9-C40                  to ST_J10-64

      From W7 P3-64 (ST J10-64)       to W7 P1A-7F
      From ID J1A-7F                  to ID A1J14.22
      From ID A1P14.22                to ID P13-92 (S202-34)

      From ID P12-90 (S202-2)         to ID A1P12.36
      From ID A1J12.36                to ID A1J10.12
      From ID A1P10.12                to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)         to ID A1P9.29
      From ID A1J9.29                 to ID BUS 2

      From ID BUS 2                   to ID A1J6.23
      From ID A1P6.23                 to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)        to ID A1P8.26
      From ID A1J8.26                 to ID A1J15.50
      From ID A1P15.50                to ID P20-3 (DMM-LO)

Step 849

Description:
This step verifies the wire path from W7 P3-7 to W7 P2-C37. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)          to ID A1P15.49
      From ID A1J15.49                to ID A1J8.28
      From ID A1P8.28                 to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)         to ID A1P6.13
      From ID A1J6.13                 to ID BUS 1

      From ID BUS 1                   to ID A1J9.15
      From ID A1P9.15                 to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)        to ID A1P10.6
      From ID A1J10.6                 to ID A1J12.42
      From ID A1P12.42                to ID P12-16 (S201-1)

      From ID P13-18 (S201-35)        to ID A1P14.14
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      From ID A1J14.14                  to ID J1A-3F
      From W7 P1A-3F                    to W7 P3-7 (ST J10-7)

      From ST_J10-7                     to ST_J9-C37

      From W7 P2-C37 (ST J9-C37)        to W7 P1B-4B
      From ID J1B-4B                    to ID A1J12.17
      From ID A1P12.17                  to ID P12-96 (S202-47)

      From ID P12-59 (S202-1)           to ID A1P12.38
      From ID A1J12.38                  to ID A1J10.10
      From ID A1P10.10                  to ID P11-177 (S509-1)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 850

Description:
This step verifies the wire path from W7 P3-8 to W7 P3-12. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.23
      From ID A1P9.23                   to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)          to ID A1P10.3
      From ID A1J10.3                   to ID A1J12.50
      From ID A1P12.50                  to ID P12-76 (S701-1)

      From ID P13-76 (S701-41)          to ID A1P15.15
      From ID A1J15.15                  to ID J1A-7C
      From W7 P1A-7C                    to W7 P3-8 (ST J10-8)

      From ST_J10-8                     to ST_J10-12

      From W7 P3-12 (ST J10-12)         to W7 P1A-4C
      From ID J1A-4C                    to ID A1J15.6
      From ID A1P15.6                   to ID P13-44 (S701-42)

      From ID P12-44 (S701-2)           to ID A1P12.48
      From ID A1J12.48                  to ID A1J10.1
      From ID A1P10.1                   to ID P11-162 (S506-2)
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      From ID P11-195 (S506-4)          to ID A1P9.33
      From ID A1J9.33                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 851

Description:
This step verifies the wire path from W7 P3-9 to W7 P3-65. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)          to ID A1P10.8
      From ID A1J10.8                   to ID A1J12.40
      From ID A1P12.40                  to ID P12-80 (S201-2)

      From ID P12-78 (S201-14)          to ID A1P13.6
      From ID A1J13.6                   to ID J1B-13C
      From W7 P1B-13C                   to W7 P3-9 (ST J10-9)

      From ST_J10-9                     to ST_J10-65

      From W7 P3-65 (ST J10-65)         to W7 P1B-14C
      From ID J1B-14C                   to ID A1J13.5
      From ID A1P13.5                   to ID P12-14 (S201-13)

      From ID P12-16 (S201-1)           to ID A1P12.42
      From ID A1J12.42                  to ID A1J10.6
      From ID A1P10.6                   to ID P11-203 (S508-1)
      From ID P11-12 (S508-4)           to ID A1P9.25
      From ID A1J9.25                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 852

Description:
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This step verifies the wire path from W7 P3-10 to W7 P2-C4. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P1-13 (DC5-HI)	to ID A1P1.14
From ID A1J1.14	to ID A1J2.2
From ID A1P2.2	to ID P10-24 (S101-9)
From ID P10-88 (S101-10)	to ID A1P2.14
From ID A1J2.14	to ID J1A-14F
From W7 P1A-14F	to W7 P3-10 (ST J10-10)
From ST_J10-10	to ST_J9-C4
From W7 P2-C4 (ST J9-C4)	to W7 P1A-3C
From ID J1A-3C	to ID A1J15.3
From ID A1P15.3	to ID P13-70 (S701-9)
From ID P12-76 (S701-1)	to ID A1P12.50
From ID A1J12.50	to ID A1J10.3
From ID A1P10.3	to ID P11-194 (S506-1)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 853

Description:
This step verifies the wire path from W7 P2-A5 to W7 P3-11. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-12 (S503-4)	to ID A1P6.23
From ID A1J6.23	to ID BUS 2
From ID BUS 2	to ID A1J9.33
From ID A1P9.33	to ID P11-195 (S506-4)
From ID P11-162 (S506-2)	to ID A1P10.1
From ID A1J10.1	to ID A1J12.48
From ID A1P12.48	to ID P12-44 (S701-2)
From ID P13-38 (S701-10)	to ID A1P15.4
From ID A1J15.4	to ID J1A-4A
From W7 P1A-4A	to W7 P2-A5 (ST J9-A5)

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      From ST_J9-A5                    to ST_J10-11

      From W7 P3-11 (ST J10-11)        to W7 P1A-12F
      From ID J1A-12F                  to ID A1J7.12
      From ID A1P7.12                  to ID P10-66 (S301-6)
      From ID P10-33 (S301-5)          to ID A1P7.40
      From ID A1J7.40                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 854

Description:
This step verifies the wire path from W7 P3-13 to W7 P3-14. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.23
      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P13-13 (S701-49)         to ID A1P15.17
      From ID A1J15.17                 to ID J1A-8B
      From W7 P1A-8B                   to W7 P3-13 (ST J10-13)

      From ST_J10-13                   to ST_J10-14

      From W7 P3-14 (ST J10-14)        to W7 P1A-9A
      From ID J1A-9A                   to ID A1J15.19
      From ID A1P15.19                 to ID P13-77 (S701-50)

      From ID P12-44 (S701-2)          to ID A1P12.48
      From ID A1J12.48                 to ID A1J10.1
      From ID A1P10.1                  to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 855

Description:
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This step verifies the wire path from W7 P3-15 to W7 P3-16. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23
From ID A1P9.23	to ID P11-164 (S506-3)
From ID P11-194 (S506-1)	to ID A1P10.3
From ID A1J10.3	to ID A1J12.50
From ID A1P12.50	to ID P12-76 (S701-1)
From ID P12-10 (S701-45)	to ID A1P13.33
From ID A1J13.33	to ID J1B-7F
From W7 P1B-7F	to W7 P3-15 (ST J10-15)
From ST_J10-15	to ST_J10-16
From W7 P3-16 (ST J10-16)	to W7 P1B-8E
From ID J1B-8E	to ID A1J13.26
From ID A1P13.26	to ID P12-74 (S701-46)
From ID P12-44 (S701-2)	to ID A1P12.48
From ID A1J12.48	to ID A1J10.1
From ID A1P10.1	to ID P11-162 (S506-2)
From ID P11-195 (S506-4)	to ID A1P9.33
From ID A1J9.33	to ID BUS 2
From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

Step 856

Description:
This step verifies the wire path from W7 P3-17 to W7 P3-37. The DMM resource will be used to measure resistance UL= 10 ohms.

Connection Path is as follows:

From ID P20-2 (DMM-HI)	to ID A1P15.49
From ID A1J15.49	to ID A1J8.28
From ID A1P8.28	to ID P10-203 (S503-1)
From ID P10-77 (S503-3)	to ID A1P6.13
From ID A1J6.13	to ID BUS 1
From ID BUS 1	to ID A1J9.23

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      From ID A1P9.23                  to ID P11-164 (S506-3)
      From ID P11-194 (S506-1)         to ID A1P10.3
      From ID A1J10.3                  to ID A1J12.50
      From ID A1P12.50                 to ID P12-76 (S701-1)

      From ID P13-78 (S701-47)         to ID A1P15.18
      From ID A1J15.18                 to ID J1A-8C
      From W7 P1A-8C                   to W7 P3-17 (ST J10-17)

      From ST_J10-17                   to ST_J10-37

      From W7 P3-37 (ST J10-37)        to W7 P1A-9B
      From ID J1A-9B                   to ID A1J15.20
      From ID A1P15.20                 to ID P13-46 (S701-48)

      From ID P12-44 (S701-2)          to ID A1P12.48
      From ID A1J12.48                 to ID A1J10.1
      From ID A1P10.1                  to ID P11-162 (S506-2)
      From ID P11-195 (S506-4)         to ID A1P9.33
      From ID A1J9.33                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 857

Description:
This step verifies the wire path from W7 P3-20 to W7 P2-C2. The DSO
will be used to verify the resolver reference voltage of 11.8V RMS.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P13-82 (S201-36)         to ID A1P14.29
      From ID A1J14.29                 to ID J1A-11E
      From W7 P1A-11E                  to W7 P3-20 (ST J10-20)

      From ST_J10-20                   to ST_J9-B18

      From W7 P2-B18 (ST J9-B18)       to W7 P1B-10C
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      From ID J1B-10C                  to ID A1J3.17
      From ID A1P3.17                  to ID J3-25
      From W204 P2-25                  to W204 P1-25 (SRS Ref Hi)

      From W204 P1-26 (SRS Ref Lo)     to W204 P2-26
      From ID J3-26                    to ID A1P3.16
      From ID A1J3.16                  to ID J1B-11A
      From W7 P1B-11A                  to W7 P2-C18 (ST J9-C18)

      From ST_J9-C18                   to ST_J9-C2

      From W7 P2-C2 (ST J9-C2)         to W7 P1B-3A
      From ID J1B-3A                   to ID A1J12.19
      From ID A1P12.19                 to ID P12-54 (S201-38)

      From ID P12-80 (S201-2)          to ID A1P12.40
      From ID A1J12.40                 to ID A1J10.8
      From ID A1P10.8                  to ID P11-139 (S508-2)
      From ID P11-12 (S508-4)          to ID A1P9.25
      From ID A1J9.25                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 858

Description:
This step verifies the wire path from W7 P3-77 to W7 P3-24. The DSO
will be used to verify the resolver sin voltage of 5.9V RMS.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-203 (S508-1)         to ID A1P10.6
      From ID A1J10.6                  to ID A1J12.42
      From ID A1P12.42                 to ID P12-16 (S201-1)

      From ID P13-21 (S201-41)         to ID A1P14.15
      From ID A1J14.15                 to ID J1A-4E
      From W7 P1A-4E                   to W7 P3-77 (ST J10-77)

      From ST_J10-77                   to ST_J9-B17

      From W7 P2-B17 (ST J9-B17)       to W7 P1B-12A
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      From ID J1B-12A                   to ID A1J3.14
      From ID A1P3.14                   to ID J3-15
      From W204 P2-15                   to W204 P1-15 (SRS Sin +)

      From W204 P1-13 (SRS Sin -)       to W204 P2-13
      From ID J3-13                     to ID A1P3.12
      From ID A1J3.12                   to ID J1B-12C
      From W7 P1B-12C                   to W7 P2-C17 (ST J9-C17)

      From ST_J9-C17                    to ST_J10-24

      From W7 P3-24 (ST J10-24)         to W7 P1B-6C
      From ID J1B-6C                    to ID A1J12.12
      From ID A1P12.12                  to ID P12-53 (S201-39)

      From ID P12-20 (S201-3)           to ID A1P12.46
      From ID A1J12.46                  to ID A1J10.2
      From ID A1P10.2                   to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)           to ID A1P9.27
      From ID A1J9.27                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)

Step 859

Description:
This step verifies the wire path for W8 RS422 Communication.

      Connection Path is as follows:

      From W8 P1-1 (ICJ6.1)             to W8 P2-1
      From ID J4-1                      to ID A1P3.19
      From ID A1J3.19                   to ID J1B-10A
      From W7 P1B-10A                   to W7 P3-32 (ST J10-32)

      From ST_J10-32                    to ST_J10-40

      From W7 P3-40 (ST J10-40)         to W7 P1B-9F
      From ID J1B-9F                    to ID A1J3.10
      From ID A1P3.10                   to ID J4-4
      From W8 P2-4                      to W8 P1-4 (ICJ6.4)

      From W8 P1-3 (ICJ6.3)             to W8 P2-3
      From ID J4-3                      to ID A1P3.9
      From ID A1J3.9                    to ID J1B-11E
      From W7 P1B-11E                   to W7 P3-44 (ST J10-44)

      From ST_J10-44                    to ST_J10-59

      From W7 P3-59 (ST J10-59)         to W7 P1B-10B
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      From ID J1B-10B                  to ID A1J3.18
      From ID A1P3.18                  to ID J4-2
      From W8 P2-2                     to W8 P1-2 (ICJ6.2)

Step 860

Description:
This step verifies the wire path from W7 P3-35 to W7 P3-38. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.19
      From ID A1P9.19                  to ID P11-18 (S509-3)
      From ID P11-242 (S509-2)         to ID A1P10.12
      From ID A1J10.12                 to ID A1J12.36
      From ID A1P12.36                 to ID P12-90 (S202-2)

      From ID P13-24 (S202-20)         to ID A1P14.20
      From ID A1J14.20                 to ID J1A-6F
      From W7 P1A-6F                   to W7 P3-35 (ST J10-35)

      From ST_J10-35                   to ST_J10-38

      From W7 P3-38 (ST J10-38)        to W7 P1B-5A
      From ID J1B-5A                   to ID A1J12.13
      From ID A1P12.13                 to ID P12-92 (S202-22)

      From ID P13-29 (S202-4)          to ID A1P14.50
      From ID A1J14.50                 to ID A1J10.50
      From ID A1P10.50                 to ID P11-244 (S510-2)
      From ID P11-147 (S510-4)         to ID A1P9.31
      From ID A1J9.31                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 861

Description:
This step verifies the wire path from W7 P3-51 to W7 P3-25. The DSO
will be used to verify the resolver cos voltage of 5.9V RMS.

      Connection Path is as follows:
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```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.15
      From ID A1P9.15                  to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)         to ID A1P10.8
      From ID A1J10.8                  to ID A1J12.40
      From ID A1P12.40                 to ID P12-80 (S201-2)

      From ID P12-84 (S201-40)         to ID A1P12.20
      From ID A1J12.20                 to ID J1B-2A
      From W7 P1B-2A                   to W7 P3-51 (ST J10-51)

      From ST_J10-51                   to ST_J9-B19

      From W7 P2-B19 (ST J9-B19)       to W7 P1B-12B
      From ID J1B-12B                  to ID A1J3.13
      From ID A1P3.13                  to ID J3-14
      From W204 P2-14                  to W204 P1-14 (SRS Cos +)

      From W204 P1-16 (SRS Cos -)      to W204 P2-16
      From ID J3-16                    to ID A1P3.15
      From ID A1J3.15                  to ID J1B-11B
      From W7 P1B-11B                  to W7 P2-C19 (ST J9-C19)

      From ST_J9-C19                   to ST_J10-25

      From W7 P3-25 (ST J10-25)        to W7 P1A-4F
      From ID J1A-4F                   to ID A1J14.16
      From ID A1P14.16                 to ID P13-86 (S201-43)

      From ID P12-20 (S201-3)          to ID A1P12.46
      From ID A1J12.46                 to ID A1J10.2
      From ID A1P10.2                  to ID P11-39 (S507-1)
      From ID P11-72 (S507-4)          to ID A1P9.27
      From ID A1J9.27                  to ID BUS 2

      From ID BUS 2                    to ID A1J6.23
      From ID A1P6.23                  to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)         to ID A1P8.26
      From ID A1J8.26                  to ID A1J15.50
      From ID A1P15.50                 to ID P20-3 (DMM-LO)

Step 862

Description:
This step verifies the wire path from W7 P3-52 to W7 P3-76. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:
```

```
      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.19
      From ID A1P9.19                  to ID P11-18 (S509-3)
      From ID P11-242 (S509-2)         to ID A1P10.12
      From ID A1J10.12                 to ID A1J12.36
      From ID A1P12.36                 to ID P12-90 (S202-2)

      From ID P13-87 (S202-10)         to ID A1P14.17
      From ID A1J14.17                 to ID J1A-5E
      From W7 P1A-5E                   to W7 P3-52 (ST J10-52)

      From ST_J10-52                   to ST_J10-76

      From W7 P3-76 (ST J10-76)        to W7 P1A-12C
      From ID J1A-12C                  to ID A1J7.6
      From ID A1P7.6                   to ID P10-100 (S301-13)
      From ID P10-228 (S301-14)        to ID A1P7.20
      From ID A1J7.20                  to ID A1J1.4
      From ID A1P1.4                   to ID P1-29 (DC10-LO)
      From ID P20-3 (DMM-LO)           to ID A1P15.50
      From ID A1J15.50                 to ID A1J7.38
      From ID A1P7.38                  to ID P10-130 (S301-23)
      From ID P10-229 (S301-24)        to ID A1P7.36
      From ID A1J7.36                  to GROUND

Step 863

Description:
This step verifies the wire path from W7 P3-62 to W7 P3-67. The DMM
resource will be used to measure resistance UL= 10 ohms.

      Connection Path is as follows:

      From ID P20-2 (DMM-HI)           to ID A1P15.49
      From ID A1J15.49                 to ID A1J8.28
      From ID A1P8.28                  to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)          to ID A1P6.13
      From ID A1J6.13                  to ID BUS 1

      From ID BUS 1                    to ID A1J9.19
      From ID A1P9.19                  to ID P11-18 (S509-3)
      From ID P11-177 (S509-1)         to ID A1P10.10
      From ID A1J10.10                 to ID A1J12.38
      From ID A1P12.38                 to ID P12-59 (S202-1)

      From ID P12-61 (S202-21)         to ID A1P12.21
      From ID A1J12.21                 to ID J1B-1A
      From W7 P1B-1A                   to W7 P3-62 (ST J10-62)
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      From ST_J10-62                    to ST_J10-67

      From W7 P3-67 (ST J10-67)         to W7 P1A-6E
      From ID J1A-6E                    to ID A1J14.19
      From ID A1P14.19                  to ID P13-88 (S202-19)

      From ID P13-93 (S202-3)           to ID A1P14.49
      From ID A1J14.49                  to ID A1J10.48
      From ID A1P10.48                  to ID P11-52 (S510-1)
      From ID P11-147 (S510-4)          to ID A1P9.31
      From ID A1J9.31                   to ID BUS 2

      From ID BUS 2                     to ID A1J6.23
      From ID A1P6.23                   to ID P10-12 (S503-4)
      From ID P10-139 (S503-2)          to ID A1P8.26
      From ID A1J8.26                   to ID A1J15.50
      From ID A1P15.50                  to ID P20-3 (DMM-LO)
```

Step 864

Description:
This step verifies the wire path from W7 P3-69 to W7 P2-C36. The DMM resource will be used to measure resistance UL= 10 ohms.

```
      Connection Path is as follows:

      From ID P20-2 (DMM-HI)            to ID A1P15.49
      From ID A1J15.49                  to ID A1J8.28
      From ID A1P8.28                   to ID P10-203 (S503-1)
      From ID P10-77 (S503-3)           to ID A1P6.13
      From ID A1J6.13                   to ID BUS 1

      From ID BUS 1                     to ID A1J9.15
      From ID A1P9.15                   to ID P11-77 (S508-3)
      From ID P11-139 (S508-2)          to ID A1P10.8
      From ID A1J10.8                   to ID A1J12.40
      From ID A1P12.40                  to ID P12-80 (S201-2)

      From ID P12-50 (S201-30)          to ID A1P12.8
      From ID A1J12.8                   to ID J1B-7B
      From W7 P1B-7B                    to W7 P3-69 (ST J10-69)

      From ST_J10-69                    to ST_J9-C36

      From W7 P2-C36 (ST J9-C36)        to W7 P1B-4C
      From ID J1B-4C                    to ID A1J12.18
      From ID A1P12.18                  to ID P12-32 (S202-48)

      From ID P12-90 (S202-2)           to ID A1P12.36
      From ID A1J12.36                  to ID A1J10.12
      From ID A1P10.12                  to ID P11-242 (S509-2)
      From ID P11-17 (S509-4)           to ID A1P9.29
      From ID A1J9.29                   to ID BUS 2
```

From ID BUS 2	to ID A1J6.23
From ID A1P6.23	to ID P10-12 (S503-4)
From ID P10-139 (S503-2)	to ID A1P8.26
From ID A1J8.26	to ID A1J15.50
From ID A1P15.50	to ID P20-3 (DMM-LO)

(NOTE TESTs marked with "" preceding test number are not part of the AN/USM-657-B (TETS) executable software as the functionality under test is not part of the TETS platform)**

** **Step 865**

Description:
Test of interconnect of CAN bus based (P4 to Data-I/O) Interfaces. Connections are in place to loop CAN1 to CAN2 from the VIPER-T through the cable and test fixed loopbacks and then back to the VIPER-T on CAN2. 8 test bytes are sent (50H - 57H) and are expected on CAN2.

Connection Path is as follows:

From W7P4-22 (BUS I/O CAN2HI)	to W7P3-62
From ST_J10-62	to ST_J10-67
From W7P3-67	to W7P4-16 (BUS I/O CAN1HI)
From W7P4-23 (BUS I/O CAN2LO)	to W7P3-38
From ST_J10-38	to ST_J10-35
From W7P3-35	to W7P4-17 (BUS I/O CAN1LO)
From W7P4-24 (BUS I/O CAN2SHLD)	to W7P3-76
From ST_J10-76	to ST_J10-52
From W7P3-52	to W7P4-18 (BUS I/O CAN1SHLD)

3.0 Functional Flow Chart