English Language Test Description

MIPR # M9545012MP24797
CDRL F001
for

Unit Under Test

CDA Control Logic B CCA
P/N 7566139-121
from

Light Armored Vehicle - 25A2
(LAV-25A2)

ATE SYSTEM

AN/USM-657B – Third Echelon Test System (TETS-B)
AN/USM-717 – Virtual Instrument Portable Equipment Repair/Test (VIPER/T)
Developed by

U.S. Army RDECOM
Armament, Research, Development and Engineering Center
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ELTD REVISION SUMMARY

Revision Number	Date	Reason	Approved By - Date Approved
-	04 Mar 2016	ORIGINAL ISSUE	B Nimmick 3/4/2016

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1.0 Reference Documents

1.1 Virtual Instrument Portable Equipment Repair/Test (VIPER/T)

IEEE Std 716-1989	IEEE Standard Common Abbreviated Test Language for All Systems
TM TBD-CD	VIPER/T IETM (Interactive Electronic Technical Manual)
System Design Document Doc # 7992008	VIPER/T AN/USM-717
VIPER/T P/N 7992021	VIPER/T CPM (Computer Programming Manual)

1.2 Third Echelon Test System (TETS-B)

TM 10530A-CD	TETS IETM (Interactive Electronic Technical Manual)
System Design Description Doc # 93006A0018	Third Echelon Test System (TETS) AN/USM-657
TETS P/N 93006A0026	TETS CPM (Computer Programming Manual)

1.3 Unit Under Test

UUT P/N: 7566139-121
UUT Nomenclature: Control Logic B CCA
UUT Type: Shop Replaceable Unit (SRU)

DESCRIPTION	NUMBER	REVISION	DATE
Parts List	7566139-121	AK	09 Sep 1998
LRU QA Specification	ES13456	None	None
Circuit Card Assy, Control Logic B	7566139-121	AK	09 Sep 1998
Schematic Diagram, Control Logic B, CCA	7566139-121	AK	09 Sep 1998

1.4 Reference Drawings

Refer to the following schematics when diagnosing connection paths.

ID Schematic

13020A0001
(SYSTEM INTERCONN

W2 Schematic

13020A7201 (CABLE,
W2, SCHEMATIC).pdf

2.0 English Language Test Description Steps

2.1 Common Procedures

The following connections are common throughout the entire test

2.1.1 UUT Power

Description:
28V Power is applied to J1-70 (HI) and J1-51 (LO) using DC4.
28V Power is applied to BUS7 (HI) and GND (LO) using DC9.

CONNECTION PATH IS AS FOLLOWS:

FROM ID P1-10 (DC4-HI)	TO ID A1P1.3
FROM ID A1J1.3	TO ID A1J2.4
FROM ID A1P2.4	TO ID P10-23 (S101-5)
FROM ID P10-87 (S101-6)	TO ID A1P2.31
FROM ID A1J2.31	TO ID J2B-14C
FROM W2 P1B-14C	TO W2 P2-70 (UUT J1-70)
FROM ID P1-25 (DC9-HI)	TO ID A1P1.13
FROM ID A1J1.13	TO ID A1J7.14
FROM ID A1P7.14	TO ID P10-197 (S301-29)
FROM ID P10-198 (S301-30)	TO ID A1P6.24
FROM ID A1J6.24	TO ID BUS 7
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.16
FROM ID A1P7.16	TO ID P10-163 (S301-12)
FROM ID P10-98 (S301-11)	TO ID A1P7.32
FROM ID A1J7.32	TO GROUND

2.1.2 APPLY IC

DESCRIPTION:
CONNECT J2-32 TO DC 4 HI
CONNECT BUS8 TO INSTR-GND

CONNECTION PATH IS AS FOLLOWS:

FROM W2 P2-69 (UUT J1-69)	TO W2 P1A-4D
FROM ID J2A-4D	TO ID A1J14.44
FROM ID A1P14.44	TO ID P13-61 (S202-33)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7

2.2 INTERFACE ID

STEP 1

DESCRIPTION:
This step verifies the correct ID is installed. The DMM is used to measure the resistance across ID resistors R111 (324 ohms) and R109 (698 ohms). The DMM should measure between 971 and 1073 Ohms.

CONNECTION PATH IS AS FOLLOWS:

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-137 (S503-7)	TO ID A1P6.47
FROM ID A1J6.47	TO ID BUS 5
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J8.26
FROM ID A1P8.26	TO ID P10-139 (S503-2)
FROM ID P10-170 (S503-8)	TO ID A1P6.38
FROM ID A1J6.38	TO ID BUS 6
FROM ID BUS 5	TO ID A1J8.47
FROM ID A1P8.47	TO ID P10-73 (S301-48)
FROM ID P10-7 (S301-47)	TO ID A1P7.23
FROM ID A1J7.23	TO ID A1J4.15
FROM ID A1P4.15	TO ID R111.1
FROM ID R111.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16

```
FROM ID A1P4.16              TO ID R109.1
FROM ID R109.2               TO ID A1P4.9
FROM ID A1J4.9               TO +28V
```

2.3 UUT ID

STEP 2

DESCRIPTION:
This step verifies the correct UUT is installed. The DMM is used to measure the continuity between UUT pins J1.15 and J1.48. The DMM should measure less than 10 ohms.

CONNECTION PATH IS AS FOLLOWS:

```
FROM W2 P2-15 (UUT J1-15)    TO W2 P1B-7B
FROM ID J2B-7B               TO ID A1J12.23
FROM ID A1P12.23             TO ID P12-85 (S201-45)

FROM ID P12-16 (S201-1)      TO ID A1P12.42
FROM ID A1J12.42             TO ID A1J10.6
FROM ID A1P10.6              TO ID P11-203 (S508-1)
FROM ID P11-77 (S508-3)      TO ID A1P9.15
FROM ID A1J9.15              TO ID BUS 1

FROM ID BUS 1                TO ID A1J6.13
FROM ID A1P6.13              TO ID P10-77 (S503-3)
FROM ID P10-203 (S503-1)     TO ID A1P8.28
FROM ID A1J8.28              TO ID A1J15.49
FROM ID A1P15.49             TO ID P20-2 (DMM-HI)

FROM W2 P2-48 (UUT J1-48)    TO W2 P1A-1D
FROM ID J2A-1D               TO ID A1J14.37
FROM ID A1P14.37             TO ID P13-54 (S201-51)

FROM ID P12-20 (S201-3)      TO ID A1P12.46
FROM ID A1J12.46             TO ID A1J10.2
FROM ID A1P10.2              TO ID P11-39 (S507-1)
FROM ID P11-72 (S507-4)      TO ID A1P9.27
FROM ID A1J9.27              TO ID BUS 2

FROM ID BUS 2                TO ID A1J6.23
FROM ID A1P6.23              TO ID P10-12 (S503-4)
FROM ID P10-139 (S503-2)     TO ID A1P8.26
FROM ID A1J8.26              TO ID A1J15.50
FROM ID A1P15.50             TO ID P20-3 (DMM-LO)
```

2.4 SAFE TO TURN ON

Refer to
1.4 Reference Drawings when diagnosing connection paths.

STEP 3

DESCRIPTION:
THIS TEST VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.69 TO UUT J1.45. THE DMM SHOULD MEASURE GREATER THAN 10K OHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-173 (S503-9)	TO ID A1P6.28
FROM ID A1J6.28	TO ID BUS 7
FROM W2 P2-69 (UUT J1-69)	TO W2 P1A-4D
FROM ID J2A-4D	TO ID A1J14.44
FROM ID A1P14.44	TO ID P13-61 (S202-33)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 4

DESCRIPTION:
THIS TEST VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.70 TO UUT J1.45. THE DMM SHOULD MEASURE GREATER THAN 5K OHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J7.44
FROM ID A1P7.44	TO ID P10-99 (S301-4)
FROM ID P10-226 (S301-3)	TO ID A1P7.13
FROM ID A1J7.13	O ID A1J2.31
FROM ID A1P2.31	TO ID P10-87 (S101-6)
FROM ID P10-23 (S101-5)	TO ID A1P2.4
FROM ID A1J2.4	TO ID A1J1.3
FROM ID A1P1.3	TO ID P1-10 (DC4-HI J1-70)
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 5

DESCRIPTION:
THIS TEST VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.10 TO UUT J1.45. THE DMM SHOULD MEASURE GREATER THAN 1K OHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM W2 P2-10 (UUT J1-10)	TO W2 P1B-9A
FROM ID J2B-9A	TO ID A1J13.27
FROM ID A1P13.27	TO ID P12-70 (S701-22)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 6

DESCRIPTION:
THIS TEST VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.18 TO UUT J1.61. THE DMM SHOULD MEASURE BETWEEN 225 AND 255 OHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM W2 P2-18 (UUT J1-18)	TO W2 P1A-3C
FROM ID J2A-3C	TO ID A1J14.32
FROM ID A1P14.32	TO ID P13-53 (S201-42)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-168 (S507-3)	TO ID A1P9.17
FROM ID A1J9.17	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)

```
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM W2 P2-61 (UUT J1-61)   TO W2 P1A-3F
            FROM ID J2A-3F              TO ID A1J14.41
            FROM ID A1P14.41            TO ID P13-57 (S202-12)

            FROM ID P12-90 (S202-2)     TO ID A1P12.36
            FROM ID A1J12.36            TO ID A1J10.12
            FROM ID A1P10.12            TO ID P11-242 (S509-2)
            FROM ID P11-146 (S509-10)   TO ID A1P9.6
            FROM ID A1J9.6              TO ID BUS 8

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J8.26
            FROM ID A1P8.26             TO ID P10-139 (S503-2)
            FROM ID P10-205 (S503-10)   TO ID A1P6.16
            FROM ID A1J6.16             TO ID BUS 8
```

STEP 7

DESCRIPTION:
THIS TEST VERIFIES THE CONTINUITY OR ISOLATION OF TWO UUT PINS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.17 TO UUT J1.62. THE DMM SHOULD MEASURE BETWEEN 225 AND 255 OHMS.

```
            CONNECTION PATH IS AS FOLLOWS:

            FROM W2 P2-17 (UUT J1-17)   TO W2 P1A-8C
            FROM ID J2A-8C              TO ID A1J15.18
            FROM ID A1P15.18            TO ID P13-78 (S701-47)

            FROM ID P12-76 (S701-1)     TO ID A1P12.50
            FROM ID A1J12.50            TO ID A1J10.3
            FROM ID A1P10.3             TO ID P11-194 (S506-1)
            FROM ID P11-164 (S506-3)    TO ID A1P9.23
            FROM ID A1J9.23             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM W2 P2-62 (UUT J1-62)   TO W2 P1B-13F
            FROM ID J2B-13F             TO ID A1J12.32
            FROM ID A1P12.32            TO ID P12-88 (S202-14)

            FROM ID P12-90 (S202-2)     TO ID A1P12.36
            FROM ID A1J12.36            TO ID A1J10.12
            FROM ID A1P10.12            TO ID P11-242 (S509-2)
            FROM ID P11-146 (S509-10)   TO ID A1P9.6
            FROM ID A1J9.6              TO ID BUS 8
```

FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J8.26
FROM ID A1P8.26	TO ID P10-139 (S503-2)
FROM ID P10-205 (S503-10)	TO ID A1P6.16
FROM ID A1J6.16	TO ID BUS 8

STEP 8

DESCRIPTION:
THIS TEST CHECKS RESISTORS R57, R42, AND RELAY K8'S B2 TO B3 CONTACTS. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.65 TO UUT J1.62. THE DMM SHOULD MEASURE BETWEEN 20.15K AND 20.35K OHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM W2 P2-65 (UUT J1-65)	TO W2 P1B-9C
FROM ID J2B-9C	TO ID A1J13.23
FROM ID A1P13.23	TO ID P12-72 (S701-28)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM W2 P2-62 (UUT J1-62)	TO W2 P1B-13F
FROM ID J2B-13F	TO ID A1J12.32
FROM ID A1P12.32	TO ID P12-88 (S202-14)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-146 (S509-10)	TO ID A1P9.6
FROM ID A1J9.6	TO ID BUS 8
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J8.26
FROM ID A1P8.26	TO ID P10-139 (S503-2)
FROM ID P10-205 (S503-10)	TO ID A1P6.16
FROM ID A1J6.16	TO ID BUS 8

STEP 9

DESCRIPTION:
THIS TEST CHECKS THE RESISTANCE AT THE COIL OF K2 (K2/CR1). THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.8 TO UUT GROUND. THE DMM SHOULD MEASURE BETWEEN 500 AND 850 OHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM W2 P2-8 (UUT J1-8)	TO W2 P1A-2B
FROM ID J2A-2B	TO ID A1J14.31
FROM ID A1P14.31	TO ID P13-30 (S202-41)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 10

DESCRIPTION:
THIS TEST CHECKS THE RESISTANCE AT THE COIL OF K4 (K4/CR2). THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.22 TO UUT GROUND. THE DMM SHOULD MEASURE BETWEEN 500 AND 850 OHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM W2 P2-22 (UUT J1-22)	TO W2 P1A-4F
FROM ID J2A-4F	TO ID A1J14.46
FROM ID A1P14.46	TO ID P13-95 (S202-43)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 11

DESCRIPTION:
THIS TEST CHECKS THE RESISTANCE AT THE COIL OF K7 (K7/CR3). THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT J1.28 TO UUT GROUND. THE DMM SHOULD MEASURE BETWEEN 500 AND 850 OHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM W2 P2-28 (UUT J1-28)	TO W2 P1B-14F
FROM ID J2B-14F	TO ID A1J12.29
FROM ID A1P12.29	TO ID P12-24 (S202-7)
FROM ID P12-59 (S202-1)	TO ID A1P12.38
FROM ID A1J12.38	TO ID A1J10.10
FROM ID A1P10.10	TO ID P11-177 (S509-1)
FROM ID P11-18 (S509-3)	TO ID A1P9.19
FROM ID A1J9.19	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 12

DESCRIPTION:
THIS TEST CHECKS THE RESISTANCE AT THE INPUT OF U6. THE DMM IS USED TO MEASURE THE RESISTANCE FROM UUT TP-11 TO UUT GROUND. THE DMM SHOULD MEASURE GREATER THAN 10K OHMS.

CONNECTION PATH IS AS FOLLOWS:

FROM W2 P3-B6 (UUT J2-12)	TO W2 P1A-14C
FROM ID J2A-14C	TO ID A1J10.19
FROM ID A1P10.19	TO ID P11-253 (S405-5)
FROM ID P11-94 (S405-1)	TO ID A1P9.47
FROM ID A1J9.47	TO ID BUS 3
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-10 (S503-5)	TO ID A1P6.31
FROM ID A1J6.31	TO ID BUS 3
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50

```
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND
```

2.5 MODULE 1: OUTPUT SIGNALS

Refer to
1.4 Reference Drawings when diagnosing connection paths.

```
STEP 101

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51. THE OUTPUT MEASURED AT PIN J2.12 SHOULD BE BETWEEN 27.0 AND
28.5 VDC RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P3-B6 (UUT J2-12)    TO W2 P1A-14C
            FROM ID J2A-14C              TO ID A1J10.19
            FROM ID A1P10.19             TO ID P11-253 (S405-5)
            FROM ID P11-94 (S405-1)      TO ID A1P9.47
            FROM ID A1J9.47              TO ID BUS 3

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-10 (S503-5)      TO ID A1P6.31
            FROM ID A1J6.31              TO ID BUS 3

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 102

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51. THE OUTPUT MEASURED AT PIN J1.10 SHOULD BE BETWEEN 14.5 AND
15.5VDC RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-10 (UUT J1-10)    TO W2 P1B-9A
            FROM ID J2B-9A               TO ID A1J13.27
            FROM ID A1P13.27             TO ID P12-70 (S701-22)

            FROM ID P12-44 (S701-2)      TO ID A1P12.48
```

```
            FROM ID A1J12.48             TO ID A1J10.1
            FROM ID A1P10.1              TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)     TO ID A1P9.23
            FROM ID A1J9.23              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 103

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.28. THE OUTPUT MEASURED AT PIN
J1.23 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-28 (UUT J1-28)    TO W2 P1B-14F
            FROM ID J2B-14F              TO ID A1J12.29
            FROM ID A1P12.29             TO ID P12-24 (S202-7)

            FROM W2 P2-23 (UUT J1-23)    TO W2 P1A-7B
            FROM ID J2A-7B               TO ID A1J15.24
            FROM ID A1P15.24             TO ID P13-9 (S701-24)

            FROM ID P12-44 (S701-2)      TO ID A1P12.48
            FROM ID A1J12.48             TO ID A1J10.1
            FROM ID A1P10.1              TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)     TO ID A1P9.23
            FROM ID A1J9.23              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND
```

STEP 104

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.28 AND J1.8. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE GREATER THAN 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-28 (UUT J1-28)	TO W2 P1B-14F
FROM ID J2B-14F	TO ID A1J12.29
FROM ID A1P12.29	TO ID P12-24 (S202-7)
FROM W2 P2-8 (UUT J1-8)	TO W2 P1A-2B
FROM ID J2A-2B	TO ID A1J14.31
FROM ID A1P14.31	TO ID P13-30 (S202-41)
FROM W2 P2-23 (UUT J1-23)	TO W2 P1A-7B
FROM ID J2A-7B	TO ID A1J15.24
FROM ID A1P15.24	TO ID P13-9 (S701-24)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 105

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.28. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"
SEE "APPLY IC"

```
            FROM W2 P2-28 (UUT J1-28)    TO W2 P1B-14F
            FROM ID J2B-14F              TO ID A1J12.29
            FROM ID A1P12.29             TO ID P12-24 (S202-7)

            FROM W2 P2-23 (UUT J1-23)    TO W2 P1A-7B
            FROM ID J2A-7B               TO ID A1J15.24
            FROM ID A1P15.24             TO ID P13-9 (S701-24)

            FROM ID P12-44 (S701-2)      TO ID A1P12.48
            FROM ID A1J12.48             TO ID A1J10.1
            FROM ID A1P10.1              TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)     TO ID A1P9.23
            FROM ID A1J9.23              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 106

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PINS J1.28 AND J1.22.  THE OUTPUT
MEASURED AT PIN J1.23 SHOULD BE GREATER THAN 27.6VDC RELATIVE TO
GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-28 (UUT J1-28)    TO W2 P1B-14F
            FROM ID J2B-14F              TO ID A1J12.29
            FROM ID A1P12.29             TO ID P12-24 (S202-7)

            FROM W2 P2-22 (UUT J1-22)    TO W2 P1A-4F
            FROM ID J2A-4F               TO ID A1J14.46
            FROM ID A1P14.46             TO ID P13-95 (S202-43)

            FROM W2 P2-23 (UUT J1-23)    TO W2 P1A-7B
            FROM ID J2A-7B               TO ID A1J15.24
            FROM ID A1P15.24             TO ID P13-9 (S701-24)

            FROM ID P12-44 (S701-2)      TO ID A1P12.48
            FROM ID A1J12.48             TO ID A1J10.1
            FROM ID A1P10.1              TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)     TO ID A1P9.23
            FROM ID A1J9.23              TO ID BUS 1
```

```
            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 107

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.28.  THE OUTPUT MEASURED AT PIN
J1.23 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-28 (UUT J1-28)   TO W2 P1B-14F
            FROM ID J2B-14F             TO ID A1J12.29
            FROM ID A1P12.29            TO ID P12-24 (S202-7)

            FROM W2 P2-23 (UUT J1-23)   TO W2 P1A-7B
            FROM ID J2A-7B              TO ID A1J15.24
            FROM ID A1P15.24            TO ID P13-9 (S701-24)

            FROM ID P12-44 (S701-2)     TO ID A1P12.48
            FROM ID A1J12.48            TO ID A1J10.1
            FROM ID A1P10.1             TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)    TO ID A1P9.23
            FROM ID A1J9.23             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND
```

STEP 108

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.28 AND J1.24. THE OUTPUT MEASURED AT PIN J1.23 SHOULD BE GREATER THAN 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-28 (UUT J1-28)	TO W2 P1B-14F
FROM ID J2B-14F	TO ID A1J12.29
FROM ID A1P12.29	TO ID P12-24 (S202-7)
FROM W2 P2-24 (UUT J1-24)	TO W2 P1B-8C
FROM ID J2B-8C	TO ID A1J12.25
FROM ID A1P12.25	TO ID P12-89 (S202-5)
FROM W2 P2-23 (UUT J1-23)	TO W2 P1A-7B
FROM ID J2A-7B	TO ID A1J15.24
FROM ID A1P15.24	TO ID P13-9 (S701-24)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 109

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.64. THE OUTPUT MEASURED AT PIN J1.32 SHOULD BE GREATER THAN 27.6VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"
SEE "APPLY IC"

FROM W2 P2-64 (UUT J1-64)	TO W2 P1B-10F
FROM ID J2B-10F	TO ID A1J12.39

```
            FROM ID A1P12.39             TO ID P12-93 (S202-31)

            FROM W2 P2-32 (UUT J1-32)    TO W2 P1B-7A
            FROM ID J2B-7A               TO ID A1J12.24
            FROM ID A1P12.24             TO ID P12-21 (S201-46)

            FROM ID P12-80 (S201-2)      TO ID A1P12.40
            FROM ID A1J12.40             TO ID A1J10.8
            FROM ID A1P10.8              TO ID P11-139 (S508-2)
            FROM ID P11-77 (S508-3)      TO ID A1P9.15
            FROM ID A1J9.15              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 110

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PINS J1.64 AND J1.28.  THE OUTPUT MEASURED AT
PIN J1.32 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-64 (UUT J1-64)    TO W2 P1B-10F
            FROM ID J2B-10F              TO ID A1J12.39
            FROM ID A1P12.39             TO ID P12-93 (S202-31)

            FROM W2 P2-28 (UUT J1-28)    TO W2 P1B-14F
            FROM ID J2B-14F              TO ID A1J12.29
            FROM ID A1P12.29             TO ID P12-24 (S202-7)

            FROM W2 P2-32 (UUT J1-32)    TO W2 P1B-7A
            FROM ID J2B-7A               TO ID A1J12.24
            FROM ID A1P12.24             TO ID P12-21 (S201-46)

            FROM ID P12-80 (S201-2)      TO ID A1P12.40
            FROM ID A1J12.40             TO ID A1J10.8
            FROM ID A1P10.8              TO ID P11-139 (S508-2)
            FROM ID P11-77 (S508-3)      TO ID A1P9.15
            FROM ID A1J9.15              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
```

```
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 111

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  THE OUTPUT MEASURED AT PIN J1.32 SHOULD BE LESS THAN 0.2VDC
RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-32 (UUT J1-32)   TO W2 P1B-7A
            FROM ID J2B-7A              TO ID A1J12.24
            FROM ID A1P12.24            TO ID P12-21 (S201-46)

            FROM ID P12-80 (S201-2)     TO ID A1P12.40
            FROM ID A1J12.40            TO ID A1J10.8
            FROM ID A1P10.8             TO ID P11-139 (S508-2)
            FROM ID P11-77 (S508-3)     TO ID A1P9.15
            FROM ID A1J9.15             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 112

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.67.  THE OUTPUT MEASURED AT PIN
J1.32 SHOULD BE GREATER THAN 27.6VDC RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-67 (UUT J1-67)   TO W2 P1B-12E
```

```
            FROM ID J2B-12E              TO ID A1J12.34
            FROM ID A1P12.34             TO ID P12-58 (S202-16)
            FROM ID P12-90 (S202-2)      TO ID A1P12.36
            FROM ID A1J12.36             TO ID A1J10.12
            FROM ID A1P10.12             TO ID P11-242 (S509-2)
            FROM ID P11-211 (S509-9)     TO ID A1P9.16
            FROM ID A1J9.16              TO ID BUS 7

            FROM W2 P2-32 (UUT J1-32)    TO W2 P1B-7A
            FROM ID J2B-7A               TO ID A1J12.24
            FROM ID A1P12.24             TO ID P12-21 (S201-46)

            FROM ID P12-80 (S201-2)      TO ID A1P12.40
            FROM ID A1J12.40             TO ID A1J10.8
            FROM ID A1P10.8              TO ID P11-139 (S508-2)
            FROM ID P11-77 (S508-3)      TO ID A1P9.15
            FROM ID A1J9.15              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 113

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  THE OUTPUT MEASURED AT J1.16 SHOULD BE LESS THAN 0.2VDC WITH
RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-16 (UUT J1-16)    TO W2 P1B-11F
            FROM ID J2B-11F              TO ID A1J12.37
            FROM ID A1P12.37             TO ID P12-60 (S202-30)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
```

```
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1
            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 114

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.42.  THE OUTPUT MEASURED AT
J1.16 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-42 (UUT J1-42)   TO W2 P1A-1C
            FROM ID J2A-1C              TO ID A1J14.34
            FROM ID A1P14.34            TO ID P13-58 (S202-17)

            FROM W2 P2-16 (UUT J1-16)   TO W2 P1B-11F
            FROM ID J2B-11F             TO ID A1J12.37
            FROM ID A1P12.37            TO ID P12-60 (S202-30)

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 115

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  THE OUTPUT MEASURED AT J1.16 SHOULD BE LESS THAN 0.2VDC WITH
RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"
```

```
            FROM W2 P2-16 (UUT J1-16)    TO W2 P1B-11F
            FROM ID J2B-11F              TO ID A1J12.37
            FROM ID A1P12.37             TO ID P12-60 (S202-30)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 116

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.41.  THE OUTPUT MEASURED AT
J1.16 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-41 (UUT J1-41)    TO W2 P1B-12D
            FROM ID J2B-12D              TO ID A1J12.33
            FROM ID A1P12.33             TO ID P12-26 (S202-15)

            FROM W2 P2-16 (UUT J1-16)    TO W2 P1B-11F
            FROM ID J2B-11F              TO ID A1J12.37
            FROM ID A1P12.37             TO ID P12-60 (S202-30)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
```

```
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 117

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  THE OUTPUT MEASURED AT J1.16 SHOULD BE LESS THAN 0.2VDC WITH
RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-16 (UUT J1-16)    TO W2 P1B-11F
            FROM ID J2B-11F              TO ID A1J12.37
            FROM ID A1P12.37             TO ID P12-60 (S202-30)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 118

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  THE OUTPUT MEASURED AT J1.12 SHOULD BE LESS THAN 0.2VDC WITH
RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-12 (UUT J1-12)    TO W2 P1B-9F
            FROM ID J2B-9F               TO ID A1J12.41
            FROM ID A1P12.41             TO ID P12-29 (S202-32)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
```

```
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 119

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.55.  THE OUTPUT MEASURED AT
J1.12 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-55 (UUT J1-55)    TO W2 P1B-12F
            FROM ID J2B-12F              TO ID A1J12.35
            FROM ID A1P12.35             TO ID P12-28 (S202-29)

            FROM W2 P2-12 (UUT J1-12)    TO W2 P1B-9F
            FROM ID J2B-9F               TO ID A1J12.41
            FROM ID A1P12.41             TO ID P12-29 (S202-32)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND
```

STEP 120

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT J1.12 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"
SEE "APPLY IC"

FROM W2 P2-12 (UUT J1-12)	TO W2 P1B-9F
FROM ID J2B-9F	TO ID A1J12.41
FROM ID A1P12.41	TO ID P12-29 (S202-32)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 121

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.54. THE OUTPUT MEASURED AT J1.12 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-54 (UUT J1-54)	TO W2 P1A-2F
FROM ID J2A-2F	TO ID A1J14.42
FROM ID A1P14.42	TO ID P13-28 (S202-27)
FROM W2 P2-12 (UUT J1-12)	TO W2 P1B-9F
FROM ID J2B-9F	TO ID A1J12.41
FROM ID A1P12.41	TO ID P12-29 (S202-32)
FROM ID P13-29 (S202-4)	TO ID A1P14.50

```
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 122

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.67.  THE OUTPUT MEASURED AT
J1.33 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-67 (UUT J1-67)    TO W2 P1B-12E
            FROM ID J2B-12E              TO ID A1J12.34
            FROM ID A1P12.34             TO ID P12-58 (S202-16)

            FROM ID P12-90 (S202-2)      TO ID A1P12.36
            FROM ID A1J12.36             TO ID A1J10.12
            FROM ID A1P10.12             TO ID P11-242 (S509-2)
            FROM ID P11-211 (S509-9)     TO ID A1P9.16
            FROM ID A1J9.16              TO ID BUS 7

            FROM W2 P2-33 (UUT J1-33)    TO W2 P1B-14E
            FROM ID J2B-14E              TO ID A1J12.28
            FROM ID A1P12.28             TO ID P12-86 (S201-48)

            FROM ID P12-80 (S201-2)      TO ID A1P12.40
            FROM ID A1J12.40             TO ID A1J10.8
            FROM ID A1P10.8              TO ID P11-139 (S508-2)
            FROM ID P11-77 (S508-3)      TO ID A1P9.15
            FROM ID A1J9.15              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1
```

```
            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 123

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PINS J1.28 AND J1.67.  THE OUTPUT
MEASURED AT PIN J1.33 SHOULD BE GREATER THAN 27.6VDC WITH RESPECT TO
GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-67 (UUT J1-67)   TO W2 P1B-12E
            FROM ID J2B-12E             TO ID A1J12.34
            FROM ID A1P12.34            TO ID P12-58 (S202-16)

            FROM ID P12-90 (S202-2)     TO ID A1P12.36
            FROM ID A1J12.36            TO ID A1J10.12
            FROM ID A1P10.12            TO ID P11-242 (S509-2)
            FROM ID P11-211 (S509-9)    TO ID A1P9.16
            FROM ID A1J9.16             TO ID BUS 7

            FROM W2 P2-28 (UUT J1-28)   TO W2 P1B-14F
            FROM ID J2B-14F             TO ID A1J12.29
            FROM ID A1P12.29            TO ID P12-24 (S202-7)

            FROM W2 P2-33 (UUT J1-33)   TO W2 P1B-14E
            FROM ID J2B-14E             TO ID A1J12.28
            FROM ID A1P12.28            TO ID P12-86 (S201-48)

            FROM ID P12-80 (S201-2)     TO ID A1P12.40
            FROM ID A1J12.40            TO ID A1J10.8
            FROM ID A1P10.8             TO ID P11-139 (S508-2)
            FROM ID P11-77 (S508-3)     TO ID A1P9.15
            FROM ID A1J9.15             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND
```

STEP 124

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT J1.34 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"
SEE "APPLY IC"

FROM W2 P2-34 (UUT J1-34)	TO W2 P1A-4E
FROM ID J2A-4E	TO ID A1J14.45
FROM ID A1P14.45	TO ID P13-27 (S202-36)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 125

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO J1.28. THE OUTPUT MEASURED AT J1.34 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-28 (UUT J1-28)	TO W2 P1B-14F
FROM ID J2B-14F	TO ID A1J12.29
FROM ID A1P12.29	TO ID P12-24 (S202-7)
FROM W2 P2-34 (UUT J1-34)	TO W2 P1A-4E
FROM ID J2A-4E	TO ID A1J14.45
FROM ID A1P14.45	TO ID P13-27 (S202-36)
FROM ID P13-29 (S202-4)	TO ID A1P14.50

```
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 126

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  THE OUTPUT MEASURED AT J1.47 SHOULD BE LESS THAN 0.2VDC WITH
RESPECT TO GROUND.
            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-47 (UUT J1-47)    TO W2 P1B-7F
            FROM ID J2B-7F               TO ID A1J12.45
            FROM ID A1P12.45             TO ID P12-95 (S202-38)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 127

DESCRIPTION:
```

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.53. THE OUTPUT MEASURED AT J1.47 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-53 (UUT J1-53)	TO W2 P1A-1B
FROM ID J2A-1B	TO ID A1J14.30
FROM ID A1P14.30	TO ID P13-59 (S202-25)
FROM W2 P2-47 (UUT J1-47)	TO W2 P1B-7F
FROM ID J2B-7F	TO ID A1J12.45
FROM ID A1P12.45	TO ID P12-95 (S202-38)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 128

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE OUTPUT MEASURED AT J1.47 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"
SEE "APPLY IC"

FROM W2 P2-47 (UUT J1-47)	TO W2 P1B-7F
FROM ID J2B-7F	TO ID A1J12.45
FROM ID A1P12.45	TO ID P12-95 (S202-38)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1

```
            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 129

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.52.  THE OUTPUT MEASURED AT
J1.47 SHOULD BE BETWEEN 27.1VDC AND 27.5VDC WITH RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-52 (UUT J1-52)   TO W2 P1B-7C
            FROM ID J2B-7C              TO ID A1J12.22
            FROM ID A1P12.22            TO ID P12-91 (S202-23)
            FROM W2 P2-47 (UUT J1-47)   TO W2 P1B-7F
            FROM ID J2B-7F              TO ID A1J12.45
            FROM ID A1P12.45            TO ID P12-95 (S202-38)

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 130

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  THE VOLTAGE MEASURED AT PIN J1.47 SHOULD BE LESS THAN 0.2VDC
RELATIVE TO GROUND.
```

```
            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-47 (UUT J1-47)    TO W2 P1B-7F
            FROM ID J2B-7F               TO ID A1J12.45
            FROM ID A1P12.45             TO ID P12-95 (S202-38)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND
STEP 131

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  THE VOLTAGE MEASURED AT PIN J1.60 SHOULD BE LESS THAN 0.2VDC
RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-60 (UUT J1-60)    TO W2 P1A-1F
            FROM ID J2A-1F               TO ID A1J14.43
            FROM ID A1P14.43             TO ID P13-60 (S202-28)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-20 (S510-8)      TO ID A1P9.28
            FROM ID A1J9.28              TO ID BUS 6

            FROM ID BUS 6                TO ID A1J8.50
            FROM ID A1P8.50              TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)      TO ID A1P7.26
            FROM ID A1J7.26              TO ID A1J4.18
            FROM ID A1P4.18              TO ID R108.1
            FROM ID R108.2               TO ID A1P4.10
            FROM ID A1J4.10              TO GROUND
```

```
            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 132

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.49. THE VOLTAGE MEASURED AT PIN
J1.60 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-49 (UUT J1-49)   TO W2 P1B-6A
            FROM ID J2B-6A              TO ID A1J12.21
            FROM ID A1P12.21            TO ID P12-61 (S202-21)

            FROM W2 P2-60 (UUT J1-60)   TO W2 P1A-1F
            FROM ID J2A-1F              TO ID A1J14.43
            FROM ID A1P14.43            TO ID P13-60 (S202-28)

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-20 (S510-8)     TO ID A1P9.28
            FROM ID A1J9.28             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
```

```
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 133

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.49 AND J1.24. THE VOLTAGE
MEASURED AT PIN J1.60 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE
TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-24 (UUT J1-24)   TO W2 P1B-8C
            FROM ID J2B-8C              TO ID A1J12.25
            FROM ID A1P12.25            TO ID P12-89 (S202-5)

            FROM W2 P2-49 (UUT J1-49)   TO W2 P1B-6A
            FROM ID J2B-6A              TO ID A1J12.21
            FROM ID A1P12.21            TO ID P12-61 (S202-21)

            FROM W2 P2-60 (UUT J1-60)   TO W2 P1A-1F
            FROM ID J2A-1F              TO ID A1J14.43
            FROM ID A1P14.43            TO ID P13-60 (S202-28)

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-20 (S510-8)     TO ID A1P9.28
            FROM ID A1J9.28             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
```

```
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 134

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.49 AND J1.24. THE 28.0VDC IS
REMOVED FROM PIN J1.24. THE VOLTAGE MEASURED AT PIN J1.60 SHOULD
DROP TO LESS THAN 0.2VDC RELATIVE TO GROUND 2 SECONDS AFTER
DISCONNECTING J1.24 FROM 28.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-49 (UUT J1-49)   TO W2 P1B-6A
            FROM ID J2B-6A              TO ID A1J12.21
            FROM ID A1P12.21            TO ID P12-61 (S202-21)

            FROM W2 P2-60 (UUT J1-60)   TO W2 P1A-1F
            FROM ID J2A-1F              TO ID A1J14.43
            FROM ID A1P14.43            TO ID P13-60 (S202-28)

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-20 (S510-8)     TO ID A1P9.28
            FROM ID A1J9.28             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
            FROM ID A1J9.21             TO ID BUS 1
```

```
            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 135

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.49 AND J1.8. THE VOLTAGE
MEASURED AT PIN J1.60 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE
TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-49 (UUT J1-49)   TO W2 P1B-6A
            FROM ID J2B-6A              TO ID A1J12.21
            FROM ID A1P12.21            TO ID P12-61 (S202-21)

            FROM W2 P2-8 (UUT J1-8)     TO W2 P1A-2B
            FROM ID J2A-2B              TO ID A1J14.31
            FROM ID A1P14.31            TO ID P13-30 (S202-41)

            FROM W2 P2-60 (UUT J1-60)   TO W2 P1A-1F
            FROM ID J2A-1F              TO ID A1J14.43
            FROM ID A1P14.43            TO ID P13-60 (S202-28)

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-20 (S510-8)     TO ID A1P9.28
            FROM ID A1J9.28             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
```

```
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 136

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.49 AND J1.8. THE 28.0VDC IS
REMOVED FROM PIN J1.8. THE VOLTAGE MEASURED AT PIN J1.60 SHOULD DROP
TO LESS THAN 0.2VDC RELATIVE TO GROUND 2 SECONDS AFTER DISCONNECTING
J1.8 FROM 28.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            FROM W2 P2-8 (UUT J1-8)     TO W2 P1A-2B
            FROM ID J2A-2B              TO ID A1J14.31
            FROM ID A1P14.31            TO ID P13-30 (S202-41)

            FROM W2 P2-60 (UUT J1-60)   TO W2 P1A-1F
            FROM ID J2A-1F              TO ID A1J14.43
            FROM ID A1P14.43            TO ID P13-60 (S202-28)

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-20 (S510-8)     TO ID A1P9.28
            FROM ID A1J9.28             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
```

```
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 137

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.49 AND J1.22. THE VOLTAGE
MEASURED AT PIN J1.60 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE
TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-8 (UUT J1-8)      TO W2 P1A-2B
            FROM ID J2A-2B               TO ID A1J14.31
            FROM ID A1P14.31             TO ID P13-30 (S202-41)
            FROM W2 P2-49 (UUT J1-49)    TO W2 P1B-6A
            FROM ID J2B-6A               TO ID A1J12.21
            FROM ID A1P12.21             TO ID P12-61 (S202-21)

            FROM W2 P2-22 (UUT J1-22)    TO W2 P1A-4F
            FROM ID J2A-4F               TO ID A1J14.46
            FROM ID A1P14.46             TO ID P13-95 (S202-43)

            FROM W2 P2-60 (UUT J1-60)    TO W2 P1A-1F
            FROM ID J2A-1F               TO ID A1J14.43
            FROM ID A1P14.43             TO ID P13-60 (S202-28)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-20 (S510-8)      TO ID A1P9.28
            FROM ID A1J9.28              TO ID BUS 6

            FROM ID BUS 6                TO ID A1J8.50
            FROM ID A1P8.50              TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)      TO ID A1P7.26
            FROM ID A1J7.26              TO ID A1J4.18
            FROM ID A1P4.18              TO ID R108.1
            FROM ID R108.2               TO ID A1P4.10
            FROM ID A1J4.10              TO GROUND

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
```

```
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 138

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.49 AND J1.22. THE 28.0VDC IS
REMOVED FROM PIN J1.22. THE VOLTAGE MEASURED AT PIN J1.60 SHOULD
DROP TO LESS THAN 0.2VDC RELATIVE TO GROUND 2 SECONDS AFTER
DISCONNECTING J1.22 FROM 28.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-8 (UUT J1-8)      TO W2 P1A-2B
            FROM ID J2A-2B               TO ID A1J14.31
            FROM ID A1P14.31             TO ID P13-30 (S202-41)

            FROM W2 P2-49 (UUT J1-49)    TO W2 P1B-6A
            FROM ID J2B-6A               TO ID A1J12.21
            FROM ID A1P12.21             TO ID P12-61 (S202-21)

            FROM W2 P2-60 (UUT J1-60)    TO W2 P1A-1F
            FROM ID J2A-1F               TO ID A1J14.43
            FROM ID A1P14.43             TO ID P13-60 (S202-28)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-20 (S510-8)      TO ID A1P9.28
            FROM ID A1J9.28              TO ID BUS 6

            FROM ID BUS 6                TO ID A1J8.50
            FROM ID A1P8.50              TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)      TO ID A1P7.26
            FROM ID A1J7.26              TO ID A1J4.18
            FROM ID A1P4.18              TO ID R108.1
            FROM ID R108.2               TO ID A1P4.10
            FROM ID A1J4.10              TO GROUND

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
```

```
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 139

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  THE VOLTAGE MEASURED AT PIN J1.25 SHOULD BE LESS THAN 0.2VDC
RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-25 (UUT J1-25)    TO W2 P1A-5D
            FROM ID J2A-5D               TO ID A1J14.47
            FROM ID A1P14.47             TO ID P13-32 (S202-49)

            FROM ID P13-93 (S202-3)      TO ID A1P14.49
            FROM ID A1J14.49             TO ID A1J10.48
            FROM ID A1P10.48             TO ID P11-52 (S510-1)
            FROM ID P11-20 (S510-8)      TO ID A1P9.28
            FROM ID A1J9.28              TO ID BUS 6

            FROM ID BUS 6                TO ID A1J8.50
            FROM ID A1P8.50              TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)      TO ID A1P7.26
            FROM ID A1J7.26              TO ID A1J4.18
            FROM ID A1P4.18              TO ID R108.1
            FROM ID R108.2               TO ID A1P4.10
            FROM ID A1J4.10              TO GROUND

            FROM ID P13-93 (S202-3)      TO ID A1P14.49
            FROM ID A1J14.49             TO ID A1J10.48
            FROM ID A1P10.48             TO ID P11-52 (S510-1)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
```

```
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 140

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.28. THE VOLTAGE MEASURED AT PIN
J1.25 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-28 (UUT J1-28)   TO W2 P1B-14F
            FROM ID J2B-14F             TO ID A1J12.29
            FROM ID A1P12.29            TO ID P12-24 (S202-7)

            FROM W2 P2-25 (UUT J1-25)   TO W2 P1A-5D
            FROM ID J2A-5D              TO ID A1J14.47
            FROM ID A1P14.47            TO ID P13-32 (S202-49)

            FROM ID P13-93 (S202-3)     TO ID A1P14.49
            FROM ID A1J14.49            TO ID A1J10.48
            FROM ID A1P10.48            TO ID P11-52 (S510-1)
            FROM ID P11-20 (S510-8)     TO ID A1P9.28
            FROM ID A1J9.28             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM ID P13-93 (S202-3)     TO ID A1P14.49
            FROM ID A1J14.49            TO ID A1J10.48
            FROM ID A1P10.48            TO ID P11-52 (S510-1)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1
```

```
            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 141

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PINS J1.28 AND J1.24. THE VOLTAGE
MEASURED AT PIN J1.25 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE
TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-28 (UUT J1-28)   TO W2 P1B-14F
            FROM ID J2B-14F             TO ID A1J12.29
            FROM ID A1P12.29            TO ID P12-24 (S202-7)

            FROM W2 P2-24 (UUT J1-24)   TO W2 P1B-8C
            FROM ID J2B-8C              TO ID A1J12.25
            FROM ID A1P12.25            TO ID P12-89 (S202-5)

            FROM W2 P2-25 (UUT J1-25)   TO W2 P1A-5D
            FROM ID J2A-5D              TO ID A1J14.47
            FROM ID A1P14.47            TO ID P13-32 (S202-49)

            FROM ID P13-93 (S202-3)     TO ID A1P14.49
            FROM ID A1J14.49            TO ID A1J10.48
            FROM ID A1P10.48            TO ID P11-52 (S510-1)
            FROM ID P11-20 (S510-8)     TO ID A1P9.28
            FROM ID A1J9.28             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM ID P13-93 (S202-3)     TO ID A1P14.49
            FROM ID A1J14.49            TO ID A1J10.48
            FROM ID A1P10.48            TO ID P11-52 (S510-1)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1
```

```
            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 142

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PINS J1.28 AND J1.24. THE 28.0VDC IS
REMOVED FROM PIN J1.24. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD
DROP TO LESS THAN 0.2VDC RELATIVE TO GROUND 2 SECONDS AFTER
DISCONNECTING J1.24 FROM 28.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-28 (UUT J1-28)   TO W2 P1B-14F
            FROM ID J2B-14F             TO ID A1J12.29
            FROM ID A1P12.29            TO ID P12-24 (S202-7)

            FROM W2 P2-25 (UUT J1-25)   TO W2 P1A-5D
            FROM ID J2A-5D              TO ID A1J14.47
            FROM ID A1P14.47            TO ID P13-32 (S202-49)

            FROM ID P13-93 (S202-3)     TO ID A1P14.49
            FROM ID A1J14.49            TO ID A1J10.48
            FROM ID A1P10.48            TO ID P11-52 (S510-1)
            FROM ID P11-20 (S510-8)     TO ID A1P9.28
            FROM ID A1J9.28             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM ID P13-93 (S202-3)     TO ID A1P14.49
            FROM ID A1J14.49            TO ID A1J10.48
            FROM ID A1P10.48            TO ID P11-52 (S510-1)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
```

```
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 143

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PINS J1.28 AND J1.8. THE VOLTAGE
MEASURED AT PIN J1.25 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE
TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-28 (UUT J1-28)    TO W2 P1B-14F
            FROM ID J2B-14F              TO ID A1J12.29
            FROM ID A1P12.29             TO ID P12-24 (S202-7)

            FROM W2 P2-8 (UUT J1-8)      TO W2 P1A-2B
            FROM ID J2A-2B               TO ID A1J14.31
            FROM ID A1P14.31             TO ID P13-30 (S202-41)

            FROM W2 P2-25 (UUT J1-25)    TO W2 P1A-5D
            FROM ID J2A-5D               TO ID A1J14.47
            FROM ID A1P14.47             TO ID P13-32 (S202-49)

            FROM ID P13-93 (S202-3)      TO ID A1P14.49
            FROM ID A1J14.49             TO ID A1J10.48
            FROM ID A1P10.48             TO ID P11-52 (S510-1)
            FROM ID P11-20 (S510-8)      TO ID A1P9.28
            FROM ID A1J9.28              TO ID BUS 6

            FROM ID BUS 6                TO ID A1J8.50
            FROM ID A1P8.50              TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)      TO ID A1P7.26
            FROM ID A1J7.26              TO ID A1J4.18
            FROM ID A1P4.18              TO ID R108.1
            FROM ID R108.2               TO ID A1P4.10
            FROM ID A1J4.10              TO GROUND

            FROM ID P13-93 (S202-3)      TO ID A1P14.49
            FROM ID A1J14.49             TO ID A1J10.48
            FROM ID A1P10.48             TO ID P11-52 (S510-1)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1
```

```
            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 144

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PINS J1.28 AND J1.8. THE 28.0VDC IS
REMOVED FROM PIN J1.8. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD DROP
TO LESS THAN 0.2VDC RELATIVE TO GROUND 2 SECONDS AFTER DISCONNECTING
J1.8 FROM 28.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-28 (UUT J1-28)   TO W2 P1B-14F
            FROM ID J2B-14F             TO ID A1J12.29
            FROM ID A1P12.29            TO ID P12-24 (S202-7)

            FROM W2 P2-25 (UUT J1-25)   TO W2 P1A-5D
            FROM ID J2A-5D              TO ID A1J14.47
            FROM ID A1P14.47            TO ID P13-32 (S202-49)
            FROM ID P13-93 (S202-3)     TO ID A1P14.49
            FROM ID A1J14.49            TO ID A1J10.48
            FROM ID A1P10.48            TO ID P11-52 (S510-1)
            FROM ID P11-20 (S510-8)     TO ID A1P9.28
            FROM ID A1J9.28             TO ID BUS 6

            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM ID P13-93 (S202-3)     TO ID A1P14.49
            FROM ID A1J14.49            TO ID A1J10.48
            FROM ID A1P10.48            TO ID P11-52 (S510-1)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND
```

STEP 145

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.28 AND J1.22. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-28 (UUT J1-28)	TO W2 P1B-14F
FROM ID J2B-14F	TO ID A1J12.29
FROM ID A1P12.29	TO ID P12-24 (S202-7)
FROM W2 P2-22 (UUT J1-22)	TO W2 P1A-4F
FROM ID J2A-4F	TO ID A1J14.46
FROM ID A1P14.46	TO ID P13-95 (S202-43)
FROM W2 P2-25 (UUT J1-25)	TO W2 P1A-5D
FROM ID J2A-5D	TO ID A1J14.47
FROM ID A1P14.47	TO ID P13-32 (S202-49)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-20 (S510-8)	TO ID A1P9.28
FROM ID A1J9.28	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36

```
            FROM ID A1J7.36              TO GROUND

STEP 146

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PINS J1.28 AND J1.22. THE 28.0VDC IS
REMOVED FROM PIN J1.22. THE VOLTAGE MEASURED AT PIN J1.25 SHOULD
DROP TO LESS THAN 0.2VDC RELATIVE TO GROUND 2 SECONDS AFTER
DISCONNECTING J1.22 FROM 28.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-28 (UUT J1-28)    TO W2 P1B-14F
            FROM ID J2B-14F              TO ID A1J12.29
            FROM ID A1P12.29             TO ID P12-24 (S202-7)

            FROM W2 P2-25 (UUT J1-25)    TO W2 P1A-5D
            FROM ID J2A-5D               TO ID A1J14.47
            FROM ID A1P14.47             TO ID P13-32 (S202-49)

            FROM ID P13-93 (S202-3)      TO ID A1P14.49
            FROM ID A1J14.49             TO ID A1J10.48
            FROM ID A1P10.48             TO ID P11-52 (S510-1)
            FROM ID P11-20 (S510-8)      TO ID A1P9.28
            FROM ID A1J9.28              TO ID BUS 6

            FROM ID BUS 6                TO ID A1J8.50
            FROM ID A1P8.50              TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)      TO ID A1P7.26
            FROM ID A1J7.26              TO ID A1J4.18
            FROM ID A1P4.18              TO ID R108.1
            FROM ID R108.2               TO ID A1P4.10
            FROM ID A1J4.10              TO GROUND

            FROM ID P13-93 (S202-3)      TO ID A1P14.49
            FROM ID A1J14.49             TO ID A1J10.48
            FROM ID A1P10.48             TO ID P11-52 (S510-1)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND
```

STEP 147

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.39 SHOULD BE GREATER THAN 27.5VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"
SEE "APPLY IC"

FROM W2 P2-39 (UUT J1-39)	TO W2 P1A-5E
FROM ID J2A-5E	TO ID A1J14.48
FROM ID A1P14.48	TO ID P13-63 (S202-51)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-115 (S510-7)	TO ID A1P9.38
FROM ID A1J9.38	TO ID BUS 5
FROM ID BUS 5	TO ID A1J8.47
FROM ID A1P8.47	TO ID P10-73 (S301-48)
FROM ID P10-7 (S301-47)	TO ID A1P7.23
FROM ID A1J7.23	TO ID A1J4.15
FROM ID A1P4.15	TO ID R111.1
FROM ID R111.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 148

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.23. THE VOLTAGE MEASURED AT PIN J1.39 SHOULD DROP TO LESS THAN 0.2VDC RELATIVE TO GROUND 100 MILLISECONDS AFTER CONNECTING J1.23 TO 28.0VDC.

```
            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-23 (UUT J1-23)    TO W2 P1A-7B
            FROM ID J2A-7B               TO ID A1J15.24
            FROM ID A1P15.24             TO ID P13-9 (S701-24)

            FROM ID P12-44 (S701-2)      TO ID A1P12.48
            FROM ID A1J12.48             TO ID A1J10.1
            FROM ID A1P10.1              TO ID P11-162 (S506-2)
            FROM ID P11-68 (S506-9)      TO ID A1P9.20
            FROM ID A1J9.20              TO ID BUS 7

            FROM W2 P2-39 (UUT J1-39)    TO W2 P1A-5E
            FROM ID J2A-5E               TO ID A1J14.48
            FROM ID A1P14.48             TO ID P13-63 (S202-51)

            FROM ID P13-93 (S202-3)      TO ID A1P14.49
            FROM ID A1J14.49             TO ID A1J10.48
            FROM ID A1P10.48             TO ID P11-52 (S510-1)
            FROM ID P11-115 (S510-7)     TO ID A1P9.38
            FROM ID A1J9.38              TO ID BUS 5

            FROM ID BUS 5                TO ID A1J8.47
            FROM ID A1P8.47              TO ID P10-73 (S301-48)
            FROM ID P10-7 (S301-47)      TO ID A1P7.23
            FROM ID A1J7.23              TO ID A1J4.15
            FROM ID A1P4.15              TO ID R111.1
            FROM ID R111.2               TO ID A1P4.9
            FROM ID A1J4.9               TO +28V

            FROM ID P13-93 (S202-3)      TO ID A1P14.49
            FROM ID A1J14.49             TO ID A1J10.48
            FROM ID A1P10.48             TO ID P11-52 (S510-1)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 149

DESCRIPTION:
```

THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.37 SHOULD BE GREATER THAN 27.5VDC RELATIVE TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"
SEE "APPLY IC"

FROM W2 P2-38 (UUT J1-38)	TO W2 P1A-1E
FROM ID J2A-1E	TO ID A1J14.40
FROM ID A1P14.40	TO ID P13-25 (S202-11)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-19 (S510-10)	TO ID A1P9.8
FROM ID A1J9.8	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.18
FROM ID A1P7.18	TO ID P10-133 (S301-27)
FROM ID P10-70 (S301-28)	TO ID A1P6.12
FROM ID J2A-2E	TO ID A1J14.39
FROM ID A1P14.39	TO ID P13-56 (S202-9)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM ID BUS 2	TO ID A1J8.31
FROM ID A1P8.31	TO ID P10-44 (S301-69)
FROM ID P10-141 (S301-70)	TO ID A1P8.2
FROM ID A1J8.2	TO ID A1J1.3
FROM ID A1P1.3	TO ID P1-10 (DC4-HI)
FROM W2 P2-37 (UUT J1-37)	TO W2 P1A-8A
FROM ID J2A-8A	TO ID A1J15.22
FROM ID A1P15.22	TO ID P13-71 (S701-18)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-161 (S506-7)	TO ID A1P9.40
FROM ID A1J9.40	TO ID BUS 5
FROM ID BUS 5	TO ID A1J8.47
FROM ID A1P8.47	TO ID P10-73 (S301-48)
FROM ID P10-7 (S301-47)	TO ID A1P7.23
FROM ID A1J7.23	TO ID A1J4.15
FROM ID A1P4.15	TO ID R111.1
FROM ID R111.2	TO ID A1P4.9

```
            FROM ID A1J4.9              TO +28V

            FROM ID P12-44 (S701-2)     TO ID A1P12.48
            FROM ID A1J12.48            TO ID A1J10.1
            FROM ID A1P10.1             TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)    TO ID A1P9.23
            FROM ID A1J9.23             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND
```

STEP 150

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.42. THE VOLTAGE MEASURED AT PIN J1.37 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

```
            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-42 (UUT J1-42)   TO W2 P1A-1C
            FROM ID J2A-1C              TO ID A1J14.34
            FROM ID A1P14.34            TO ID P13-58 (S202-17)

            FROM W2 P2-38 (UUT J1-38)   TO W2 P1A-1E
            FROM ID J2A-1E              TO ID A1J14.40
            FROM ID A1P14.40            TO ID P13-25 (S202-11)

            FROM ID P13-93 (S202-3)     TO ID A1P14.49
            FROM ID A1J14.49            TO ID A1J10.48
            FROM ID A1P10.48            TO ID P11-52 (S510-1)
            FROM ID P11-19 (S510-10)    TO ID A1P9.8
            FROM ID A1J9.8              TO ID BUS 8

            FROM ID P1-26 (DC9-LO)      TO ID A1P1.5
            FROM ID A1J1.5              TO ID A1J7.18
            FROM ID A1P7.18             TO ID P10-133 (S301-27)
            FROM ID P10-70 (S301-28)    TO ID A1P6.12
```

```
FROM ID A1J6.12              TO ID BUS 8

FROM W2 P2-36 (UUT J1-36)    TO W2 P1A-2E
FROM ID J2A-2E               TO ID A1J14.39
FROM ID A1P14.39             TO ID P13-56 (S202-9)

FROM ID P13-93 (S202-3)      TO ID A1P14.49
FROM ID A1J14.49             TO ID A1J10.48
FROM ID A1P10.48             TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)     TO ID A1P9.31
FROM ID A1J9.31              TO ID BUS 2

FROM ID BUS 2                TO ID A1J8.31
FROM ID A1P8.31              TO ID P10-44 (S301-69)
FROM ID P10-141 (S301-70)    TO ID A1P8.2
FROM ID A1J8.2               TO ID A1J1.3
FROM ID A1P1.3               TO ID P1-10 (DC4-HI)

FROM W2 P2-37 (UUT J1-37)    TO W2 P1A-8A
FROM ID J2A-8A               TO ID A1J15.22
FROM ID A1P15.22             TO ID P13-71 (S701-18)

FROM ID P12-44 (S701-2)      TO ID A1P12.48
FROM ID A1J12.48             TO ID A1J10.1
FROM ID A1P10.1              TO ID P11-162 (S506-2)
FROM ID P11-161 (S506-7)     TO ID A1P9.40
FROM ID A1J9.40              TO ID BUS 5

FROM ID BUS 5                TO ID A1J8.47
FROM ID A1P8.47              TO ID P10-73 (S301-48)
FROM ID P10-7 (S301-47)      TO ID A1P7.23
FROM ID A1J7.23              TO ID A1J4.15
FROM ID A1P4.15              TO ID R111.1
FROM ID R111.2               TO ID A1P4.9
FROM ID A1J4.9               TO +28V

FROM ID P12-44 (S701-2)      TO ID A1P12.48
FROM ID A1J12.48             TO ID A1J10.1
FROM ID A1P10.1              TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)     TO ID A1P9.23
FROM ID A1J9.23              TO ID BUS 1

FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
FROM ID A1J15.49             TO ID A1J8.28
FROM ID A1P8.28              TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)      TO ID A1P6.13
FROM ID A1J6.13              TO ID BUS 1

FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
FROM ID A1J15.50             TO ID A1J7.38
FROM ID A1P7.38              TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)    TO ID A1P7.36
FROM ID A1J7.36              TO GROUND
```

STEP 151

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.42. THE 28.0VDC IS REMOVED FROM PIN J1.42. THE VOLTAGE AT PIN J1.37 SHOULD INCREASE TO 28.0VDC +/- 0.5VDC 15 SECONDS AFTER REMOVING J1.42 FROM 28.0VDC.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-38 (UUT J1-38)	TO W2 P1A-1E
FROM ID J2A-1E	TO ID A1J14.40
FROM ID A1P14.40	TO ID P13-25 (S202-11)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-19 (S510-10)	TO ID A1P9.8
FROM ID A1J9.8	TO ID BUS 8
FROM ID P1-26 (DC9-LO)	TO ID A1P1.5
FROM ID A1J1.5	TO ID A1J7.18
FROM ID A1P7.18	TO ID P10-133 (S301-27)
FROM ID P10-70 (S301-28)	TO ID A1P6.12
FROM ID A1J6.12	TO ID BUS 8
FROM W2 P2-36 (UUT J1-36)	TO W2 P1A-2E
FROM ID J2A-2E	TO ID A1J14.39
FROM ID A1P14.39	TO ID P13-56 (S202-9)
FROM ID P13-93 (S202-3)	TO ID A1P14.49
FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM ID BUS 2	TO ID A1J8.31
FROM ID A1P8.31	TO ID P10-44 (S301-69)
FROM ID P10-141 (S301-70)	TO ID A1P8.2
FROM ID A1J8.2	TO ID A1J1.3
FROM ID A1P1.3	TO ID P1-10 (DC4-HI)
FROM W2 P2-37 (UUT J1-37)	TO W2 P1A-8A
FROM ID J2A-8A	TO ID A1J15.22
FROM ID A1P15.22	TO ID P13-71 (S701-18)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-161 (S506-7)	TO ID A1P9.40
FROM ID A1J9.40	TO ID BUS 5
FROM ID BUS 5	TO ID A1J8.47
FROM ID A1P8.47	TO ID P10-73 (S301-48)

```
            FROM ID P10-7 (S301-47)      TO ID A1P7.23
            FROM ID A1J7.23              TO ID A1J4.15
            FROM ID A1P4.15              TO ID R111.1
            FROM ID R111.2               TO ID A1P4.9
            FROM ID A1J4.9               TO +28V

            FROM ID P12-44 (S701-2)      TO ID A1P12.48
            FROM ID A1J12.48             TO ID A1J10.1
            FROM ID A1P10.1              TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)     TO ID A1P9.23
            FROM ID A1J9.23              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND



STEP 152

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51. THE VOLTAGE MEASURED AT PINJ1.37 SHOULD BE GREATER THAN
27.5VDC RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-36 (UUT J1-36)    TO W2 P1A-2E
            FROM ID J2A-2E               TO ID A1J14.39
            FROM ID A1P14.39             TO ID P13-56 (S202-9)

            FROM ID P13-93 (S202-3)      TO ID A1P14.49
            FROM ID A1J14.49             TO ID A1J10.48
            FROM ID A1P10.48             TO ID P11-52 (S510-1)
            FROM ID P11-147 (S510-4)     TO ID A1P9.31
            FROM ID A1J9.31              TO ID BUS 2

            FROM ID BUS 2                TO ID A1J8.31
            FROM ID A1P8.31              TO ID P10-44 (S301-69)
            FROM ID P10-141 (S301-70)    TO ID A1P8.2
            FROM ID A1J8.2               TO ID A1J1.3
            FROM ID A1P1.3               TO ID P1-10 (DC4-HI)

            FROM W2 P2-37 (UUT J1-37)    TO W2 P1A-8A
```

```
            FROM ID J2A-8A              TO ID A1J15.22
            FROM ID A1P15.22            TO ID P13-71 (S701-18)

            FROM ID P12-44 (S701-2)     TO ID A1P12.48
            FROM ID A1J12.48            TO ID A1J10.1
            FROM ID A1P10.1             TO ID P11-162 (S506-2)
            FROM ID P11-161 (S506-7)    TO ID A1P9.40
            FROM ID A1J9.40             TO ID BUS 5

            FROM ID BUS 5               TO ID A1J8.47
            FROM ID A1P8.47             TO ID P10-73 (S301-48)
            FROM ID P10-7 (S301-47)     TO ID A1P7.23
            FROM ID A1J7.23             TO ID A1J4.15
            FROM ID A1P4.15             TO ID R111.1
            FROM ID R111.2              TO ID A1P4.9
            FROM ID A1J4.9              TO +28V

            FROM ID P12-44 (S701-2)     TO ID A1P12.48
            FROM ID A1J12.48            TO ID A1J10.1
            FROM ID A1P10.1             TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)    TO ID A1P9.23
            FROM ID A1J9.23             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 153

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN J1.38. THE VOLTAGE MEASURED AT PIN
J1.37 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-38 (UUT J1-38)   TO W2 P1A-1E
            FROM ID J2A-1E              TO ID A1J14.40
            FROM ID A1P14.40            TO ID P13-25 (S202-11)

            FROM W2 P2-36 (UUT J1-36)   TO W2 P1A-2E
            FROM ID J2A-2E              TO ID A1J14.39
            FROM ID A1P14.39            TO ID P13-56 (S202-9)

            FROM ID P13-93 (S202-3)     TO ID A1P14.49
```

FROM ID A1J14.49	TO ID A1J10.48
FROM ID A1P10.48	TO ID P11-52 (S510-1)
FROM ID P11-147 (S510-4)	TO ID A1P9.31
FROM ID A1J9.31	TO ID BUS 2
FROM ID BUS 2	TO ID A1J8.31
FROM ID A1P8.31	TO ID P10-44 (S301-69)
FROM ID P10-141 (S301-70)	TO ID A1P8.2
FROM ID A1J8.2	TO ID A1J1.3
FROM ID A1P1.3	TO ID P1-10 (DC4-HI)
FROM W2 P2-37 (UUT J1-37)	TO W2 P1A-8A
FROM ID J2A-8A	TO ID A1J15.22
FROM ID A1P15.22	TO ID P13-71 (S701-18)
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-161 (S506-7)	TO ID A1P9.40
FROM ID A1J9.40	TO ID BUS 5
FROM ID BUS 5	TO ID A1J8.47
FROM ID A1P8.47	TO ID P10-73 (S301-48)
FROM ID P10-7 (S301-47)	TO ID A1P7.23
FROM ID A1J7.23	TO ID A1J4.15
FROM ID A1P4.15	TO ID R111.1
FROM ID R111.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM ID P12-44 (S701-2)	TO ID A1P12.48
FROM ID A1J12.48	TO ID A1J10.1
FROM ID A1P10.1	TO ID P11-162 (S506-2)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 154

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.7 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

```
            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-7 (UUT J1-7)      TO W2 P1B-8A
            FROM ID J2B-8A               TO ID A1J13.25
            FROM ID A1P13.25             TO ID P12-39 (S701-20)

            FROM ID P12-44 (S701-2)      TO ID A1P12.48
            FROM ID A1J12.48             TO ID A1J10.1
            FROM ID A1P10.1              TO ID P11-162 (S506-2)
            FROM ID P11-225 (S506-5)     TO ID A1P9.45
            FROM ID A1J9.45              TO ID BUS 3

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-10 (S503-5)      TO ID A1P6.31
            FROM ID A1J6.31              TO ID BUS 3

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 155

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51. 28.0VDC IS APPLIED TO PIN J1.42. THE VOLTAGE MEASURED AT PIN
J1.7 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-6 (UUT J1-6)      TO W2 P1A-1A
            FROM ID J2A-1A               TO ID A1J14.28
            FROM ID A1P14.28             TO ID P13-16 (S201-18)

            FROM ID P12-52 (S201-4)      TO ID A1P12.44
            FROM ID A1J12.44             TO ID A1J10.4
            FROM ID A1P10.4              TO ID P11-71 (S507-2)
            FROM ID P11-5 (S507-8)       TO ID A1P9.24
            FROM ID A1J9.24              TO ID BUS 6

            FROM ID BUS 6                TO ID A1J8.42
            FROM ID A1P8.42              TO ID P10-78 (S301-83)
            FROM ID P10-81 (S301-84)     TO ID A1P8.5
            FROM ID A1J8.5               TO ID R7.1
            FROM ID R7.2                 TO GROUND

            FROM W2 P2-42 (UUT J1-42)    TO W2 P1A-1C
            FROM ID J2A-1C               TO ID A1J14.34
```

```
            FROM ID A1P14.34             TO ID P13-58 (S202-17)

            FROM W2 P2-7 (UUT J1-7)      TO W2 P1B-8A
            FROM ID J2B-8A               TO ID A1J13.25
            FROM ID A1P13.25             TO ID P12-39 (S701-20)

            FROM ID P12-44 (S701-2)      TO ID A1P12.48
            FROM ID A1J12.48             TO ID A1J10.1
            FROM ID A1P10.1              TO ID P11-162 (S506-2)
            FROM ID P11-225 (S506-5)     TO ID A1P9.45
            FROM ID A1J9.45              TO ID BUS 3

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-10 (S503-5)      TO ID A1P6.31
            FROM ID A1J6.31              TO ID BUS 3

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND



STEP 156

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51. 28.0VDC IS APPLIED TO PINS J1.42 AND J1.6. THE VOLTAGE
MEASURED AT PIN J1.7 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC RELATIVE
TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM ID P1-4 (DC2-HI)        TO ID A1P1.2
            FROM ID A1J1.2               TO ID A1J8.4
            FROM ID A1P8.4               TO ID P10-174 (S301-68)
            FROM ID P10-204 (S301-67)    TO ID A1P8.29
            FROM ID A1J8.29              TO ID BUS 1

            FROM ID P1-5 (DC2-LO)        TO ID A1P1.10
            FROM ID A1J1.10              TO GROUND

            FROM W2 P2-6 (UUT J1-6)      TO W2 P1A-1A
            FROM ID J2A-1A               TO ID A1J14.28
            FROM ID A1P14.28             TO ID P13-16 (S201-18)

            FROM ID P12-52 (S201-4)      TO ID A1P12.44
            FROM ID A1J12.44             TO ID A1J10.4
            FROM ID A1P10.4              TO ID P11-71 (S507-2)
            FROM ID P11-168 (S507-3)     TO ID A1P9.17
```

```
            FROM ID A1J9.17              TO ID BUS 1

            FROM W2 P2-42 (UUT J1-42)    TO W2 P1A-1C
            FROM ID J2A-1C               TO ID A1J14.34
            FROM ID A1P14.34             TO ID P13-58 (S202-17)

            FROM W2 P2-7 (UUT J1-7)      TO W2 P1B-8A
            FROM ID J2B-8A               TO ID A1J13.25
            FROM ID A1P13.25             TO ID P12-39 (S701-20)

            FROM ID P12-44 (S701-2)      TO ID A1P12.48
            FROM ID A1J12.48             TO ID A1J10.1
            FROM ID A1P10.1              TO ID P11-162 (S506-2)
            FROM ID P11-225 (S506-5)     TO ID A1P9.45
            FROM ID A1J9.45              TO ID BUS 3

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-10 (S503-5)      TO ID A1P6.31
            FROM ID A1J6.31              TO ID BUS 3

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 157

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51. 28.0VDC IS APPLIED TO PINS J1.49 AND J1.24. THE VOLTAGE
MEASURED AT PIN J1.58 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-49 (UUT J1-49)    TO W2 P1B-6A
            FROM ID J2B-6A               TO ID A1J12.21
            FROM ID A1P12.21             TO ID P12-61 (S202-21)

            FROM W2 P2-24 (UUT J1-24)    TO W2 P1B-8C
            FROM ID J2B-8C               TO ID A1J12.25
            FROM ID A1P12.25             TO ID P12-89 (S202-5)

            FROM W2 P2-58 (UUT J1-58)    TO W2 P1A-11E
            FROM ID J1A-11E              TO ID A1J15.17
            FROM ID A1P15.17             TO ID P13.13 (S701-49)

            FROM ID P12-76 (S701-1)      TO ID A1P12.50
            FROM ID A1J12.50             TO ID A1J10.3
            FROM ID A1P10.3              TO ID P11-194 (S506-1)
```

```
            FROM ID P11-225 (S506-5)     TO ID A1P9.45
            FROM ID A1J9.45              TO ID BUS 3

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-10 (S503-5)      TO ID A1P6.31
            FROM ID A1J6.31              TO ID BUS 3

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 158

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51. 28.0VDC IS APPLIED TO PINS J1.49, J1.24 AND J1.1. THE VOLTAGE
MEASURED AT PIN J1.58 SHOULD BE BETWEEN 14.5VDC AND 15.5VDC WITH
RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM ID P1-4 (DC2-HI)        TO ID A1P1.2
            FROM ID A1J1.2               TO ID A1J8.4
            FROM ID A1P8.4               TO ID P10-174 (S301-68)
            FROM ID P10-204 (S301-67)    TO ID A1P8.29
            FROM ID A1J8.29              TO ID BUS 1

            FROM ID P1-5 (DC2-LO)        TO ID A1P1.10
            FROM ID A1J1.10              TO GROUND

            FROM W2 P2-49 (UUT J1-49)    TO W2 P1B-6A
            FROM ID J2B-6A               TO ID A1J12.21
            FROM ID A1P12.21             TO ID P12-61 (S202-21)

            FROM W2 P2-24 (UUT J1-24)    TO W2 P1B-8C
            FROM ID J2B-8C               TO ID A1J12.25
            FROM ID A1P12.25             TO ID P12-89 (S202-5)

            FROM W2 P2-1 (UUT J1-1)      TO W2 P1A-7C
            FROM ID J2A-7C               TO ID A1J15.16
            FROM ID A1P15.16             TO ID P13-40 (S701-16)

            FROM ID P12-44 (S701-2)      TO ID A1P12.48
            FROM ID A1J12.48             TO ID A1J10.1
            FROM ID A1P10.1              TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)     TO ID A1P9.23
            FROM ID A1J9.23              TO ID BUS 1

            FROM W2 P2-58 (UUT J1-58)    TO W2 P1A-11E
```

```
            FROM ID J1A-11E             TO ID A1J15.17
            FROM ID A1P15.17            TO ID P13.13 (S701-49)

            FROM ID P12-76 (S701-1)     TO ID A1P12.50
            FROM ID A1J12.50            TO ID A1J10.3
            FROM ID A1P10.3             TO ID P11-194 (S506-1)
            FROM ID P11-225 (S506-5)    TO ID A1P9.45
            FROM ID A1J9.45             TO ID BUS 3

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-10 (S503-5)     TO ID A1P6.31
            FROM ID A1J6.31             TO ID BUS 3

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 159

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51. 28.0VDC IS APPLIED TO PINS J1.49 AND J1.24. THE VOLTAGE
MEASURED AT PIN J1.58 SHOULD BE LESS THAN 0.2VDC RELATIVE TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM ID P1-4 (DC2-HI)       TO ID A1P1.2
            FROM ID A1J1.2              TO ID A1J8.4
            FROM ID A1P8.4              TO ID P10-174 (S301-68)
            FROM ID P10-204 (S301-67)   TO ID A1P8.29
            FROM ID A1J8.29             TO ID BUS 1

            FROM ID P1-5 (DC2-LO)       TO ID A1P1.10
            FROM ID A1J1.10             TO GROUND

            FROM W2 P2-49 (UUT J1-49)   TO W2 P1B-6A
            FROM ID J2B-6A              TO ID A1J12.21
            FROM ID A1P12.21            TO ID P12-61 (S202-21)

            FROM W2 P2-24 (UUT J1-24)   TO W2 P1B-8C

            FROM ID A1P12.25            TO ID P12-89 (S202-5)

            FROM ID P12-44 (S701-2)     TO ID A1P12.48
            FROM ID A1J12.48            TO ID A1J10.1
            FROM ID A1P10.1             TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)    TO ID A1P9.23
            FROM ID A1J9.23             TO ID BUS 1
```

```
FROM W2 P2-58 (UUT J1-58)    TO W2 P1A-11E
FROM ID J1A-11E              TO ID A1J15.17
FROM ID A1P15.17             TO ID P13.13 (S701-49)

FROM ID P12-76 (S701-1)      TO ID A1P12.50
FROM ID A1J12.50             TO ID A1J10.3
FROM ID A1P10.3              TO ID P11-194 (S506-1)
FROM ID P11-225 (S506-5)     TO ID A1P9.45
FROM ID A1J9.45              TO ID BUS 3

FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
FROM ID A1J15.49             TO ID A1J8.28
FROM ID A1P8.28              TO ID P10-203 (S503-1)
FROM ID P10-10 (S503-5)      TO ID A1P6.31
FROM ID A1J6.31              TO ID BUS 3

FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
FROM ID A1J15.50             TO ID A1J7.38
FROM ID A1P7.38              TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)    TO ID A1P7.36
FROM ID A1J7.36              TO GROUND
```

2.6 MODULE 2: RATE COMMAND AND LAMP DRIVERS

Refer to
1.4 Reference Drawings when diagnosing connection paths.

```
STEP 201

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.  THE
VOLTAGE MEASURED AT PIN J1.20 SHOULD BE LESS THAN 0.2VDC WITH
RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-20 (UUT J1-20)    TO W2 P1B-14D
            FROM ID J2B-14D              TO ID A1J12.27
            FROM ID A1P12.27             TO ID P12-55 (S201-47)

            FROM ID P12-16 (S201-1)      TO ID A1P12.42
            FROM ID A1J12.42             TO ID A1J10.6
            FROM ID A1P10.6              TO ID P11-203 (S508-1)
            FROM ID P11-77 (S508-3)      TO ID A1P9.15
            FROM ID A1J9.15              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
```

```
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 202

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.59. THE VOLTAGE MEASURED AT PIN J1.20
SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-20 (UUT J1-20)    TO W2 P1B-14D
            FROM ID J2B-14D              TO ID A1J12.27
            FROM ID A1P12.27             TO ID P12-55 (S201-47)

            FROM ID P12-16 (S201-1)      TO ID A1P12.42
            FROM ID A1J12.42             TO ID A1J10.6
            FROM ID A1P10.6              TO ID P11-203 (S508-1)
            FROM ID P11-77 (S508-3)      TO ID A1P9.15
            FROM ID A1J9.15              TO ID BUS 1

            FROM W2 P2-59 (UUT J1-59)    TO W2 P1B-13D
            FROM ID J2B-13D              TO ID A1J12.30
            FROM ID A1P12.30             TO ID P12-56 (S202-8)

            FROM ID P12-90 (S202-2)      TO ID A1P12.36
            FROM ID A1J12.36             TO ID A1J10.12
            FROM ID A1P10.12             TO ID P11-242 (S509-2)
            FROM ID P11-211 (S509-9)     TO ID A1P9.16
            FROM ID A1J9.16              TO ID BUS 7

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 203

DESCRIPTION:
```

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.59. THE VOLTAGE MEASURED AT PIN J1.19
SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-59 (UUT J1-59)	TO W2 P1B-13D
FROM ID J2B-13D	TO ID A1J12.30
FROM ID A1P12.30	TO ID P12-56 (S202-8)
FROM ID P12-90 (S202-2)	TO ID A1P12.36
FROM ID A1J12.36	TO ID A1J10.12
FROM ID A1P10.12	TO ID P11-242 (S509-2)
FROM ID P11-211 (S509-9)	TO ID A1P9.16
FROM ID A1J9.16	TO ID BUS 7
FROM W2 P2-19 (UUT J1-19)	TO W2 P1A-2C
FROM ID J2A-2C	TO ID A1J14.33
FROM ID A1P14.33	TO ID P13-22 (S201-44)
FROM ID P12-80 (S201-2)	TO ID A1P12.40
FROM ID A1J12.40	TO ID A1J10.8
FROM ID A1P10.8	TO ID P11-139 (S508-2)
FROM ID P11-77 (S508-3)	TO ID A1P9.15
FROM ID A1J9.15	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 204

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.59. THE VOLTAGE MEASURED AT PIN J1.21
SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-59 (UUT J1-59)	TO W2 P1B-13D
FROM ID J2B-13D	TO ID A1J12.30
FROM ID A1P12.30	TO ID P12-56 (S202-8)
FROM ID P12-90 (S202-2)	TO ID A1P12.36

```
            FROM ID A1J12.36             TO ID A1J10.12
            FROM ID A1P10.12             TO ID P11-242 (S509-2)
            FROM ID P11-211 (S509-9)     TO ID A1P9.16
            FROM ID A1J9.16              TO ID BUS 7

            FROM W2 P2-21 (UUT J1-21)    TO W2 P1A-3D
            FROM ID J2A-3D               TO ID A1J14.35
            FROM ID A1P14.35             TO ID P13-23 (S201-49)

            FROM ID P12-16 (S201-1)      TO ID A1P12.42
            FROM ID A1J12.42             TO ID A1J10.6
            FROM ID A1P10.6              TO ID P11-203 (S508-1)
            FROM ID P11-77 (S508-3)      TO ID A1P9.15
            FROM ID A1J9.15              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 205

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PINS J1.59 AND J1.8. THE VOLTAGE MEASURED AT
PIN J1.20 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-59 (UUT J1-59)    TO W2 P1B-13D
            FROM ID J2B-13D              TO ID A1J12.30
            FROM ID A1P12.30             TO ID P12-56 (S202-8)

            FROM W2 P2-8 (UUT J1-8)      TO W2 P1A-2B
            FROM ID J2A-2B               TO ID A1J14.31
            FROM ID A1P14.31             TO ID P13-30 (S202-41)

            FROM ID P12-90 (S202-2)      TO ID A1P12.36
            FROM ID A1J12.36             TO ID A1J10.12
            FROM ID A1P10.12             TO ID P11-242 (S509-2)
            FROM ID P11-211 (S509-9)     TO ID A1P9.16
            FROM ID A1J9.16              TO ID BUS 7

            FROM W2 P2-20 (UUT J1-20)    TO W2 P1B-14D
            FROM ID J2B-14D              TO ID A1J12.27
            FROM ID A1P12.27             TO ID P12-55 (S201-47)
```

```
            FROM ID P12-16 (S201-1)     TO ID A1P12.42
            FROM ID A1J12.42            TO ID A1J10.6
            FROM ID A1P10.6             TO ID P11-203 (S508-1)
            FROM ID P11-77 (S508-3)     TO ID A1P9.15
            FROM ID A1J9.15             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 206

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PINS J1.57 AND J1.8. THE VOLTAGE MEASURED AT
PIN J1.20 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO
GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-8 (UUT J1-8)     TO W2 P1A-2B
            FROM ID J2A-2B              TO ID A1J14.31
            FROM ID A1P14.31            TO ID P13-30 (S202-41)

            FROM W2 P2-57 (UUT J1-57)   TO W2 P1B-10A
            FROM ID J2B-10A             TO ID A1J13.31
            FROM ID A1P13.31            TO ID P12-7 (S701-30)

            FROM ID P12-44 (S701-2)     TO ID A1P12.48
            FROM ID A1J12.48            TO ID A1J10.1
            FROM ID A1P10.1             TO ID P11-162 (S506-2)
            FROM ID P11-68 (S506-9)     TO ID A1P9.20
            FROM ID A1J9.20             TO ID BUS 7

            FROM W2 P2-20 (UUT J1-20)   TO W2 P1B-14D
            FROM ID J2B-14D             TO ID A1J12.27
            FROM ID A1P12.27            TO ID P12-55 (S201-47)

            FROM ID P12-16 (S201-1)     TO ID A1P12.42
            FROM ID A1J12.42            TO ID A1J10.6
            FROM ID A1P10.6             TO ID P11-203 (S508-1)
            FROM ID P11-77 (S508-3)     TO ID A1P9.15
            FROM ID A1J9.15             TO ID BUS 1
```

```
            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 207

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.57. THE VOLTAGE MEASURED AT PIN J1.20
SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-57 (UUT J1-57)   TO W2 P1B-10A
            FROM ID J2B-10A             TO ID A1J13.31
            FROM ID A1P13.31            TO ID P12-7 (S701-30)

            FROM ID P12-44 (S701-2)     TO ID A1P12.48
            FROM ID A1J12.48            TO ID A1J10.1
            FROM ID A1P10.1             TO ID P11-162 (S506-2)
            FROM ID P11-68 (S506-9)     TO ID A1P9.20
            FROM ID A1J9.20             TO ID BUS 7

            FROM W2 P2-20 (UUT J1-20)   TO W2 P1B-14D
            FROM ID J2B-14D             TO ID A1J12.27
            FROM ID A1P12.27            TO ID P12-55 (S201-47)

            FROM ID P12-16 (S201-1)     TO ID A1P12.42
            FROM ID A1J12.42            TO ID A1J10.6
            FROM ID A1P10.6             TO ID P11-203 (S508-1)
            FROM ID P11-77 (S508-3)     TO ID A1P9.15
            FROM ID A1J9.15             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND
```

STEP 208

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.50 WITH RESPECT TO GROUND SHOULD BE LESS THAN 0.2VDC.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"
SEE "APPLY IC"

FROM W2 P2-50 (UUT J1-50)	TO W2 P1B-8B
FROM ID J2B-8B	TO ID A1J12.26
FROM ID A1P12.26	TO ID P12-25 (S202-6)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 209

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.14. THE VOLTAGE MEASURED AT PIN J1.50 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-14 (UUT J1-14)	TO W2 P1B-6C
FROM ID J2B-6C	TO ID A1J12.19
FROM ID A1P12.19	TO ID P12-54 (S201-38)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-232 (S507-9)	TO ID A1P9.14
FROM ID A1J9.14	TO ID BUS 7
FROM W2 P2-50 (UUT J1-50)	TO W2 P1B-8B

```
            FROM ID J2B-8B              TO ID A1J12.26
            FROM ID A1P12.26            TO ID P12-25 (S202-6)

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND


STEP 210

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.14. THE VOLTAGE MEASURED AT PIN J1.15
SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-14 (UUT J1-14)   TO W2 P1B-6C
            FROM ID J2B-6C              TO ID A1J12.19
            FROM ID A1P12.19            TO ID P12-54 (S201-38)

            FROM ID P12-52 (S201-4)     TO ID A1P12.44
            FROM ID A1J12.44            TO ID A1J10.4
            FROM ID A1P10.4             TO ID P11-71 (S507-2)
            FROM ID P11-232 (S507-9)    TO ID A1P9.14
            FROM ID A1J9.14             TO ID BUS 7

            FROM W2 P2-15 (UUT J1-15)   TO W2 P1B-7B
            FROM ID J2B-7B              TO ID A1J12.23
            FROM ID A1P12.23            TO ID P12-85 (S201-45)

            FROM ID P12-16 (S201-1)     TO ID A1P12.42
            FROM ID A1J12.42            TO ID A1J10.6
            FROM ID A1P10.6             TO ID P11-203 (S508-1)
            FROM ID P11-77 (S508-3)     TO ID A1P9.15
            FROM ID A1J9.15             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
```

```
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 211

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.14. THE VOLTAGE MEASURED AT PIN J1.48
SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-14 (UUT J1-14)   TO W2 P1B-6C
            FROM ID J2B-6C              TO ID A1J12.19
            FROM ID A1P12.19            TO ID P12-54 (S201-38)

            FROM ID P12-52 (S201-4)     TO ID A1P12.44
            FROM ID A1J12.44            TO ID A1J10.4
            FROM ID A1P10.4             TO ID P11-71 (S507-2)
            FROM ID P11-232 (S507-9)    TO ID A1P9.14
            FROM ID A1J9.14             TO ID BUS 7

            FROM W2 P2-48 (UUT J1-48)   TO W2 P1A-1D
            FROM ID J2A-1D              TO ID A1J14.37
            FROM ID A1P14.37            TO ID P13-54 (S201-51)

            FROM ID P12-16 (S201-1)     TO ID A1P12.42
            FROM ID A1J12.42            TO ID A1J10.6
            FROM ID A1P10.6             TO ID P11-203 (S508-1)
            FROM ID P11-77 (S508-3)     TO ID A1P9.15
            FROM ID A1J9.15             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 212
```

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.14 AND J1.8. THE VOLTAGE MEASURED AT PIN J1.50 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-14 (UUT J1-14)	TO W2 P1B-6C
FROM ID J2B-6C	TO ID A1J12.19
FROM ID A1P12.19	TO ID P12-54 (S201-38)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-232 (S507-9)	TO ID A1P9.14
FROM ID A1J9.14	TO ID BUS 7
FROM W2 P2-8 (UUT J1-8)	TO W2 P1A-2B
FROM ID J2A-2B	TO ID A1J14.31
FROM ID A1P14.31	TO ID P13-30 (S202-41)
FROM W2 P2-50 (UUT J1-50)	TO W2 P1B-8B
FROM ID J2B-8B	TO ID A1J12.26
FROM ID A1P12.26	TO ID P12-25 (S202-6)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 213

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.43 AND J1.8. THE VOLTAGE MEASURED AT PIN J1.50 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

```
            FROM W2 P2-43 (UUT J1-43)    TO W2 P1A-2D
            FROM ID J2A-2D               TO ID A1J14.36
            FROM ID A1P14.36             TO ID P13-55 (S201-50)

            FROM ID P12-52 (S201-4)      TO ID A1P12.44
            FROM ID A1J12.44             TO ID A1J10.4
            FROM ID A1P10.4              TO ID P11-71 (S507-2)
            FROM ID P11-232 (S507-9)     TO ID A1P9.14
            FROM ID A1J9.14              TO ID BUS 7

            FROM W2 P2-8 (UUT J1-8)      TO W2 P1A-2B
            FROM ID J2A-2B               TO ID A1J14.31
            FROM ID A1P14.31             TO ID P13-30 (S202-41)

            FROM W2 P2-50 (UUT J1-50)    TO W2 P1B-8B
            FROM ID J2B-8B               TO ID A1J12.26
            FROM ID A1P12.26             TO ID P12-25 (S202-6)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 214

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.43. THE VOLTAGE MEASURED AT PIN J1.50
SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-43 (UUT J1-43)    TO W2 P1A-2D
            FROM ID J2A-2D               TO ID A1J14.36
            FROM ID A1P14.36             TO ID P13-55 (S201-50)

            FROM ID P12-52 (S201-4)      TO ID A1P12.44
            FROM ID A1J12.44             TO ID A1J10.4
            FROM ID A1P10.4              TO ID P11-71 (S507-2)
```

```
            FROM ID P11-232 (S507-9)    TO ID A1P9.14
            FROM ID A1J9.14             TO ID BUS 7

            FROM W2 P2-50 (UUT J1-50)   TO W2 P1B-8B
            FROM ID J2B-8B              TO ID A1J12.26
            FROM ID A1P12.26            TO ID P12-25 (S202-6)

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 215

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.  THE
VOLTAGE MEASURED AT PIN J1.20 WITH RESPECT TO GROUND SHOULD BE LESS
THAN 0.2VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-20 (UUT J1-20)   TO W2 P1B-14D
            FROM ID J2B-14D             TO ID A1J12.27
            FROM ID A1P12.27            TO ID P12-55 (S201-47)

            FROM ID P12-16 (S201-1)     TO ID A1P12.42
            FROM ID A1J12.42            TO ID A1J10.6
            FROM ID A1P10.6             TO ID P11-203 (S508-1)
            FROM ID P11-77 (S508-3)     TO ID A1P9.15
            FROM ID A1J9.15             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
```

```
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 216

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.56. THE VOLTAGE MEASURED AT PIN J1.20
SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-56 (UUT J1-56)    TO W2 P1A-3E
            FROM ID J2A-3E               TO ID A1J14.38
            FROM ID A1P14.38             TO ID P13-85 (S201-52)

            FROM ID P12-52 (S201-4)      TO ID A1P12.44
            FROM ID A1J12.44             TO ID A1J10.4
            FROM ID A1P10.4              TO ID P11-71 (S507-2)
            FROM ID P11-232 (S507-9)     TO ID A1P9.14
            FROM ID A1J9.14              TO ID BUS 7

            FROM W2 P2-20 (UUT J1-20)    TO W2 P1B-14D
            FROM ID J2B-14D              TO ID A1J12.27
            FROM ID A1P12.27             TO ID P12-55 (S201-47)

            FROM ID P12-16 (S201-1)      TO ID A1P12.42
            FROM ID A1J12.42             TO ID A1J10.6
            FROM ID A1P10.6              TO ID P11-203 (S508-1)
            FROM ID P11-77 (S508-3)      TO ID A1P9.15
            FROM ID A1J9.15              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 217

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PINS J1.56 AND J1.22. THE VOLTAGE MEASURED AT
PIN J1.20 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO
GROUND.
```

```
            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-56 (UUT J1-56)    TO W2 P1A-3E
            FROM ID J2A-3E               TO ID A1J14.38
            FROM ID A1P14.38             TO ID P13-85 (S201-52)

            FROM ID P12-52 (S201-4)      TO ID A1P12.44
            FROM ID A1J12.44             TO ID A1J10.4
            FROM ID A1P10.4              TO ID P11-71 (S507-2)
            FROM ID P11-232 (S507-9)     TO ID A1P9.14
            FROM ID A1J9.14              TO ID BUS 7

            FROM W2 P2-22 (UUT J1-22)    TO W2 P1A-4F
            FROM ID J2A-4F               TO ID A1J14.46
            FROM ID A1P14.46             TO ID P13-95 (S202-43)

            FROM W2 P2-20 (UUT J1-20)    TO W2 P1B-14D
            FROM ID J2B-14D              TO ID A1J12.27
            FROM ID A1P12.27             TO ID P12-55 (S201-47)

            FROM ID P12-16 (S201-1)      TO ID A1P12.42
            FROM ID A1J12.42             TO ID A1J10.6
            FROM ID A1P10.6              TO ID P11-203 (S508-1)
            FROM ID P11-77 (S508-3)      TO ID A1P9.15
            FROM ID A1J9.15              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 218

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.56. THE VOLTAGE MEASURED AT PIN J1.20
SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-56 (UUT J1-56)    TO W2 P1A-3E
            FROM ID J2A-3E               TO ID A1J14.38
            FROM ID A1P14.38             TO ID P13-85 (S201-52)

            FROM ID P12-52 (S201-4)      TO ID A1P12.44
```

```
FROM ID A1J12.44            TO ID A1J10.4
FROM ID A1P10.4             TO ID P11-71 (S507-2)
FROM ID P11-232 (S507-9)    TO ID A1P9.14
FROM ID A1J9.14             TO ID BUS 7

FROM W2 P2-20 (UUT J1-20)   TO W2 P1B-14D
FROM ID J2B-14D             TO ID A1J12.27
FROM ID A1P12.27            TO ID P12-55 (S201-47)

FROM ID P12-16 (S201-1)     TO ID A1P12.42
FROM ID A1J12.42            TO ID A1J10.6
FROM ID A1P10.6             TO ID P11-203 (S508-1)
FROM ID P11-77 (S508-3)     TO ID A1P9.15
FROM ID A1J9.15             TO ID BUS 1

FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
FROM ID A1J15.49            TO ID A1J8.28
FROM ID A1P8.28             TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)     TO ID A1P6.13
FROM ID A1J6.13             TO ID BUS 1

FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
FROM ID A1J15.50            TO ID A1J7.38
FROM ID A1P7.38             TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)   TO ID A1P7.36
FROM ID A1J7.36             TO GROUND
```

STEP 219

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT PIN J1.50 WITH RESPECT TO GROUND SHOULD BE LESS THAN 0.2VDC.

```
CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"
SEE "APPLY IC"

FROM W2 P2-50 (UUT J1-50)   TO W2 P1B-8B
FROM ID J2B-8B              TO ID A1J12.26
FROM ID A1P12.26            TO ID P12-25 (S202-6)

FROM ID P13-29 (S202-4)     TO ID A1P14.50
FROM ID A1J14.50            TO ID A1J10.50
FROM ID A1P10.50            TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3)    TO ID A1P9.21
FROM ID A1J9.21             TO ID BUS 1

FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
FROM ID A1J15.49            TO ID A1J8.28
FROM ID A1P8.28             TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)     TO ID A1P6.13
FROM ID A1J6.13             TO ID BUS 1
```

```
            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND
```

STEP 220

```
DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.13. THE VOLTAGE MEASURED AT PIN J1.50
WITH RESPECT TO GROUND SHOULD BE LESS THAN 0.2VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-13 (UUT J1-13)   TO W2 P1A-2A
            FROM ID J2A-2A              TO ID A1J14.29
            FROM ID A1P14.29            TO ID P13-82 (S201-36)

            FROM ID P12-52 (S201-4)     TO ID A1P12.44
            FROM ID A1J12.44            TO ID A1J10.4
            FROM ID A1P10.4             TO ID P11-71 (S507-2)
            FROM ID P11-232 (S507-9)    TO ID A1P9.14
            FROM ID A1J9.14             TO ID BUS 7

            FROM W2 P2-50 (UUT J1-50)   TO W2 P1B-8B
            FROM ID J2B-8B              TO ID A1J12.26
            FROM ID A1P12.26            TO ID P12-25 (S202-6)

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)    TO ID A1P9.21
            FROM ID A1J9.21             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND
```

STEP 221

```
DESCRIPTION:
```

THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PINS J1.13 AND J1.22. THE VOLTAGE MEASURED AT PIN J1.50 SHOULD BE BETWEEN 27.5VDC AND 28.5VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-13 (UUT J1-13)	TO W2 P1A-2A
FROM ID J2A-2A	TO ID A1J14.29
FROM ID A1P14.29	TO ID P13-82 (S201-36)
FROM ID P12-52 (S201-4)	TO ID A1P12.44
FROM ID A1J12.44	TO ID A1J10.4
FROM ID A1P10.4	TO ID P11-71 (S507-2)
FROM ID P11-232 (S507-9)	TO ID A1P9.14
FROM ID A1J9.14	TO ID BUS 7
FROM W2 P2-22 (UUT J1-22)	TO W2 P1A-4F
FROM ID J2A-4F	TO ID A1J14.46
FROM ID A1P14.46	TO ID P13-95 (S202-43)
FROM W2 P2-50 (UUT J1-50)	TO W2 P1B-8B
FROM ID J2B-8B	TO ID A1J12.26
FROM ID A1P12.26	TO ID P12-25 (S202-6)
FROM ID P13-29 (S202-4)	TO ID A1P14.50
FROM ID A1J14.50	TO ID A1J10.50
FROM ID A1P10.50	TO ID P11-244 (S510-2)
FROM ID P11-180 (S510-3)	TO ID A1P9.21
FROM ID A1J9.21	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 222

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PIN J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.13. THE VOLTAGE MEASURED AT PIN J1.50 SHOULD BE LESS THAN 0.2VDC WITH RESPECT TO GROUND.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

```
            FROM W2 P2-13 (UUT J1-13)    TO W2 P1A-2A
            FROM ID J2A-2A               TO ID A1J14.29
            FROM ID A1P14.29             TO ID P13-82 (S201-36)

            FROM ID P12-52 (S201-4)      TO ID A1P12.44
            FROM ID A1J12.44             TO ID A1J10.4
            FROM ID A1P10.4              TO ID P11-71 (S507-2)
            FROM ID P11-232 (S507-9)     TO ID A1P9.14
            FROM ID A1J9.14              TO ID BUS 7

            FROM W2 P2-50 (UUT J1-50)    TO W2 P1B-8B
            FROM ID J2B-8B               TO ID A1J12.26
            FROM ID A1P12.26             TO ID P12-25 (S202-6)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 223

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  THE VOLTAGE MEASURED AT PINS J1.2 SHOULD BE GREATER THAN
27.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-2 (UUT J1-2)      TO W2 P1A-6B
            FROM ID J2A-6B               TO ID A1J15.26
            FROM ID A1P15.26             TO ID P13-73 (S701-25)

            FROM ID P12-76 (S701-1)      TO ID A1P12.50
            FROM ID A1J12.50             TO ID A1J10.3
            FROM ID A1P10.3              TO ID P11-194 (S506-1)
            FROM ID P11-129 (S506-8)     TO ID A1P9.30
            FROM ID A1J9.30              TO ID BUS 6

            FROM ID BUS 6                TO ID A1J8.48
```

```
            FROM ID A1P8.48             TO ID P10-171 (S301-50)
            FROM ID P10-42 (S301-49)    TO ID A1P7.24
            FROM ID A1J7.24             TO ID A1J4.16
            FROM ID A1P4.16             TO ID R109.1
            FROM ID R109.2              TO ID A1P4.9
            FROM ID A1J4.9              TO +28V

            FROM ID P12-76 (S701-1)     TO ID A1P12.50
            FROM ID A1J12.50            TO ID A1J10.3
            FROM ID A1P10.3             TO ID P11-194 (S506-1)
            FROM ID P11-164 (S506-3)    TO ID A1P9.23
            FROM ID A1J9.23             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 224

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  THE VOLTAGE MEASURED AT PINS J1.4 SHOULD BE GREATER THAN
27.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-4 (UUT J1-4)     TO W2 P1B-9B
            FROM ID J2B-9B              TO ID A1J13.29
            FROM ID A1P13.29            TO ID P12-8 (S701-27)

            FROM ID P12-76 (S701-1)     TO ID A1P12.50
            FROM ID A1J12.50            TO ID A1J10.3
            FROM ID A1P10.3             TO ID P11-194 (S506-1)
            FROM ID P11-129 (S506-8)    TO ID A1P9.30
            FROM ID A1J9.30             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.48
            FROM ID A1P8.48             TO ID P10-171 (S301-50)
            FROM ID P10-42 (S301-49)    TO ID A1P7.24
            FROM ID A1J7.24             TO ID A1J4.16
            FROM ID A1P4.16             TO ID R109.1
            FROM ID R109.2              TO ID A1P4.9
            FROM ID A1J4.9              TO +28V
```

```
            FROM ID P12-76 (S701-1)      TO ID A1P12.50
            FROM ID A1J12.50             TO ID A1J10.3
            FROM ID A1P10.3              TO ID P11-194 (S506-1)
            FROM ID P11-164 (S506-3)     TO ID A1P9.23
            FROM ID A1J9.23              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 225

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  THE VOLTAGE MEASURED AT PINS J1.63 SHOULD BE GREATER THAN
27.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-63 (UUT J1-63)    TO W2 P1B-5F
            FROM ID J2B-5F               TO ID A1J12.49
            FROM ID A1P12.49             TO ID P12-30 (S202-40)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-20 (S510-8)      TO ID A1P9.28
            FROM ID A1J9.28              TO ID BUS 6

            FROM ID BUS 6                TO ID A1J8.50
            FROM ID A1P8.50              TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)      TO ID A1P7.26
            FROM ID A1J7.26              TO ID A1J4.18
            FROM ID A1P4.18              TO ID R108.1
            FROM ID R108.2               TO ID A1P4.10
            FROM ID A1J4.10              TO GROUND

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
```

```
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 226

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51. THE VOLTAGE MEASURED AT J1.46 SHOULD BE LESS THAN 0.9 VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-46 (UUT J1-46)   TO W2 P1A-8B
            FROM ID J2A-8B              TO ID A1J15.20
            FROM ID A1P15.20            TO ID P13-46 (S701-48)

            FROM ID P12-44 (S701-2)     TO ID A1P12.48
            FROM ID A1J12.48            TO ID A1J10.1
            FROM ID A1P10.1             TO ID P11-162 (S506-2)
            FROM ID P11-129 (S506-8)    TO ID A1P9.30
            FROM ID A1J9.30             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM ID P12-44 (S701-2)     TO ID A1P12.48
            FROM ID A1J12.48            TO ID A1J10.1
            FROM ID A1P10.1             TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)    TO ID A1P9.23
            FROM ID A1J9.23             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND
```

STEP 227

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. THE VOLTAGE MEASURED AT J1.9 SHOULD BE LESS THAN 0.9 VDC.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-9 (UUT J1-9)	TO W2 P1B-10B
FROM ID J2B-10B	TO ID A1J13.33
FROM ID A1P13.33	TO ID P12-10 (S701-45)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.50
FROM ID A1P8.50	TO ID P10-138 (S301-54)
FROM ID P10-9 (S301-53)	TO ID A1P7.26
FROM ID A1J7.26	TO ID A1J4.18
FROM ID A1P4.18	TO ID R108.1
FROM ID R108.2	TO ID A1P4.10
FROM ID A1J4.10	TO GROUND
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

STEP 228

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.3. THE VOLTAGE MEASURED AT J1.2 SHOULD BE LESS THAN 0.9 VDC.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

```
            FROM W2 P2-2 (UUT J1-2)      TO W2 P1A-6B
            FROM ID J2A-6B               TO ID A1J15.26
            FROM ID A1P15.26             TO ID P13-73 (S701-25)

            FROM ID P12-76 (S701-1)      TO ID A1P12.50
            FROM ID A1J12.50             TO ID A1J10.3
            FROM ID A1P10.3              TO ID P11-194 (S506-1)
            FROM ID P11-129 (S506-8)     TO ID A1P9.30
            FROM ID A1J9.30              TO ID BUS 6

            FROM ID BUS 6                TO ID A1J8.48
            FROM ID A1P8.48              TO ID P10-171 (S301-50)
            FROM ID P10-42 (S301-49)     TO ID A1P7.24
            FROM ID A1J7.24              TO ID A1J4.16
            FROM ID A1P4.16              TO ID R109.1
            FROM ID R109.2               TO ID A1P4.9
            FROM ID A1J4.9               TO +28V

            FROM W2 P2-3 (UUT J1-3)      TO W2 P1B-8F
            FROM ID J2B-8F               TO ID A1J12.43
            FROM ID A1P12.43             TO ID P12-64 (S202-37)

            FROM ID P12-76 (S701-1)      TO ID A1P12.50
            FROM ID A1J12.50             TO ID A1J10.3
            FROM ID A1P10.3              TO ID P11-194 (S506-1)
            FROM ID P11-164 (S506-3)     TO ID A1P9.23
            FROM ID A1J9.23              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 229

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.   28.0VDC IS APPLIED TO PIN J1.5. THE VOLTAGE MEASURED AT
J1.4 SHOULD BE LESS THAN 0.9 VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-4 (UUT J1-4)      TO W2 P1B-9B
            FROM ID J2B-9B               TO ID A1J13.29
            FROM ID A1P13.29             TO ID P12-8 (S701-27)
```

```
            FROM ID P12-76 (S701-1)     TO ID A1P12.50
            FROM ID A1J12.50            TO ID A1J10.3
            FROM ID A1P10.3             TO ID P11-194 (S506-1)
            FROM ID P11-129 (S506-8)    TO ID A1P9.30
            FROM ID A1J9.30             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.48
            FROM ID A1P8.48             TO ID P10-171 (S301-50)
            FROM ID P10-42 (S301-49)    TO ID A1P7.24
            FROM ID A1J7.24             TO ID A1J4.16
            FROM ID A1P4.16             TO ID R109.1
            FROM ID R109.2              TO ID A1P4.9
            FROM ID A1J4.9              TO +28V

            FROM W2 P2-5 (UUT J1-5)     TO W2 P1B-6F
            FROM ID J2B-6F              TO ID A1J12.47
            FROM ID A1P12.47            TO ID P12-94 (S202-39)

            FROM ID P12-76 (S701-1)     TO ID A1P12.50
            FROM ID A1J12.50            TO ID A1J10.3
            FROM ID A1P10.3             TO ID P11-194 (S506-1)
            FROM ID P11-164 (S506-3)    TO ID A1P9.23
            FROM ID A1J9.23             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 230

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN AND J1.49. THE VOLTAGE MEASURED AT
PIN J1.9 SHOULD BE GREATER THAN 27.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-9 (UUT J1-9)     TO W2 P1B-10B
            FROM ID J2B-10B             TO ID A1J13.33
            FROM ID A1P13.33            TO ID P12-10 (S701-45)

            FROM ID P12-76 (S701-1)     TO ID A1P12.50
            FROM ID A1J12.50            TO ID A1J10.3
            FROM ID A1P10.3             TO ID P11-194 (S506-1)
```

```
            FROM ID P11-129 (S506-8)    TO ID A1P9.30
            FROM ID A1J9.30             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM W2 P2-49 (UUT J1-49)   TO W2 P1B-6A
            FROM ID J2B-6A              TO ID A1J12.21
            FROM ID A1P12.21            TO ID P12-61 (S202-21)

            FROM ID P12-76 (S701-1)     TO ID A1P12.50
            FROM ID A1J12.50            TO ID A1J10.3
            FROM ID A1P10.3             TO ID P11-194 (S506-1)
            FROM ID P11-164 (S506-3)    TO ID A1P9.23
            FROM ID A1J9.23             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 231

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN AND J1.49. THE VOLTAGE MEASURED AT
PIN J1.46 SHOULD BE GREATER THAN 27.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-46 (UUT J1-46)   TO W2 P1A-8B
            FROM ID J2A-8B              TO ID A1J15.20
            FROM ID A1P15.20            TO ID P13-46 (S701-48)

            FROM ID P12-44 (S701-2)     TO ID A1P12.48
            FROM ID A1J12.48            TO ID A1J10.1
            FROM ID A1P10.1             TO ID P11-162 (S506-2)
            FROM ID P11-129 (S506-8)    TO ID A1P9.30
            FROM ID A1J9.30             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
```

```
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM W2 P2-49 (UUT J1-49)   TO W2 P1B-6A
            FROM ID J2B-6A              TO ID A1J12.21
            FROM ID A1P12.21            TO ID P12-61 (S202-21)

            FROM ID P12-44 (S701-2)     TO ID A1P12.48
            FROM ID A1J12.48            TO ID A1J10.1
            FROM ID A1P10.1             TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)    TO ID A1P9.23
            FROM ID A1J9.23             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 231

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.69 AND J1.70 WITH RESPECT TO
J1.51.  28.0VDC IS APPLIED TO PIN AND J1.49. THE VOLTAGE MEASURED AT
PIN J1.46 SHOULD BE GREATER THAN 27.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-46 (UUT J1-46)   TO W2 P1A-8B
            FROM ID J2A-8B              TO ID A1J15.20
            FROM ID A1P15.20            TO ID P13-46 (S701-48)

            FROM ID P12-44 (S701-2)     TO ID A1P12.48
            FROM ID A1J12.48            TO ID A1J10.1
            FROM ID A1P10.1             TO ID P11-162 (S506-2)
            FROM ID P11-129 (S506-8)    TO ID A1P9.30
            FROM ID A1J9.30             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
```

```
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM W2 P2-49 (UUT J1-49)   TO W2 P1B-6A
            FROM ID J2B-6A              TO ID A1J12.21
            FROM ID A1P12.21            TO ID P12-61 (S202-21)

            FROM ID P12-44 (S701-2)     TO ID A1P12.48
            FROM ID A1J12.48            TO ID A1J10.1
            FROM ID A1P10.1             TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)    TO ID A1P9.23
            FROM ID A1J9.23             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 232

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51.  THE
VOLTAGE MEASURED AT J1.63 SHOULD BE LESS THAN 0.9 VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"
            SEE "APPLY IC"

            FROM W2 P2-63 (UUT J1-63)   TO W2 P1B-5F
            FROM ID J2B-5F              TO ID A1J12.49
            FROM ID A1P12.49            TO ID P12-30 (S202-40)

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
            FROM ID A1J14.50            TO ID A1J10.50
            FROM ID A1P10.50            TO ID P11-244 (S510-2)
            FROM ID P11-20 (S510-8)     TO ID A1P9.28
            FROM ID A1J9.28             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM ID P13-29 (S202-4)     TO ID A1P14.50
```

```
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 233

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.40. THE VOLTAGE MEASURED AT PINS J1.63
SHOULD BE GREATER THAN 27.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-63 (UUT J1-63)    TO W2 P1B-5F
            FROM ID J2B-5F               TO ID A1J12.49
            FROM ID A1P12.49             TO ID P12-30 (S202-40)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-20 (S510-8)      TO ID A1P9.28
            FROM ID A1J9.28              TO ID BUS 6

            FROM ID BUS 6                TO ID A1J8.50
            FROM ID A1P8.50              TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)      TO ID A1P7.26
            FROM ID A1J7.26              TO ID A1J4.18
            FROM ID A1P4.18              TO ID R108.1
            FROM ID R108.2               TO ID A1P4.10
            FROM ID A1J4.10              TO GROUND

            FROM W2 P2-40 (UUT J1-40)    TO W2 P1B-13E
            FROM ID J2B-13E              TO ID A1J12.31
            FROM ID A1P12.31             TO ID P12-57 (S202-13)

            FROM ID P13-29 (S202-4)      TO ID A1P14.50
            FROM ID A1J14.50             TO ID A1J10.50
            FROM ID A1P10.50             TO ID P11-244 (S510-2)
            FROM ID P11-180 (S510-3)     TO ID A1P9.21
            FROM ID A1J9.21              TO ID BUS 1
```

```
            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)     TO ID A1P6.13
            FROM ID A1J6.13             TO ID BUS 1

            FROM ID P20-3 (DMM-LO)      TO ID A1P15.50
            FROM ID A1J15.50            TO ID A1J7.38
            FROM ID A1P7.38             TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)   TO ID A1P7.36
            FROM ID A1J7.36             TO GROUND

STEP 234

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.40. THE VOLTAGE MEASURED AT PINS J1.9
SHOULD BE GREATER THAN 27.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-9 (UUT J1-9)     TO W2 P1B-10B
            FROM ID J2B-10B             TO ID A1J13.33
            FROM ID A1P13.33            TO ID P12-10 (S701-45)

            FROM ID P12-76 (S701-1)     TO ID A1P12.50
            FROM ID A1J12.50            TO ID A1J10.3
            FROM ID A1P10.3             TO ID P11-194 (S506-1)
            FROM ID P11-129 (S506-8)    TO ID A1P9.30
            FROM ID A1J9.30             TO ID BUS 6

            FROM ID BUS 6               TO ID A1J8.50
            FROM ID A1P8.50             TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)     TO ID A1P7.26
            FROM ID A1J7.26             TO ID A1J4.18
            FROM ID A1P4.18             TO ID R108.1
            FROM ID R108.2              TO ID A1P4.10
            FROM ID A1J4.10             TO GROUND

            FROM W2 P2-40 (UUT J1-40)   TO W2 P1B-13E
            FROM ID J2B-13E             TO ID A1J12.31
            FROM ID A1P12.31            TO ID P12-57 (S202-13)

            FROM ID P12-76 (S701-1)     TO ID A1P12.50
            FROM ID A1J12.50            TO ID A1J10.3
            FROM ID A1P10.3             TO ID P11-194 (S506-1)
            FROM ID P11-164 (S506-3)    TO ID A1P9.23
            FROM ID A1J9.23             TO ID BUS 1

            FROM ID P20-2 (DMM-HI)      TO ID A1P15.49
            FROM ID A1J15.49            TO ID A1J8.28
            FROM ID A1P8.28             TO ID P10-203 (S503-1)
```

```
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 235

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.40. THE VOLTAGE MEASURED AT PINS J1.46
SHOULD BE GREATER THAN 27.0VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-46 (UUT J1-46)    TO W2 P1A-8B
            FROM ID J2A-8B               TO ID A1J15.20
            FROM ID A1P15.20             TO ID P13-46 (S701-48)

            FROM ID P12-44 (S701-2)      TO ID A1P12.48
            FROM ID A1J12.48             TO ID A1J10.1
            FROM ID A1P10.1              TO ID P11-162 (S506-2)
            FROM ID P11-129 (S506-8)     TO ID A1P9.30
            FROM ID A1J9.30              TO ID BUS 6

            FROM ID BUS 6                TO ID A1J8.50
            FROM ID A1P8.50              TO ID P10-138 (S301-54)
            FROM ID P10-9 (S301-53)      TO ID A1P7.26
            FROM ID A1J7.26              TO ID A1J4.18
            FROM ID A1P4.18              TO ID R108.1
            FROM ID R108.2               TO ID A1P4.10
            FROM ID A1J4.10              TO GROUND

            FROM W2 P2-40 (UUT J1-40)    TO W2 P1B-13E
            FROM ID J2B-13E              TO ID A1J12.31
            FROM ID A1P12.31             TO ID P12-57 (S202-13)

            FROM ID P12-44 (S701-2)      TO ID A1P12.48
            FROM ID A1J12.48             TO ID A1J10.1
            FROM ID A1P10.1              TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)     TO ID A1P9.23
            FROM ID A1J9.23              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
```

```
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND

STEP 236

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51.
28.0VDC IS APPLIED TO PIN J1.40. THE VOLTAGE MEASURED AT J1.4 SHOULD
BE LESS THAN 0.9 VDC.

            CONNECTION PATH IS AS FOLLOWS:
            SEE "UUT POWER"

            FROM W2 P2-4 (UUT J1-4)      TO W2 P1B-9B
            FROM ID J2B-9B               TO ID A1J13.29
            FROM ID A1P13.29             TO ID P12-8 (S701-27)

            FROM ID P12-76 (S701-1)      TO ID A1P12.50
            FROM ID A1J12.50             TO ID A1J10.3
            FROM ID A1P10.3              TO ID P11-194 (S506-1)
            FROM ID P11-129 (S506-8)     TO ID A1P9.30
            FROM ID A1J9.30              TO ID BUS 6

            FROM ID BUS 6                TO ID A1J8.48
            FROM ID A1P8.48              TO ID P10-171 (S301-50)
            FROM ID P10-42 (S301-49)     TO ID A1P7.24
            FROM ID A1J7.24              TO ID A1J4.16
            FROM ID A1P4.16              TO ID R109.1
            FROM ID R109.2               TO ID A1P4.9
            FROM ID A1J4.9               TO +28V

            FROM W2 P2-40 (UUT J1-40)    TO W2 P1B-13E
            FROM ID J2B-13E              TO ID A1J12.31
            FROM ID A1P12.31             TO ID P12-57 (S202-13)

            FROM ID P12-44 (S701-2)      TO ID A1P12.48
            FROM ID A1J12.48             TO ID A1J10.1
            FROM ID A1P10.1              TO ID P11-162 (S506-2)
            FROM ID P11-164 (S506-3)     TO ID A1P9.23
            FROM ID A1J9.23              TO ID BUS 1

            FROM ID P20-2 (DMM-HI)       TO ID A1P15.49
            FROM ID A1J15.49             TO ID A1J8.28
            FROM ID A1P8.28              TO ID P10-203 (S503-1)
            FROM ID P10-77 (S503-3)      TO ID A1P6.13
            FROM ID A1J6.13              TO ID BUS 1

            FROM ID P20-3 (DMM-LO)       TO ID A1P15.50
            FROM ID A1J15.50             TO ID A1J7.38
            FROM ID A1P7.38              TO ID P10-130 (S301-23)
            FROM ID P10-229 (S301-24)    TO ID A1P7.36
            FROM ID A1J7.36              TO GROUND
```

STEP 237

DESCRIPTION:
THIS STEP APPLIES 28.0VDC TO PINS J1.70 WITH RESPECT TO J1.51. 28.0VDC IS APPLIED TO PIN J1.40. THE VOLTAGE MEASURED AT J1.2 SHOULD BE LESS THAN 0.9 VDC.

CONNECTION PATH IS AS FOLLOWS:
SEE "UUT POWER"

FROM W2 P2-2 (UUT J1-2)	TO W2 P1A-6B
FROM ID J2A-6B	TO ID A1J15.26
FROM ID A1P15.26	TO ID P13-73 (S701-25)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-129 (S506-8)	TO ID A1P9.30
FROM ID A1J9.30	TO ID BUS 6
FROM ID BUS 6	TO ID A1J8.48
FROM ID A1P8.48	TO ID P10-171 (S301-50)
FROM ID P10-42 (S301-49)	TO ID A1P7.24
FROM ID A1J7.24	TO ID A1J4.16
FROM ID A1P4.16	TO ID R109.1
FROM ID R109.2	TO ID A1P4.9
FROM ID A1J4.9	TO +28V
FROM W2 P2-40 (UUT J1-40)	TO W2 P1B-13E
FROM ID J2B-13E	TO ID A1J12.31
FROM ID A1P12.31	TO ID P12-57 (S202-13)
FROM ID P12-76 (S701-1)	TO ID A1P12.50
FROM ID A1J12.50	TO ID A1J10.3
FROM ID A1P10.3	TO ID P11-194 (S506-1)
FROM ID P11-164 (S506-3)	TO ID A1P9.23
FROM ID A1J9.23	TO ID BUS 1
FROM ID P20-2 (DMM-HI)	TO ID A1P15.49
FROM ID A1J15.49	TO ID A1J8.28
FROM ID A1P8.28	TO ID P10-203 (S503-1)
FROM ID P10-77 (S503-3)	TO ID A1P6.13
FROM ID A1J6.13	TO ID BUS 1
FROM ID P20-3 (DMM-LO)	TO ID A1P15.50
FROM ID A1J15.50	TO ID A1J7.38
FROM ID A1P7.38	TO ID P10-130 (S301-23)
FROM ID P10-229 (S301-24)	TO ID A1P7.36
FROM ID A1J7.36	TO GROUND

3.0 Functional Flow Chart (FFC)