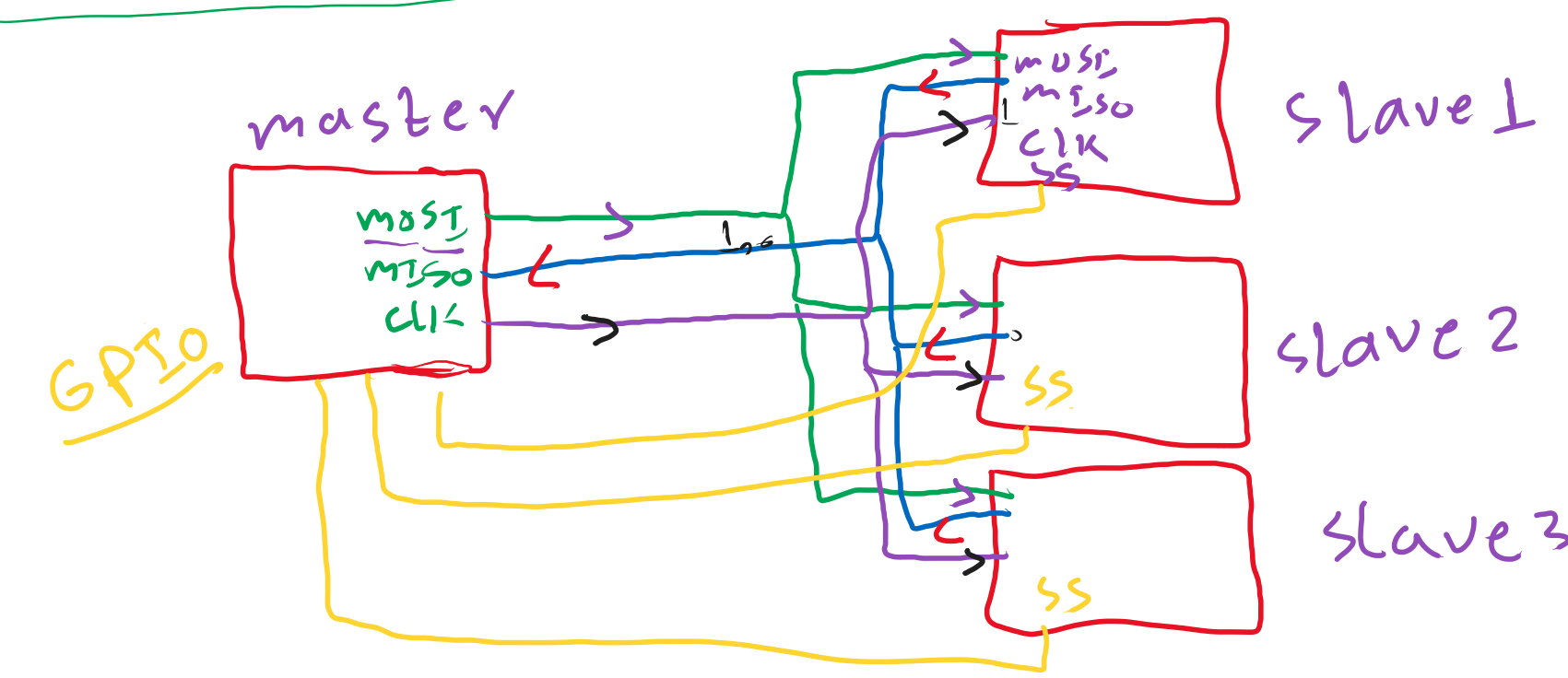


SPI → serial peripheral Interface

Hardware Interface



MOSI: master out put slave Input
MISO: master Input slave output
CLK: CLK → clock

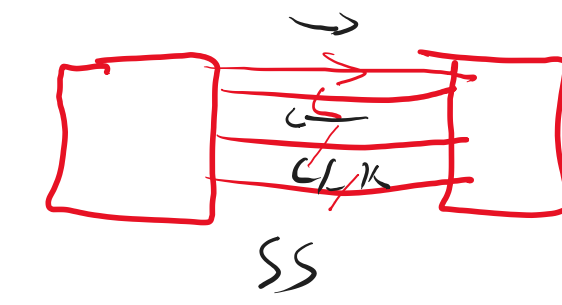
SS-CS-NSS-NCS → slave select

Note:-

if I want to allow Broadcasting
Disconnect the MISO line
↳ simplex

you can connect SS to GND to enable
the slave → if I only have one
slave

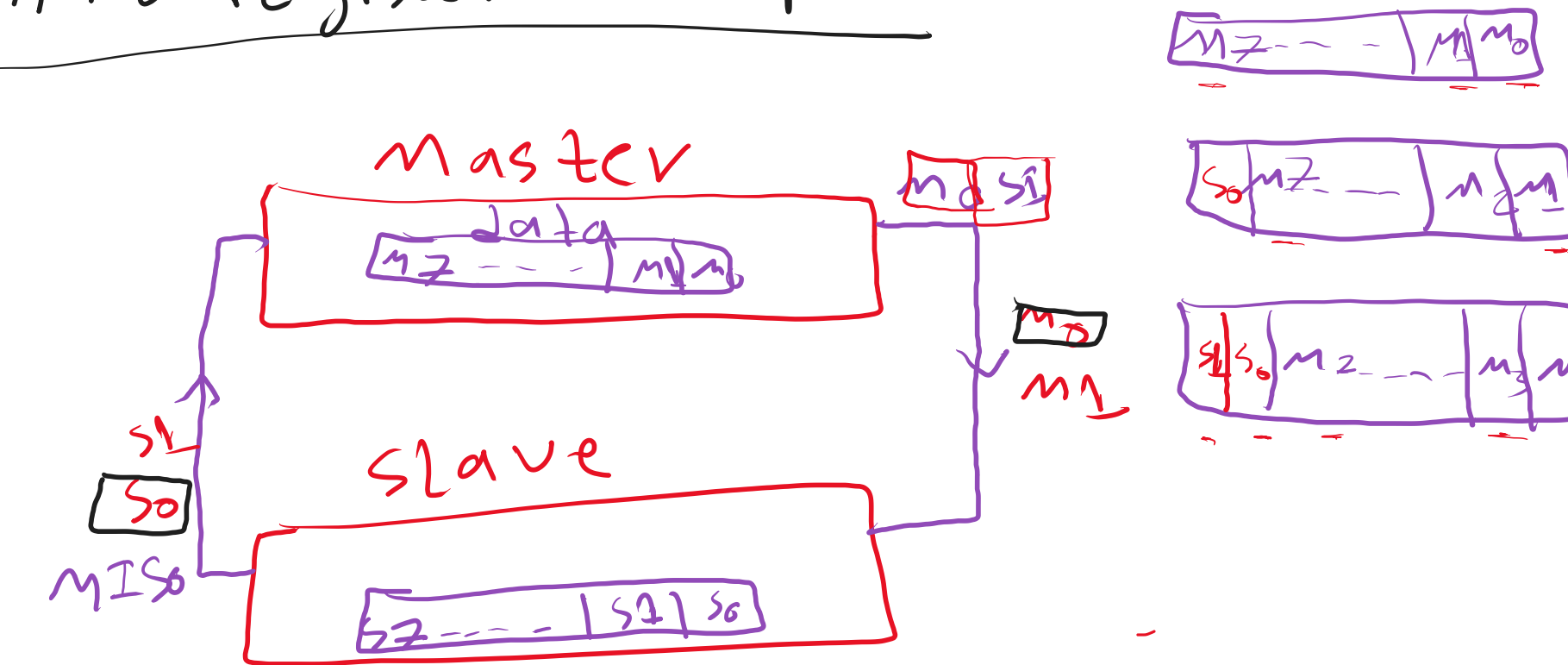
3 - number of slaves in SPI network
depend on (number of GPIO pins in)
master



specifications

- 1 - wired
- 2 - serial
- 3 - full duplex
- 4 - synchronous
- 5 - 5MMS
- 6 - through 100% put

shift register concept



through = $\frac{\text{No of data bits}}{\text{put total frame}}$ = 8

Data frame format

