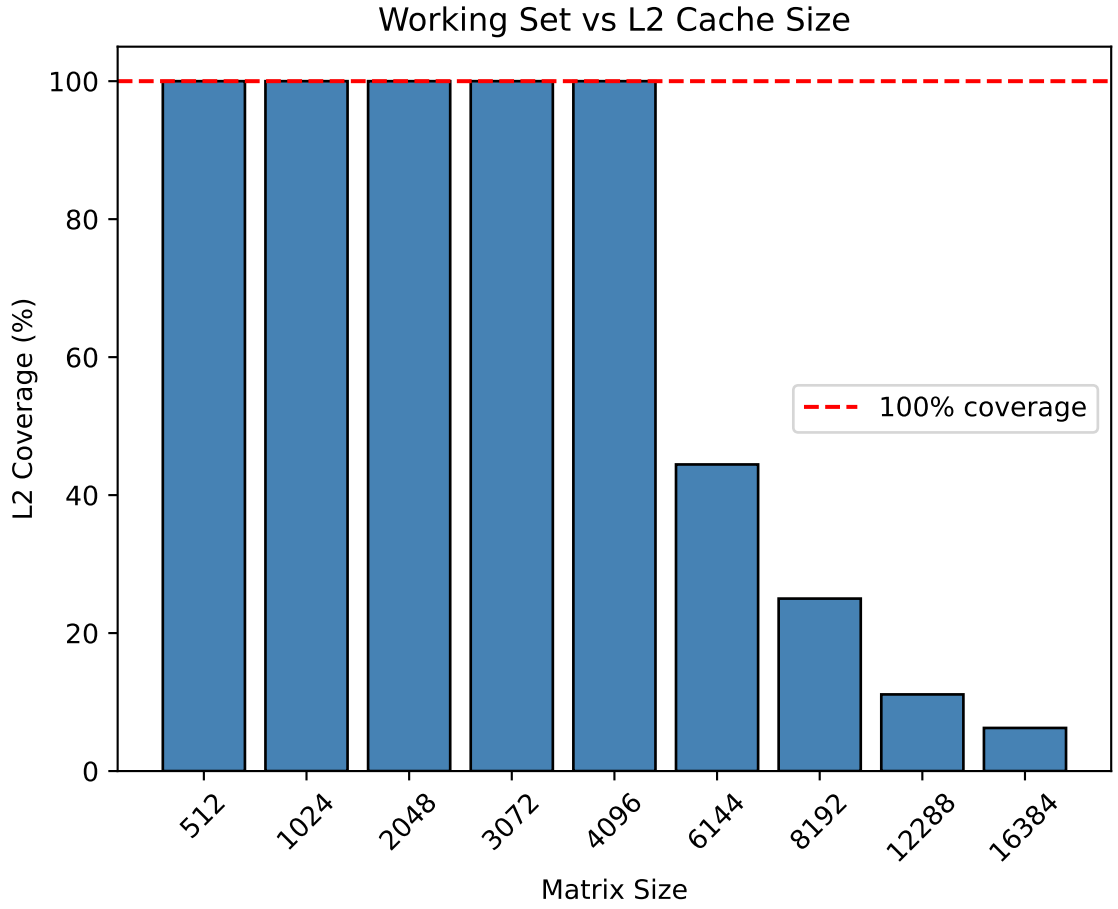
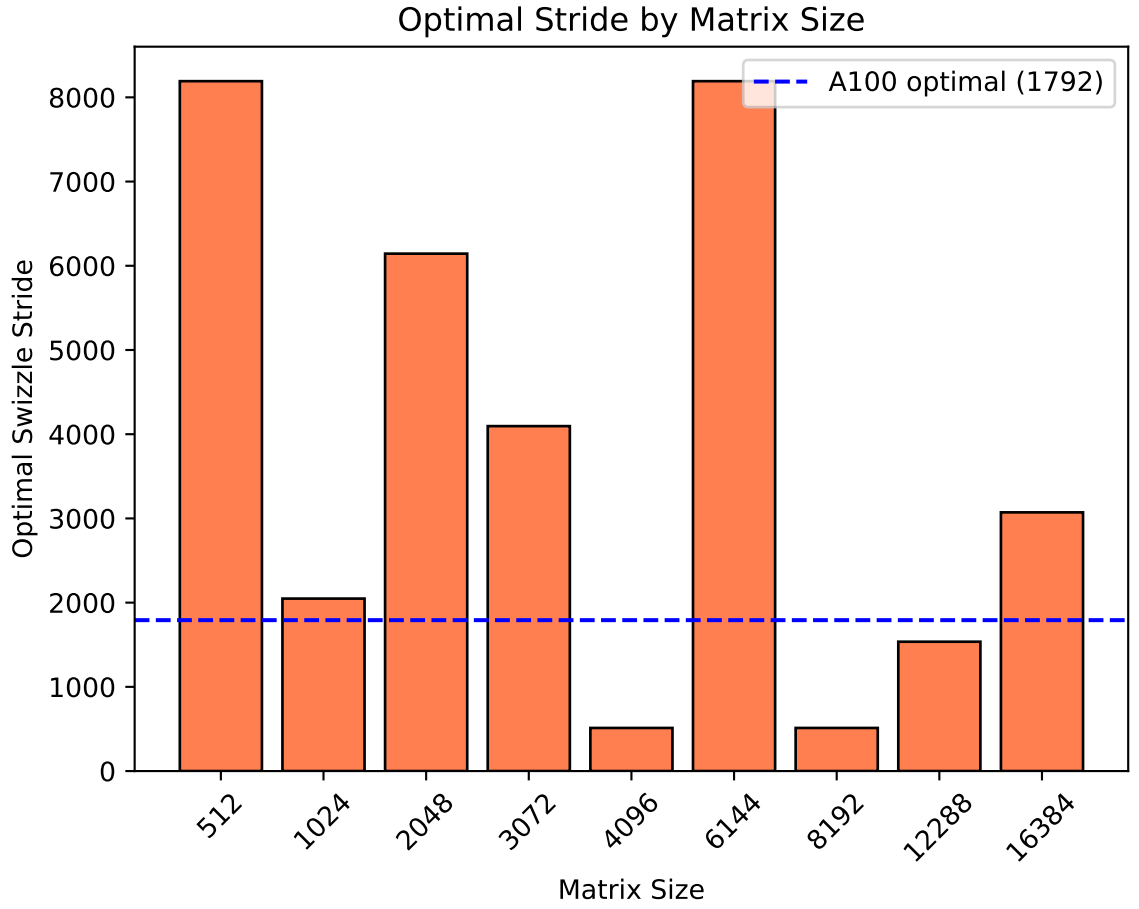

Working Set vs L2 Cache Size
L2 Coverage (%)
100
80
60
40
20
0
100% coverage
512
1024
2048
3072
4096
6144
8192
12288
16384
Matrix Size


Optimal Stride by Matrix Size
Optimal Swizzle Stride
8000
7000
6000
5000
4000
3000
2000
1000
0
A100 optimal (1792)
512
1024
2048
3072
4096
6144
8192
12288
16384
Matrix Size