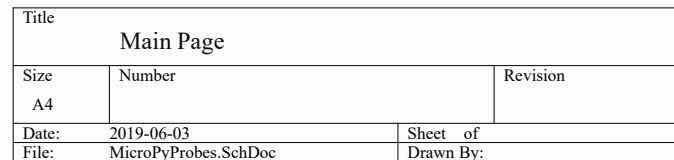
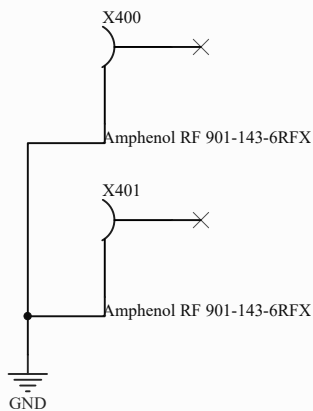
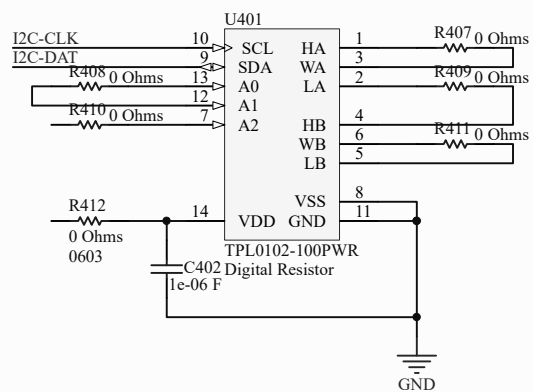
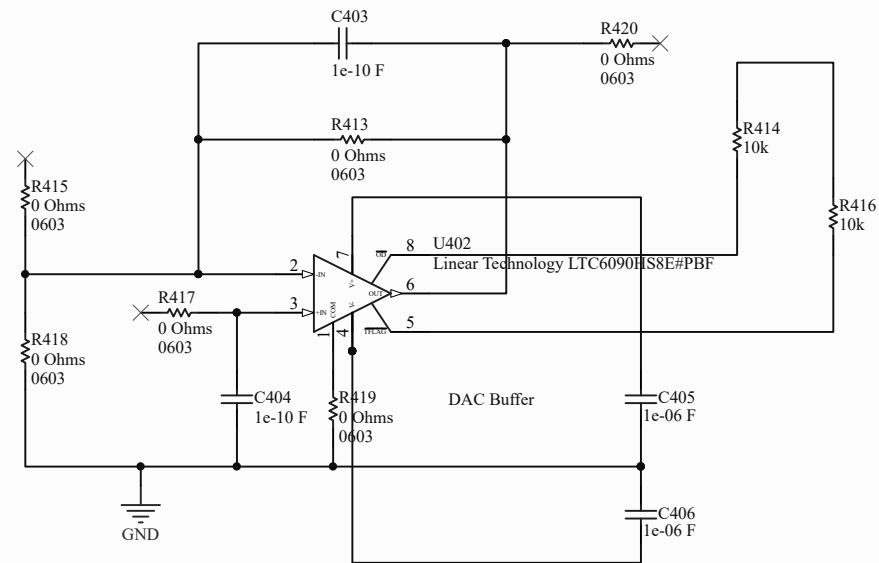
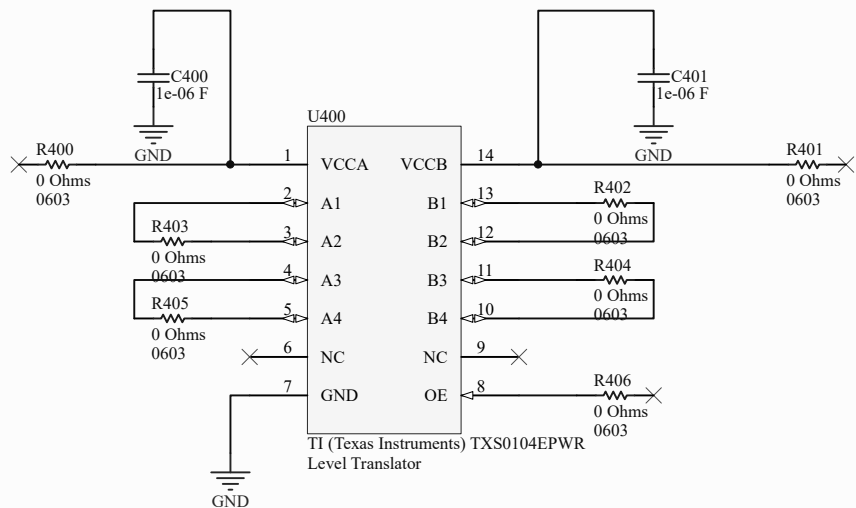


Title		
INA220 Current Measurements		
Size	Number	Revision
A4		
Date:	2019-06-03	Sheet of
File:	CurrMeas.SchDoc	Drawn By:

- 1) Think traces mean thick routes to reduce resistance.
- 2) compnoents in a box outline should be placed close togeth.
- 3) Jig Closed (SW101/102) switch should be placed on bottom of PCB.
- 4) Test with "SS: blah" means this note added to silk screen.





Title			
Level Translator		Digital Resistor	DAC Buffer
Size	Number		Revision
A4			
Date:	2019-06-03		Sheet of
File:	Misc.SchDoc		Drawn By:

1

2

3

4

A

A

B

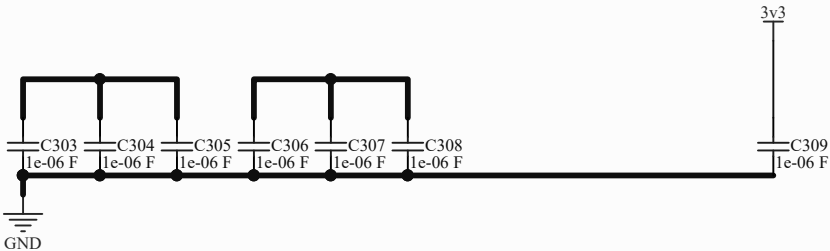
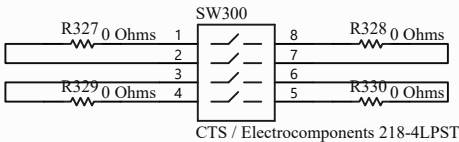
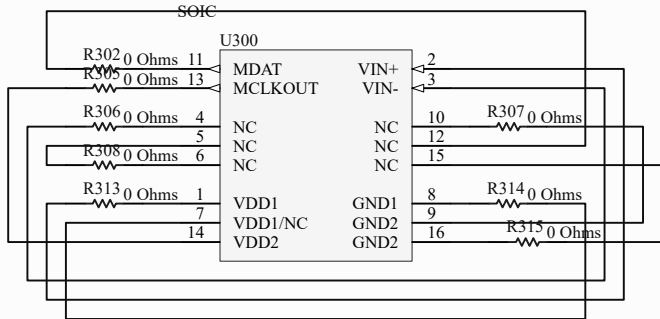
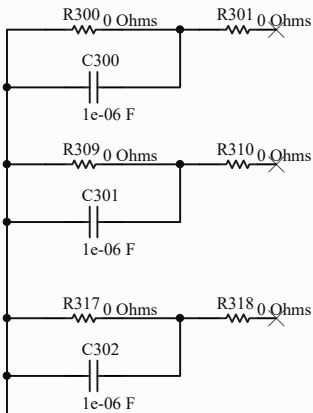
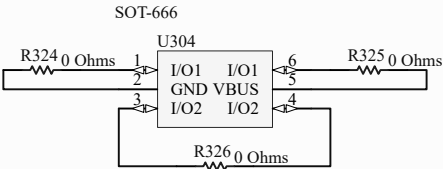
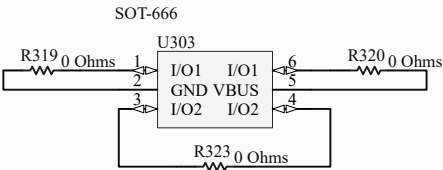
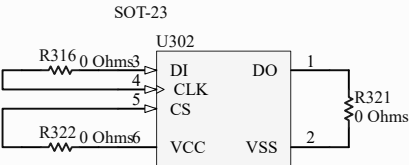
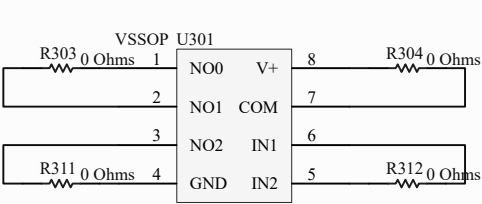
B

C

C

D

D



Title		
Prototype Components		
Size	Number	Revision
A4		
Date:	2019-06-03	Sheet of
File:	Prototype.SchDoc	Drawn By:

1

2

3

4

