

A Spatially Coupled LDPC Coding Scheme with Scalable Decoders for Space Division Multiplexing

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Abstract In this paper, we study the application of spatially coupled LDPC codes with sub-block locality for space division multiplexing. We focus on the information exchange between the sub-blocks and compare decoding strategies with respect to the complexity, performance and the information flow.

Introduction

Low-density parity-check (LDPC) codes are a large family of error correcting codes with good correction capabilities at moderate decoder complexity 1057683, leven2014status. Spatial coupling (SC) further improves their performance and provides the possibility to achieve the channel capacity with ubiquitous belief propagation (BP) decoding 6589171, 5695130 and has proven to be very useful in optical communications schmalen2015spatially. In 9594186, 9174265, spatially coupled (SC) LDPC codes with sub-block locality and their corresponding decoders were proposed for data storage systems and analyzed on a binary erasure channel (BEC). This new class of codes offer random access to every sub-block in an SC-LDPC code with flexible decoding complexity and performance.

While this class of codes was designed for random access of small data units with low latency in storage systems, it can also be beneficially used in optical communications with space division multiplexing (SDM). SDM is attractive due to the scaling of the data rate with the number of spatial channels (e.g., cores in a multi-core fiber or modes in a multi-mode fiber). To achieve the best possible performance, we jointly encode the different SDM channels and assign each channel a sub-block of the SC-LDPC code. The architecture of future SDM receivers is not yet fully clear: On the one hand, the receiver may be fully integrated, processing all SDM channels in a single circuit. On the other hand, a single circuit may not be able to handle the high data rate in SDM and the receiver must be distributed to multiple processing units, each decoding a separate channel. In this paper,

we use the SC-LDPC codes with sub-block locality to jointly encode different SDM channels and enable various receiver options: joint decoding in an integrated receiver, fully separate decoding with distributed circuits and semi-joint decoding, where the different receiver circuits can exchange a limited amount of information. We propose decoder variants that improve decoding and reduce the information flow between processing units; these are candidates for future, scalable SDM systems.

Background

A (d_v, d_c) LDPC code is a linear block code given by its parity check matrix \mathbf{H} , which has d_v 1s in each column and d_c 1s in each row. SC-LDPC codes divide the codeword into sub-blocks and the parity check equations (check nodes) connect neighboring sub-blocks. In this work, we focus on unit-memory SC-LDPC codes schmalen2016design, where only the directly neighboring sub-blocks contribute to the current sub-block. SC-LDPC codes enable a simple windowed decoding scheme schmalen2015spatially.

The authors in 9594186, 9174265 introduced an additional constraint during the construction of SC-LDPC codes, where a fraction of t check nodes (called coupled checks, CC) in a sub-block are allowed to connect to other sub-blocks, and the remaining check nodes (local checks, LC) only connect to code bits in the same sub-block. LCs allow us to decode a single sub-block without information from other sub-blocks; the CCs are the bridge to exchange information between sub-blocks. The parity-check matrix of SC-LDPC codes is composed of $\mathbf{H}_{\text{local}}$, which defines the LCs and both \mathbf{H}_{left} and $\mathbf{H}_{\text{right}}$, which define the CCs and connections to neighboring sub-blocks. This new SC-LDPC codes offer three decoding modes: separate local decoding, joint decoding of all sub-blocks, and semi-joint decoding. More details of the decoders can be found in 9594186, 9174265. In the remainder of this paper, we use

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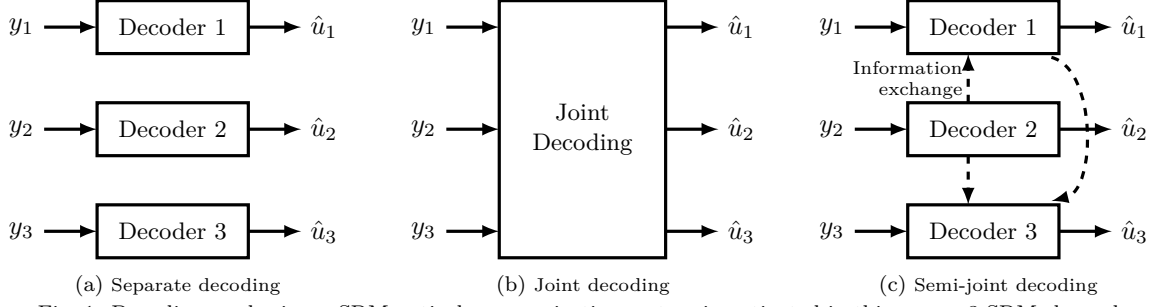


Fig. 1: Decoding modes in an SDM optical communication system investigated in this paper, 3 SDM channels

the ($d_v = 4, d_c = 20, t = \frac{1}{4}$) SC-LDPCL code as an example, due to the good decoding performance of these parameters schmalen2015spatially, schmalen2016design.

We employ the concept of SC-LDPCL codes to jointly encode the spatial channels of SDM. SC-LDPCL codes enable a future-proof system design that allows different, scalable and flexible decoders, depending on the application. We assign each sub-block of the SC-LDPCL code to an SDM channel. In the decoder, we either consider decoding of each sub-block separately (Fig. ??(a)) or allow an information exchange between the sub-channels (Fig. ??(b)), which should be kept as little as possible (Fig. ??(c)). Our work extends SC-LDPCL codes towards communications. In SDM receivers, we aim at limiting information exchange between decoders, possibly requiring slow communication channels on circuit boards between different decoder circuits. We propose two variants of the semi-joint decoder to cope with different levels of information exchange.

Semi-joint Decoder Variants

In this section, we first introduce the semi-joint (SJ) decoder and its variants. The SJ decoder is equivalent to the SG decoder from 9594186, 9174265, extended to communications over general channels.

SJ Decoder (Conventional SG Decoder) 8625294

In SJ decoding, we decode a specific target sub-block T with the help of d neighboring sub-blocks, called helpers. We assume that the number of involved sub-blocks does not exceed the total number of the sub-blocks and we neglect boundary effects. Furthermore, we assume d to be even, so that the helpers lie symmetrically on both sides of the target. The decoder is described by:

1. Decode the two furthest sub-blocks $T + \frac{d}{2}$ and $T - \frac{d}{2}$ locally, i.e. carry out BP decoding using the LCs only (effectively using a parity-check matrix $\mathbf{H}_{\text{local}}$) using the sub-block channel outputs $\mathbf{y}_{T-d/2}$ and $\mathbf{y}_{T+d/2}$, respectively.
2. Decode the left helpers: for the i -th helper,

$T - \frac{d}{2} < i < T$, decode the sub-block with information from its left neighbor, i.e. carry out BP decoding with the parity check matrix $\begin{bmatrix} \mathbf{H}_{\text{right}} & \mathbf{H}_{\text{left}} \\ \mathbf{0} & \mathbf{H}_{\text{local}} \end{bmatrix}$ and $[\hat{\mathbf{y}}_{i-1}, \mathbf{y}_i]$, with $\hat{\mathbf{y}}_{i-1}$ the updated information of the left neighbor.

3. Decode the right helpers: for the i -th helper, $T < i < T + \frac{d}{2}$, decode the sub-block with information from its right neighbor, i.e. do BP decoding with the parity check matrix $\begin{bmatrix} \mathbf{H}_{\text{local}} & \mathbf{0} \\ \mathbf{H}_{\text{right}} & \mathbf{H}_{\text{left}} \end{bmatrix}$ and $[\mathbf{y}_i, \hat{\mathbf{y}}_{i+1}]$, where $\hat{\mathbf{y}}_{i+1}$ is the updated information of the right neighbor.

4. Decode the target: carry out BP decoding with the parity check matrix $\begin{bmatrix} \mathbf{H}_{\text{right}} & \mathbf{H}_{\text{left}} & \mathbf{0} \\ \mathbf{0} & \mathbf{H}_{\text{local}} & \mathbf{0} \\ \mathbf{0} & \mathbf{H}_{\text{right}} & \mathbf{H}_{\text{left}} \end{bmatrix}$ and $[\hat{\mathbf{y}}_{T-1}, \mathbf{y}_T, \hat{\mathbf{y}}_{T+1}]$.

It should be noted that the decoding of helpers on both sides can be carried out in parallel. In Figs. ?? and ??, the performance of our exemplary code under SJ decoding is given together with the separate and fully joint decoding performance as reference. An increasing number of helpers leads to lower bit error rates (BERs); separate and fully joint decoding are lower and upper bound on the SJ decoding performance. We can see that already for small d , SJ decoding closes the significant gap between joint and separate decoding, the latter effectively using a code of higher rate but with complete separate decoders.

SJ Decoder Variant

As stated before, the information flow between the decoder in SJ decoding is an important performance parameter, especially if the decoder are realized in different circuits. In the SJ decoder, we exchange d times soft information between sub-blocks for decoding a single sub-block. In this variant, we exploit that in an SDM system, the channel information from different sub-channels is available at the same time. The proposed variant

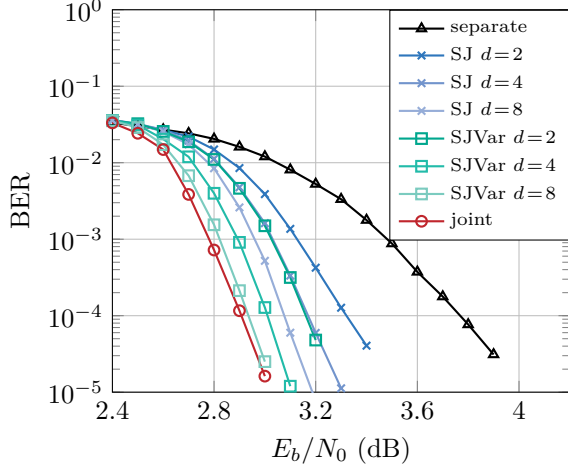


Fig. 2: Performance of $(d_v = 4, d_c = 20, t = \frac{1}{4})$ code under SJ decoding and the proposed variant

is described in what follows:

1. For decoding target sub-block T , the helpers $T - \frac{d}{2}, \dots, T - 1$ and $T + 1, \dots, T + \frac{d}{2}$ transmit their channel information to the target.
2. Carry out BP decoding using the parity check matrix

$$B = \begin{bmatrix} \mathbf{H}_{\text{local}} & & & & & & & & \\ \mathbf{H}_{\text{right}} & \mathbf{H}_{\text{left}} & & & & & & & \\ & \mathbf{H}_{\text{local}} & & & & & & & \\ & \mathbf{H}_{\text{right}} & & & & & & & \\ & & \ddots & & & & & & \\ & & & \mathbf{H}_{\text{right}} & \mathbf{H}_{\text{left}} & & & & \\ & & & & \mathbf{H}_{\text{local}} & & & & \end{bmatrix}$$

$d+1$ sub-blocks

and the channel information

$$[\mathbf{y}_{T-\frac{d}{2}}, \dots, \mathbf{y}_{T-1}, \mathbf{y}_T, \mathbf{y}_{T+1}, \dots, \mathbf{y}_{T+\frac{d}{2}}].$$

It is easy to show that the complexity of this approach is identical to the SJ decoder and the different decoders only need to exchange information before starting decoding and not during the execution of the decoder. The performance of our example code using the new decoder variant (denoted “SJVar”) is shown in Fig. ?? . With the same number of helpers d , our new variant provides a performance gain of approximately 0.2dB, without increasing the information flow between the sub-blocks and the complexity of the decoder.

SJ Decoding with Hard Information Exchange

In both the SJ decoder and the variant, we exchange soft information between the sub-blocks. Soft information is usually quantized using q bits (typically $q = 5, \dots, 7$). Exchanging hard information significantly reduces the information flow in the system by a factor of q . Therefore, we propose

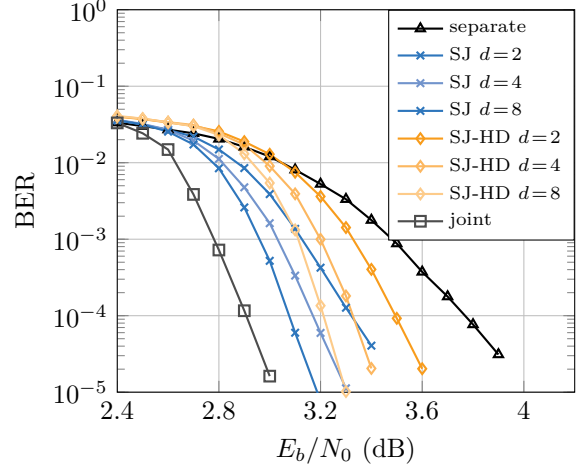


Fig. 3: Performance of $(d_v = 4, d_c = 20, t = \frac{1}{4})$ code SJ and HD-SJ decoding

a SJ decoder variant, denoted “SJ-HD”, which allows hard information exchange while degrading the performance only slightly. The idea is that after BP decoding of a helper, the hard decisions of the variable nodes are transmitted along with an estimate $\hat{\delta}_b$ of the BER. Ther BER estimate is obtained from the fraction δ_c of unfulfilled check equations through

$$\hat{\delta}_b = \frac{1}{2} \left(1 - (1 - 2\delta_c)^{\frac{1}{d_c}} \right). \quad (1)$$

The hard decision of the helper $\hat{\mathbf{x}}$ and the corresponding BER are then used to calculate soft information (in terms of log-likelihood ratios (LLRs)) for the next stage (next helper or final decoder) via $\mathbf{y} = \hat{\mathbf{x}} \cdot \ln \left(\frac{1-\hat{\delta}_b}{\hat{\delta}_b} \right)$. We compare the the decoding performance with conventional SJ decoding in Fig. ?? . We observe that the reduction of information flow towards hard decision by a factor q is achieved at the cost of only around 0.1 dB in BER, which offers a reasonable trade-off between information flow and decoding performance.

Unfortunately, both variants cannot be trivially combined as the SJ decoder uses soft information to get an estimate of the local symbols, while the variant uses soft information from the beginning.

Conclusions

In this paper, we have discussed the application of SC-LDPCL ensembles for scalable optical communication systems with SDM. We have adapted the SC-LDPCL to the new application scenario and proposed the semi-joint decoder for decoders that are distributed on multiple processors together with two variants: The first variant improves the BER performance without increasing the information exchange, and the second reduces

the information exchange significantly with only small performance penalty.