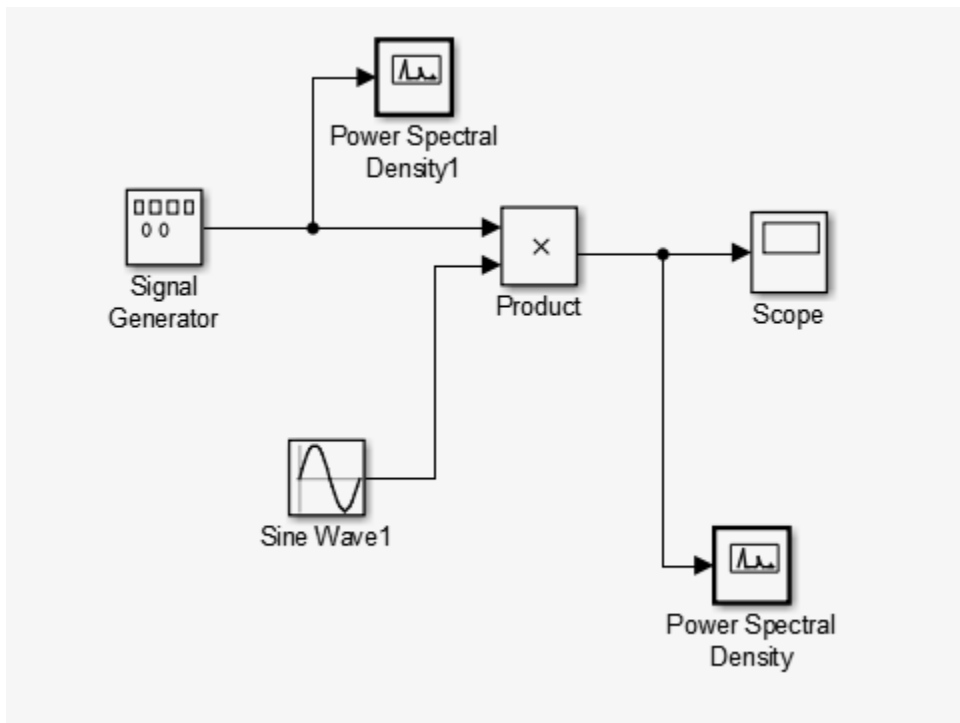


Joseph Palicke

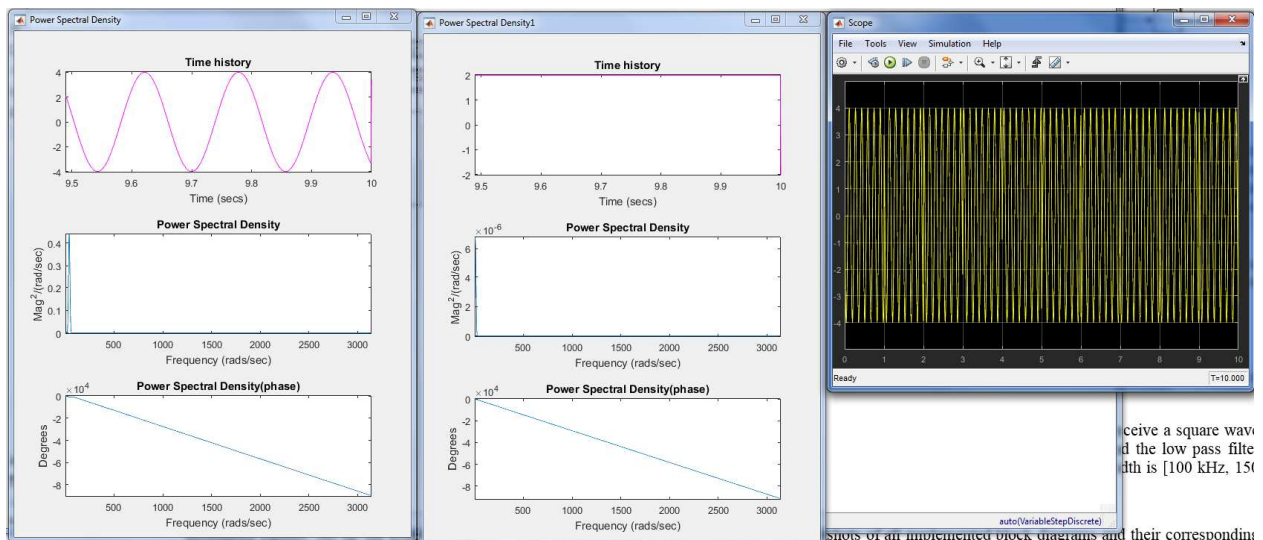
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LAB 5 Task 2

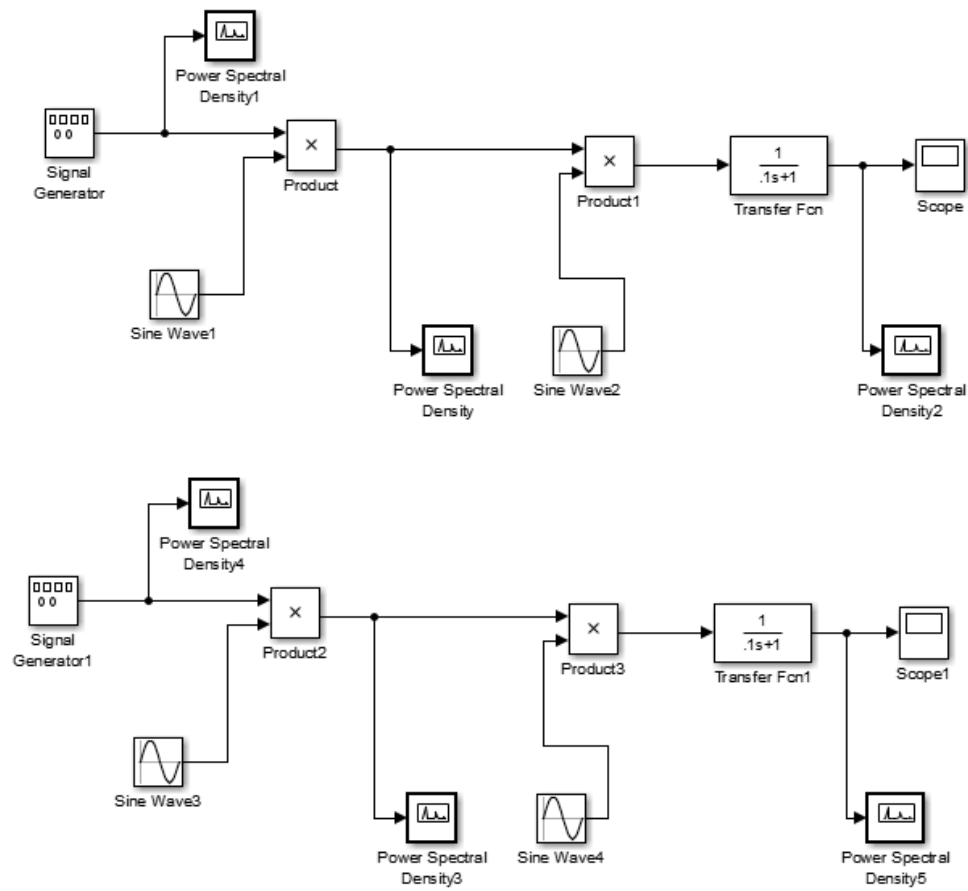
1) DSB_SC Modulator Block Diagram



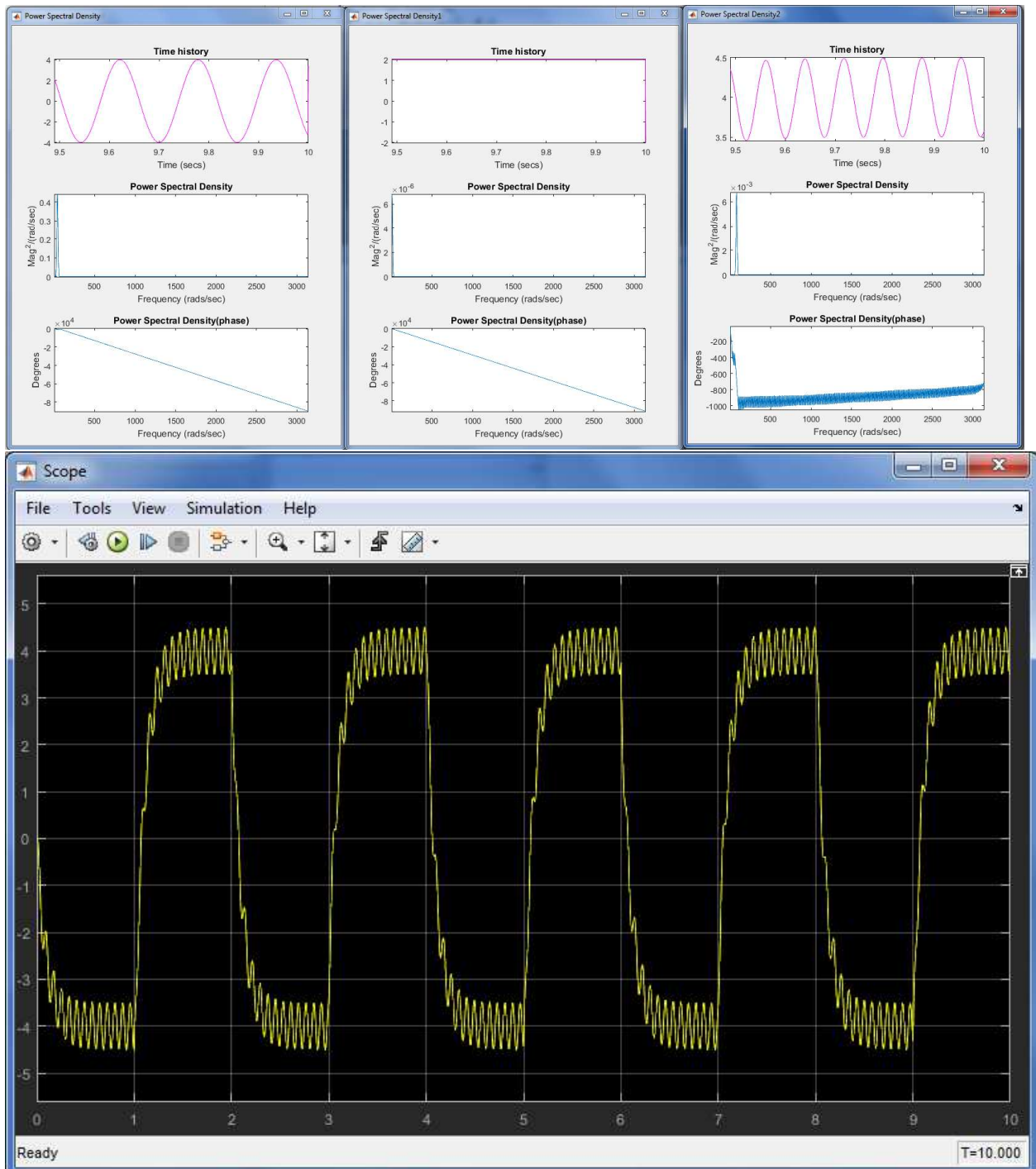
2) DSB_SC Graphs



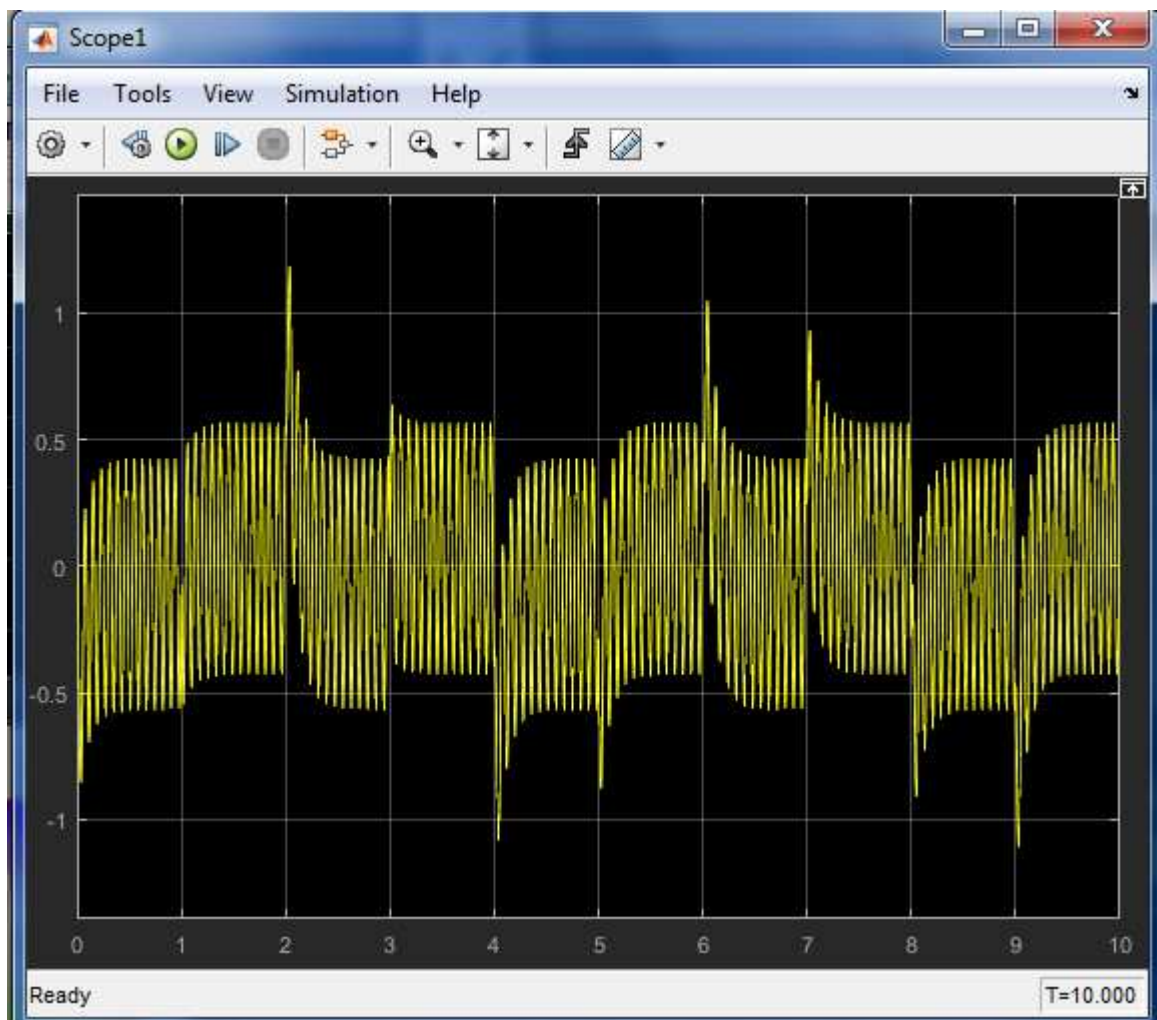
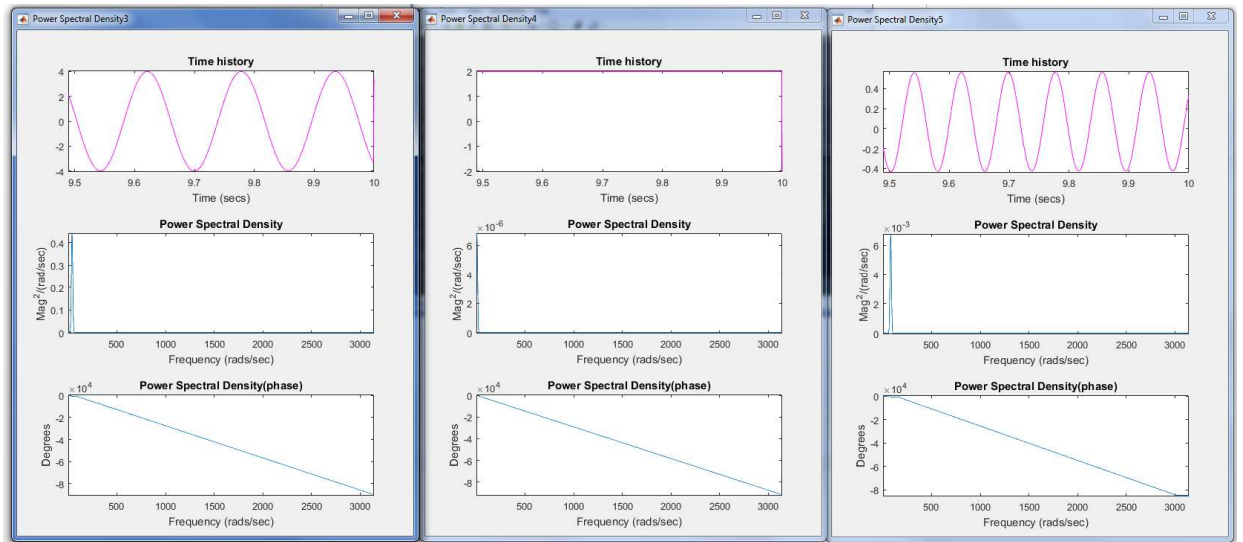
3) DSB_SC Modulator/Demodulator Block



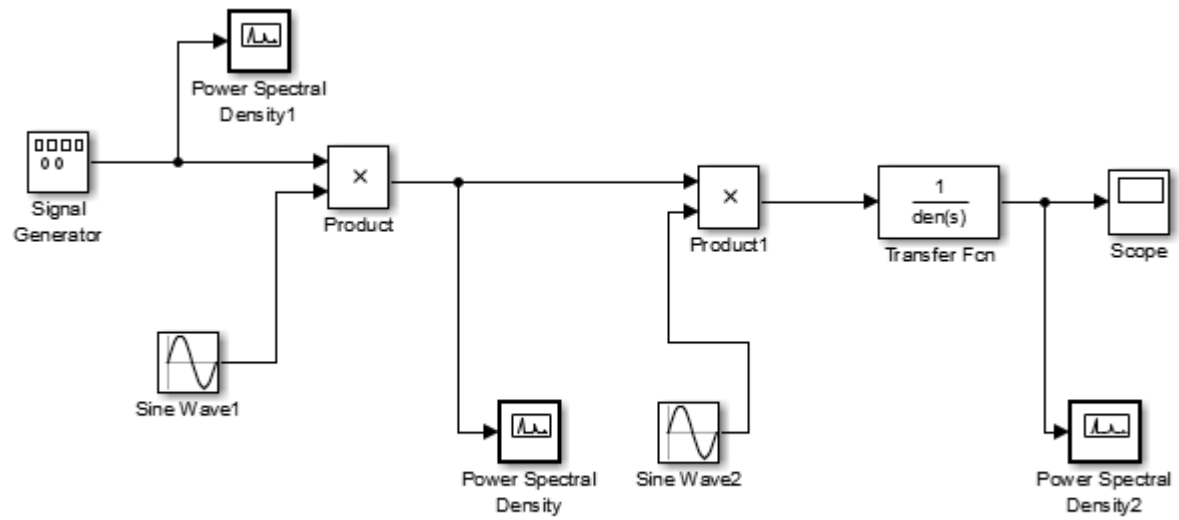
4)DSB_SC Modulator/Demodulator Graphs $\phi = 0$



4) DSB_SC Modulator/Demodulator Graphs $\phi = 89$ degrees



5) Block Diagram for the Design task



6) Design Task Graphs

