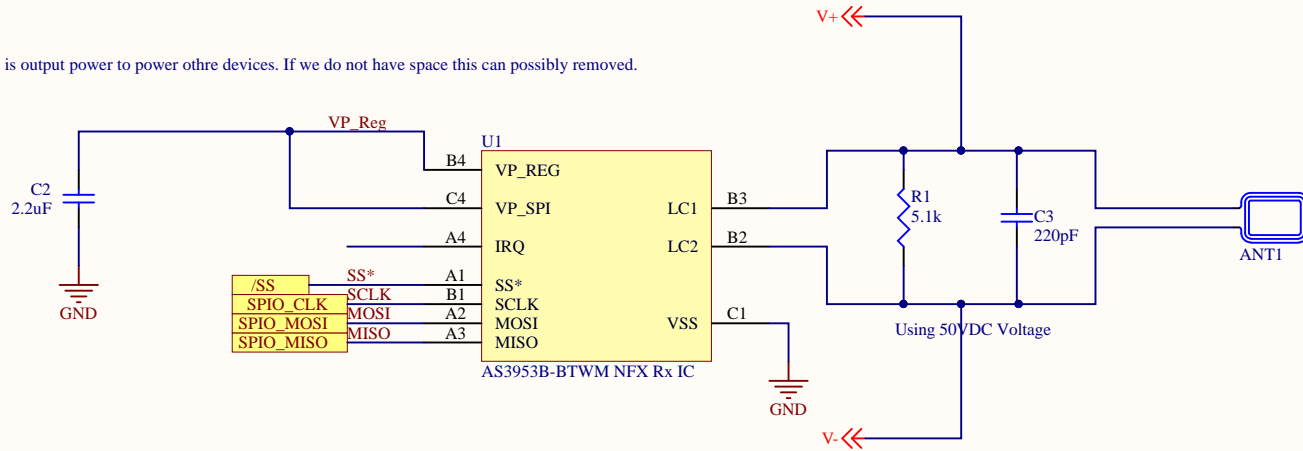


V\_REG is output power to power othre devices. If we do not have space this can possibly removed.





|   | 1   | 2   | 3 | 4 |
|---|---|---|---|---|
| A | <div><div>Revision Changes</div><div><div>SCHEMATICS</div><div>REV 01</div><div><div>*Changed U2 to Onsemi LDO NCP161BFCT250T2G</div><div>*Changed R1 to 5.1k</div><div>*Changed C3 to 220pF</div><div>*Changed C1 and C2 to 2.2uF</div><div>*Changed Q1 - G1 to GPIO from KL03</div><div>*Added GPIO to B1 pin on KL03</div></div></div></div> | <div><div>SCHEMATICS</div><div>REV 02</div><div>03/22/18</div><div><div>*changed R2 to 82ohms</div><div>*changed R3 to 47K ohms</div></div></div> |   |   |
| B |   |   |   |   |
| C |   |   |   |   |
| D |   |   |   |   |
|   | 1   | 2   | 3 | 4 |