

# FACTORY ACCEPTANCE TEST



Title: FAT-200219-TM01-Hold\_Attachment02

Parent Document ID: FAT-200219-TM01-Hold

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## 1.1 Control Module - Alarms

### 1.1.1 Acceptance Criteria

For each test item, complete the following steps:

1. Configure the alarm to exist on the PLC, if necessary.
2. If the enable condition is not 'Always' verify alarm is enabled when enable condition is true.
3. Verify the alarm and the correct 'Alarm Name' appears in the Alarm Summary window.
4. Verify the alarm group is 'PCS.Large\_Solution\_Prep.TM01\_Hold'.
5. Acknowledge the alarm. If the alarm requires reset, reset the alarm.
6. Verify the alarm is acknowledged and is removed from the Alarm Summary window.
7. Revert any configuration changes made.

If the content in the test step meets the acceptance criteria steps with no discrepancies, mark the test step as 'Pass' If the content in the test step does not meet the acceptance criteria, evaluate if the discrepancy requires a deviation resolution as detailed in FAT-200219-TM01-Hold Section 9.4 (Protocol Deviation Resolution).

### 1.1.2 Test Traceability

This test section verifies the alarms required by the control modules as specified in the following specification sections:

- Document # SPC-200219-SLP\_FS, Section 7.2 "Large Solution Prep Hold Tank (TM01-Hold) Unit"
- Document # SPC-200219-PCS\_FS, Section 7 "Object Definitions"

#### Comments

☐ N/A

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## 1.1.3 Analog Inputs

No.	Control Module	Alarm	Alarm Name	Enabled	Pass / Fail	Initial / Date
1	FI-TM01-001	Fail	[CNTR_SLP_001]FI_TM01_001@Alarms.Alm_Fail	Always		
2	LI-TM01-002	Fail	[CNTR_SLP_001]LI_TM01_002@Alarms.Alm_Fail	Always		
3	LI-TM01-002	Low Low	[CNTR_SLP_001]LI_TM01_002@Alarms.Alm_LoLo	Always		
4	LI-TM01-002	High High	[CNTR_SLP_001]LI_TM01_002@Alarms.Alm_HiHi	Always		
5	PI-TM01-001	Fail	[CNTR_SLP_001]PI_TM01_001@Alarms.Alm_Fail	Always		
6	PI-TM01-001	High High	[CNTR_SLP_001]PI_TM01_001@Alarms.Alm_HiHi	Always		

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## 1.1.4 Analog Outputs

No.	Control Module	Alarm	Alarm Name	Enabled	Pass / Fail	Initial / Date
1	FIC-TM01-001	IO Fault	[CNTR_SLP_001]FIC_TM01_001@Alarms.Alm_IOFault	Always		
2	FIC-TM01-001	Interlock Trip	[CNTR_SLP_001]FIC_TM01_001@Alarms.Alm_IntlkTrip	Always		
3	PC-TM01-001	IO Fault	[CNTR_SLP_001]PC_TM01_001@Alarms.Alm_IOFault	Always		
4	PC-TM01-001	Interlock Trip	[CNTR_SLP_001]PC_TM01_001@Alarms.Alm_IntlkTrip	Always		

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## 1.1.5 Discrete Inputs

No.	Control Module	Alarm	Alarm Name	Enabled	Pass / Fail	Initial / Date
1	LAH-TM01-001	IO Fault	[CNTR_SLP_001]LAH_TM01_001@Alarms.Alm_IOFault	Always		
2	LAH_TM01_001	Target Disagree	[CNTR_SLP_001]LAH_TM01_001@Alarms.Alm_TgtDisagree	Always		
3	PAH_TM01_001	IO Fault	[CNTR_SLP_001]PAH_TM01_001@Alarms.Alm_IOFault	Always		
4	PAH_TM01_001	Target Disagree	[CNTR_SLP_001]PAH_TM01_001@Alarms.Alm_TgtDisagree	Always		
5	XC_TM01_006	IO Fault	[CNTR_SLP_001]XC_TM01_006@Alarms.Alm_IOFault	Always		
6	ZIC_TM01_001	IO Fault	[CNTR_SLP_001]ZIC_TM01_001@Alarms.Alm_IOFault	Always		
7	ZIC_TM01_001	Target Disagree	[CNTR_SLP_001]ZIC_TM01_001@Alarms.Alm_TgtDisagree	Always		

## Comments

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## 1.1.6 Valves

No.	Control Module	Alarm	Alarm Name	Enabled	Pass / Fail	Initial / Date
1	XS_TM01_003	Interlock Trip	[CNTR_SLP_001]XS_TM01_003@Alarms.Alm_IntlkTrip	Always		
2	XS_TM01_003	IO Fault	[CNTR_SLP_001]XS_TM01_003@Alarms.Alm_IOFault	Always		
3	XS_TM01_005	Interlock Trip	[CNTR_SLP_001]XS_TM01_005@Alarms.Alm_IntlkTrip	Always		
4	XS_TM01_005	IO Fault	[CNTR_SLP_001]XS_TM01_005@Alarms.Alm_IOFault	Always		
5	XS_TM01_008A	Interlock Trip	[CNTR_SLP_001]XS_TM01_008A@Alarms.Alm_IntlkTrip	Always		
6	XS_TM01_008A	IO Fault	[CNTR_SLP_001]XS_TM01_008A@Alarms.Alm_IOFault	Always		
7	XS_TM01_008B	Interlock Trip	[CNTR_SLP_001]XS_TM01_008B@Alarms.Alm_IntlkTrip	Always		

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No.	Control Module	Alarm	Alarm Name	Enabled	Pass / Fail	Initial / Date
8	XS_TM01_008B	IO Fault	[CNTR_SLP_001]XS_TM01_008B@Alarms.Alm_IOFault	Always		
9	XS_TM01_008C	Interlock Trip	[CNTR_SLP_001]XS_TM01_008C@Alarms.Alm_IntlkTrip	Always		
10	XS_TM01_008C	IO Fault	[CNTR_SLP_001]XS_TM01_008C@Alarms.Alm_IOFault	Always		
11	XS_TM01_009	Interlock Trip	[CNTR_SLP_001]XS_TM01_009@Alarms.Alm_IntlkTrip	Always		
12	XS_TM01_009	IO Fault	[CNTR_SLP_001]XS_TM01_009@Alarms.Alm_IOFault	Always		
13	XS_TM01_010A	Interlock Trip	[CNTR_SLP_001]XS_TM01_010A@Alarms.Alm_IntlkTrip	Always		
14	XS_TM01_010A	IO Fault	[CNTR_SLP_001]XS_TM01_010A@Alarms.Alm_IOFault	Always		
15	XS_TM01_010B	Interlock Trip	[CNTR_SLP_001]XS_TM01_010B@Alarms.Alm_IntlkTrip	Always		

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No.	Control Module	Alarm	Alarm Name	Enabled	Pass / Fail	Initial / Date
16	XS_TM01_010B	IO Fault	[CNTR_SLP_001]XS_TM01_010B@Alarms.Alm_IOFault	Always		
17	XS_TM01_011	Interlock Trip	[CNTR_SLP_001]XS_TM01_011@Alarms.Alm_IntlkTrip	Always		
18	XS_TM01_011	IO Fault	[CNTR_SLP_001]XS_TM01_011@Alarms.Alm_IOFault	Always		
19	XS_TM01_021A	Interlock Trip	[CNTR_SLP_001]XS_TM01_021A@Alarms.Alm_IntlkTrip	Always		
20	XS_TM01_021A	IO Fault	[CNTR_SLP_001]XS_TM01_021A@Alarms.Alm_IOFault	Always		
21	XS_TM01_021B	Interlock Trip	[CNTR_SLP_001]XS_TM01_021B@Alarms.Alm_IntlkTrip	Always		
22	XS_TM01_021B	IO Fault	[CNTR_SLP_001]XS_TM01_021B@Alarms.Alm_IOFault	Always		
23	XS_TM01_022A	Interlock Trip	[CNTR_SLP_001]XS_TM01_022A@Alarms.Alm_IntlkTrip	Always		

## Comments

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No.	Control Module	Alarm	Alarm Name	Enabled	Pass / Fail	Initial / Date
24	XS_TM01_022A	IO Fault	[CNTR_SLP_001]XS_TM01_022A@Alarms.Alm_IOFault	Always		
25	XS_TM01_022B	Interlock Trip	[CNTR_SLP_001]XS_TM01_022B@Alarms.Alm_IntlkTrip	Always		
26	XS_TM01_022B	IO Fault	[CNTR_SLP_001]XS_TM01_022B@Alarms.Alm_IOFault	Always		
27	XS_TM01_024	Interlock Trip	[CNTR_SLP_001]XS_TM01_024@Alarms.Alm_IntlkTrip	Always		
28	XS_TM01_024	IO Fault	[CNTR_SLP_001]XS_TM01_024@Alarms.Alm_IOFault	Always		
29	XS_TM01_026	Interlock Trip	[CNTR_SLP_001]XS_TM01_026@Alarms.Alm_IntlkTrip	Always		
30	XS_TM01_026	IO Fault	[CNTR_SLP_001]XS_TM01_026@Alarms.Alm_IOFault	Always		
31	XS_TM01_029	Interlock Trip	[CNTR_SLP_001]XS_TM01_029@Alarms.Alm_IntlkTrip	Always		

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No.	Control Module	Alarm	Alarm Name	Enabled	Pass / Fail	Initial / Date
32	XS_TM01_029	IO Fault	[CNTR_SLP_001]XS_TM01_029@Alarms.Alm_IOFault	Always		
33	XS_TM01_031	Interlock Trip	[CNTR_SLP_001]XS_TM01_031@Alarms.Alm_IntlkTrip	Always		
34	XS_TM01_031	IO Fault	[CNTR_SLP_001]XS_TM01_031@Alarms.Alm_IOFault	Always		
35	XS_TM01_033	Interlock Trip	[CNTR_SLP_001]XS_TM01_033@Alarms.Alm_IntlkTrip	Always		
36	XS_TM01_033	IO Fault	[CNTR_SLP_001]XS_TM01_033@Alarms.Alm_IOFault	Always		
37	XS_TM01_034	Interlock Trip	[CNTR_SLP_001]XS_TM01_034@Alarms.Alm_IntlkTrip	Always		
38	XS_TM01_034	IO Fault	[CNTR_SLP_001]XS_TM01_034@Alarms.Alm_IOFault	Always		
39	XS_TM01_035	Interlock Trip	[CNTR_SLP_001]XS_TM01_035@Alarms.Alm_IntlkTrip	Always		

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No.	Control Module	Alarm	Alarm Name	Enabled	Pass / Fail	Initial / Date
40	XS_TM01_035	IO Fault	[CNTR_SLP_001]XS_TM01_035@Alarms.Alm_IOFault	Always		

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## 1.2 Equipment Module - Key Configuration Parameters

### 1.2.1 Acceptance Criteria

For each test item, verify the content in the table matches the same values as indicated by the HMI faceplate for the listed equipment module. If the content matches, mark the test step as 'Pass'. If the content does not match, evaluate if the discrepancy requires a deviation resolution as detailed in FAT-200219-TM01-Hold Section 9.4 (Protocol Deviation Resolution).

### 1.2.2 Test Traceability

This test section verifies the key configuration parameters required by the equipment modules as specified in the following specification sections:

- Document # SPC-200219-SLP\_FS, Section 7.2 "Large Solution Prep Hold Tank (TM01-Hold) Unit"
- Document # SPC-200219-SLP\_DS, Section 6.2 "Large Solution Prep Hold Tank (TM01-Hold) Unit"

#### Comments

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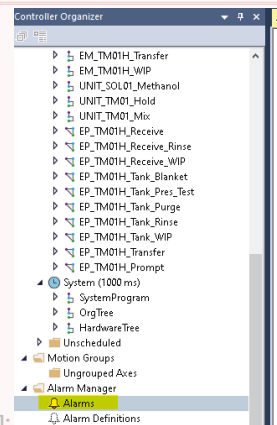
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## 1.2.3 Generic Parameters

No.	EM ID	Interlock Trip Alarm		Device Alarm		Report Data Alarm		Extended Alarm		Pass / Fail	Initial / Date
		Has	Stop	Has	Stop	Has	Stop	Has	Stop		
9	EM-TM01-Hold-Methanol	YES	All Commands	NO	NO	NO	NO	YES	NO		
10	EM-TM01-Hold-Nitrogen	YES	All Commands	NO	NO	NO	NO	YES	NO		
11	EM-TM01-Hold-Receive	YES	All Commands	NO	NO	NO	NO	YES	NO		
12	EM-TM01-Hold-Transfer	YES	All Commands	NO	NO	NO	NO	YES	NO		
13	EM-TM01-Hold-WIP	YES	All Commands	NO	NO	NO	NO	YES	NO		

Commented [JP1]: How to enable interlock alarm for EMs/EPs?



Commented [JP2R11]:

## Comments

□ N/A

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## 1.3 Equipment Module - Alarms

### 1.3.1 Acceptance Criteria

For each test item, complete the following steps:

1. Configure the alarm to exist on the PLC, if necessary.
2. Verify the alarm and the correct 'Alarm Name' appears in the Alarm Summary window.
3. Verify the alarm group is 'PCS.Large\_Solution\_Prep.TM01\_Hold'.
4. Acknowledge the alarm. If the alarm requires reset, reset the alarm.
5. Verify the alarm is acknowledged and is removed from the Alarm Summary window.
6. Revert any configuration changes made.

If the content in the test step meets the acceptance criteria steps with no discrepancies, mark the test step as 'Pass' If the content in the test step does not meet the acceptance criteria, evaluate if the discrepancy requires a deviation resolution as detailed in FAT-200219-TM01-Hold Section 9.4 (Protocol Deviation Resolution).

### 1.3.2 Test Traceability

This test section verifies the alarms required by the equipment modules as specified in the following specification sections:

- Document # SPC-200219-SLP\_FS, Section 7.2 "Large Solution Prep Hold Tank (TM01-Hold) Unit"
- Document # SPC-200219-PCS\_FS, Section 7 "Object Definitions"

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### 1.3.3 EM-TM01-Hold-Methanol

No.	Alarm	Alarm Name	Pass / Fail	Initial / Date
1	Interlock Trip	[CNTR_SLP_001]Program:EM_TM01H_Methanol.EM@Alarms.Alm_IntlkTrip		

### 1.3.4 EM-TM01-Hold-Nitrogen

No.	Alarm	Alarm Name	Pass / Fail	Initial / Date
1	Interlock Trip	[CNTR_SLP_001]Program:EM_TM01H_Nitrogen.EM@Alarms.Alm_IntlkTrip		

### 1.3.5 EM-TM01-Hold-Receive

No.	Alarm	Alarm Name	Pass / Fail	Initial / Date
1	Interlock Trip	[CNTR_SLP_001]Program:EM_TM01H_Receive.EM@Alarms.Alm_IntlkTrip		

#### Comments

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### 1.3.6 EM-TM01-Hold-Transfer

No.	Alarm	Alarm Name	Pass / Fail	Initial / Date
1	Interlock Trip	[CNTR_SLP_001]Program:EM_TM01H_Transfer.EM@Alarms.Alm_IntlkTrip		

### 1.3.7 EM-TM01-Hold-WIP

No.	Alarm	Alarm Name	Pass / Fail	Initial / Date
1	Interlock Trip	[CNTR_SLP_001]Program:EM_TM01H_WIP.EM@Alarms.Alm_IntlkTrip		

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## 1.4 Equipment Phase - Key Configuration Parameters

### 1.4.1 Acceptance Criteria

For each test item, verify the content in the table matches the same values as indicated by the HMI faceplate for the listed equipment phase. If the content matches, mark the test step as 'Pass'. If the content does not match, evaluate if the discrepancy requires a deviation resolution as detailed in FAT-200219-TM01-Hold Section 9.4 (Protocol Deviation Resolution).

### 1.4.2 Test Traceability

This test section verifies the key configuration parameters required by the equipment phases as specified in the following specification sections:

- Document # SPC-200219-SLP\_FS, Section 7.2 "Large Solution Prep Hold Tank (TM01-Hold) Unit"
- Document # SPC-200219-SLP\_DS, Section 6.2 "Large Solution Prep Hold Tank (TM01-Hold) Unit"

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No.	EP ID	Interlock Trip Alarm		Device Alarm		Report Data Alarm		Timeout Alarm		Pass / Fail	Initial / Date
		Has	Stop	Has	Stop	Has	Stop	Has	Stop		
1	EP-TM01-Hold-Prompt	NO	NO	NO	NO	NO	NO	NO	NO		
2	EP-TM01-Hold-Tank-Blanket	YES	NO	NO	NO	NO	NO	YES	NO		
3	EP-TM01-Hold-Tank-Pressure-Test	YES	NO	NO	NO	NO	NO	YES	NO		
4	EP-TM01-Hold-Tank-Purge	YES	NO	NO	NO	NO	NO	YES	NO		
5	EP-TM01-Hold-Tank-Rinse	YES	NO	NO	NO	NO	NO	YES	NO		
6	EP-TM01-Hold-Tank-WIP	YES	NO	NO	NO	NO	NO	YES	NO		
7	EP-TM01-Hold-Transfer	YES	NO	NO	NO	NO	NO	YES	NO		
8	EP-TM01-Hold-Receive	YES	NO	NO	NO	NO	NO	YES	NO		

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No.	EP ID	Interlock Trip Alarm		Device Alarm		Report Data Alarm		Timeout Alarm		Pass / Fail	Initial / Date
		Has	Stop	Has	Stop	Has	Stop	Has	Stop		
9	EP-TM01-Hold-Receive-Rinse	YES	NO	NO	NO	NO	NO	YES	NO		
10	EP-TM01-Hold-Receive-WIP	YES	NO	NO	NO	NO	NO	YES	NO		

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