1.1 Control Module - Interlocks

1.1.1 Acceptance Criteria

For each test section, step No. 0 must be completed before completing the subsequent steps if it exists. Complete the 'Step Action' in Step 0, record the results in the 'Actual Results', and verify the 'Actual Results' match the 'Expected Results'. If the content matches, mark the test step as 'Pass'. If the content does not match, evaluate if the discrepancy requires a deviation resolution as detailed in Section 9.4 (Protocol Deviation Resolution).

Verify when interlocked by the listed system stop or other interlock condition, the control module is commanded to its 'Fail State' from its 'Active State' and cannot be commanded to an 'Active State' until the interlock condition is removed and reset (if applicable). If a result is unexpected, evaluate if the discrepancy requires a deviation resolution as detailed in FAT-200219-TM01-Hold Section 9.4 (Protocol Deviation Resolution).

1.1.2 Test Traceability

This test section verifies the key configuration parameters required by the control modules as specified in the following specification sections:

- Document # SPC-200219-SLP_FS, Section 7.2 "Large Solution Prep Hold Tank (TM01-Hold) Unit"

Comments			
			□ N/A
Reviewer Signature		**Review Date**	

1.1.3 Process Interlocks

No.	Control Module	Interlock Condition	Active State	Fail State	Requires Reset	Pass / Fail	Initial / Date
1	PC-TM01-001	Room 3105 Low Oxygen Alarm	CV > 0%	CV = 0%	NO		
2	PC-TM01-001	BAS PLC Connection Fault	CV > 0%	CV = 0%	NO		
3	PC-TM01-001	PI-TM01-001 High-High Status	CV > 0%	CV = 0%	NO		
4	PC-TM01-001	PI-TM01-001 IO Fault	CV > 0%	CV = 0%	NO		
5	PC-TM01-001	PAH-TM01-001 High Status	CV > 0%	CV = 0%	NO		
6	PC-TM01-001	PAH-TM01-001 IO Fault	CV > 0%	CV = 0%	NO		
7	PC-TM01-001	ZIC-TM01-001 Open Status	CV > 0%	CV = 0%	NO		
8	PC-TM01-001	ZIC-TM01-001 IO Fault	CV > 0%	CV = 0%	NO		

Comments			
□ N/A			
Reviewer Signature		**Review Date**	