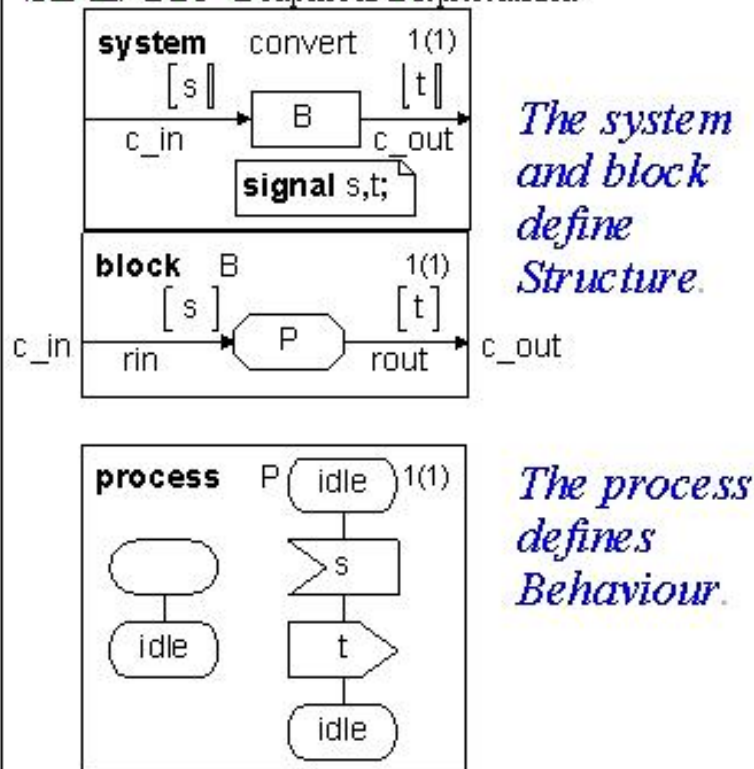


Drawings compared with text

SDL/GR Graphical Representation



```

system convert;
signal s,t;
channel c_out nodelay from B to env with t;
endchannel c_in;
channel c_in nodelay from env to B with s;
endchannel c_out;
block B referenced;
endsystem convert;

block B;
channel rin nodelay from env to P with s;
endchannel rin;
channel rout nodelay from P to env with t;
endchannel rout;
process P referenced;
connect c_out and rout;
connect c_in and rin;
endblock B;

process P;
start;
nextstate idle;
state idle;
input s;
output t;
nextstate idle;
endstate idle;
endprocess P;
    
```

SDL/PR textual Phrase Representation
only used for interchange