

Effortlessly Build High-Performance AI/ML Pipelines With Accelerator Chaining and K8s Native Tech



Output

ISB handle the communication

UDF

Vertex

📆 Read/ 👩

Support deployment from edge to cloud

(*1:)Open Kasugai (Previous Project)

numaproj-demo (Current Project)

Sink

Vertex

Read

Introduction

- Data Centers require higher performance while reducing power consumption
- In Al processing, accelerators are only used for certain tasks(e.g. Inference)
- It's important to leverage suitable accelerators for each Al processing task



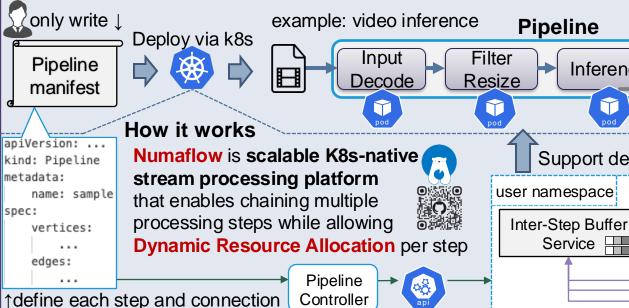
Accelerator Chaining(*1)

 Preparing a processing infrastructure using accelerators is difficult, we provide a method to effortlessly

build processing pipelines

PoC with K8s Native Tech (Numaflow and DRA)

This project is in progress to reimplement (*1:) the presentation at KubeCon EU 24 using OSS



New Scheduler Function

Source

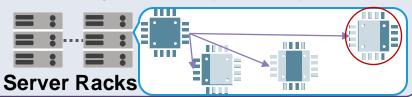
Vertex

 As the inter-pod communication speed increases, the range of allocatable resources expands

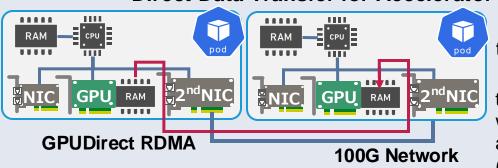
Inference

Service ##

 The scheduler allocates resources considering resource efficiency and App latency constraint while dividing resources dynamically



Future Work Direct Data Transfer for Accelerator Chaining



- Realize Data transfer bypassing CPU to utilize "GPUDirect RDMA" and "DRA"
- Our work towards enabling the assignment of the 2nd NIC, which is on the same PCIe bus as the GPU(*2), to the pod using DRA (= improve CNI driver for DRA)
- *2: The current GPUDirect only works when two devices share the same upstream PCI Express root complex