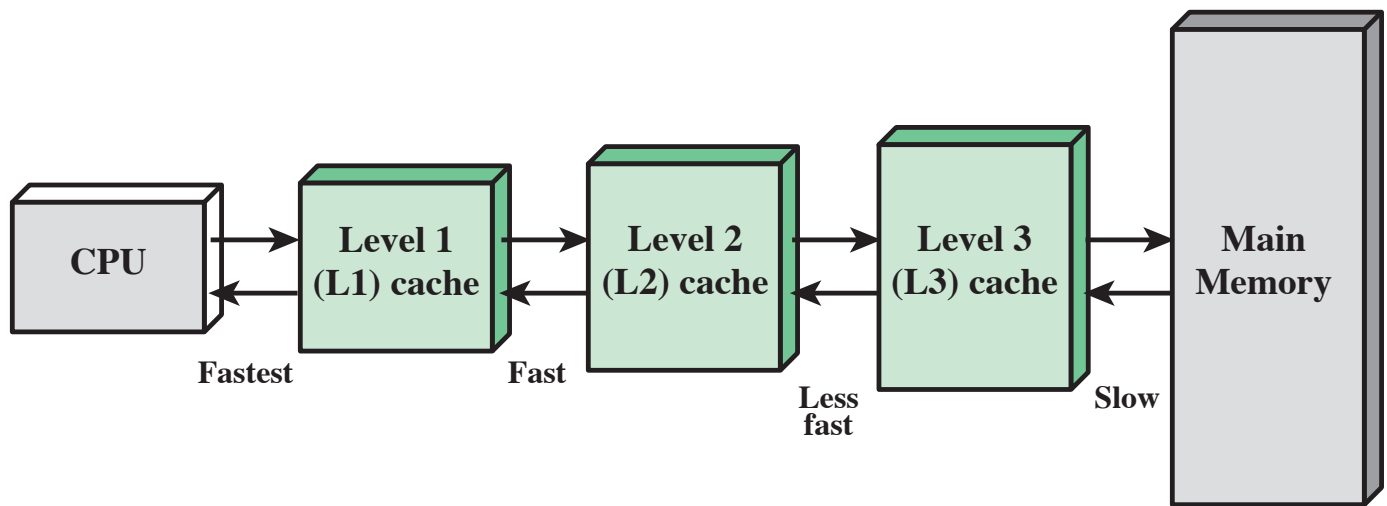


(a) Single cache



(b) Three-level cache organization

Figure 4.3 Cache and Main Memory