

# Exemplos de código de montagem

## Arquitetura Intel

Endereço	OPcode	Operandos
7C90EBAF	pushfd	
7C90EBB0	sub	esp,2D0h
7C90EBB6	mov	dword ptr [ebp+FFFFFFDDCh],eax
7C90EBBC	mov	dword ptr [ebp+FFFFFFDD8h],ecx
7C90EBC2	mov	eax,dword ptr [ebp+8]
7C90EBC5	mov	ecx,dword ptr [ebp+4]
7C90EBC8	mov	dword ptr [eax+0Ch],ecx
7C90EBCB	lea	eax,[ebp+FFFFFFD2Ch]
7C90EBD1	mov	dword ptr [eax+000000B8h],ecx
7C90EBD7	mov	dword ptr [eax+000000A4h],ebx
7C90EBDD	mov	dword ptr [eax+000000A8h],edx
7C90EBE3	mov	dword ptr [eax+000000A0h],esi
7C90EBE9	mov	dword ptr [eax+0000009Ch],edi
7C90EBEF	lea	ecx,[ebp+0Ch]
7C90EBF2	mov	dword ptr [eax+000000C4h],ecx
7C90EBF8	mov	ecx,dword ptr [ebp]
7C90EBFB	mov	dword ptr [eax+000000B4h],ecx
7C90EC01	mov	ecx,dword ptr [ebp-4]
7C90EC04	mov	dword ptr [eax+000000C0h],ecx
7C90EC0A	mov	word ptr [eax+000000BCh],cs
7C90EC10	mov	word ptr [eax+00000098h],ds
7C90EC16	mov	word ptr [eax+00000094h],es
7C90EC1C	mov	word ptr [eax+00000090h],fs
7C90EC22	mov	word ptr [eax+0000008Ch],gs
7C90EC28	mov	word ptr [eax+000000C8h],ss
7C90EC2E	mov	dword ptr [eax],10007h
7C90EC34	push	1
7C90EC36	push	eax
7C90EC37	push	dword ptr [ebp+8]
7C90EC3A	call	7C90E252
7C90EC3F	sub	esp,20h
7C90EC42	mov	dword ptr [esp],eax
7C90EC45	mov	dword ptr [esp+4],1
7C90EC4D	mov	dword ptr [esp+10h],0
7C90EC55	mov	eax,dword ptr [ebp+8]
7C90EC58	mov	dword ptr [esp+8],eax
7C90EC5C	mov	eax,esp
7C90EC5E	push	eax
7C90EC5F	call	7C90EBAC

# Arquitetura MIPS

<pre>swap(int v[], int k); {     int temp;     temp = v[k];     v[k] = v[k+1];     v[k+1] = temp; }</pre>	<pre>swap:     muli    \$2, \$5, 4     add     \$2, \$4, \$2     lw      \$15, 0(\$2)     lw      \$16, 4(\$2)     sw      \$16, 0(\$2)     sw      \$15, 4(\$2)      jr      \$31</pre>
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v0 2 Expression evaluation and

v1 3 results of a function

a0 4 Argument 1(**v**)

a1 5 Argument 2(**k**)

t7 15 Temporary (not preserved across call)

s0 16 Saved temporary (preserved across call)